

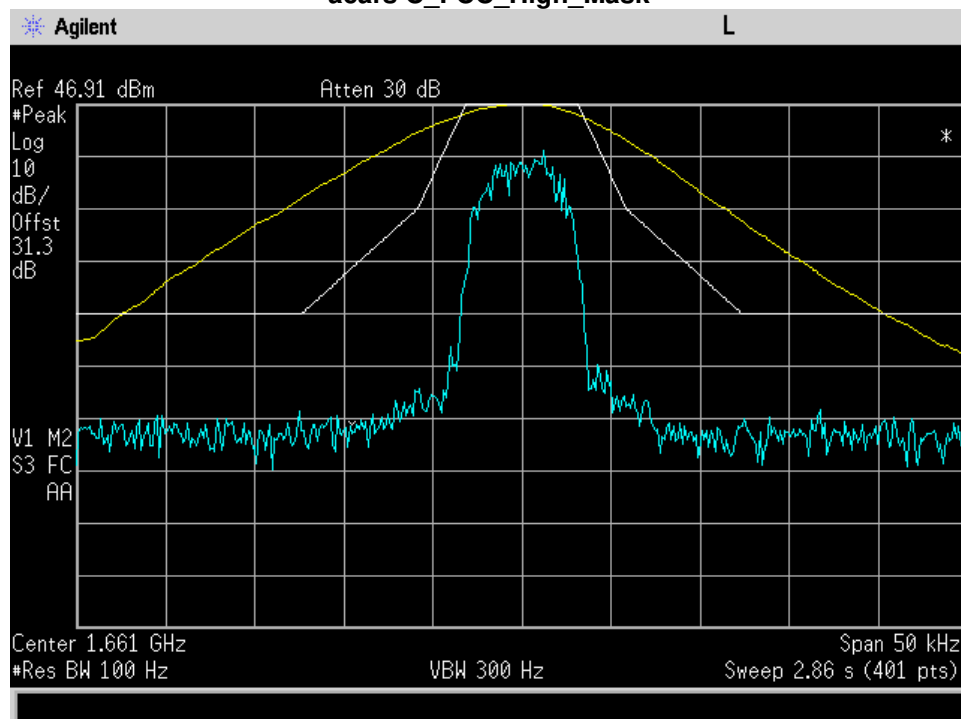


# Annex C

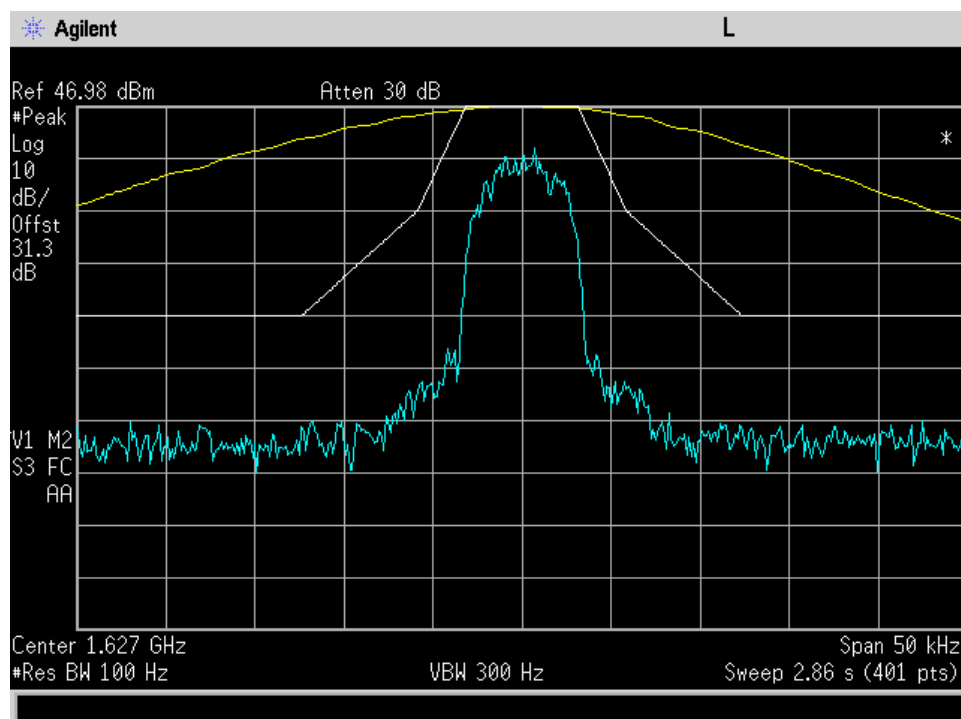
## Emissions Masks



### acars C\_FCC\_High\_Mask

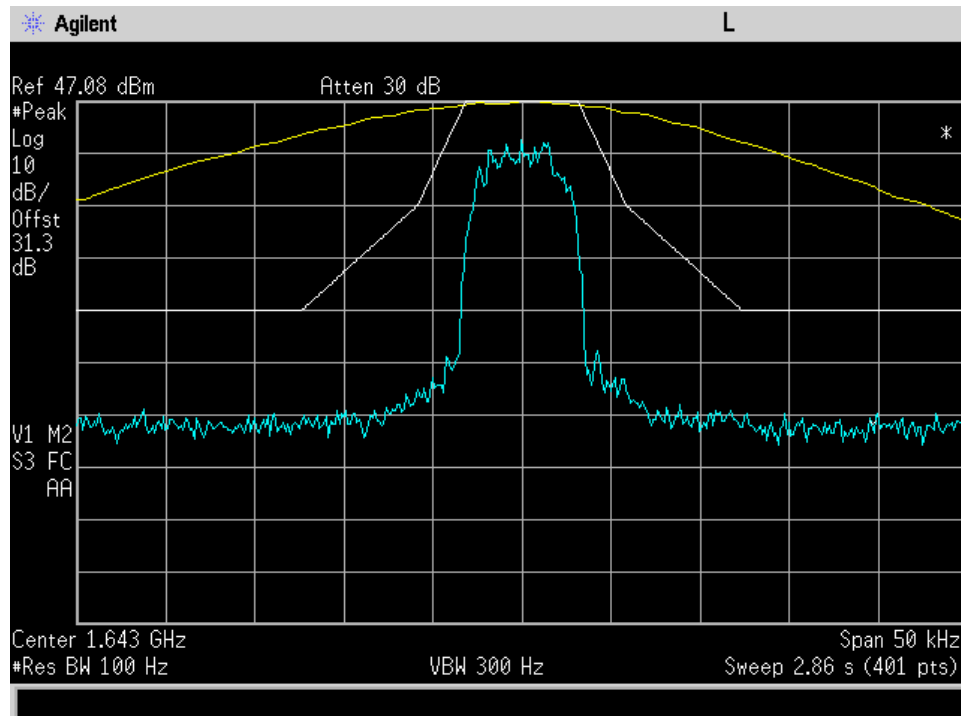


### acars C\_FCC\_Low\_Mask

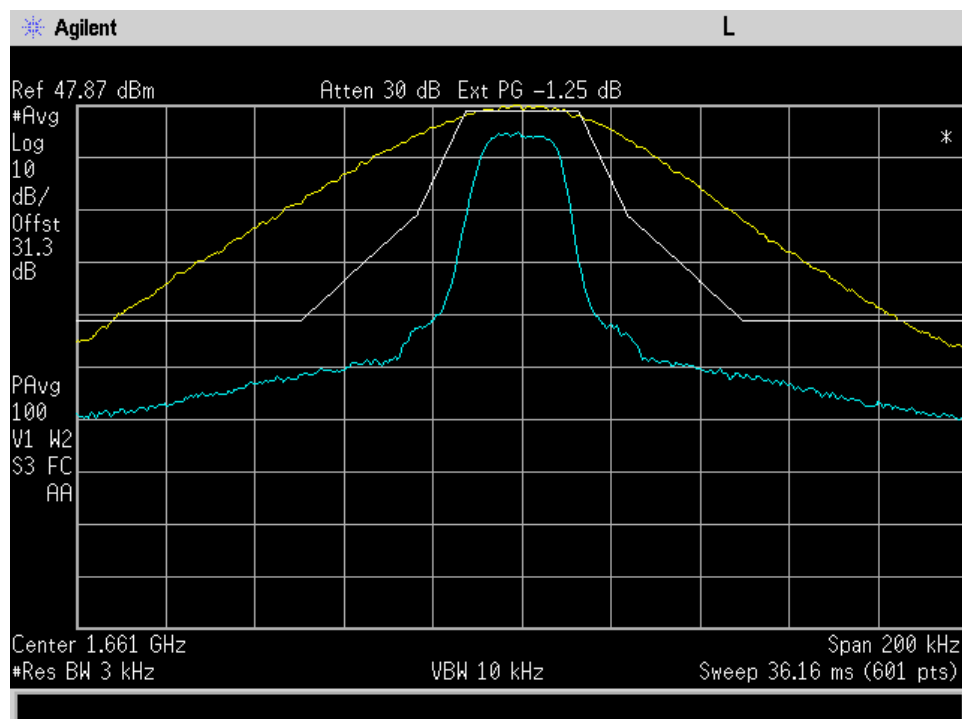




### acars C\_FCC\_Mid\_Mask

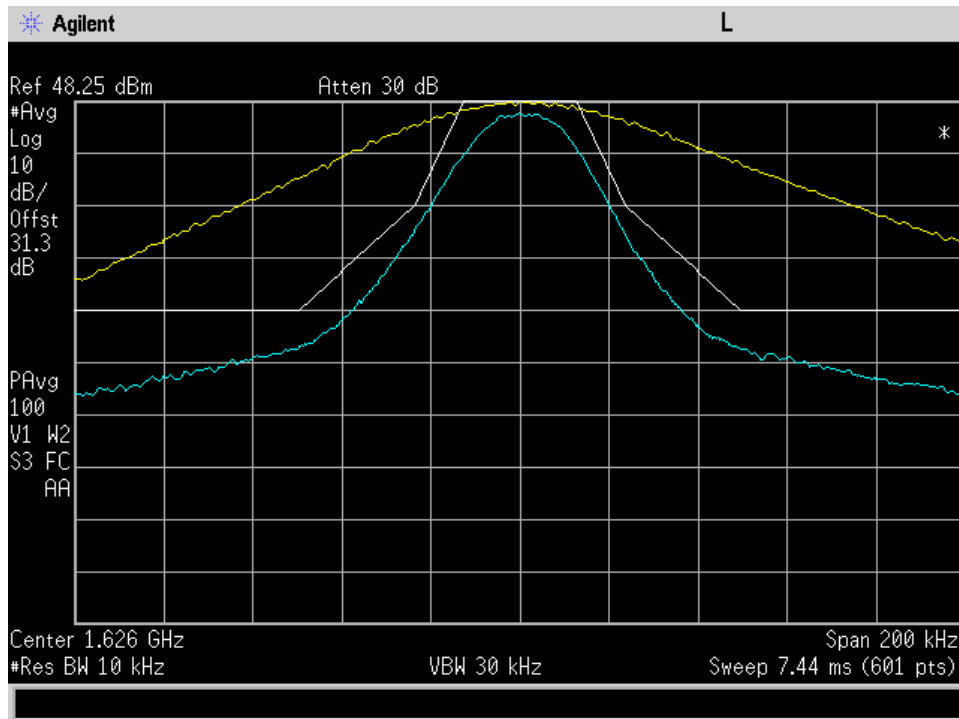


### RT05Q\_FCC\_High\_Mask

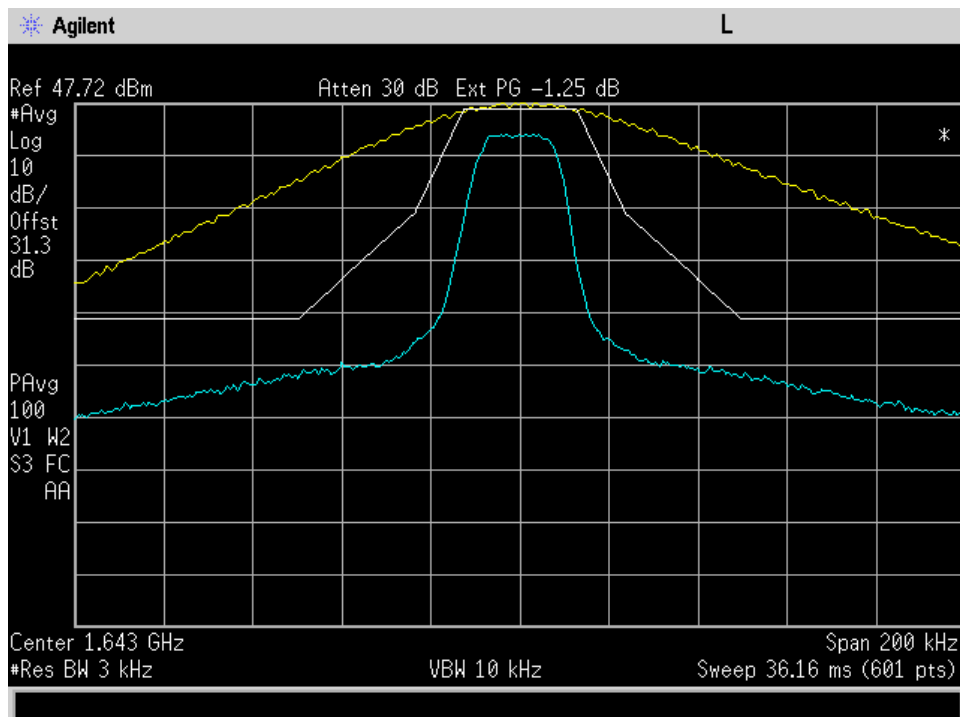




### RT05Q\_FCC\_Low\_Mask

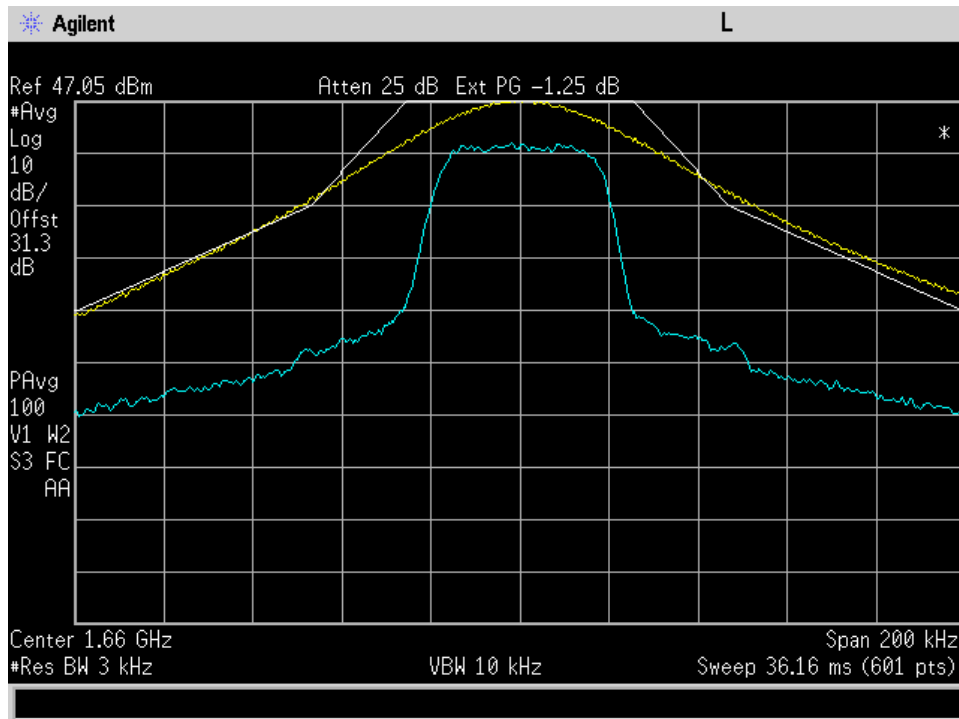


### RT05Q\_FCC\_Mid\_Mask

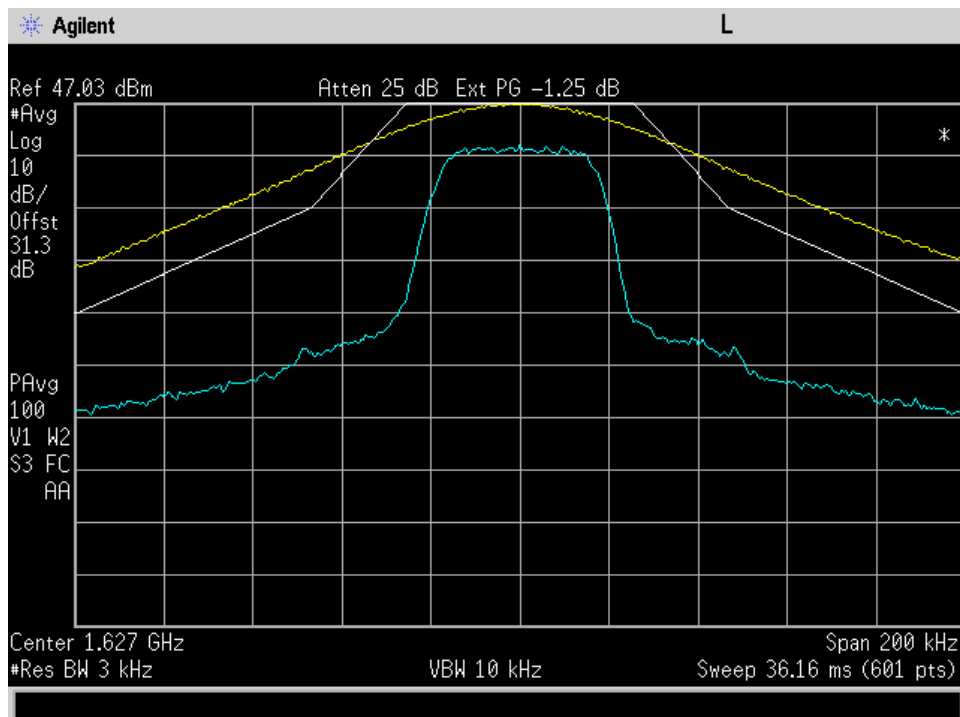




### RT1Q\_FCC\_High\_Mask

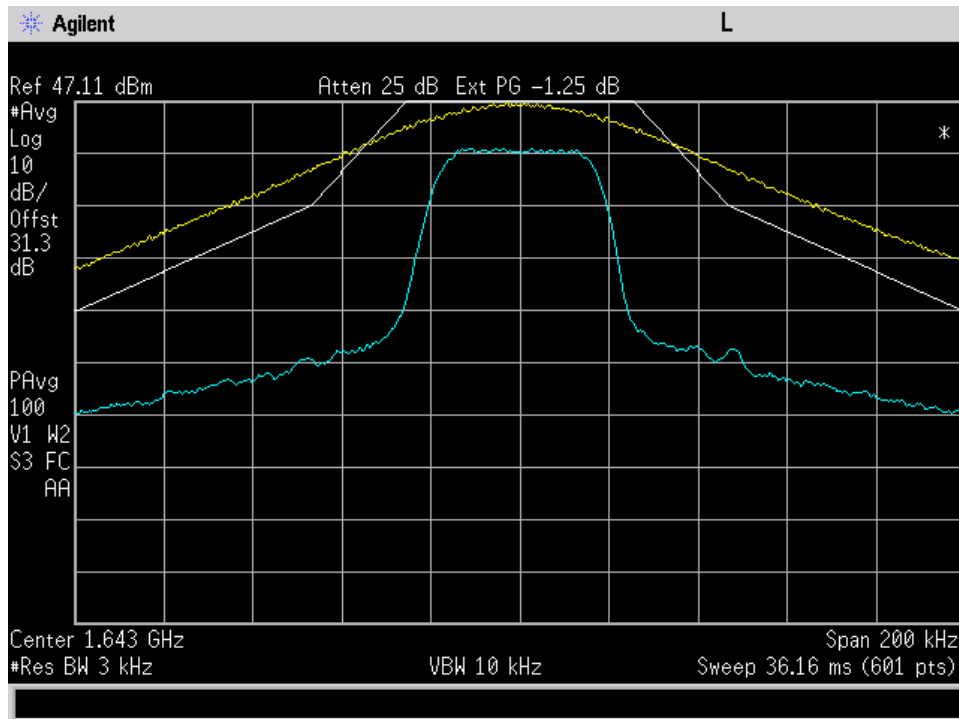


### RT1Q\_FCC\_Low\_Mask

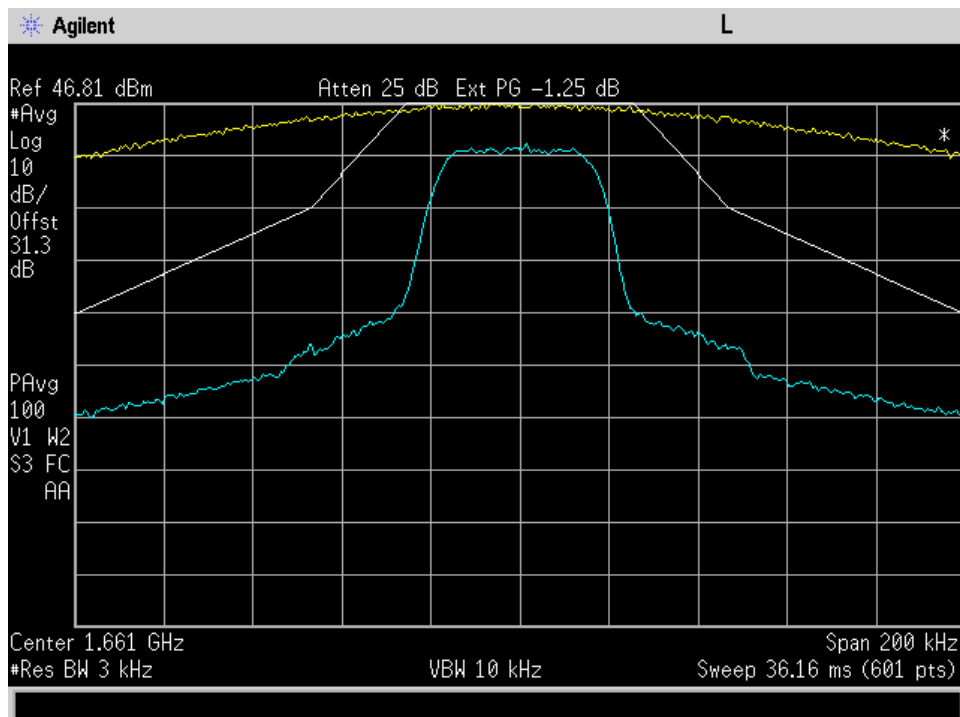




### RT1Q\_FCC\_Mid\_Mask

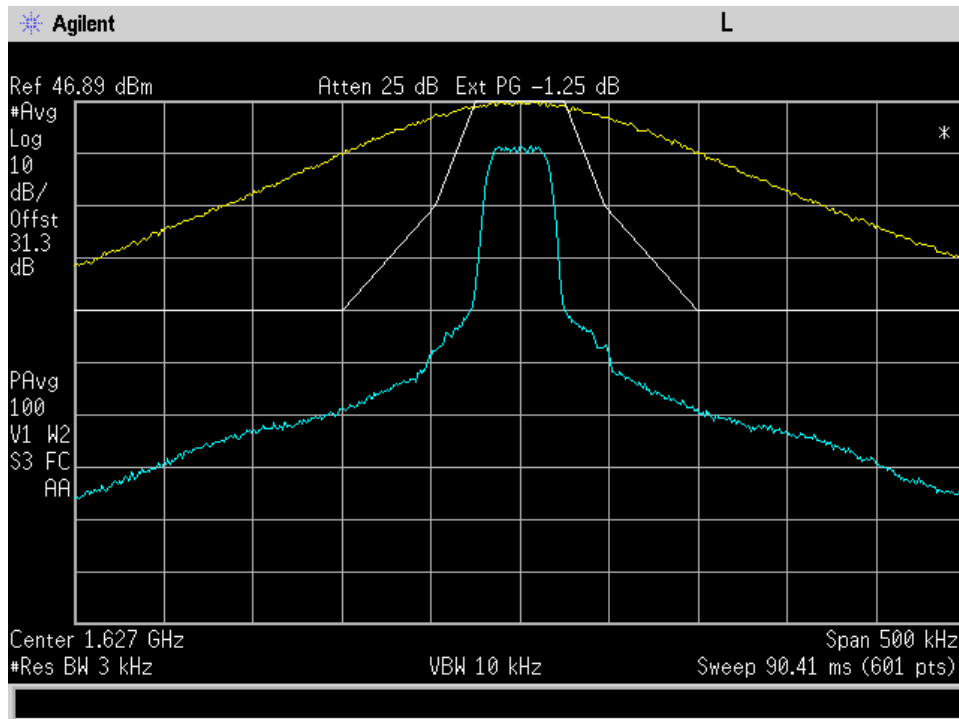


### RT1X\_FCC\_High\_Mask

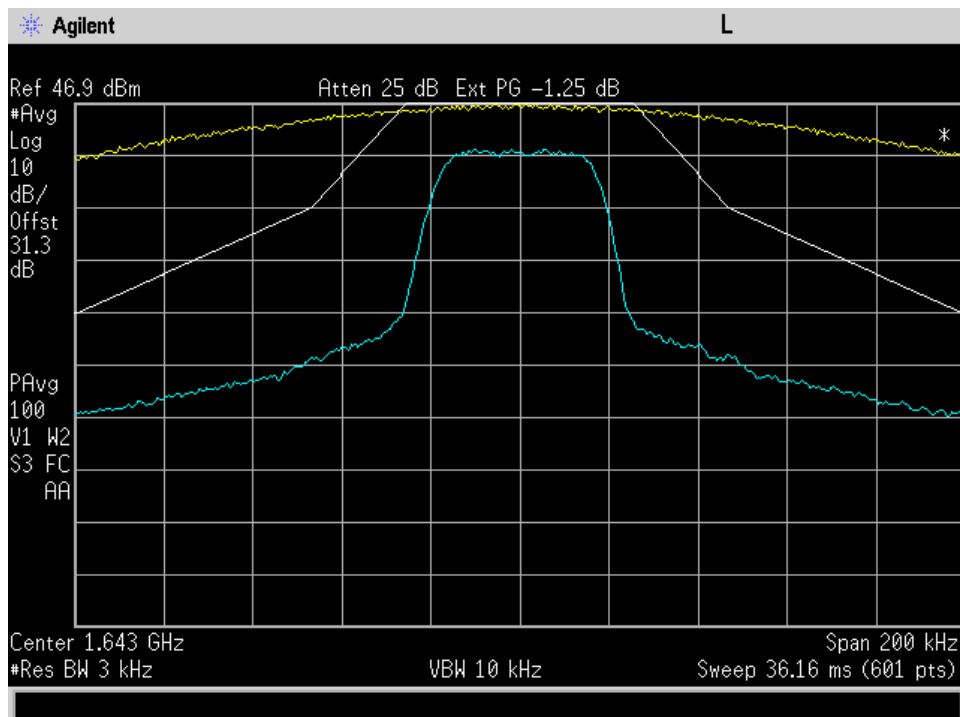




### RT1X\_FCC\_Low\_Mask

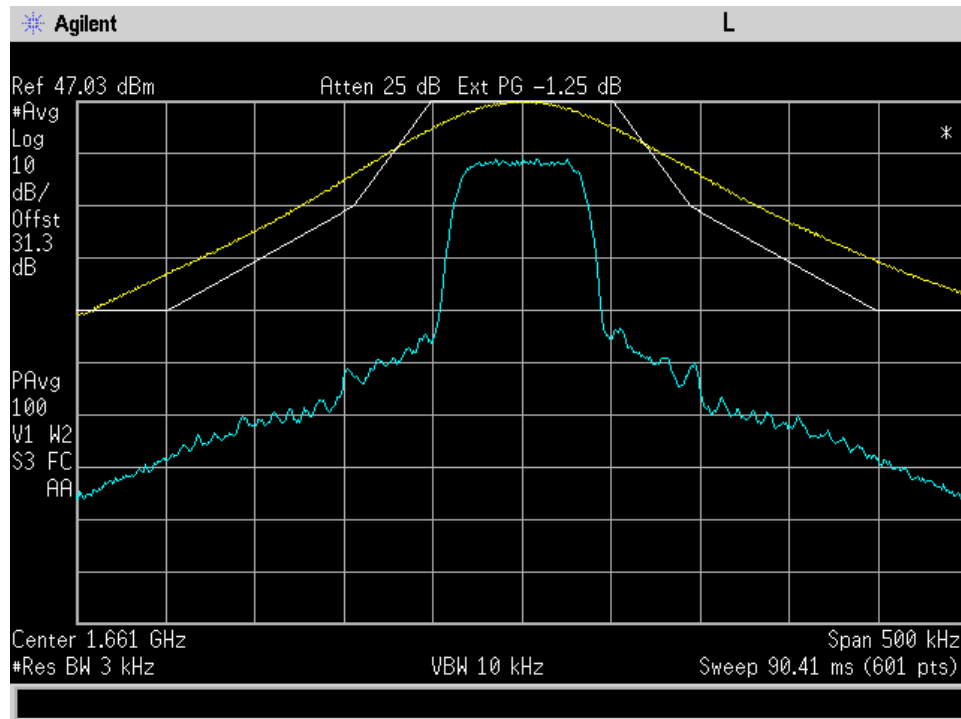


### RT1X\_FCC\_Mid\_Mask

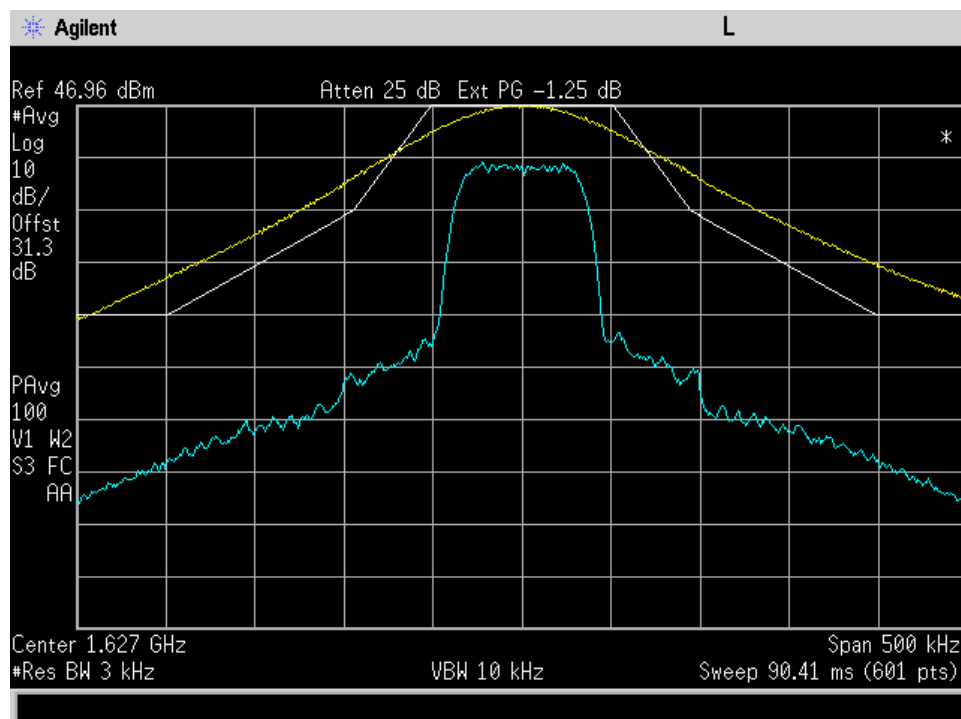




### RT2Q\_FCC\_High\_Mask



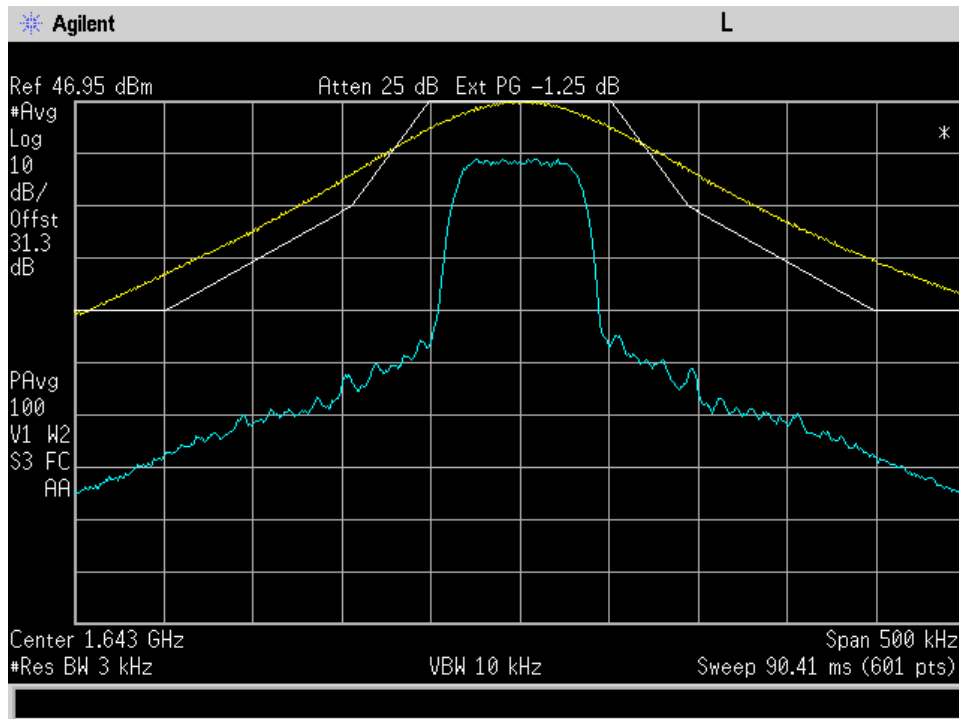
### RT2Q\_FCC\_Low\_Mask



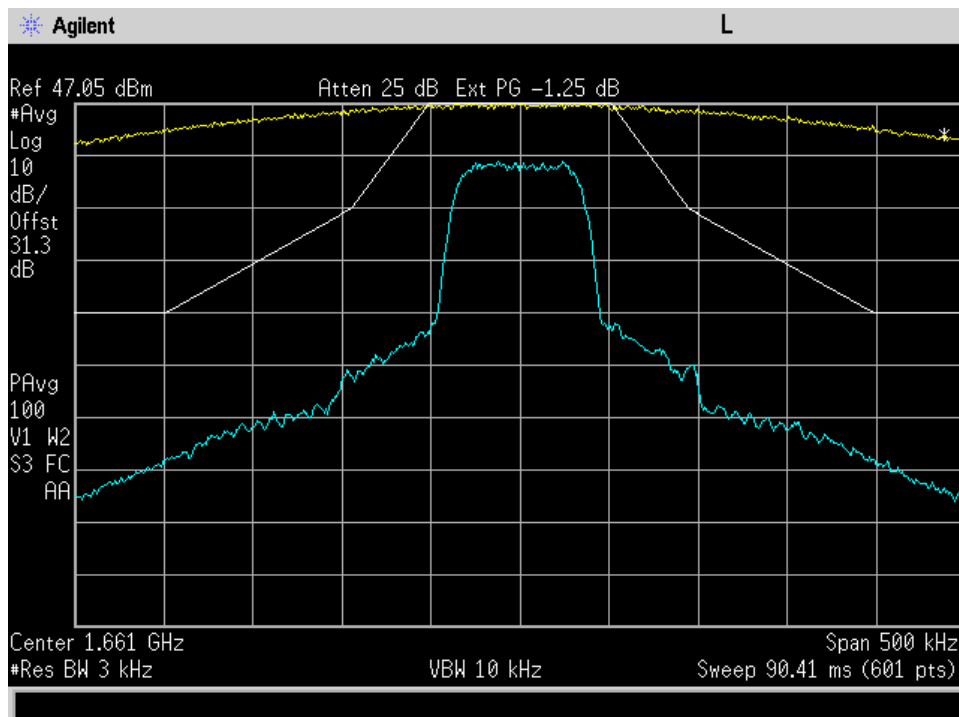




### RT2Q\_FCC\_Mid\_Mask

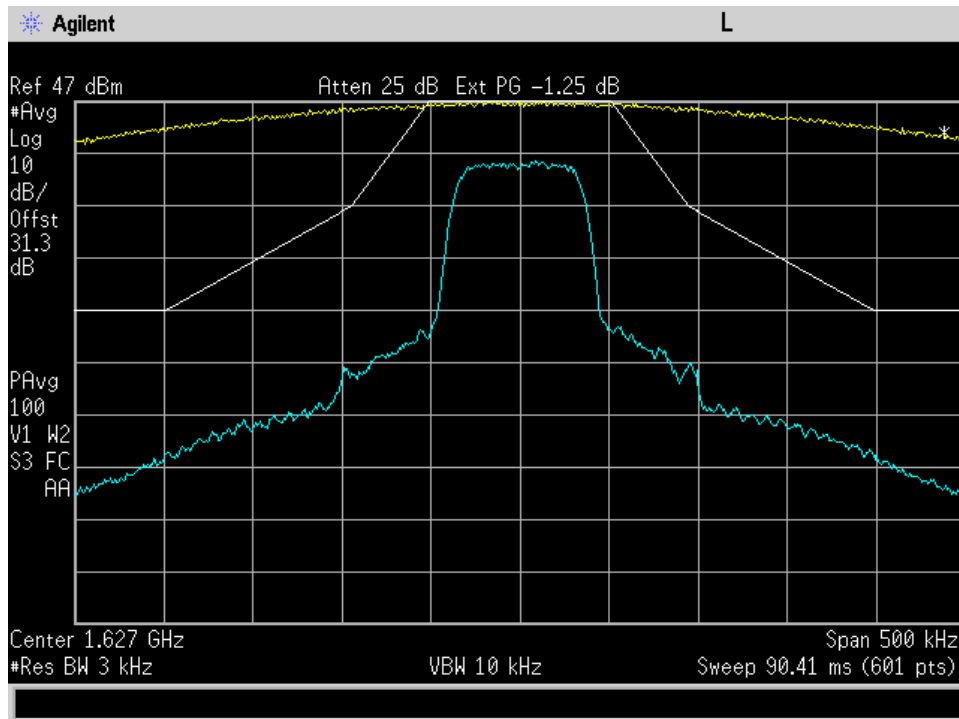


### RT2X\_FCC\_High\_Mask

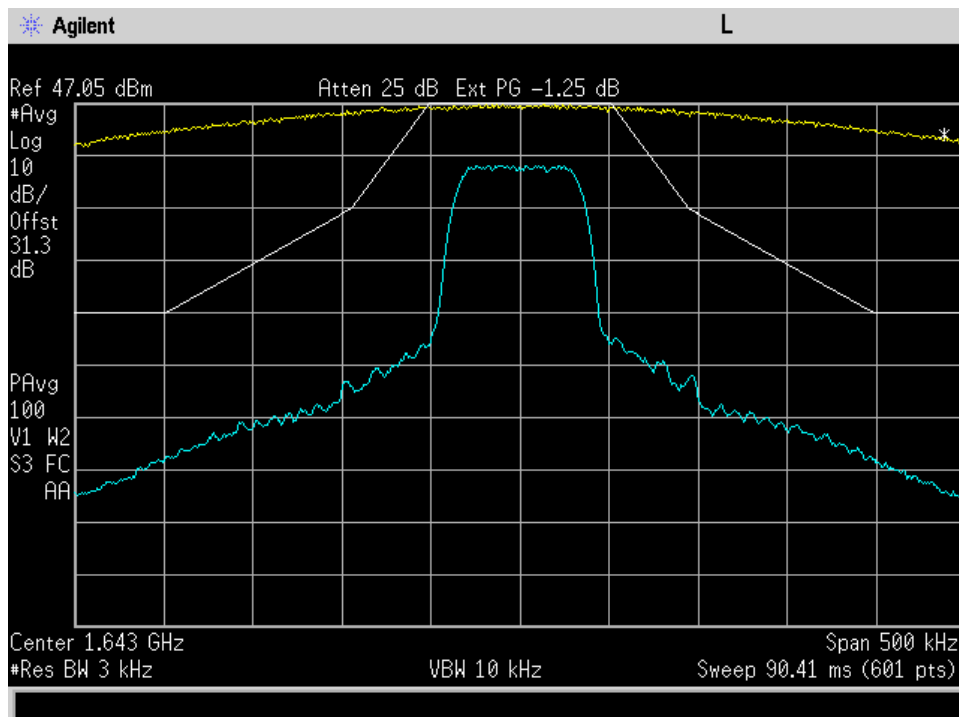




### RT2X\_FCC\_Low\_Mask

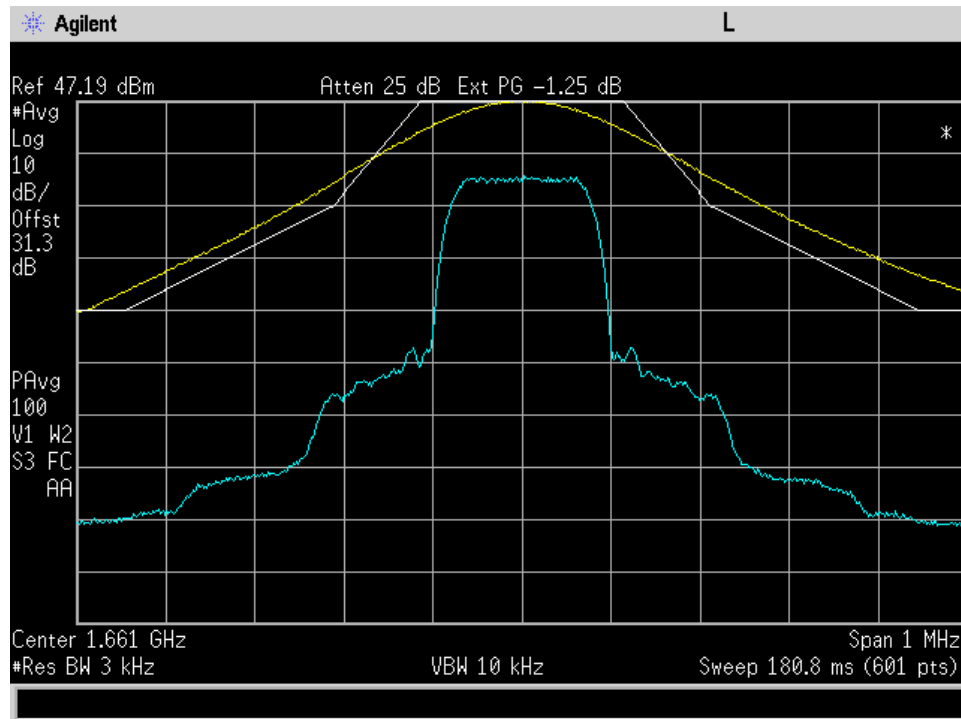


### RT2X\_FCC\_Mid\_Mask

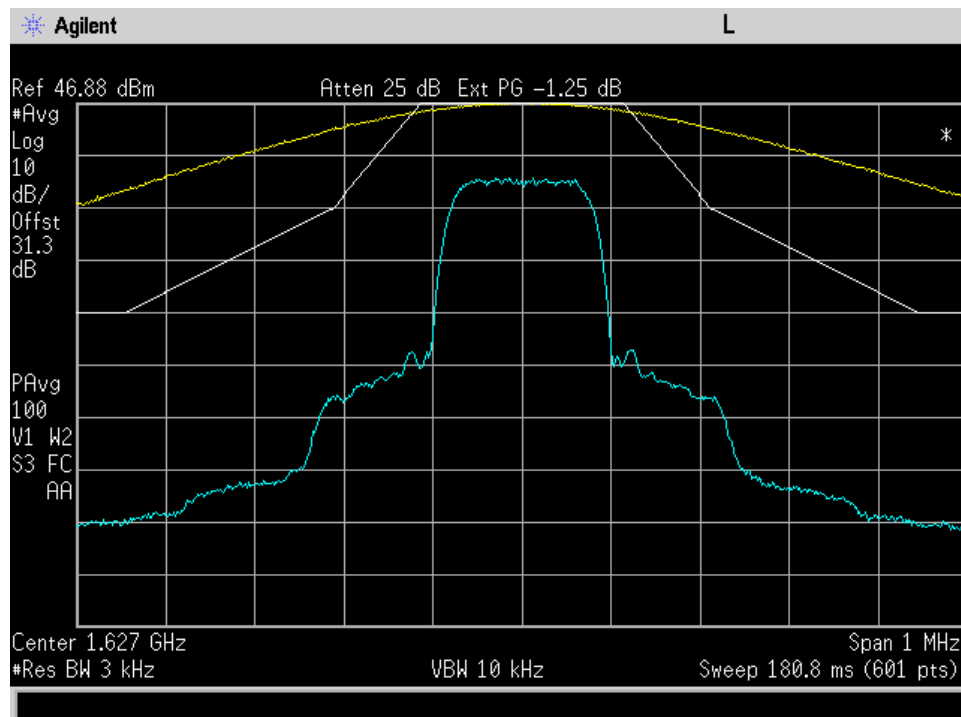




### RT45Q\_FCC\_High\_Mask

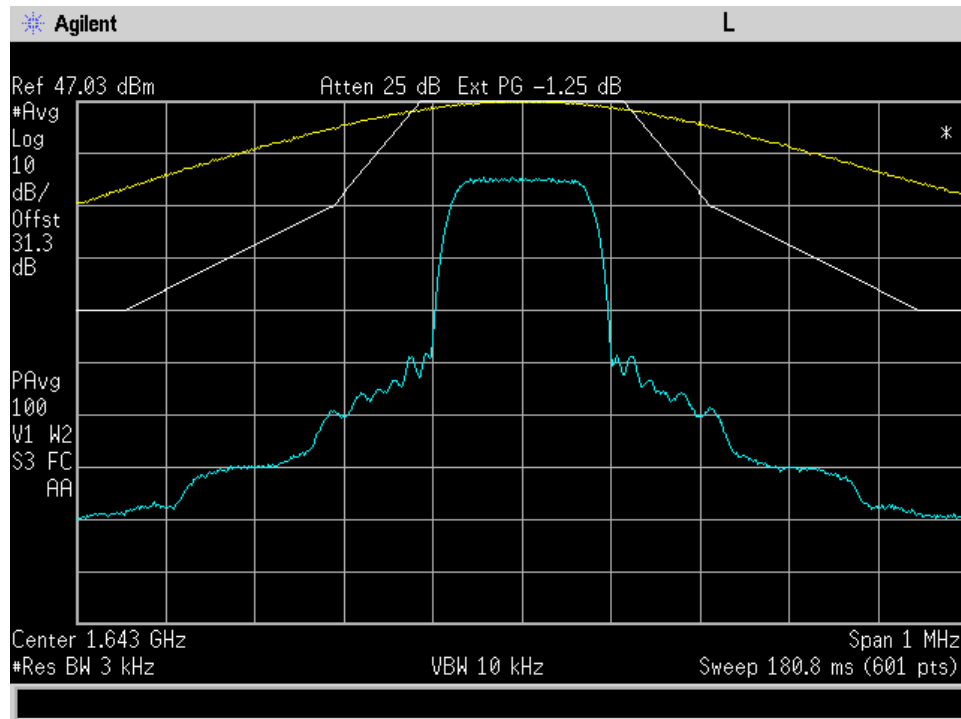


### RT45Q\_FCC\_Low\_Mask

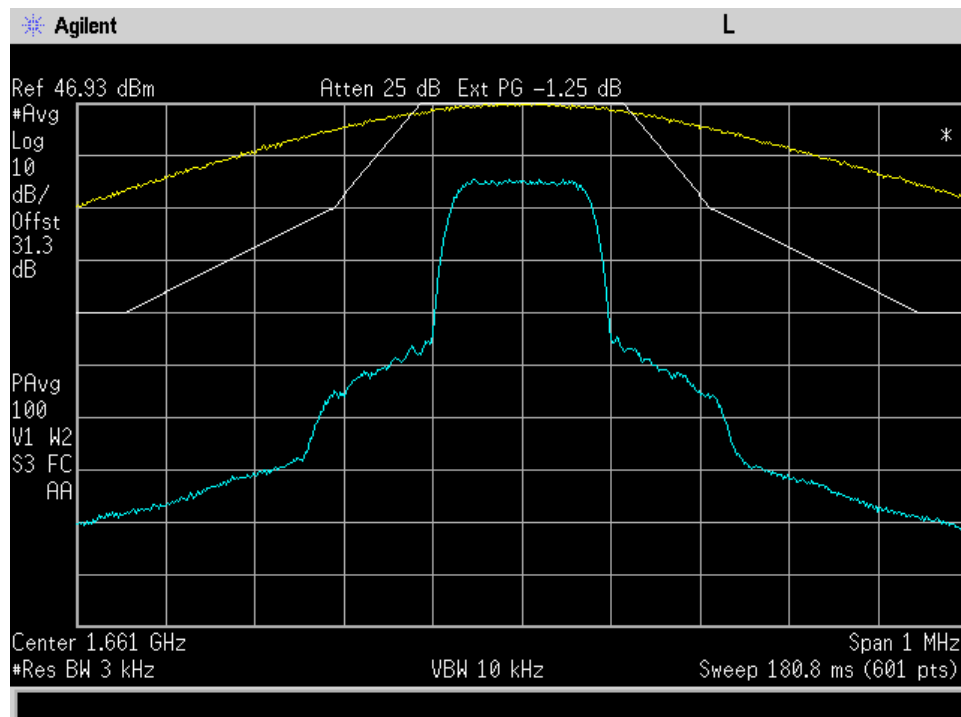




### RT45Q\_FCC\_Mid\_Mask

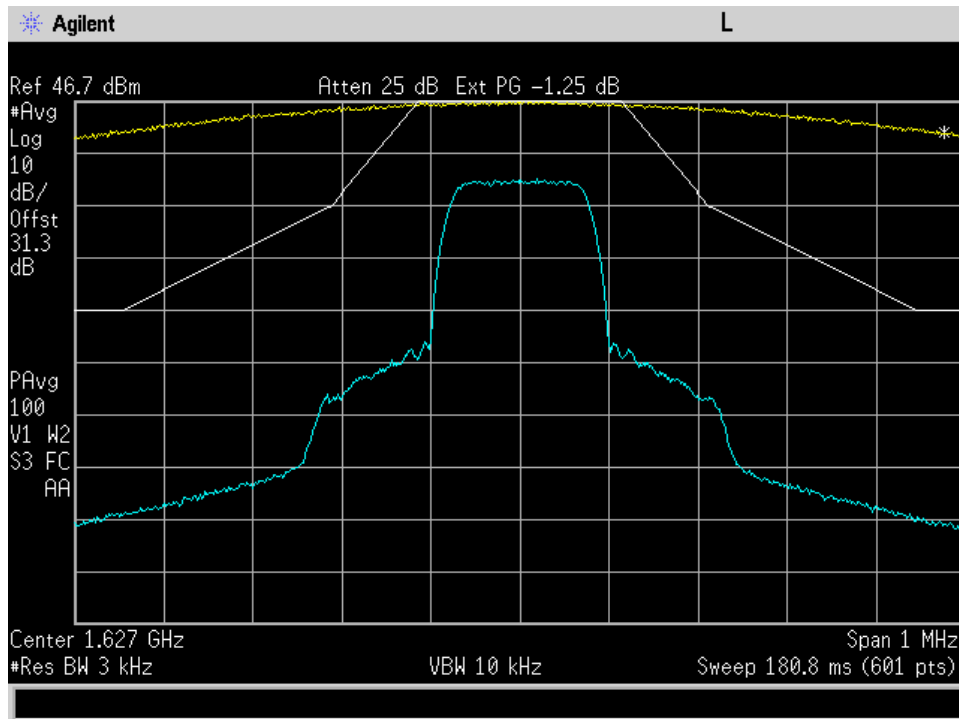


### RT45X\_FCC\_High\_Mask

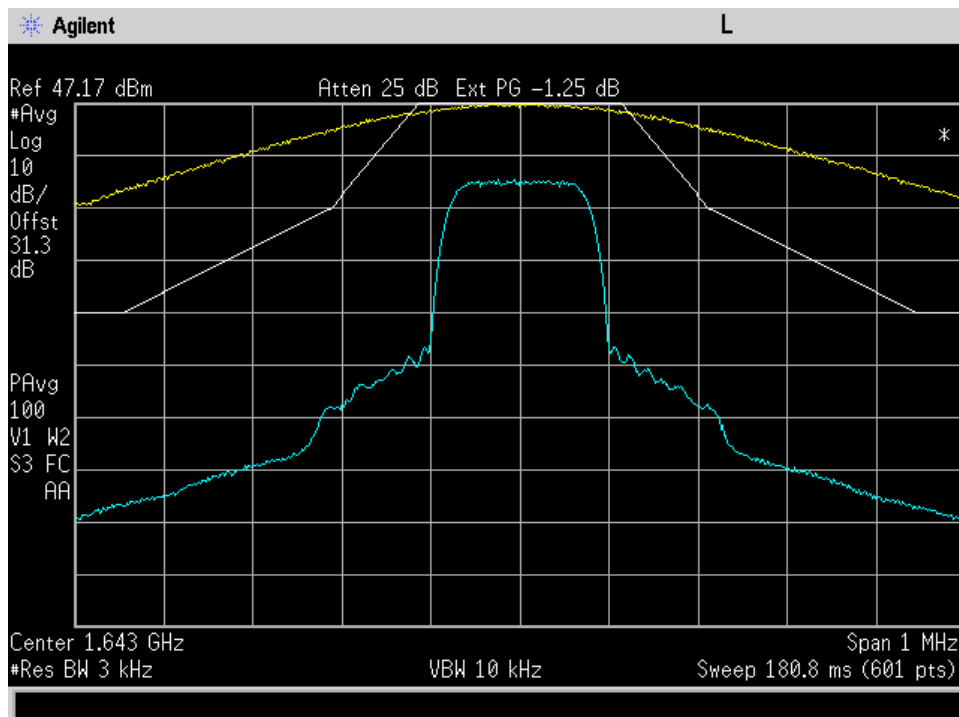




### RT45X\_FCC\_Low\_Mask

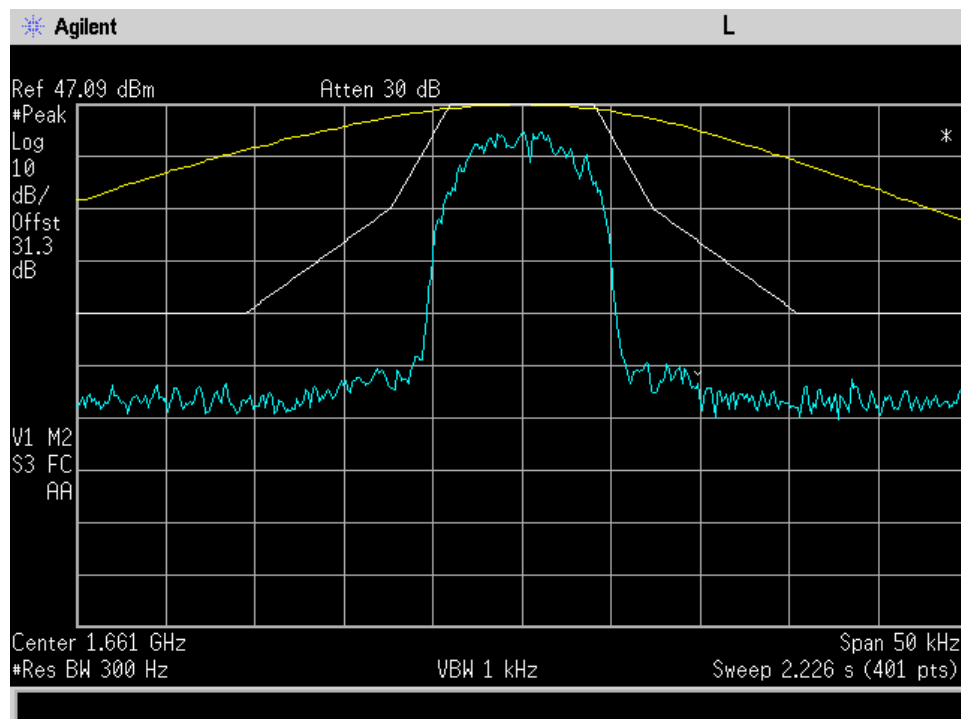


### RT45X\_FCC\_Mid\_Mask

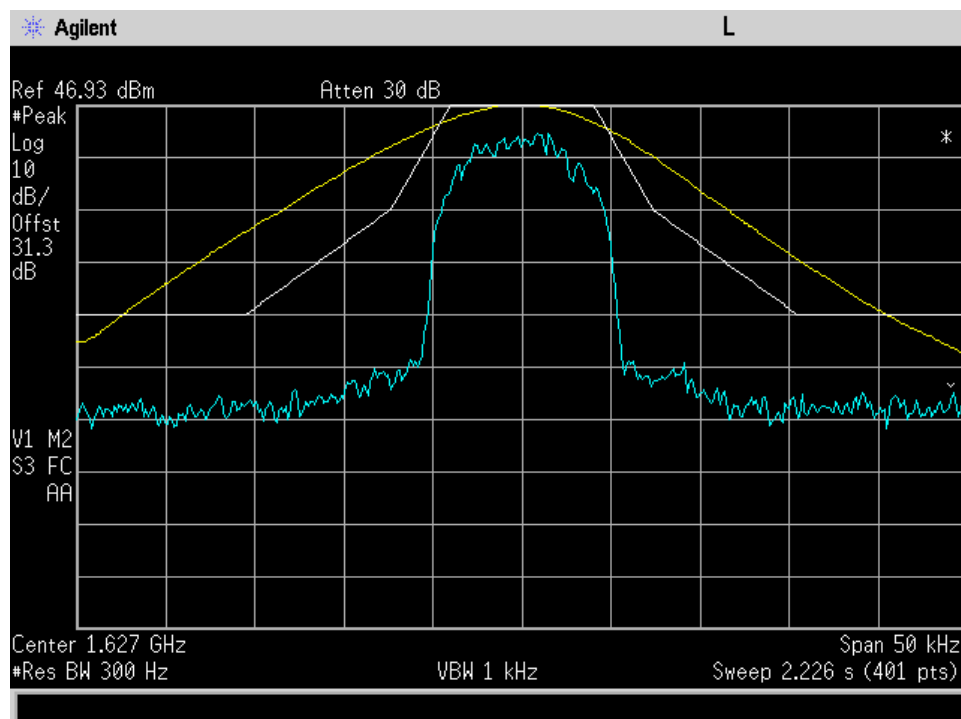




### RTS 10500\_FCC\_High\_Mask

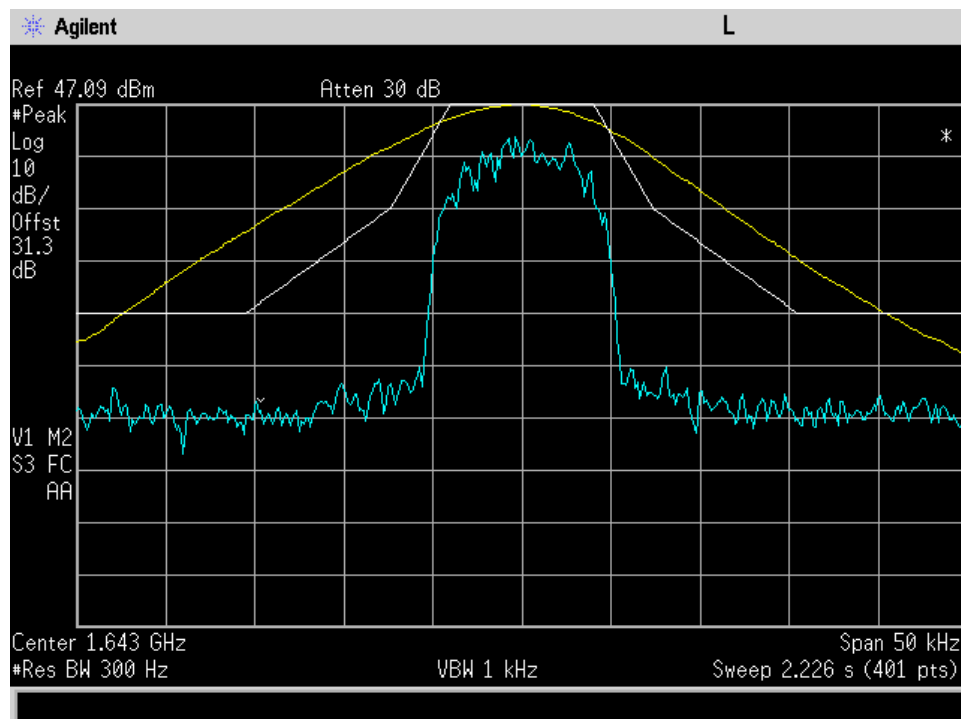


### RTS 10500\_FCC\_Low\_Mask

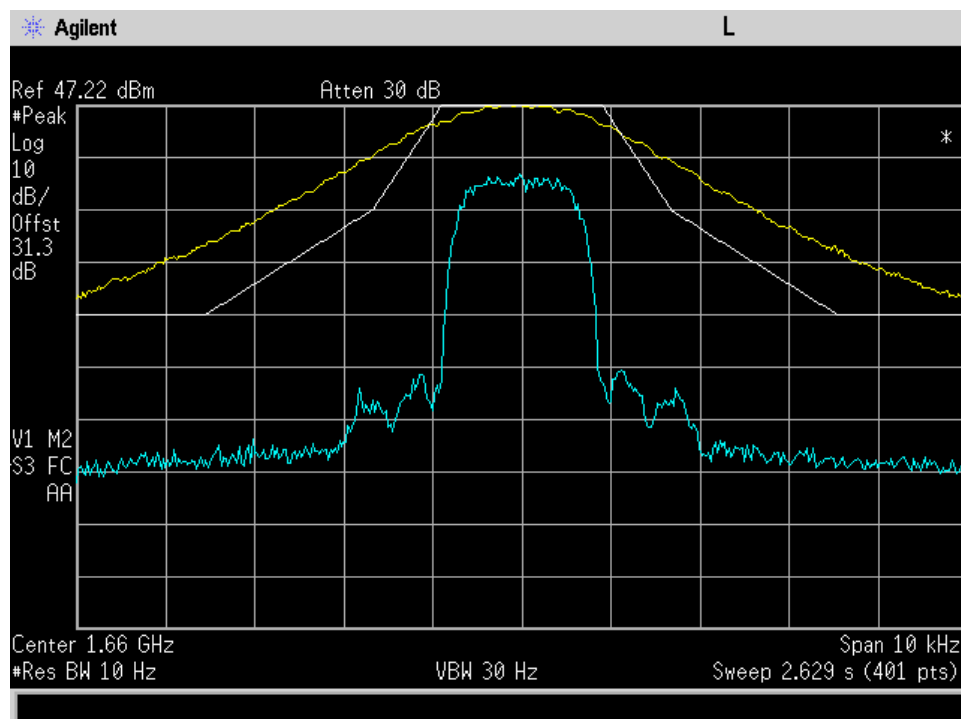




### RTS 10500\_FCC\_Mid\_Mask

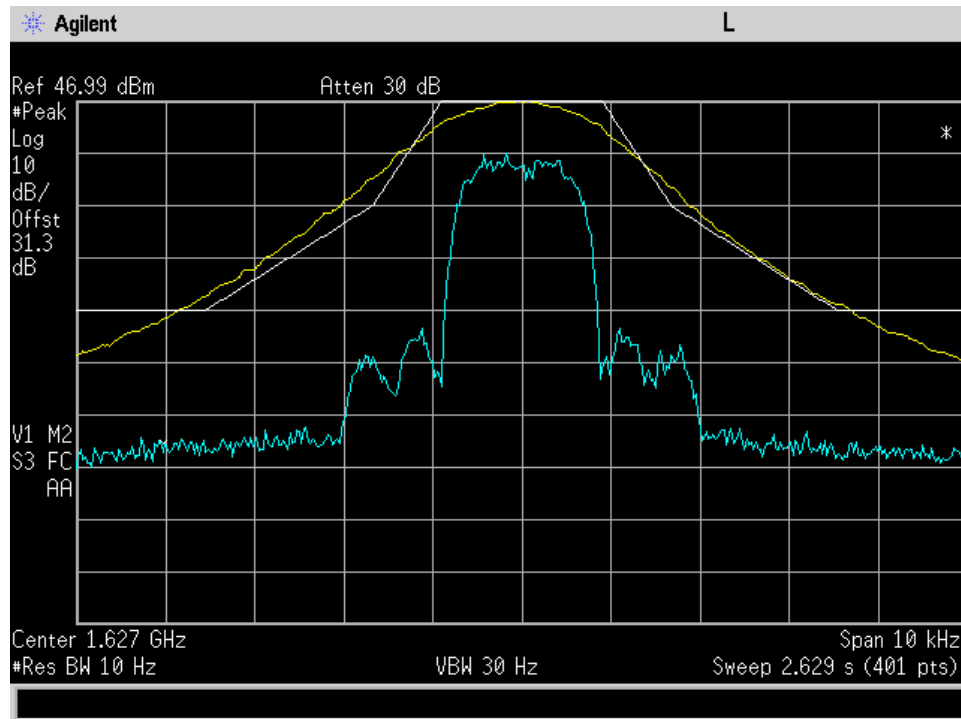


### RTS 1200\_FCC\_High\_Mask

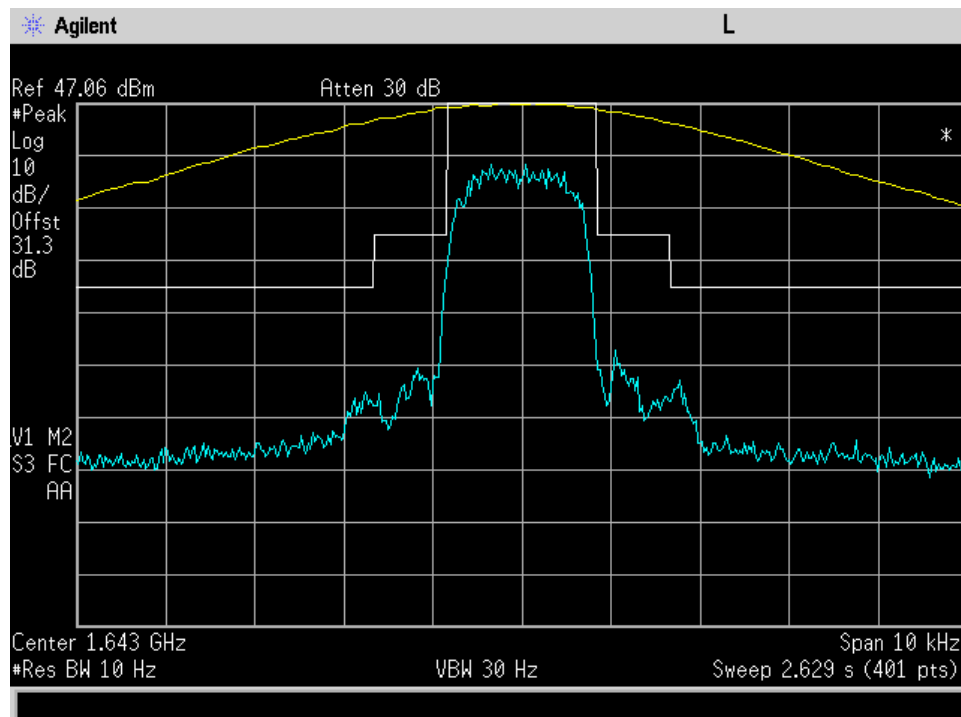




### RTS 1200\_FCC\_Low\_Mask



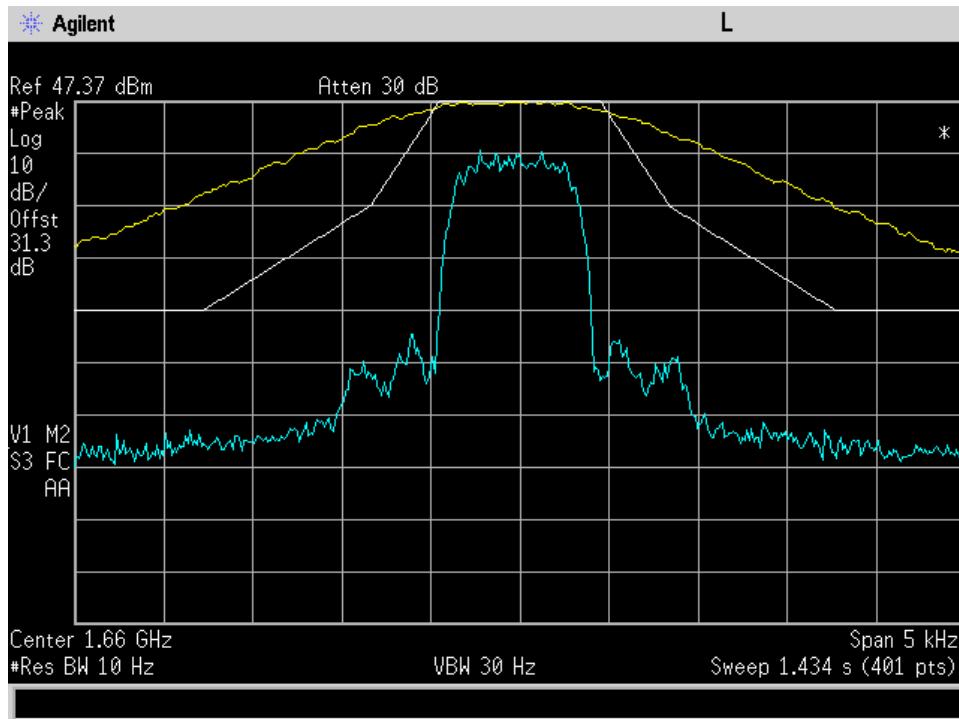
### RTS 1200\_FCC\_Mid\_Mask



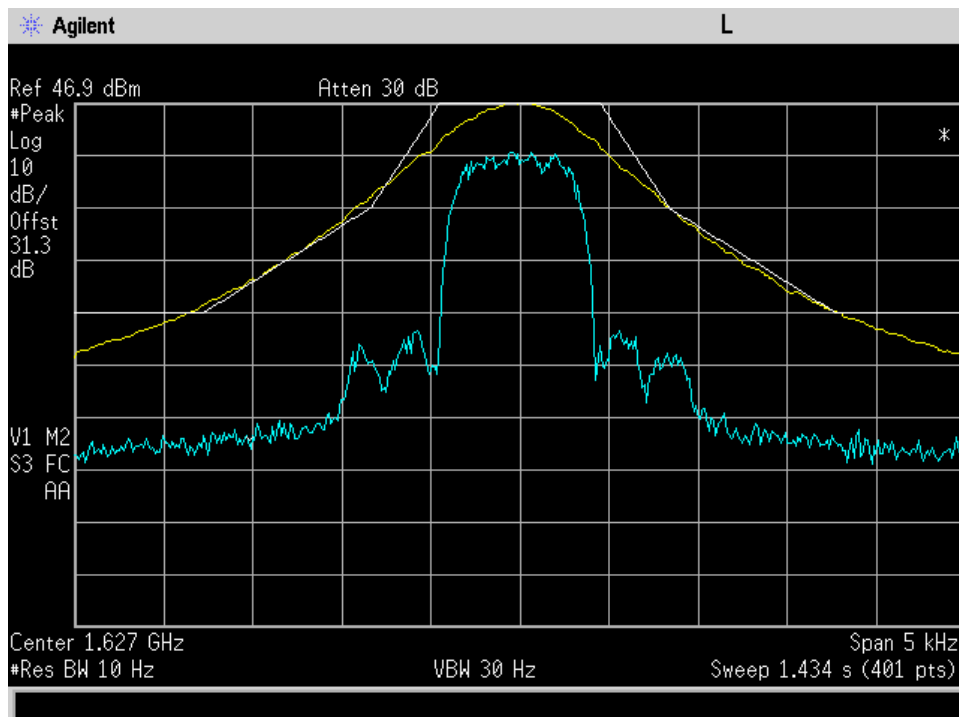




### RTS 600\_FCC\_High\_Mask

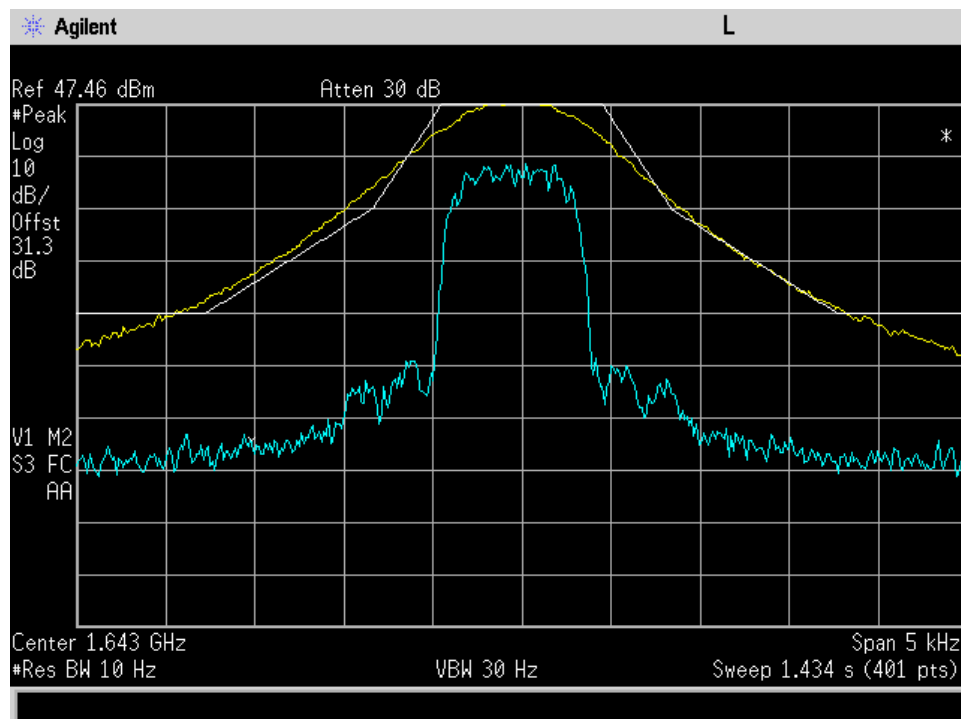


### RTS 600\_FCC\_Low\_Mask

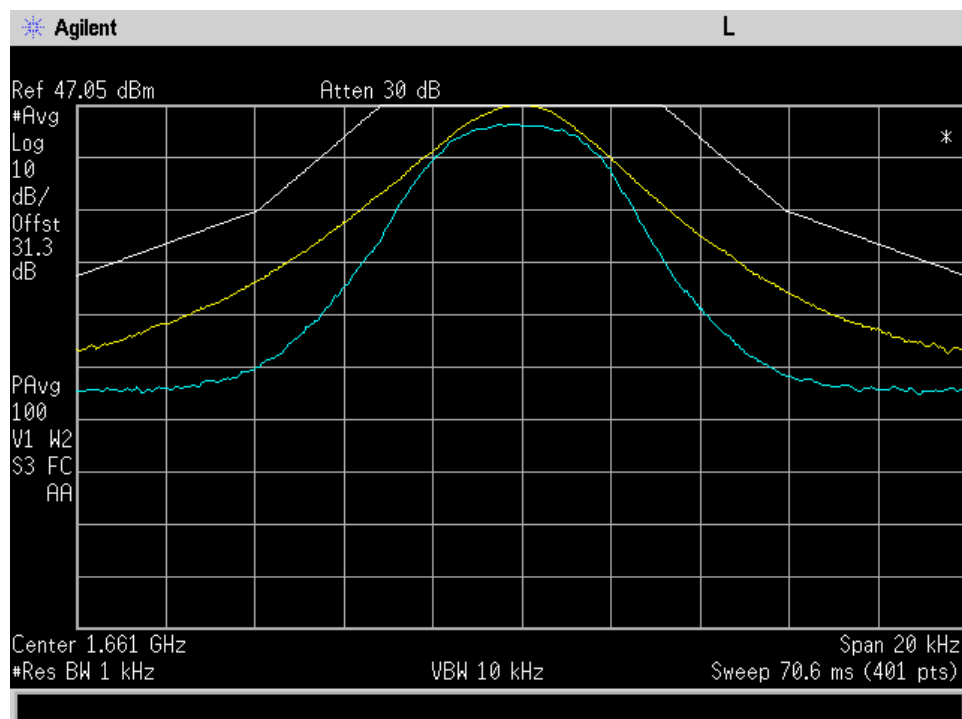




### RTS 600\_FCC\_Mid\_Mask

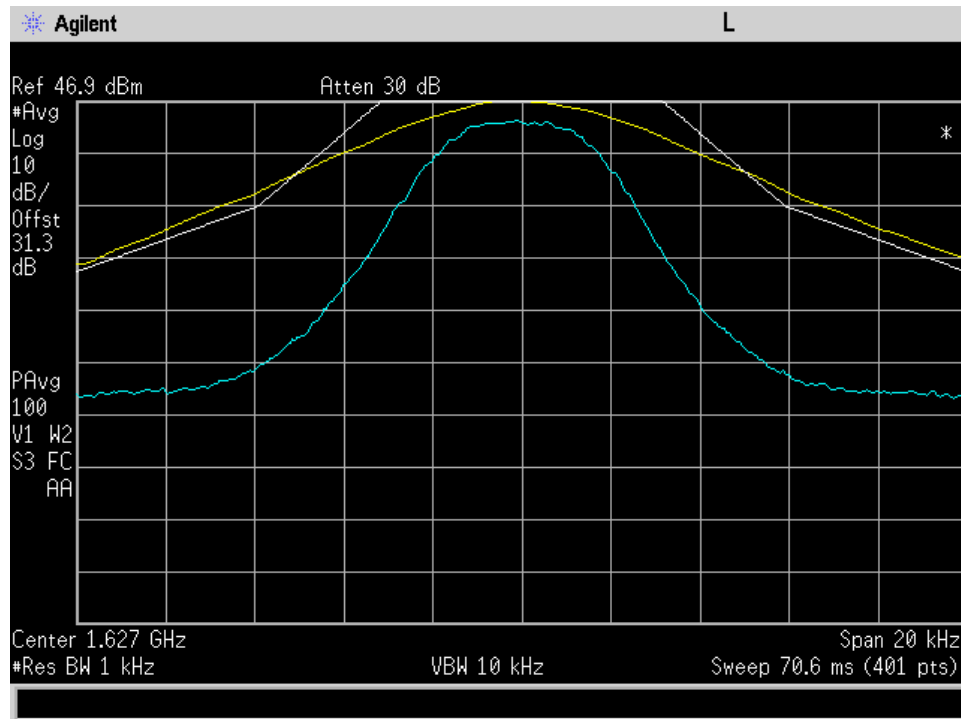


### S2K8\_FCC\_High\_Mask

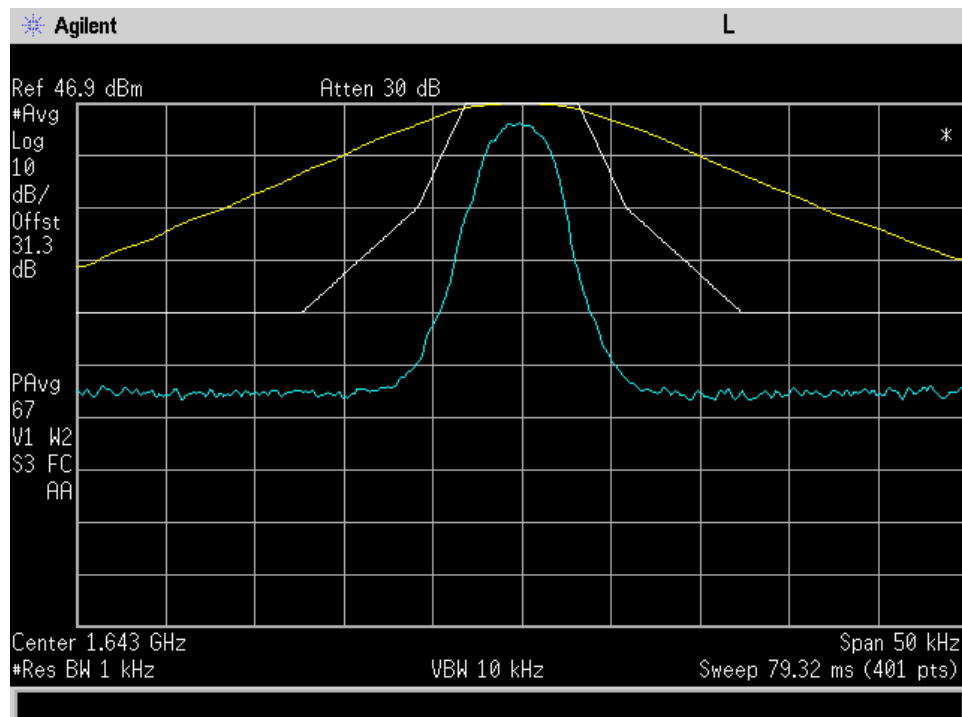




### S2K8\_FCC\_Low\_Mask

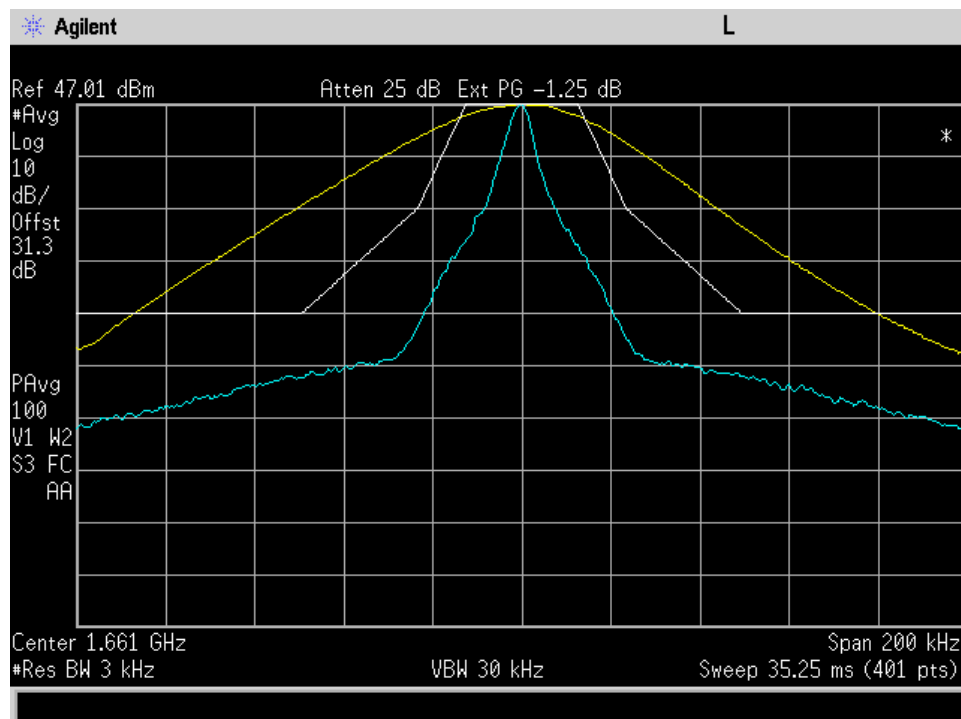


### S2K8\_FCC\_Mid\_Mask

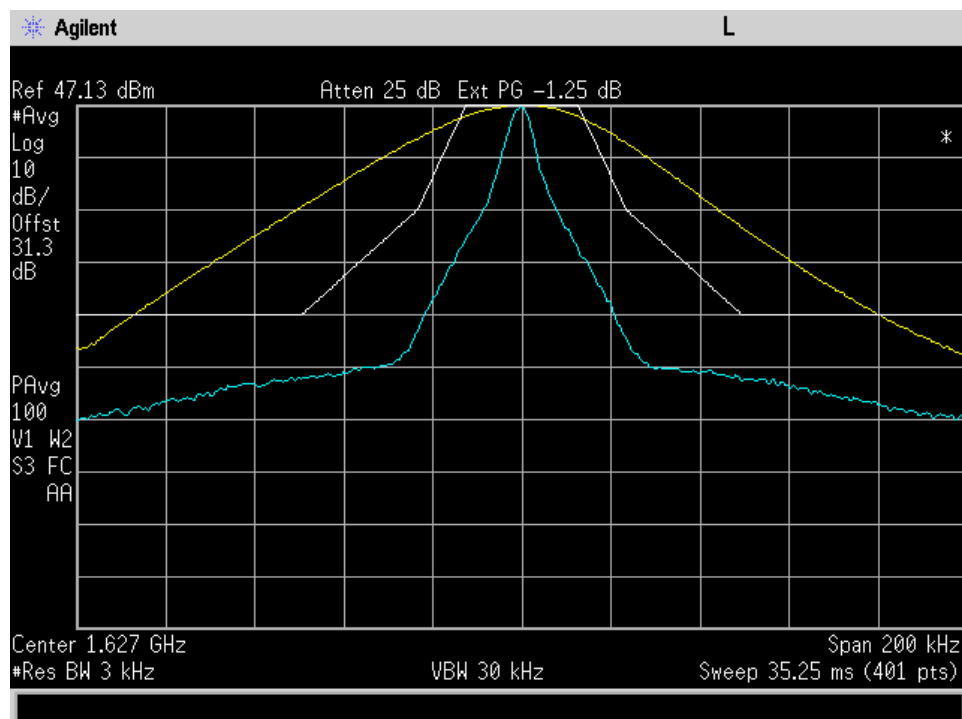




### S3K\_FCC\_High\_Mask



### S3K\_FCC\_Low\_Mask



**S3K\_FCC\_Mid\_Mask**

