

APPENDIX E

DATA SHEETS

RADIATED EMISSIONS

DATA SHEETS

RADIATED EMISSIONS (FCC SECTION 15.249)

COMPANY	Embedded Processor Designs, Inc.	DATE	1/18/05
EUT	InfoCube Base	DUTY CYCLE	N/A %
MODEL	1	PEAK TO AVG	N/A dB
S/N	N/A	TEST DIST.	3 Meters
TEST ENGINEER	Kyle Fujimoto	LAB	A

Frequency MHz	Peak Reading (dBuV)	Average (A) or Quasi- Peak (QP)	Antenna Polar. (V or H)	Antenna Height (meters)	EUT Azimuth (degrees)	EUT Axis (X,Y,Z)	EUT Tx Channel	Antenna Factor (dB)	Cable Loss (dB)	Amplifier Gain (dB)	Distance Factor (dB)	Mixer Factor (dB)	*Corrected Reading (dBuV/m)	Delta ** (dB)	Spec Limit (dBuV/m)	Comments
902.9140	95.6	QP	V	1.0	180		LOW	20.6	6.1	31.0	0.0	0.0	91.3	-2.7	94.0	
902.9100	88.1	QP	H	1.0	190		LOW	20.6	6.1	31.0	0.0	0.0	83.8	-10.2	94.0	
915.0760	97.5	QP	V	1.0	345		MID	20.9	6.3	31.1	0.0	0.0	93.2	-0.8	94.0	
915.0780	85.6	QP	H	1.0	340		MID	20.9	6.3	31.1	0.0	0.0	81.7	-12.3	94.0	
927.2400	94.8	QP	V	1.0	345		HIGH	21.1	6.4	31.2	0.0	0.0	91.1	-2.9	94.0	
927.2380	88.7	QP	H	1.0	340		HIGH	21.1	6.4	31.2	0.0	0.0	85.0	-9.0	94.0	

* CORRECTED READING = METER READING + ANTENNA FACTOR + CABLE LOSS - AMPLIFIER GAIN
 ** DELTA = SPEC LIMIT - CORRECTED READING

FCC 15.249

Embedded Processor Designs, Inc.

InfoCube Base

Model: 1

Date: 01/18/05

Lab: B

Tested By: Kyle Fujimoto

**Low Channel
Transmit Mode**

Freq. (MHz)	Level (dBuV)	Pol (v/h)	Limit	Margin	Peak / QP / Avg	Ant. Height (m)	Table Angle (deg)	Comments
1805.83	50.53	V	74	-23.47	Peak	1.24	180	
1805.83	49.08	V	54	-4.92	Avg	1.24	180	
2708.6	41.75	V	74	-32.25	Peak	1.59	180	
2708.6	35.11	V	54	-18.89	Avg	1.59	180	
3611	44.48	V	74	-29.52	Peak	1.97	225	
3611	38.26	V	54	-15.74	Avg	1.97	225	
4515	43.32	V	74	-30.68	Peak	2.43	180	
4515	33.5	V	54	-20.5	Avg	2.43	180	
5417	44.81	V	74	-29.19	Peak	2.43	180	
5417	32.52	V	54	-21.48	Avg	2.43	180	
6319.2		V	74	-74	Peak			No Emissions
6319.2		V	54	-54	Avg			Detected
7222		V	74	-74	Peak			No Emissions
7222		V	54	-54	Avg			Detected
8125		V	74	-74	Peak			No Emissions
8125		V	54	-54	Avg			Detected
9028		V	74	-74	Peak			No Emissions
9028		V	54	-54	Avg			Detected

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Embedded Processor Designs, Inc.

InfoCube Base

Model: 1

Date: 01/18/05

Lab: B

Tested By: Kyle Fujimoto

**Low Channel
Transmit Mode**

Freq. (MHz)	Level (dBuV)	Pol (v/h)	Limit	Margin	Peak / QP / Avg	Ant. Height (m)	Table Angle (deg)	Comments
1805.83	49.94	H	74	-24.06	Peak	1.63	225	
1805.83	48.64	H	54	-5.36	Avg	1.63	225	
2708.6	42.09	H	74	-31.91	Peak	2.46	225	
2708.6	35.75	H	54	-18.25	Avg	2.46	225	
3611	44.91	H	74	-29.09	Peak	2.46	270	
3611	39.79	H	54	-14.21	Avg	2.46	270	
4515	40.32	H	74	-33.68	Peak	1.51	135	
4515	28.88	H	54	-25.12	Avg	1.51	135	
5417	41.95	H	74	-32.05	Peak	2.46	225	
5417	29.88	H	54	-24.12	Avg	2.46	225	
6319.2		H	74	-74	Peak			No Emissions
6319.2		H	54	-54	Avg			Detected
7222		H	74	-74	Peak			No Emissions
7222		H	54	-54	Avg			Detected
8125		H	74	-74	Peak			No Emissions
8125		H	54	-54	Avg			Detected
9028		H	74	-74	Peak			No Emissions
9028		H	54	-54	Avg			Detected

FCC 15.249

Embedded Processor Designs, Inc.
 InfoCube Base
 Model: 1

Date: 01/18/05

Lab: B

Tested By: Kyle Fujimoto

Middle Channel
Transmit Mode

Freq. (MHz)	Level (dBuV)	Pol (v/h)	Limit	Margin	Peak / QP / Avg	Ant. Height (m)	Table Angle (deg)	Comments
1830	51.75	V	74	-22.25	Peak	1.44	45	
1830	50.41	V	54	-3.59	Avg	1.44	45	
2745	41.68	V	74	-32.32	Peak	1.5	180	
2745	35.4	V	54	-18.6	Avg	1.5	180	
3660	45.51	V	74	-28.49	Peak	1.95	180	
3660	40.77	V	54	-13.23	Avg	1.95	180	
4575	43.52	V	74	-30.48	Peak	2.16	180	
4575	34.12	V	54	-19.88	Avg	2.16	180	
5490	42.2	V	74	-31.8	Peak	1.54	225	
5490	29.97	V	54	-24.03	Avg	1.54	225	
6405	44.75	V	74	-29.25	Peak	1.54	270	
6405	32.07	V	54	-21.93	Avg	1.54	270	
7320		V	74	-74	Peak			No Emissions
7320		V	54	-54	Avg			Detected
8235		V	74	-74	Peak			No Emissions
8235		V	54	-54	Avg			Detected
9150		V	74	-74	Peak			No Emissions
9150		V	54	-54	Avg			Detected

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Embedded Processor Designs, Inc.
 InfoCube Base
 Model: 1

Date: 01/18/05

Lab: B

Tested By: Kyle Fujimoto

Middle Channel
Transmit Mode

Freq. (MHz)	Level (dBuV)	Pol (v/h)	Limit	Margin	Peak / QP / Avg	Ant. Height (m)	Table Angle (deg)	Comments
1830	53.04	H	74	-20.96	Peak	1.09	225	
1830	52.17	H	54	-1.83	Avg	1.09	225	
2745	43.36	H	74	-30.64	Peak	2.05	225	
2745	38.03	H	54	-15.97	Avg	2.05	225	
3660	46.32	H	74	-27.68	Peak	2.04	180	
3660	40.7	H	54	-13.3	Avg	2.04	180	
4575	42.68	H	74	-31.32	Peak	2.56	180	
4575	30.26	H	54	-23.74	Avg	2.56	180	
5490	42.22	H	74	-31.78	Peak	2.12	135	
5490	30.09	H	54	-23.91	Avg	2.12	135	
6405	44.64	H	74	-29.36	Peak	2.12	135	
6405	32.31	H	54	-21.69	Avg	2.12	135	
7320		H	74	-74	Peak			No Emissions
7320		H	54	-54	Avg			Detected
8235		H	74	-74	Peak			No Emissions
8235		H	54	-54	Avg			Detected
9150		H	74	-74	Peak			No Emissions
9150		H	54	-54	Avg			Detected

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Embedded Processor Designs, Inc.
 InfoCube Base
 Model: 1

Date: 01/18/05

Lab: B

Tested By: Kyle Fujimoto

High Channel
Transmit Mode

Freq. (MHz)	Level (dBuV)	Pol (v/h)	Limit	Margin	Peak / QP / Avg	Ant. Height (m)	Table Angle (deg)	Comments
1854.6	50.36	V	74	-23.64	Peak	2.29	225	
1854.6	48.83	V	54	-5.17	Avg	2.29	225	
2781.7	41.84	V	74	-32.16	Peak	2.31	225	
2781.7	33.47	V	54	-20.53	Avg	2.31	225	
3708.9	45.55	V	74	-28.45	Peak	2.33	180	
3708.9	36.54	V	54	-17.46	Avg	2.33	180	
4636.15	45.65	V	74	-28.35	Peak	2.14	180	
4636.2	31.75	V	54	-22.25	Avg	2.14	180	
5563.3	45.85	V	74	-28.15	Peak	2.14	180	
5563.3	33.79	V	54	-20.21	Avg	2.14	180	
6490.6		V	74	-74	Peak			No Emissions
6490.6		V	54	-54	Avg			Detected
7417.8		V	74	-74	Peak			No Emissions
7417.8		V	54	-54	Avg			Detected
8345.1		V	74	-74	Peak			No Emissions
8345.1		V	54	-54	Avg			Detected
9272.2		V	74	-74	Peak			No Emissions
9272.2		V	54	-54	Avg			Detected

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Embedded Processor Designs, Inc.
 InfoCube Base
 Model: 1

Date: 01/18/05
 Lab: B
 Tested By: Kyle Fujimoto

High Channel
Transmit Mode

Freq. (MHz)	Level (dBuV)	Pol (v/h)	Limit	Margin	Peak / QP / Avg	Ant. Height (m)	Table Angle (deg)	Comments
1854.6	47.79	H	74	-26.21	Peak	3.64	225	
1854.6	45.45	H	54	-8.55	Avg	3.64	225	
2781.7	44.9	H	74	-29.1	Peak	1.7	135	
2781.7	39.66	H	54	-14.34	Avg	1.7	135	
3708.9	46.57	H	74	-27.43	Peak	1.94	225	
3708.9	40.26	H	54	-13.74	Avg	1.94	225	
4636.15	44	H	74	-30	Peak	1.85	225	
4636.2	29.49	H	54	-24.51	Avg	1.85	225	
5563.3	47.21	H	74	-26.79	Peak	1.85	225	
5563.3	33.98	H	54	-20.02	Avg	1.85	225	
6490.6		H	74	-74	Peak			No Emissions
6490.6		H	54	-54	Avg			Detected
7417.8		H	74	-74	Peak			No Emissions
7417.8		H	54	-54	Avg			Detected
8345.1		H	74	-74	Peak			No Emissions
8345.1		H	54	-54	Avg			Detected
9272.2		H	74	-74	Peak			No Emissions
9272.2		H	54	-54	Avg			Detected

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Embedded Processor Designs, Inc.
InfoCube Base
Model: 1

Date: 01/18/05

Lab: B

Tested By: Kyle Fujimoto

Receive Mode
Low Channel

Freq. (MHz)	Level (dBuV)	Pol (v/h)	Limit	Margin	Peak / QP / Avg	Ant. Height (m)	Table Angle (deg)	Comments
903.077	45.87	V	66	-20.13	Peak	1.5	90	
903.077	45.21	V	46	-0.79	QP	1.5	90	
1805.83	45.13	V	74	-28.87	Peak	2.15	180	
1805.83	42.28	V	54	-11.72	Avg	2.15	180	
2708.6	37.1	V	74	-36.9	Peak	2.15	225	
2708.6	25.97	V	54	-28.03	Avg	2.15	225	
3611	41.76	V	74	-32.24	Peak	2.17	225	
3611	28.48	V	54	-25.52	Avg	2.17	225	
4515	43.88	V	74	-30.12	Peak	2.17	270	
4515	29.53	V	54	-24.47	Avg	2.17	270	
5417		V	74	-74	Peak			No Emissions
5417		V	54	-54	Avg			Detected
6319.2		V	74	-74	Peak			No Emissions
6319.2		V	54	-54	Avg			Detected
7222		V	74	-74	Peak			No Emissions
7222		V	54	-54	Avg			Detected
8125		V	74	-74	Peak			No Emissions
8125		V	54	-54	Avg			Detected
9028		V	74	-74	Peak			No Emissions
9028		V	54	-54	Peak			Detected

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Embedded Processor Designs, Inc.
InfoCube Base
Model: 1

Date: 01/18/05

Lab: B

Tested By: Kyle Fujimoto

Receive Mode
Low Channel

Freq. (MHz)	Level (dBuV)	Pol (v/h)	Limit	Margin	Peak / QP / Avg	Ant. Height (m)	Table Angle (deg)	Comments
903.077	44.21	H	66	-21.79	Peak	1.5	90	
903.077	43.67	H	46	-2.33	QP	1.5	90	
1805.83	48.23	H	74	-25.77	Peak	1.14	225	
1805.83	45.91	H	54	-8.09	Avg	1.14	225	
2708.6	37.95	H	74	-36.05	Peak	1.8	225	
2708.6	25.99	H	54	-28.01	Avg	1.8	225	
3611	43.21	H	74	-30.79	Peak	2.34	180	
3611	30.22	H	54	-23.78	Avg	2.34	180	
4515	41.79	H	74	-32.21	Peak	2.34	180	
4515	29.62	H	54	-24.38	Avg	2.34	180	
5417		H	74	-74	Peak			No Emissions
5417		H	54	-54	Avg			Detected
6319.2	42.55	H	74	-31.45	Peak	1.97	180	
6319.2	30.12	H	54	-23.88	Avg	1.97	180	
7222		H	74	-74	Peak			No Emissions
7222		H	54	-54	Avg			Detected
8125		H	74	-74	Peak			No Emissions
8125		H	54	-54	Avg			Detected
9028		H	74	-74	Peak			No Emissions
9028		H	54	-54	Peak			Detected

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Embedded Processor Designs, Inc.
InfoCube Base
Model: 1

Date: 01/18/05

Lab: B

Tested By: Kyle Fujimoto

Receive Mode
Middle Channel

Freq. (MHz)	Level (dBuV)	Pol (v/h)	Limit	Margin	Peak / QP / Avg	Ant. Height (m)	Table Angle (deg)	Comments
915.25	44.98	V	66	-21.02	Peak	1.5	90	
915.25	44.52	V	46	-1.48	QP	1.5	90	
1830	47	V	74	-27	Peak	2.09	315	
1830	43.87	V	54	-10.13	Avg	2.09	315	
2745	38.26	V	74	-35.74	Peak	2.09	270	
2745	26.43	V	54	-27.57	Avg	2.09	270	
3660	40.88	V	74	-33.12	Peak	2.1	135	
3660	28.65	V	54	-25.35	Avg	2.1	135	
4575	42.01	V	74	-31.99	Peak	2.09	225	
4575	29.95	V	54	-24.05	Avg	2.09	225	
5490	46.02	V	74	-27.98	Peak	2.09	225	
5490	33.56	V	54	-20.44	Avg	2.09	225	
6405		V	74	-74	Peak			No Emissions
6405		V	54	-54	Avg			Detected
7320		V	74	-74	Peak			No Emissions
7320		V	54	-54	Avg			Detected
8235		V	74	-74	Peak			No Emissions
8235		V	54	-54	Avg			Detected
9150		V	74	-74	Peak			No Emissions
9150		V	54	-54	Peak			Detected

FCC 15.249

Embedded Processor Designs, Inc.
 InfoCube Base
 Model: 1

Date: 01/18/05

Lab: B

Tested By: Kyle Fujimoto

Receive Mode
Middle Channel

Freq. (MHz)	Level (dBuV)	Pol (v/h)	Limit	Margin	Peak / QP / Avg	Ant. Height (m)	Table Angle (deg)	Comments
915.269	44.21	H	66	-21.79	Peak	1.25	90	
915.269	43.69	H	46	-2.31	QP	1.25	90	
1830	49.24	H	74	-24.76	Peak	1.12	225	
1830	47.24	H	54	-6.76	Avg	1.12	225	
2745	38.95	H	74	-35.05	Peak	2.08	225	
2745	26.28	H	54	-27.72	Avg	2.08	225	
3660	41.82	H	74	-32.18	Peak	2.36	135	
3660	29.12	H	54	-24.88	Avg	2.36	135	
4575	41.91	H	74	-32.09	Peak	2.36	135	
4575	29.84	H	54	-24.16	Avg	2.36	135	
5490		H	74	-74	Peak			No Emissions
5490		H	54	-54	Avg			Detected
6405		H	74	-74	Peak			No Emissions
6405		H	54	-54	Avg			Detected
7320		H	74	-74	Peak			No Emissions
7320		H	54	-54	Avg			Detected
8235		H	74	-74	Peak			No Emissions
8235		H	54	-54	Avg			Detected
9150		H	74	-74	Peak			No Emissions
9150		H	54	-54	Peak			Detected

FCC 15.249

Embedded Processor Designs, Inc.
 InfoCube Base
 Model: 1

Date: 01/18/05

Lab: B

Tested By: Kyle Fujimoto

Receive Mode
High Channel

Freq. (MHz)	Level (dBuV)	Pol (v/h)	Limit	Margin	Peak / QP / Avg	Ant. Height (m)	Table Angle (deg)	Comments
927.418	45.87	V	66	-20.13	Peak	1.5	270	
927.418	45.41	V	46	-0.59	QP	1.5	270	
1854.6	44.93	V	74	-29.07	Peak	2.08	45	
1854.6	41.5	V	54	-12.5	Avg	2.08	45	
2781.7	38.85	V	74	-35.15	Peak	2.08	45	
2781.7	25.97	V	54	-28.03	Avg	2.08	45	
3708.9	42.58	V	74	-31.42	Peak	2.08	45	
3708.9	28.47	V	54	-25.53	Avg	2.08	45	
4636.15	42.87	V	74	-31.13	Peak	2.08	225	
4636.2	29.68	V	54	-24.32	Avg	2.08	225	
5563.3	46.54	V	74	-27.46	Peak	2.08	225	
5563.3	34.36	V	54	-19.64	Avg	2.08	225	
6490.6		V	74	-74	Peak			No Emissions
6490.6		V	54	-54	Avg			Detected
7417.8		V	74	-74	Peak			No Emissions
7417.8		V	54	-54	Avg			Detected
8345.1		V	74	-74	Peak			No Emissions
8345.1		V	54	-54	Avg			Detected
9272.2		V	74	-74	Peak			No Emissions
9272.2		V	54	-54	Peak			Detected

FCC 15.249

Embedded Processor Designs, Inc.
InfoCube Base
Model: 1

Date: 01/18/05

Lab: B

Tested By: Kyle Fujimoto

Receive Mode
High Channel

Freq. (MHz)	Level (dBuV)	Pol (v/h)	Limit	Margin	Peak / QP / Avg	Ant. Height (m)	Table Angle (deg)	Comments
927.423	45.12	H	66	-20.88	Peak	1.5	90	
927.423	44.78	H	46	-1.22	QP	1.5	90	
1854.6	47.87	H	74	-26.13	Peak	1.07	180	
1854.6	45.69	H	54	-8.31	Avg	1.07	180	
2781.7	38.69	H	74	-35.31	Peak	1.94	225	
2781.7	26.04	H	54	-27.96	Avg	1.94	225	
3708.9	43.18	H	74	-30.82	Peak	2.3	180	
3708.9	30.63	H	54	-23.37	Avg	2.3	180	
4636.15	41.92	H	74	-32.08	Peak	2.3	180	
4636.2	29.68	H	54	-24.32	Avg	2.3	180	
5563.3		H	74	-74	Peak			No Emissions
5563.3		H	54	-54	Avg			Detected
6490.6		H	74	-74	Peak			No Emissions
6490.6		H	54	-54	Avg			Detected
7417.8		H	74	-74	Peak			No Emissions
7417.8		H	54	-54	Avg			Detected
8345.1		H	74	-74	Peak			No Emissions
8345.1		H	54	-54	Avg			Detected
9272.2		H	74	-74	Peak			No Emissions
9272.2		H	54	-54	Avg			Detected

FCC 15.249

Embedded Processor Designs, Inc.
InfoCube Base
Model: 1

Date: 01/19/05

Lab: B

Tested By: Kyle Fujimoto

Digital Portion From 1 GHz to 9.3 GHz

[illegible]

Test Location : Compatible Electronics
 Customer : Embedded Processor Designs, Inc.
 Manufacturer : Embedded Processor Designs, Inc.
 Eut name : InfoCube Base
 Model : 1
 Serial # : N/A
 Specification : FCC B
 Distance correction factor ($20 * \log(\text{test}/\text{spec})$) : 0.00
 Test Mode : Radiated Emissions - 10 kHz to 1000 MHz
 Vertical and Horizontal Polarization

Page : 1/2
 Date : 1/20/2005
 Time : 9:23:13
 Lab : A
 Test Distance : 3.0 Meters

Tested By: Kyle Fujimoto

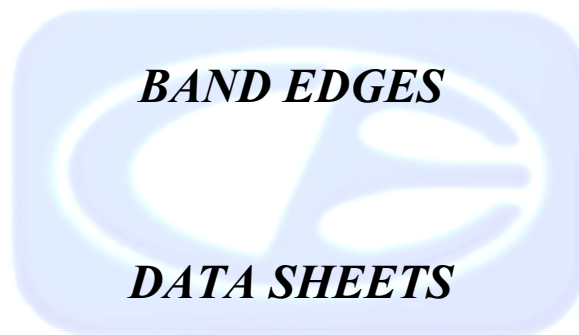
Pol	Freq MHz	Rdng dBuV	Cable loss dB	Ant factor dB	Amp gain dB	Cor'd rdg = R dBuV	Limit = L dBuV/m	Delta R-L dB
1V	39.330	60.30	1.80	11.15	32.40	40.85	40.00	0.85
2V	39.331Qp	58.64	1.80	11.15	32.40	39.19	40.00	-0.81
3H	39.332	43.70	1.80	11.15	32.40	24.25	40.00	-15.75
4V	42.353	55.60	1.85	10.13	32.40	35.18	40.00	-4.82
5V	58.789	57.60	2.09	10.42	32.49	37.62	40.00	-2.38
6V	58.789Qp	54.98	2.09	10.42	32.49	35.00	40.00	-5.00
7H	58.809	44.50	2.09	10.42	32.49	24.52	40.00	-15.48
8V	60.903	55.60	2.13	10.09	32.49	35.33	40.00	-4.67
9V	83.030	54.20	2.40	6.43	32.30	30.73	40.00	-9.27
10H	111.688	40.10	2.50	12.37	32.35	22.62	43.50	-20.88
11H	116.173	53.60	2.53	12.83	32.37	36.59	43.50	-6.91
12H	121.713	51.90	2.58	13.23	32.39	35.32	43.50	-8.18
13H	129.072	46.80	2.69	13.07	32.36	30.20	43.50	-13.30
14V	132.781	54.20	2.77	12.87	32.33	37.51	43.50	-5.99
15V	136.718	49.90	2.85	12.67	32.30	33.11	43.50	-10.39
16V	139.310	55.20	2.90	12.53	32.28	38.35	43.50	-5.15
17V	145.714	46.70	3.02	12.27	32.23	29.76	43.50	-13.74
18H	149.368	55.50	3.09	12.12	32.20	38.51	43.50	-4.99
19V	149.369	54.90	3.09	12.12	32.20	37.91	43.50	-5.59
20V	154.902	55.10	3.06	12.45	32.24	38.37	43.50	-5.13
21V	160.423	52.20	3.01	12.88	32.29	35.81	43.50	-7.69
22V	169.664	57.60	2.94	14.63	32.36	42.81	43.50	-0.69
23V	169.670Qp	52.82	2.94	14.63	32.36	38.03	43.50	-5.47
24V	170.830	46.50	2.93	14.85	32.37	31.91	43.50	-11.59
25V	177.002	55.40	2.91	15.64	32.40	41.55	43.50	-1.95
26V	177.002Qp	54.98	2.91	15.64	32.40	41.13	43.50	-2.37
27V	226.790	47.50	3.12	15.55	32.48	33.69	46.00	-12.31
28V	235.959	45.10	3.19	15.29	32.41	31.17	46.00	-14.83
29V	243.368	48.10	3.25	15.08	32.35	34.08	46.00	-11.92
30H	245.130	45.50	3.26	15.03	32.34	31.46	46.00	-14.54
31V	252.600	42.20	3.32	15.27	32.28	28.51	46.00	-17.49
32V	282.067	49.60	3.53	19.64	32.16	40.61	46.00	-5.39
33V	293.127	47.00	3.57	20.16	32.25	38.49	46.00	-7.51
34H	304.183	52.10	3.62	12.97	32.29	36.40	46.00	-9.60
35H	305.168	53.40	3.62	12.99	32.29	37.72	46.00	-8.28

Test Location : Compatible Electronics
Customer : Embedded Processor Designs, Inc.
Manufacturer : Embedded Processor Designs, Inc.
Eut name : InfoCube Base
Model : 1
Serial # : N/A
Specification : FCC B
Distance correction factor ($20 * \log(\text{test}/\text{spec})$) : 0.00
Test Mode : Radiated Emissions - 10 kHz to 1000 MHz
Vertical and Horizontal Polarization

Page : 2/2
Date : 1/20/2005
Time : 9:23:13
Lab : A
Test Distance : 3.0 Meters

Tested By: Kyle Fujimoto

Pol	Freq MHz	Rdng dBuV	Cable loss dB	Ant factor dB	Amp gain dB	Cor'd rdg = R dBuV	Limit = L dBuV/m	Delta R-L dB
36V	309.745	44.00	3.64	13.07	32.28	28.43	46.00	-17.57
37H	314.705	50.90	3.66	13.15	32.27	35.44	46.00	-10.56
38V	320.773	43.70	3.69	13.25	32.26	28.38	46.00	-17.62
39V	324.464	50.10	3.70	13.31	32.25	34.86	46.00	-11.14
40V	328.123	49.70	3.72	13.37	32.24	34.54	46.00	-11.46
41V	352.133	49.60	3.82	13.74	32.20	34.96	46.00	-11.04
42V	357.624	47.00	3.88	13.82	32.20	32.50	46.00	-13.50
43H	447.969	51.40	4.78	15.92	32.01	40.10	46.00	-5.90
44H	459.008	45.00	4.88	16.25	32.00	34.13	46.00	-11.87
45V	519.858	37.10	5.28	17.72	31.92	28.18	46.00	-17.82
46V	635.858	36.70	5.76	19.16	31.70	29.93	46.00	-16.07
47H	774.246	37.30	6.05	21.26	31.65	32.96	46.00	-13.04
48V	794.258	35.10	6.09	21.68	31.46	31.41	46.00	-14.59



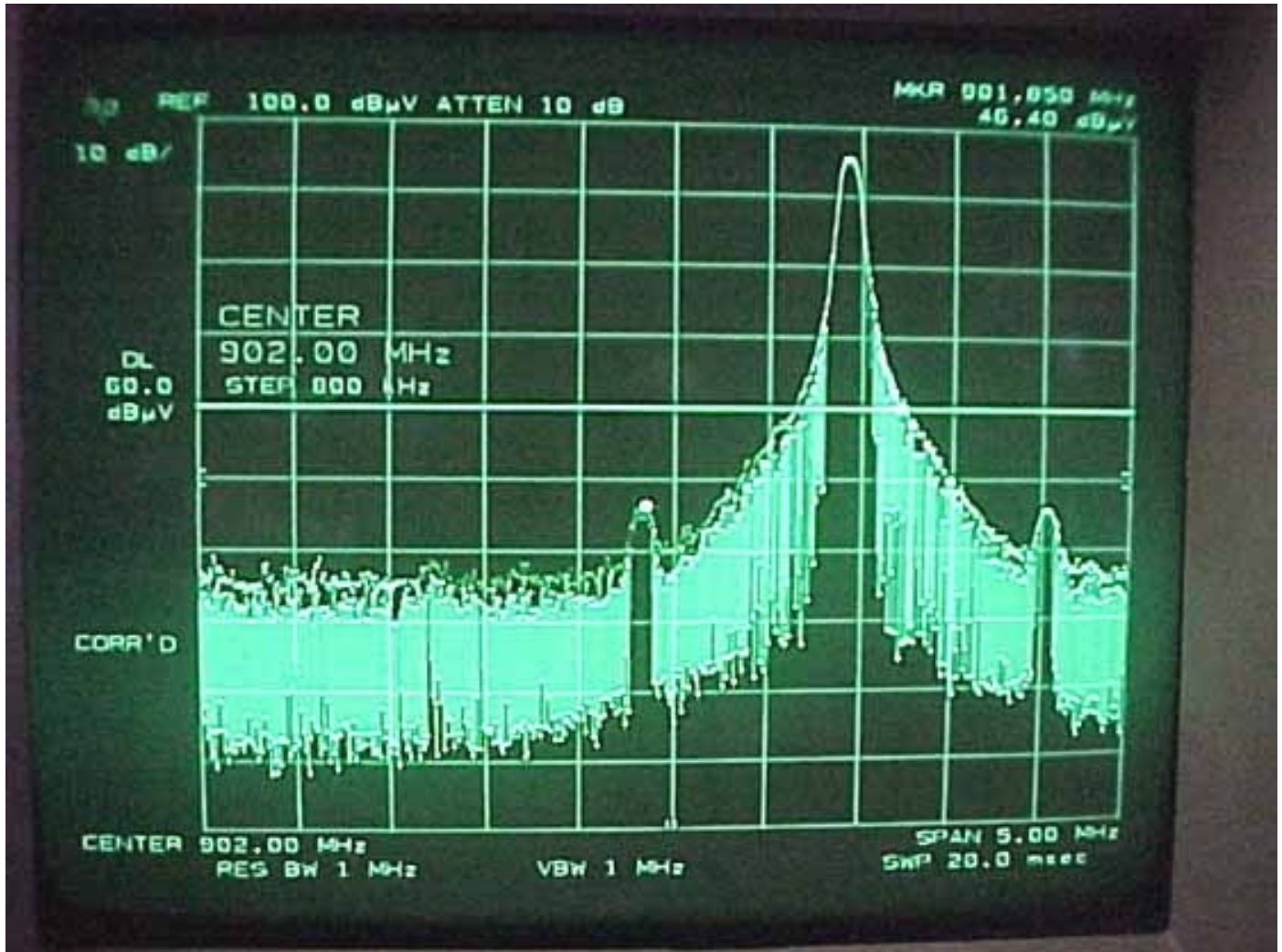
RADIATED EMISSIONS (FCC SECTION 15.249)

COMPANY	Embedded Processor Designs, Inc.	DATE	1/18/05
EUT	InfoCube Base	DUTY CYCLE	N/A %
MODEL	1	PEAK TO AVG	N/A dB
S/N	N/A	TEST DIST.	3 Meters
TEST ENGINEER	Kyle Fujimoto	LAB	A

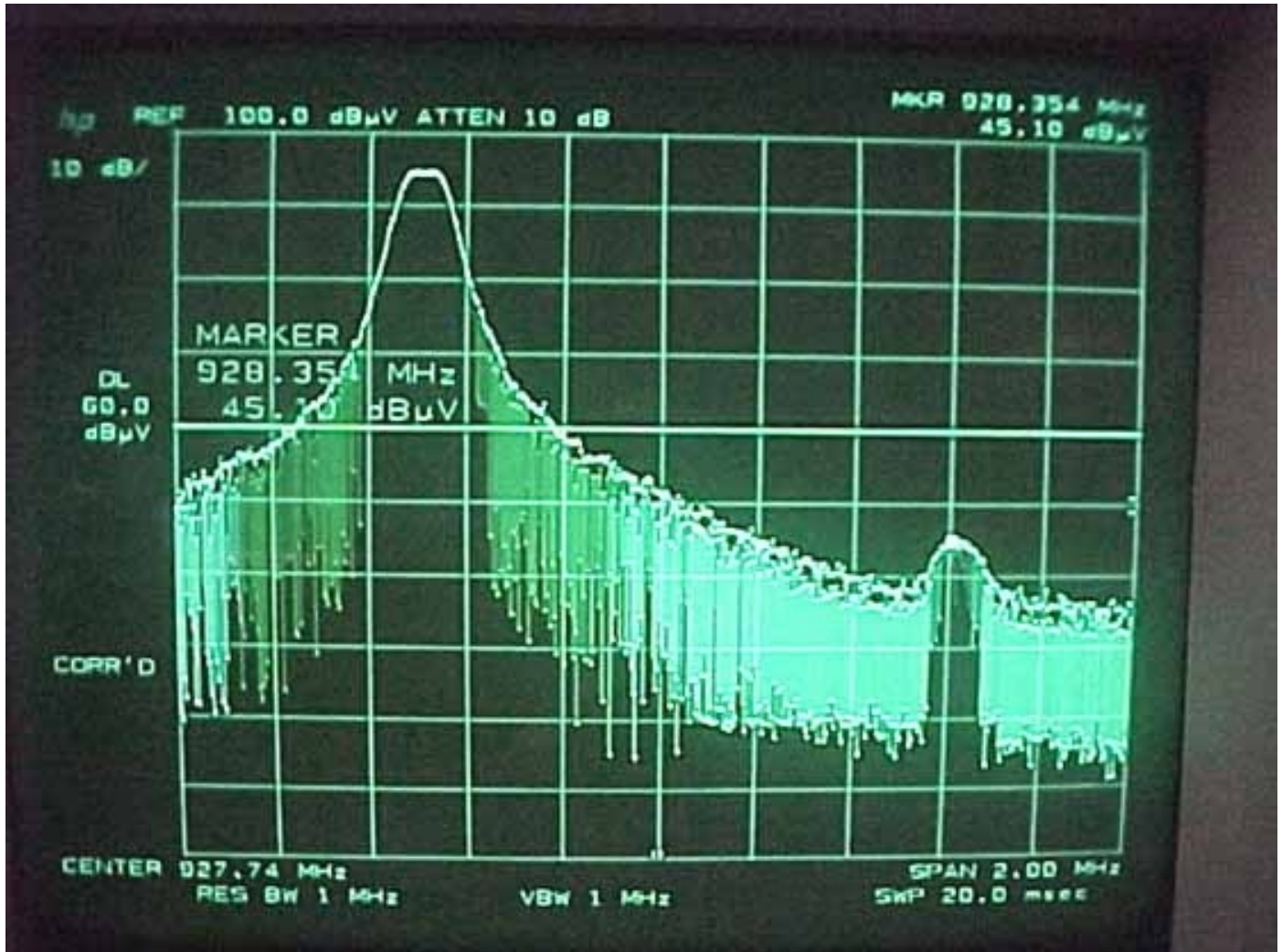
Frequency MHz	Peak Reading (dBuV)	Average (A) or Quasi- Peak (QP)	Antenna Polar. (V or H)	Antenna Height (meters)	EUT Azimuth (degrees)	EUT Axis (X,Y,Z)	EUT Tx Channel	Antenna Factor (dB)	Cable Loss (dB)	Amplifier Gain (dB)	Distance Factor (dB)	Mixer Factor (dB)	*Corrected Reading (dBuV/m)	Delta ** (dB)	Spec Limit (dBuV/m)	Comments
901.8500	46.4	QP	V	1.0	180		LOW	20.5	6.1	31.0	0.0	0.0	42.0	-4.0	46.0	Band Edge - Low Channel
																Vertical Polarization
																(Worst Case)
928.3540	45.1	QP	V	1.0	340		HIGH	21.2	6.4	31.2	0.0	0.0	41.4	-4.6	46.0	Band Edge - High Channel
																Vertical Polarization
																(Worst Case)

* CORRECTED READING = METER READING + ANTENNA FACTOR + CABLE LOSS - AMPLIFIER GAIN
 ** DELTA = SPEC LIMIT - CORRECTED READING

PAGE 1 of PAGE 1



**PHOTOGRAPH SHOWING THE BAND EDGE
FOR THE LOW CHANNEL**

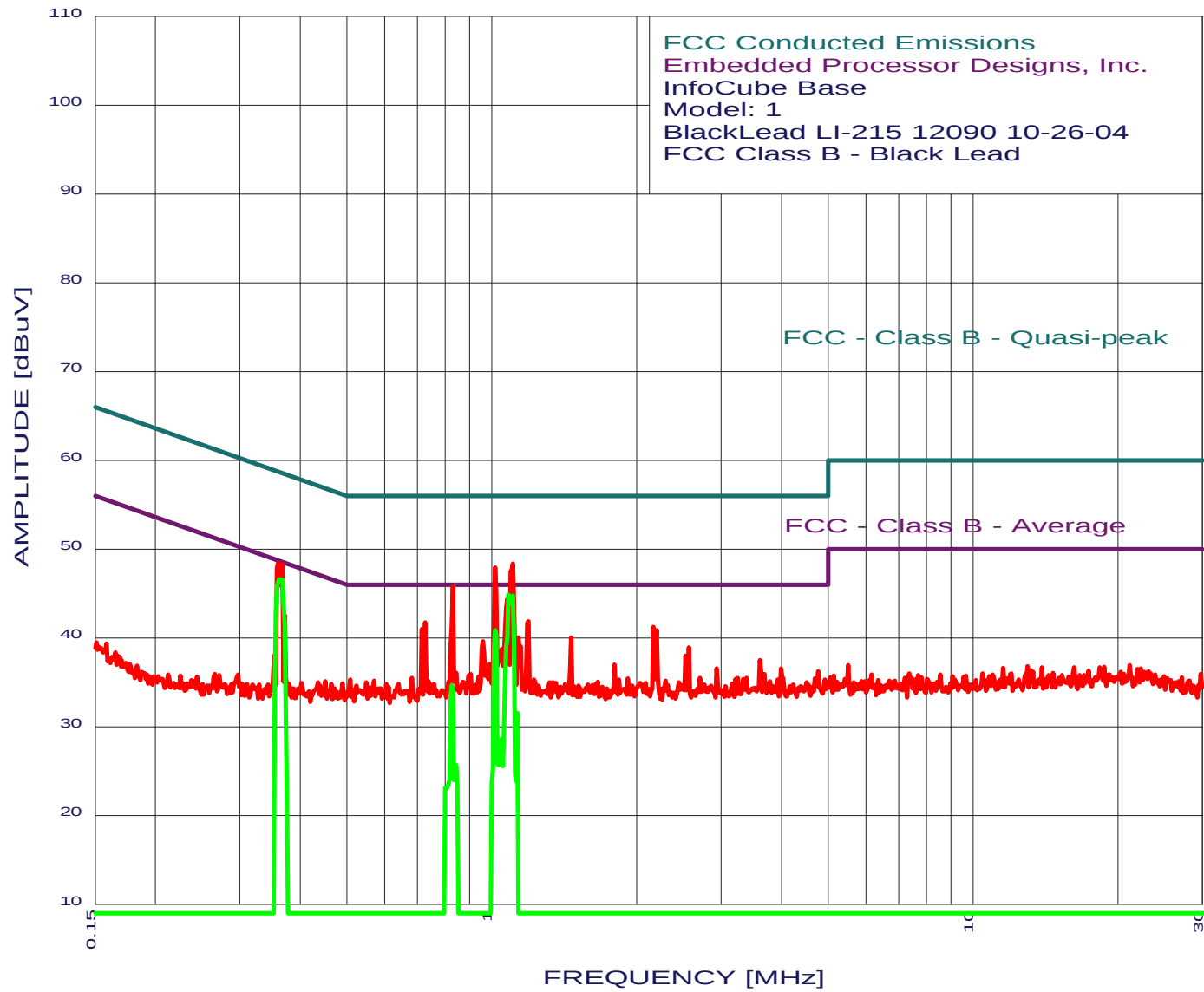


**PHOTOGRAPH SHOWING THE BAND EDGE
FOR THE HIGH CHANNEL**

CONDUCTED EMISSIONS
DATA SHEETS

EMISSION LEVEL [dBuV] PEAK
Graph for Peak & Average

1/20/2005 14:05:25



Embedded Processor Designs, Inc.
 InfoCube Base
 Model: 1
 FCC Class B - Black Lead
 TEST ENGINEER : Kyle Fujimoto

 45 highest peaks above -50.00 dB of FCC - Class B - Average limit line

Peak criteria : 1.00 dB, Curve : Peak

Peak#	Freq(MHz)	Amp(dBuV)	Limit(dB)	Delta(dB)
1	1.106	48.34	46.00	2.34*
2	1.016	47.92	46.00	1.92*
3	0.831	45.80	46.00	-0.20*
4	0.367	48.31	48.56	-0.25*
5	0.360	48.42	48.73	-0.32*
6	0.363	47.91	48.65	-0.73*
7	1.077	44.34	46.00	-1.66*
8	1.191	41.86	46.00	-4.14
9	0.728	41.69	46.00	-4.31
10	2.168	41.24	46.00	-4.76
11	0.716	40.99	46.00	-5.01
12	2.201	40.84	46.00	-5.16
13	1.136	40.05	46.00	-5.95
14	0.371	42.51	48.47	-5.96
15	1.464	40.01	46.00	-5.99
16	0.958	39.62	46.00	-6.38
17	2.568	38.88	46.00	-7.12
18	1.049	38.73	46.00	-7.27
19	2.527	37.97	46.00	-8.03
20	3.605	37.48	46.00	-8.52
21	0.990	37.02	46.00	-8.98
22	1.800	36.98	46.00	-9.02
23	3.987	36.52	46.00	-9.48
24	2.932	36.51	46.00	-9.49
25	4.774	36.20	46.00	-9.80
26	0.844	36.01	46.00	-9.99*
27	0.904	35.91	46.00	-10.09
28	3.683	35.89	46.00	-10.11
29	0.939	35.62	46.00	-10.38
30	2.707	35.49	46.00	-10.51
31	0.683	35.49	46.00	-10.51
32	3.383	35.46	46.00	-10.54
33	3.243	35.44	46.00	-10.56
34	1.918	35.40	46.00	-10.60
35	3.328	35.35	46.00	-10.65
36	3.209	35.34	46.00	-10.66
37	3.924	35.31	46.00	-10.69
38	4.928	35.31	46.00	-10.69
39	1.840	35.29	46.00	-10.71
40	0.508	35.27	46.00	-10.73
41	1.223	35.27	46.00	-10.73
42	0.570	35.18	46.00	-10.82
43	0.354	38.02	48.87	-10.84
44	2.298	35.15	46.00	-10.85
45	3.781	35.10	46.00	-10.90

 * Please See the Average Readings on the Next Page and on the Plot

Embedded Processor Designs, Inc.
InfoCube Base
Model: 1
FCC Class B - Black Lead
TEST ENGINEER : Kyle Fujimoto

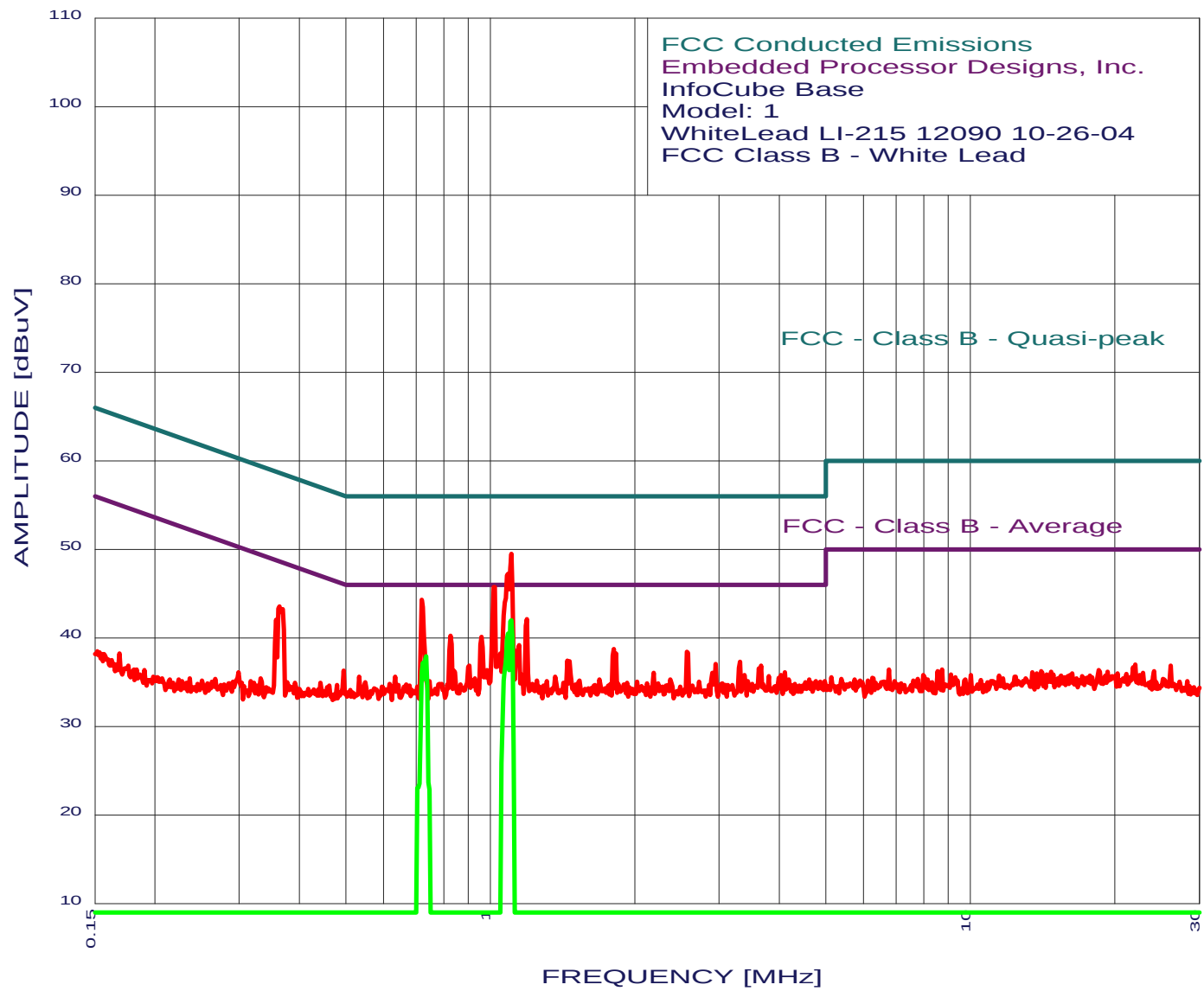
10 highest peaks above -50.00 dB of FCC - Class B - Average limit line

Peak criteria : 0.00 dB, Curve : Average

Peak#	Freq(MHz)	Amp(dBuV)	Limit(dB)	Delta(dB)
1	1.083	44.84	46.00	-1.16
2	1.106	44.73	46.00	-1.27
3	0.367	46.55	48.56	-2.01
4	0.362	46.62	48.69	-2.08
5	1.016	40.84	46.00	-5.16
6	0.826	34.65	46.00	-11.35
7	1.130	31.56	46.00	-14.44
8	1.049	28.61	46.00	-17.39
9	0.844	25.63	46.00	-20.37
10	0.801	23.08	46.00	-22.92

EMISSION LEVEL [dBuV] PEAK
Graph for Peak & Average

1/20/2005 14:10:06



EP Designs
InfoCube Base
Model: 1
FCC Class B - White Lead
TEST ENGINEER : Kyle Fujimoto

45 highest peaks above -50.00 dB of FCC - Class B - Average limit line

Peak criteria : 1.00 dB, Curve : Peak

Peak#	Freq(MHz)	Amp(dBuV)	Limit(dB)	Delta(dB)
1	1.106	49.45	46.00	3.45*
2	1.089	47.25	46.00	1.25*
3	1.021	45.73	46.00	-0.27*
4	0.720	44.30	46.00	-1.70*
5	1.191	42.07	46.00	-3.93
6	0.363	43.50	48.65	-5.15
7	0.826	40.21	46.00	-5.79
8	0.958	40.12	46.00	-5.88
9	0.358	42.00	48.78	-6.78
10	1.148	39.16	46.00	-6.84
11	1.810	38.70	46.00	-7.30
12	2.568	38.40	46.00	-7.60
13	1.049	38.24	46.00	-7.76
14	1.830	38.20	46.00	-7.80
15	1.449	37.42	46.00	-8.58
16	1.464	37.33	46.00	-8.67
17	3.311	37.29	46.00	-8.71
18	2.948	37.05	46.00	-8.95
19	3.663	36.83	46.00	-9.17
20	0.904	36.82	46.00	-9.18
21	3.624	36.52	46.00	-9.48
22	0.839	36.31	46.00	-9.69
23	0.494	36.27	46.09	-9.82
24	4.114	36.08	46.00	-9.92
25	2.190	36.06	46.00	-9.94
26	2.900	36.04	46.00	-9.96
27	1.160	35.86	46.00	-10.14
28	3.383	35.79	46.00	-10.21
29	4.029	35.77	46.00	-10.23
30	3.800	35.64	46.00	-10.36
31	1.367	35.61	46.00	-10.39
32	0.634	35.59	46.00	-10.41
33	0.532	35.57	46.00	-10.43
34	3.987	35.46	46.00	-10.54
35	0.929	35.42	46.00	-10.58
36	4.624	35.30	46.00	-10.70
37	2.371	35.28	46.00	-10.72
38	4.182	35.28	46.00	-10.72
39	1.690	35.27	46.00	-10.73
40	1.249	35.18	46.00	-10.82
41	1.536	35.14	46.00	-10.86
42	3.438	35.10	46.00	-10.90
43	0.747	35.10	46.00	-10.90
44	1.311	35.09	46.00	-10.91
45	2.023	35.04	46.00	-10.96

*Please See the Average Readings on the Next Page and on the Plot

Embedded Processor Designs, Inc.
InfoCube Base
Model: 1
FCC Class B - White Lead
TEST ENGINEER : Kyle Fujimoto

4 highest peaks above -50.00 dB of FCC - Class B - Average limit line

Peak criteria : 0.00 dB, Curve : Average

Peak#	Freq(MHz)	Amp(dBuV)	Limit(dB)	Delta(dB)
1	1.106	41.99	46.00	-4.01
2	1.089	40.49	46.00	-5.51
3	0.735	37.88	46.00	-8.12
4	0.724	37.16	46.00	-8.84
