

□ Product System (PS)

Subject: Circuit Operating Theory

Part No.:

Rev.: 0

Doc. No. 318-C01

Project Code: 99.L0872.001

Effective date

Page 1 of 11

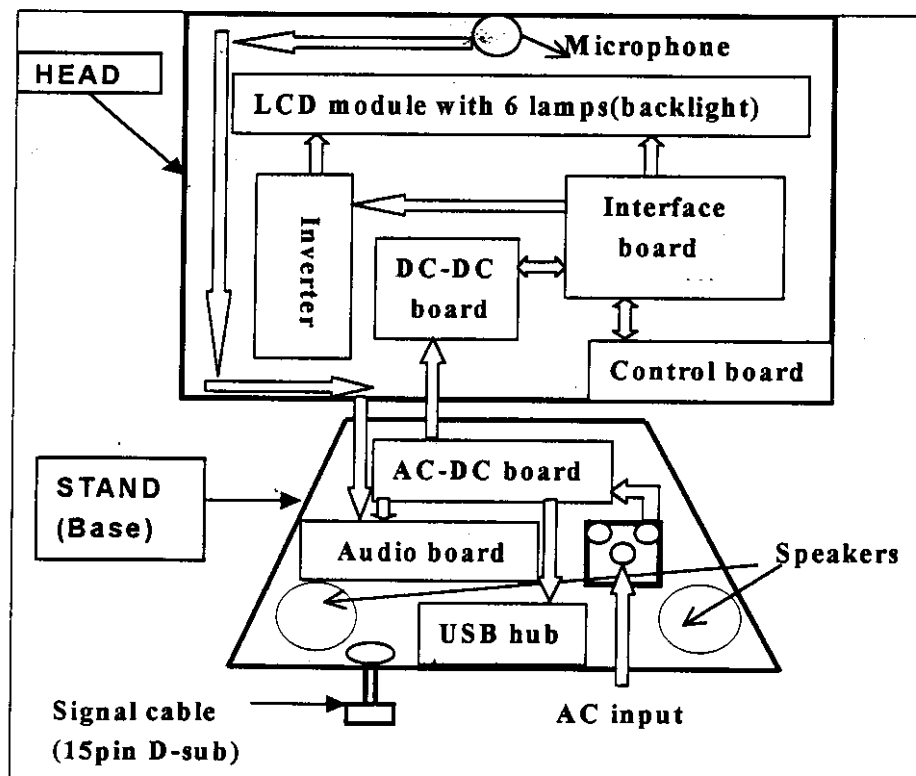
Model Name: FP855

III. Introduction:

The FP855 is an 18.1" SXGA(1280x1024) 24 bits color TFT LCD monitor with multi-media function and an optional USB hub. It's an analog interface LCD monitor with an undetachable 15 pins D-sub signal cable and it's compliant with VESA specification to offer a smart power management and power saving function. It also offers OSD menu for users to control the adjustable items and get some information about this monitor, and the best function is to offer users an easy method to set all adjustable items well just by pressing one key, we called it "i-key"(intelligent key) which can auto adjusting all controlled items.

II. Block diagram

The FP855 consists of a head and a stand(base). The head consists of a LCD module with 6 lamps, an Inverter, an Interface board, a DC-DC board, a Control board and a microphone. The stand consists of an AC-DC board, an Audio board, two speakers and an optional USB hub. The block diagram is shown as below,



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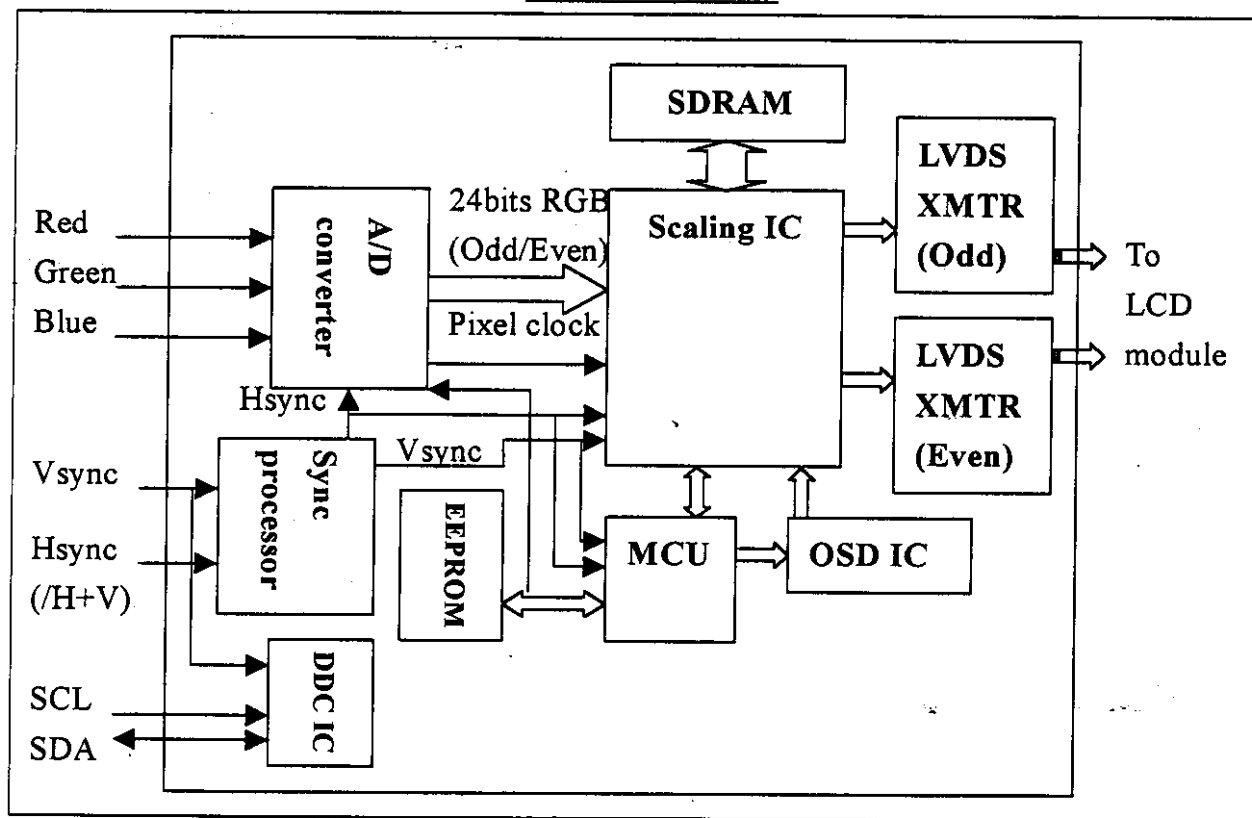
Model Name: FP855

III. Circuit operation theory:

A.) HEAD:

A-1.)Interface board diagram:

Interface Board



(a) Circuit operation theory:

A basic operation theory for this interface board is to convert analog signals of Red, Green and Blue to digital signals of Red, Green and Blue, and by the internal PLL circuit of A/D converter to generate the pixel clock to output to the scaling IC, then the scaling IC use the SDRAM to be the frame buffer to process the different input signals which are operating in the specification of the A/D converter and the scaling IC, finally the scaling IC output the digital RGB data, the fixed frequency of Hsync, Vsync and pixel clock to LCD panel driver IC by LVDS transmission.

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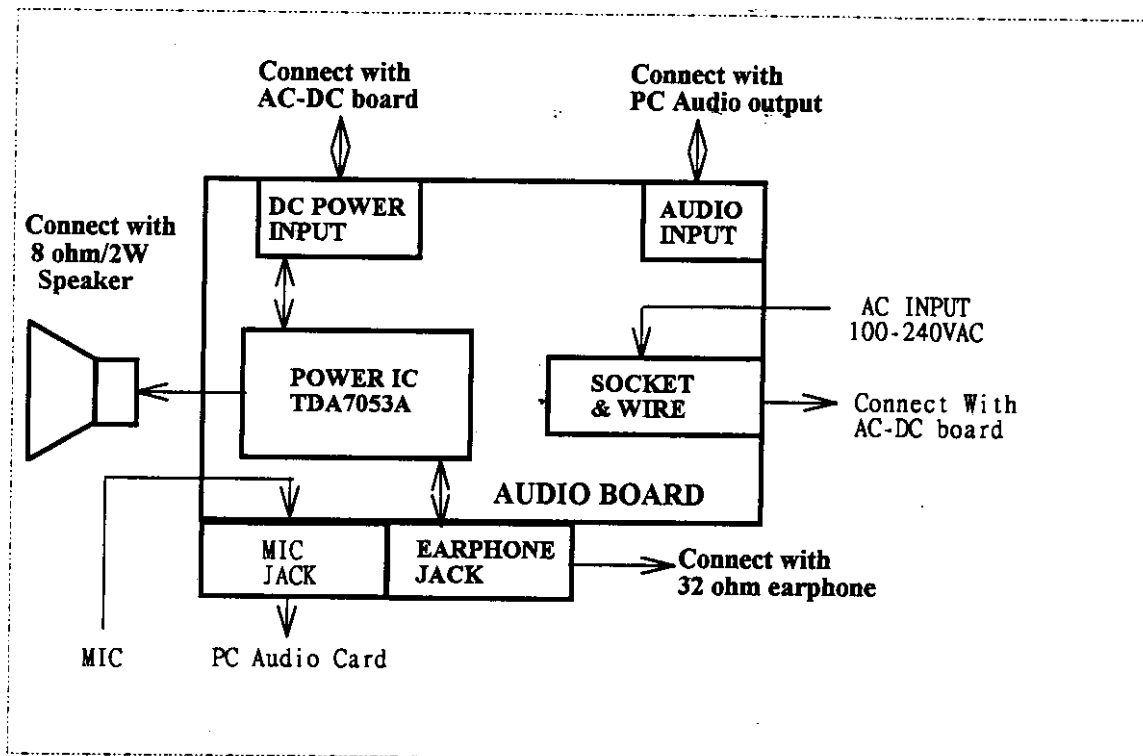
Doc. No. 318-C01

Project Code: 99.L0872.001

Effective date

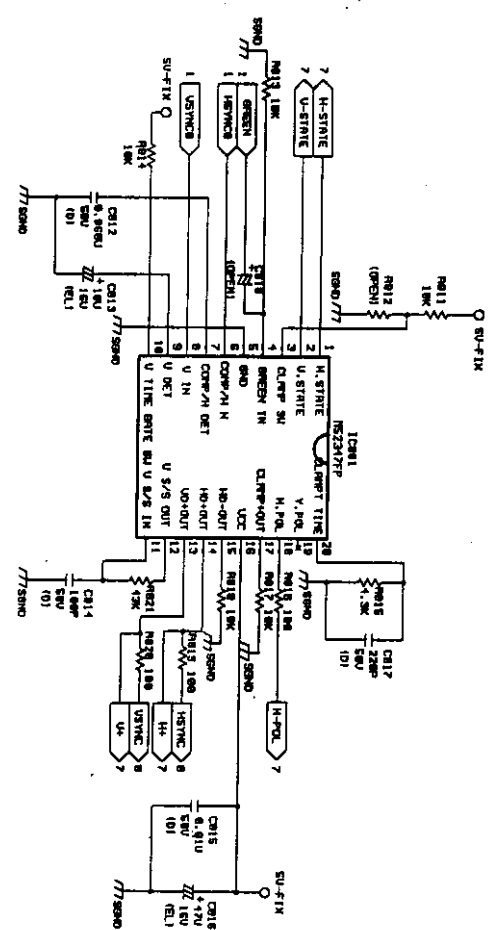
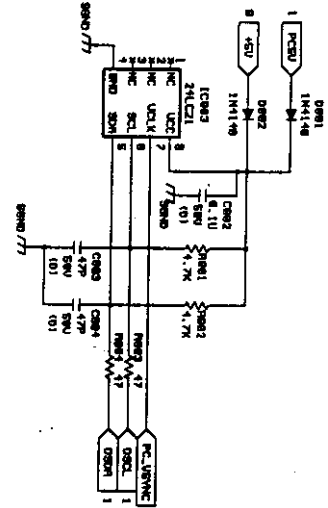
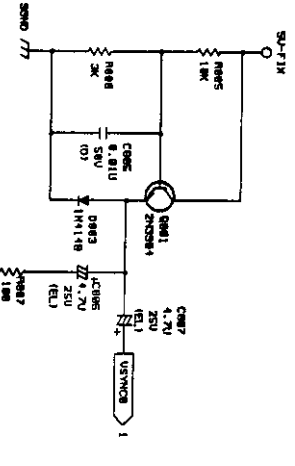
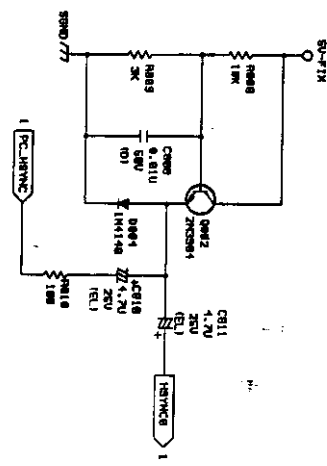
Page 8 of 11

Model Name: FP855



The Audio Speaker is consist of a Audio board. The Audio Speaker have DC Volumn control, Earphone jack, use 28mmX40mm Speaker (2W/per chennal), power supply from AC-DC board and Audio input from PC Audio output (Line Out).

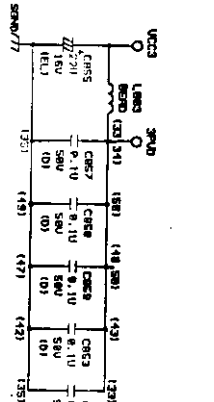
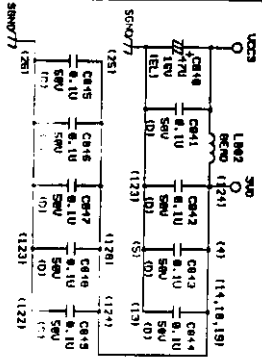
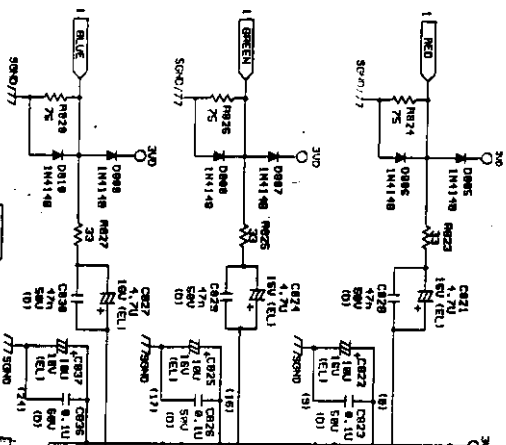
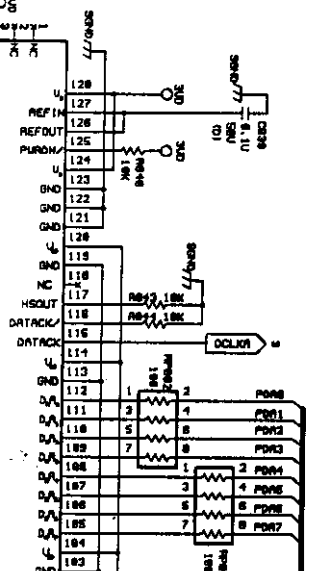
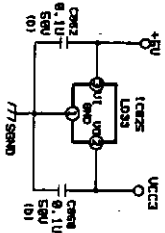
- (a) **Power IC:** Use Philips POWER IC TDA7053A. The IC are stereo BTL output amplifiers with DC Volume control. The devices are designed for use in TV and monitor, but are also suitable for battery-Fed portable recorders and radios. Use +12V from AC-DC Board and connect speaker to offer 1W per chennal.
- (b) **DC Power Input:** To supply +12V to be VCC source Voltage for TDA7053A and connected with AC-DC board.
- (c) **Audio Input:** connect with PC Audio output in 3.5mm to 3.5mm signal line.
- (d) **Speaker:** Use 8 ohm and 28mmX40mm speaker (1W/per chennal)
- (e) **DC Volumn Control:** The two DC volume control stages are integrated into the input stages so that no Coupling capacitors are required and a low offset voltage is still maintained. The minimum supply voltage also remains low. If the DC volume control voltage falls below 0.4V, the device will switch to the mute mode.
- (H) **Earphone jack:** To connect 32 ohm earphone.



FP885

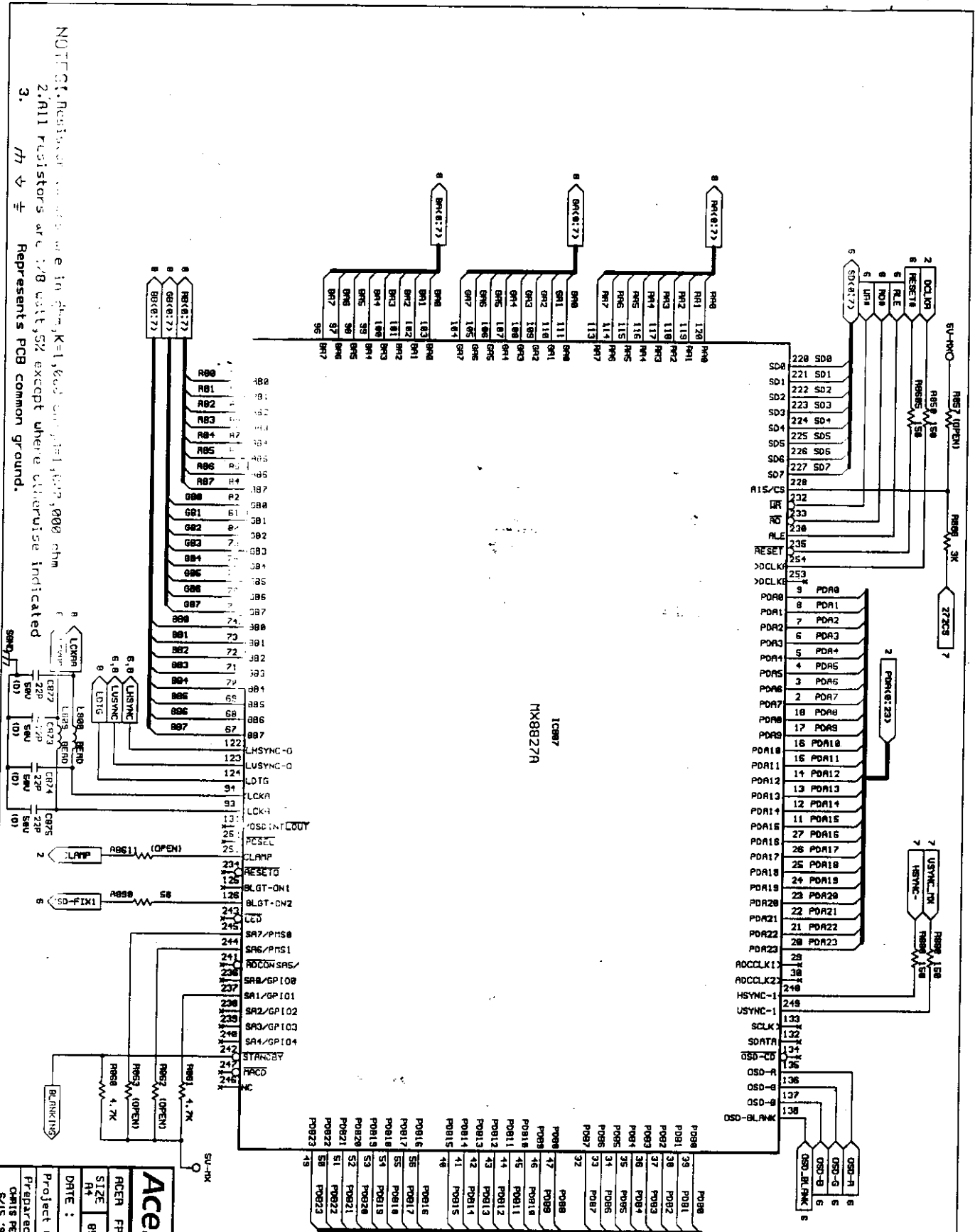
NOTES: Resistor values are in ohm, K=1,000 ohm, M=1,000,000 ohm
 2. All resistors are 1/8 watt, 5% except where otherwise indicated
 3. ∇ ∇ ∇ Represents PCB common ground.

ACER 宏碁電腦股份有限公司 Acer Peripherals, Inc.	
RCER-FT888 (INPUT.CKT) SCHEMATIC	
SIZE	8501F-S5.SCH
DATE	8/15/1995
Project Code	91.78082.001
Prepared By	CHIS PENG
Reviewed By	ELIY HANG
Approved By	WEX LI
	8/15 '95



NOTE: Resistor values are in ohms, K=1,000 ohms, M=1,000,000 ohms
 2. All resistors are 1/8 watt, 5% except where otherwise indicated
 3. // represents PCB common ground.

ACER 宏碁電腦股份有限公司 Acer Peripherals, Inc.			
ACER P9850 (P9850A, DKT) SCHEMATICS			
SIZE	R4	8501-SS, SCH	Doc. No. REV.
DATE	6/15/1999	Sheet	2 OF 9
Project Code: 51.79902.001		Reviewed By: ELEY HUNN	
Prepared By: CHEN FENG		Approved By: CHEN FENG	
6/15 '98		6/15 '98	

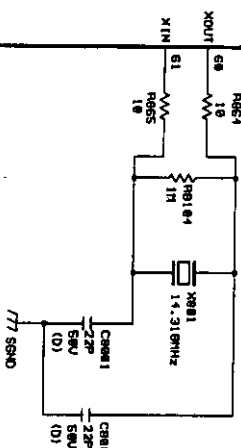


NOTES:
 1. Resistor values are in Ohms, K, and M, unless otherwise indicated.
 2. All resistors are 1/8 watt, 5% except where otherwise indicated.
 3. ∇ Represents PCB common ground.

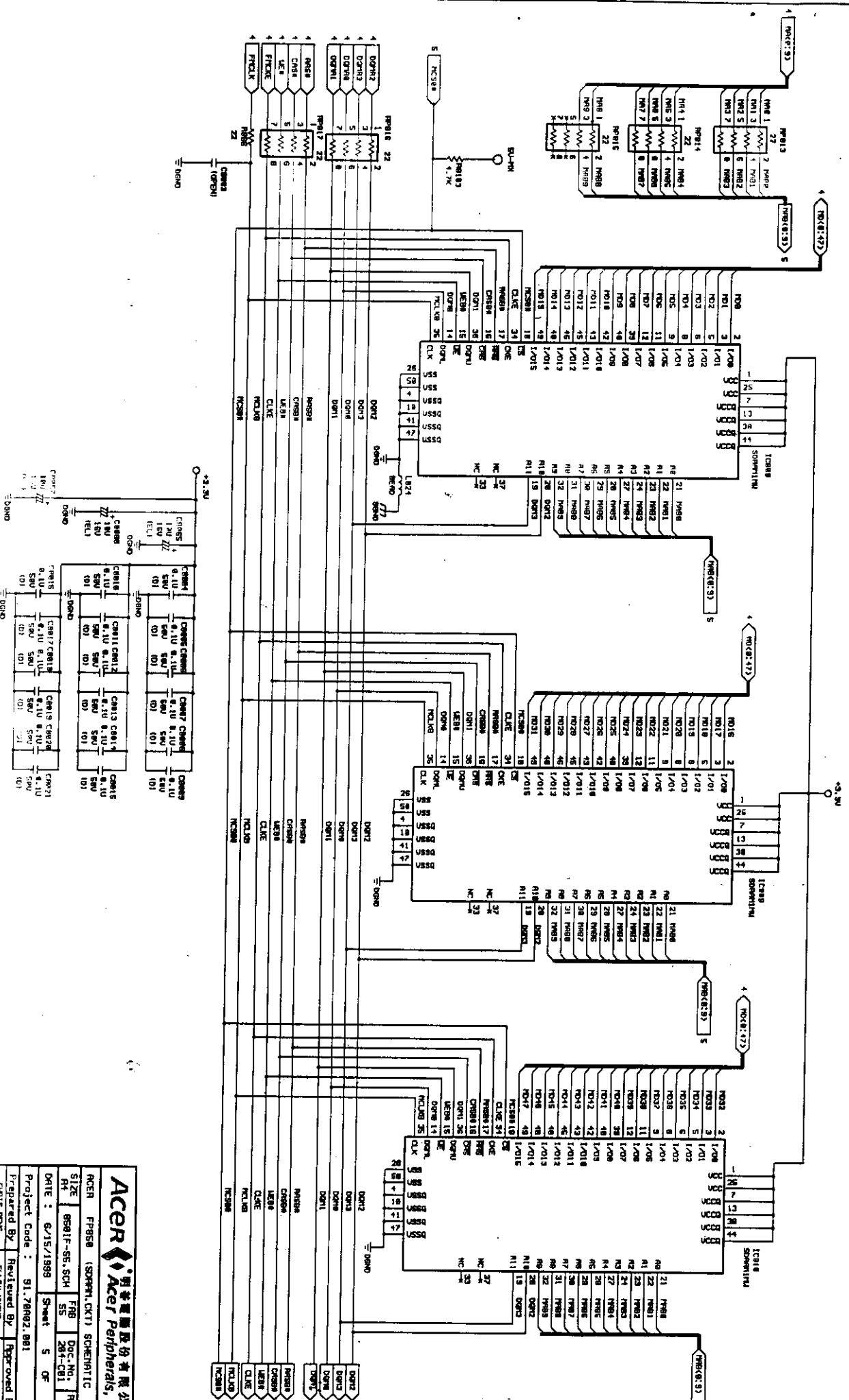
ACER 宏碁電腦股份有限公司
 Acer Peripherals, Inc.

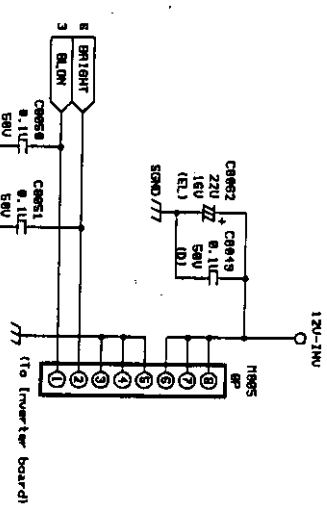
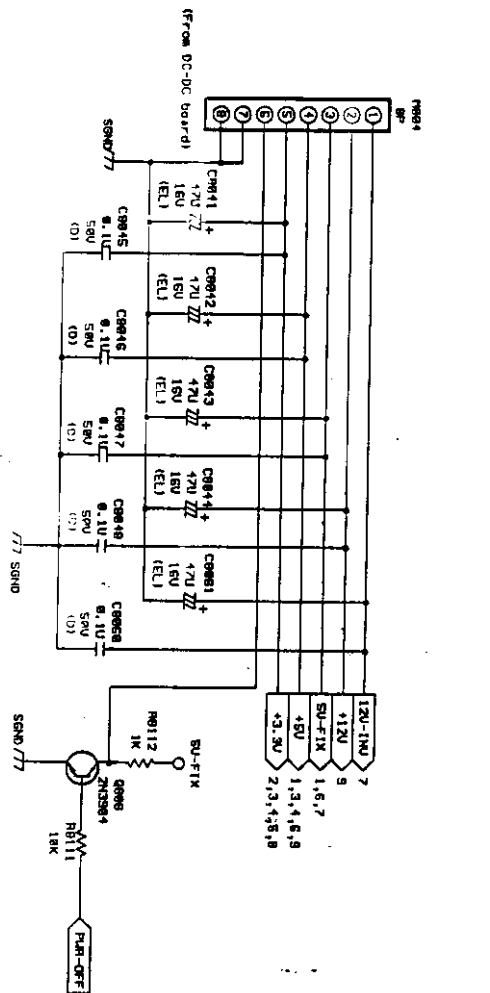
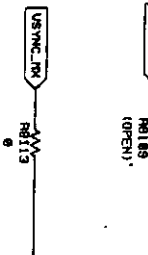
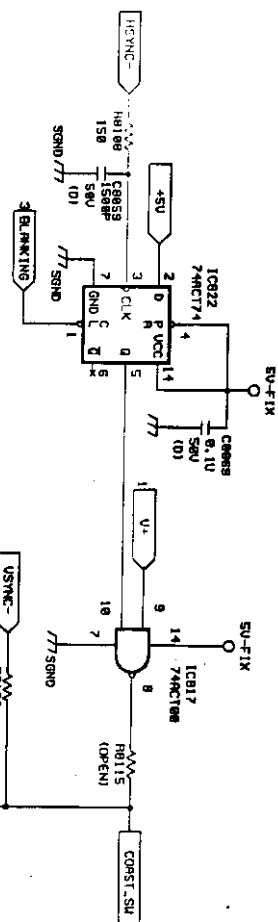
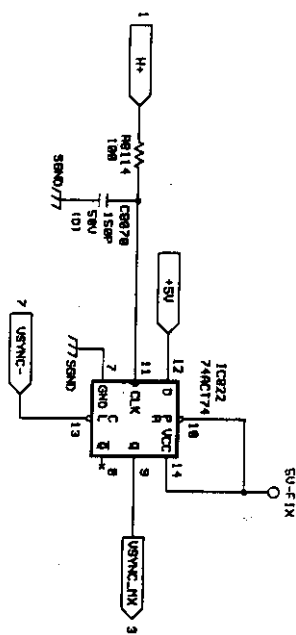
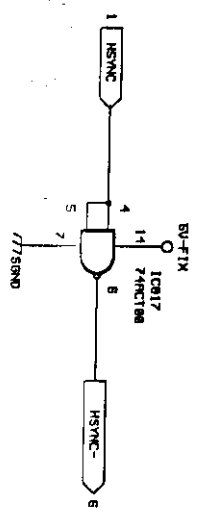
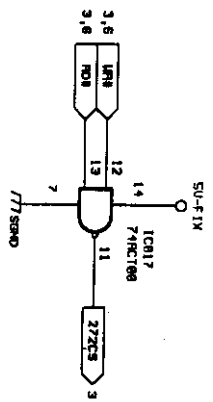
ACER FP858 (80L282-1.CKT) SCHEMATIC
 SIZE: 8501F-55.SCH
 DATE: 6/15/1999
 Project Code: 91.70602.001
 Prepared By: CHRIS FONG
 Reviewed By: EILEY KENN
 Approved By: REX HU
 6/15/99

RES	RES	FUNCTION
80G2	80G3	FUNCTION
OPEN	OPEN	Direct mode
4.7K	OPEN	Reserved
4.7K	OPEN	Indirect mode
4.7K	4.7K	Set I/O Bus

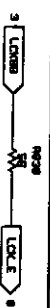


2. All resistors are 1/8 watt, 5% except where otherwise indicated

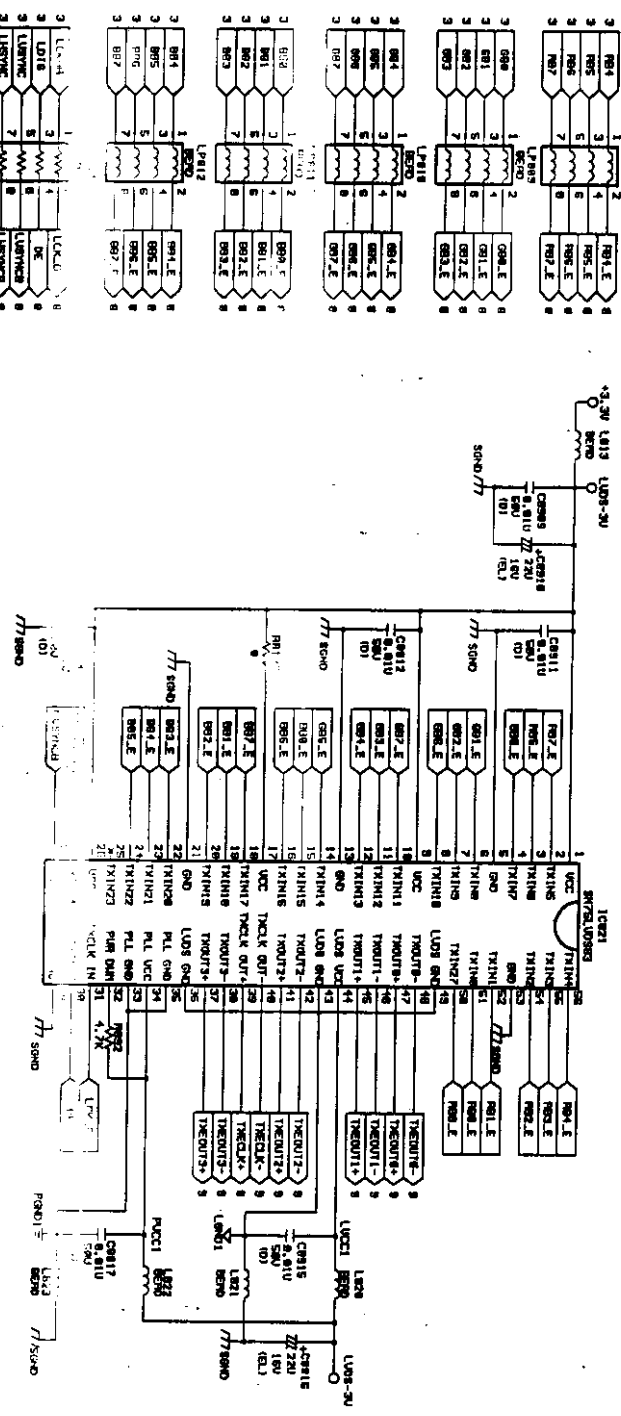
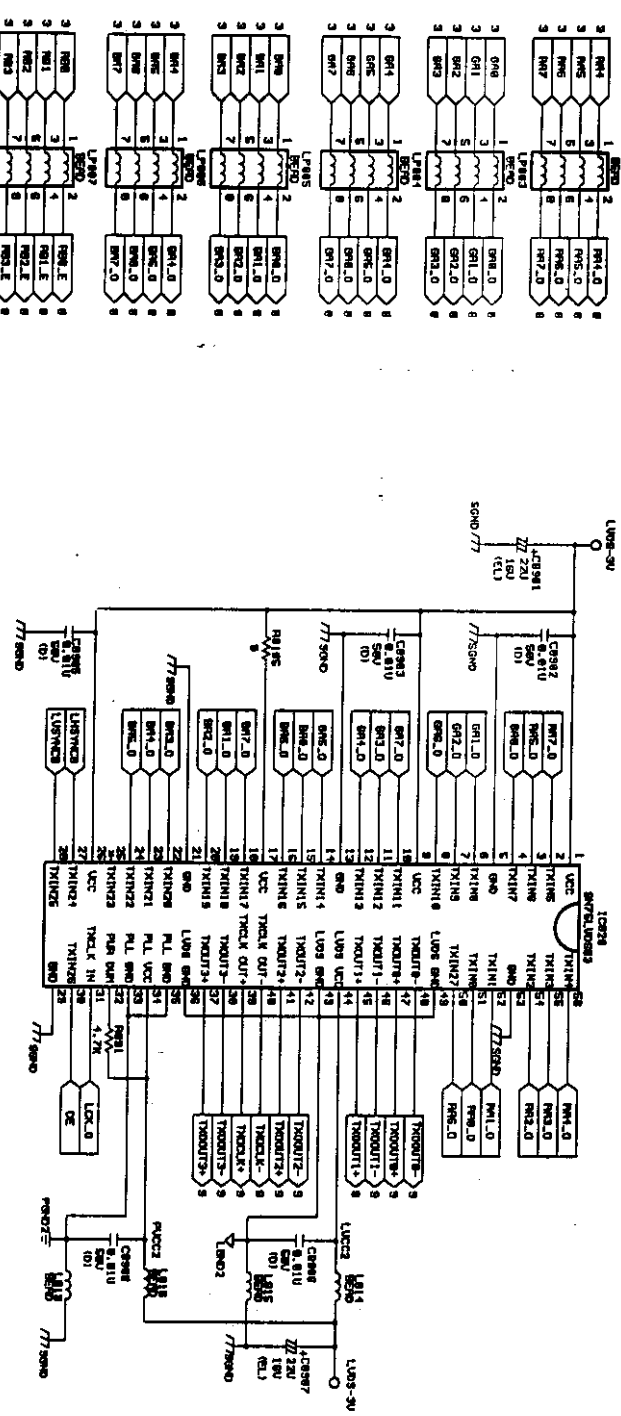




3. $R \nabla \pm$ Represents PCB common ground.

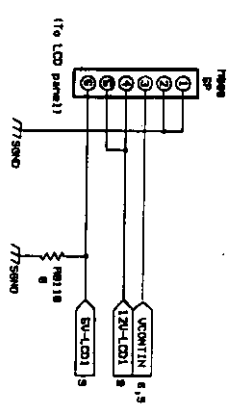
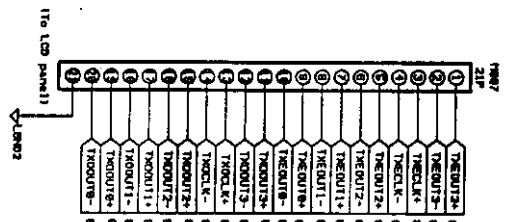
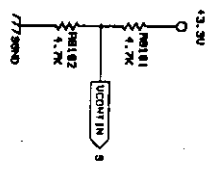
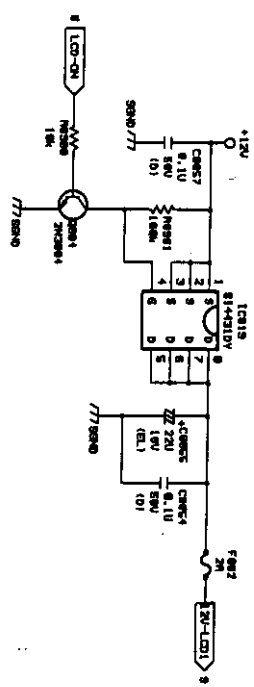
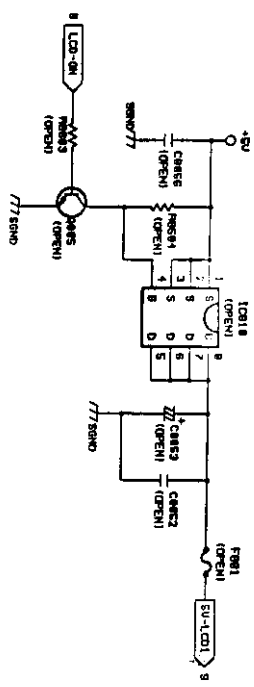


NOTE: Resistor values are in ohm, K=1,000 ohm, M=1,000,000 ohm
 2. All resistors are 1/8 watt 5% except where otherwise indicated
 3. // $\pm$ Represents PCB common ground.



IC1000 & IC1001	MAXLINE	MAXLINE
IC1002 & IC1003	MAXLINE	MAXLINE
IC1004 & IC1005	MAXLINE	MAXLINE
IC1006 & IC1007	MAXLINE	MAXLINE
IC1008 & IC1009	MAXLINE	MAXLINE

ACER 宏碁電腦股份有限公司 Acer Peripherals, Inc.	
PCB P0856 (L1000 TRANSMITTER, CKT1 SCHEMATICS)	REV. 0
SIZE: 8581F-S5 SCH	REV. 0
DATE: 1/15/1999	Sheet 8 OF 9
Project Code: 81.7002.001	
Prepared By: CHIEF NAME	Reviewed By: CHIEF NAME
DATE: 1/15/99	DATE: 1/15/99



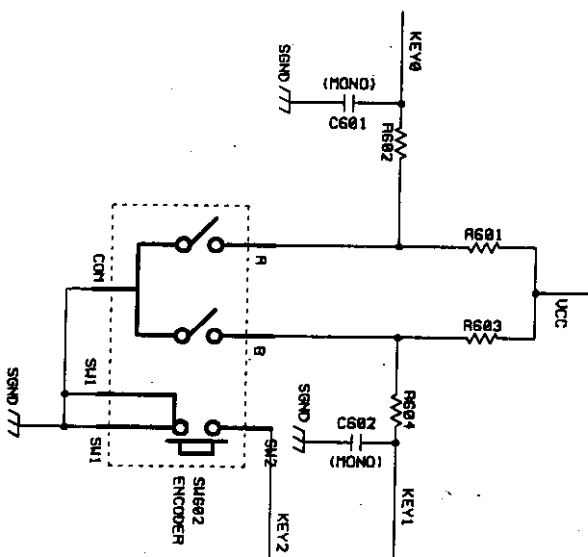
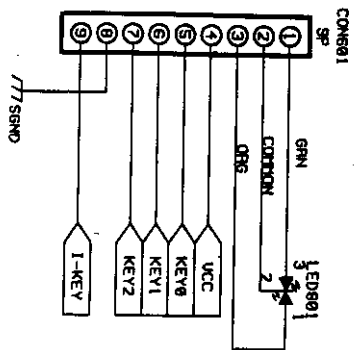
Part Type	IC18	QMS	RES3	RES4	C856	C853	C852	F801	M812	M818
IC18 (81431DV)	OPEN	OPEN	OPEN	OPEN	OPEN	OPEN	OPEN	OPEN	4.7K	8
QMS (1081E068)	514431DV	202084	10K	10K	8.10K	220F	8.10K	202084	OPEN	OPEN
RES3 (1081E068)	OPEN	OPEN	OPEN	OPEN	OPEN	OPEN	OPEN	OPEN	4.7K	OPEN

Acer 宏碁電腦股份有限公司
Acer Peripherals, Inc.

SIZE	ACER FB85B (OUTPUT, CKT)	SCHEMATICS
44	6501F-SS, SCH	FAB
SS	281-C81	REV.

Project Code:		51.70802.001	
Drawn By	Reviewed By	Approved By	
CHIS TEO 6/15 '98	ELLY HUNG 6/15 '98	REY HUNG 6/15 '98	

3. Represents PCB common ground.



NOTES: Resistor values are in ohm, K=1,000 ohm, M=1,000,000 ohm

2. All resistors are 1/8 watt, 5% except where otherwise indicated

3. ∇ Represents PCB common ground.

ACER 明基電腦股份有限公司
Acer Peripherals

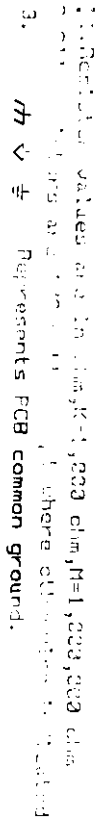
RCERUIEM 2651X SCHEMATICS

S1/E	CONTROL.SCH	FRB	Doc.No.
R4		S1	204-C01

DATE : 9/10/1998 Sheet 1 OF

Project Code, 91.71002.001

Prepared By Reviewed By Approved



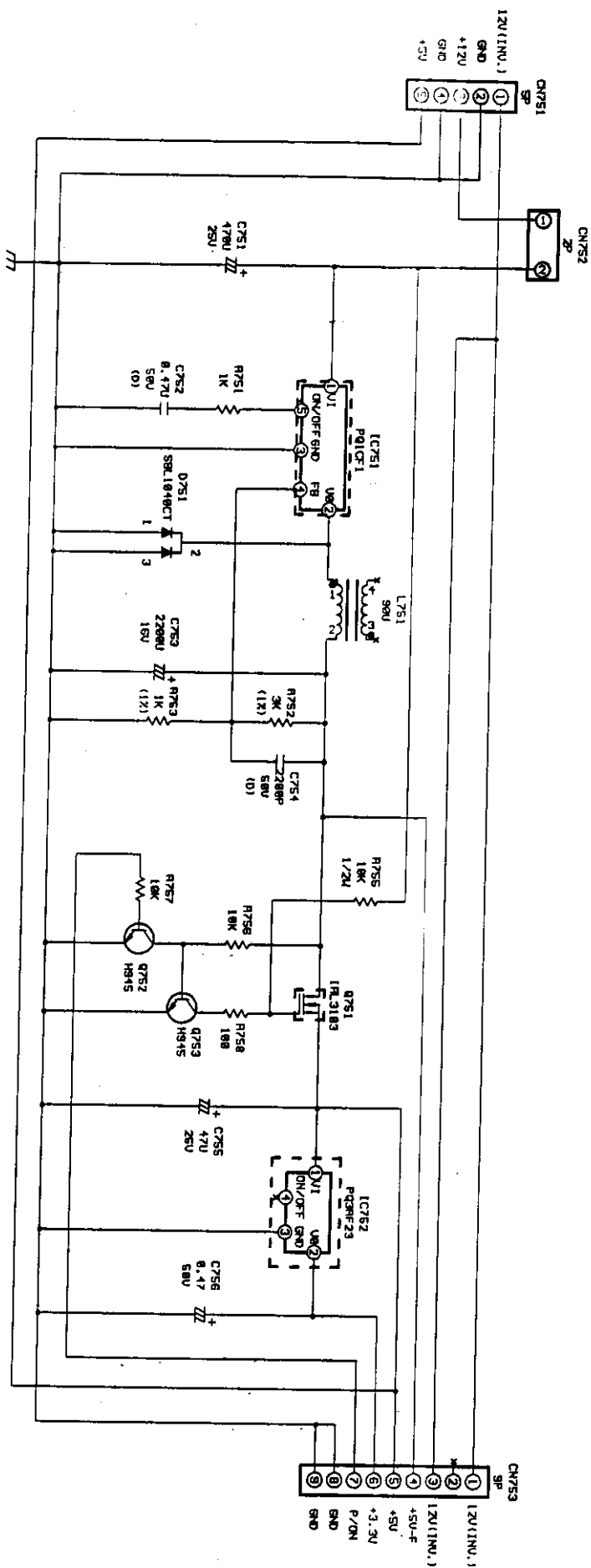
ACERULEU	FP855	SPS	SCHEMATICS
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DATE :	15/07/1979	Sheet	1	of	1
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Project Code. _____

Prepared By	Reviewed By	Approved By
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[illegible]



NOTES: Resistor values are in ohms, 1,000 ohm, 10,000 ohm
 2. All resistors are 1/8 watt except where otherwise indicated
 3. All components are represented PCB common ground.

ACER 明基電腦股份有限公司
Acer Peripherals, Inc.

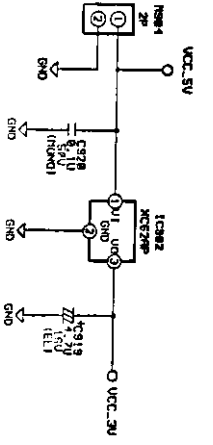
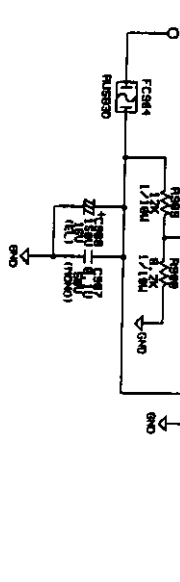
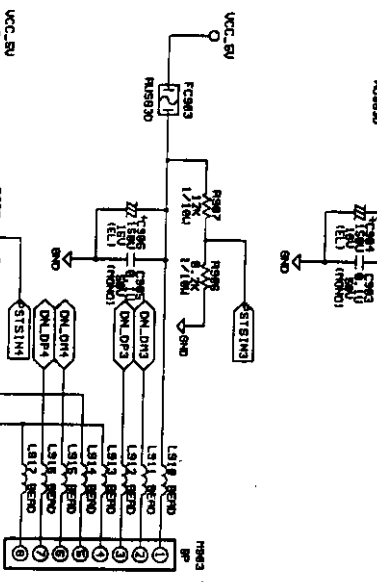
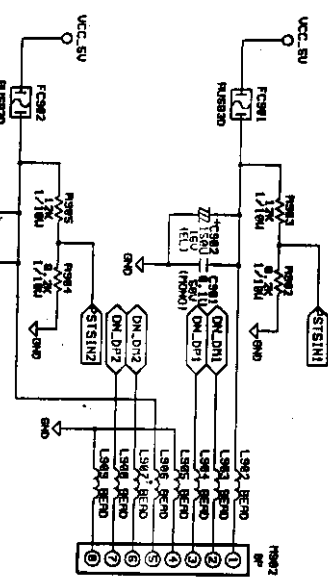
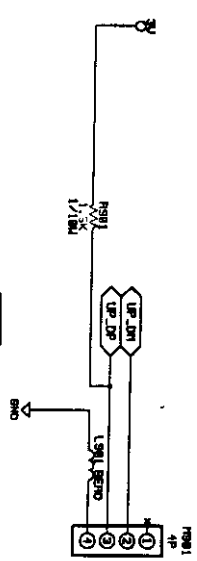
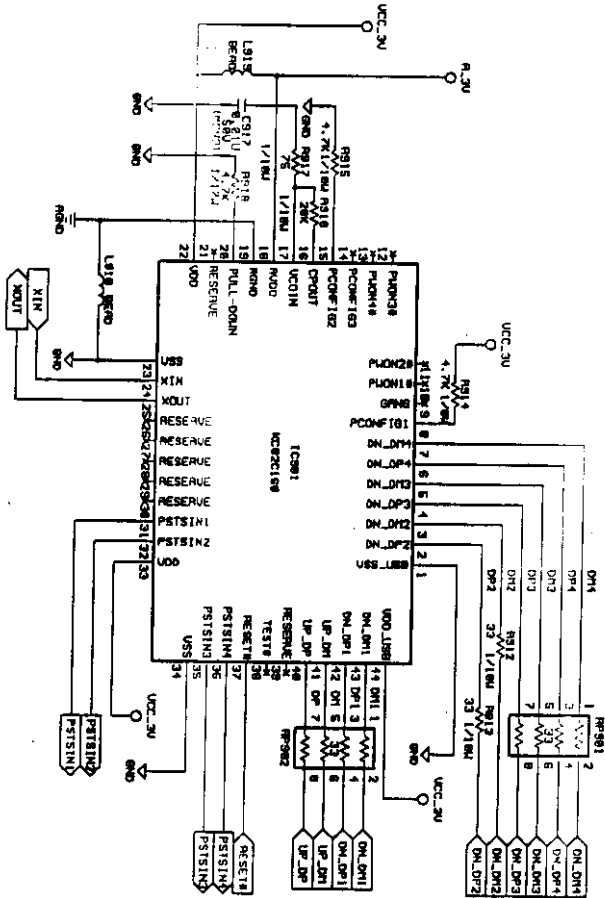
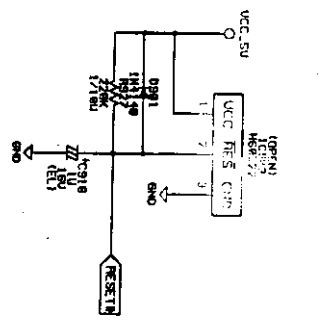
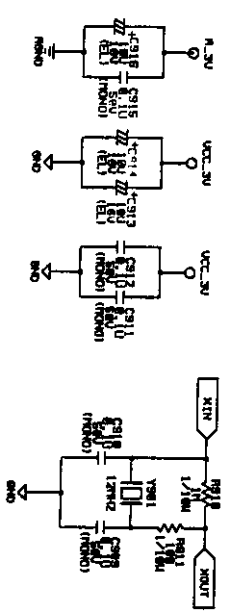
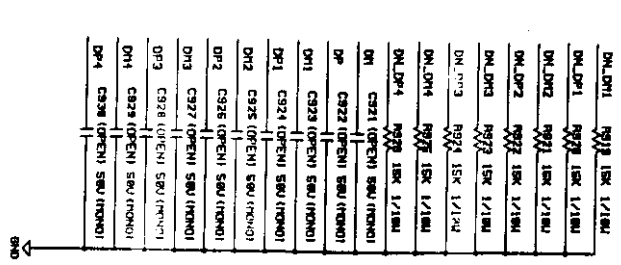
ACERVIEW FP855 SPS SCHEMATICS

SIZE: 11x17 F8B Doc. No. REV. 1

DATE: 10/7/1999 Sheet 1 OF 1

Project Code:

Prepared By: Reviewed By: Approved By:



NOTES: 1. Resistor values are in ohm, K=1,000 ohm, M=1,000,000 ohm
 2. All resistors are 1/8 watt, 5% except where otherwise indicated
 3. Represents PCB common ground.

ACER
 Acer Peripherals, Inc.

SIZE	2571U-S1 SCH	FR8	Doc. No.	REU
DATE	4/1/1898	S1	284-CB3	8
Project Code	91.71202.001	Sheet	1	OF 1
Designed By	CHITS KHEW	Reviewed By	NITIZ CHEN	Approved By
4/2/98		4/2/98		4/2/98