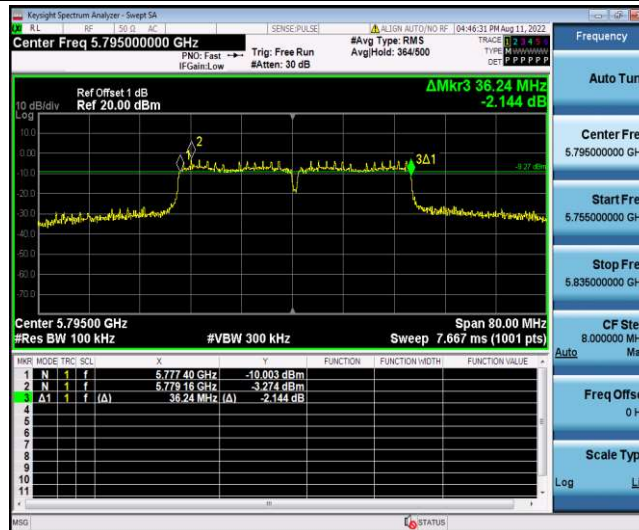




11AC40SISO_Ant1_5795



11AC40SISO_Ant2_5795



11AC80SISO_Ant1_5775



11AC80SISO_Ant2_5775

Appendix C: Maximum conducted output power

Test Result

TestMode	Antenna	Channel	Result[dBm]	Limit[dBm]	Verdict
11A	Ant1	5180	13.19	≤23.98	PASS
	Ant2	5180	13.41	≤23.98	PASS
	Ant1	5200	13.49	≤23.98	PASS
	Ant2	5200	13.61	≤23.98	PASS
	Ant1	5240	13.67	≤23.98	PASS
	Ant2	5240	13.85	≤23.98	PASS
	Ant1	5745	12.24	≤30	PASS
	Ant2	5745	12.33	≤30	PASS
	Ant1	5785	11.47	≤30	PASS
	Ant2	5785	11.74	≤30	PASS
	Ant1	5825	11.91	≤30	PASS
	Ant2	5825	11.37	≤30	PASS
11N20SISO	Ant1	5180	13.35	≤23.98	PASS
	Ant2	5180	12.69	≤23.98	PASS
	Ant1	5200	13.16	≤23.98	PASS
	Ant2	5200	13.06	≤23.98	PASS
	Ant1	5240	13.83	≤23.98	PASS
	Ant2	5240	13.25	≤23.98	PASS
	Ant1	5745	12.38	≤30	PASS
	Ant2	5745	12.50	≤30	PASS
	Ant1	5785	11.65	≤30	PASS
	Ant2	5785	11.84	≤30	PASS
	Ant1	5825	11.09	≤30	PASS
	Ant2	5825	11.09	≤30	PASS
11N40SISO	Ant1	5190	13.39	≤23.98	PASS
	Ant2	5190	12.71	≤23.98	PASS
	Ant1	5230	12.96	≤23.98	PASS
	Ant2	5230	13.17	≤23.98	PASS
	Ant1	5755	12.18	≤30	PASS
	Ant2	5755	12.27	≤30	PASS
	Ant1	5795	11.40	≤30	PASS
	Ant2	5795	11.67	≤30	PASS
11AC20SISO	Ant1	5180	13.39	≤23.98	PASS
	Ant2	5180	12.63	≤23.98	PASS
	Ant1	5200	13.14	≤23.98	PASS
	Ant2	5200	12.98	≤23.98	PASS
	Ant1	5240	13.28	≤23.98	PASS
	Ant2	5240	13.26	≤23.98	PASS

	Ant1	5745	11.43	≤30	PASS
	Ant2	5745	11.42	≤30	PASS
	Ant1	5785	10.56	≤30	PASS
	Ant2	5785	10.61	≤30	PASS
	Ant1	5825	9.97	≤30	PASS
	Ant2	5825	10.32	≤30	PASS
11AC40SISO	Ant1	5190	13.23	≤23.98	PASS
	Ant2	5190	12.61	≤23.98	PASS
	Ant1	5230	13.22	≤23.98	PASS
	Ant2	5230	13.30	≤23.98	PASS
	Ant1	5755	11.26	≤30	PASS
	Ant2	5755	11.17	≤30	PASS
	Ant1	5795	10.36	≤30	PASS
	Ant2	5795	10.56	≤30	PASS
11AC80SISO	Ant1	5210	13.90	≤23.98	PASS
	Ant2	5210	13.08	≤23.98	PASS
	Ant1	5775	11.68	≤30	PASS
	Ant2	5775	11.78	≤30	PASS

Note: 1. The Duty Cycle Factor was compensated in the spectrum analyzer Ref offset.

2. Spectrum analyzer Ref offset=Cable Loss(1dB) + Duty Cycle Factor.

3. Duty Cycle Factor=10Log(1/X), where X is the Duty Cycle.

Test Graphs



11A_Ant1_5180



11A_Ant2_5180



11A_Ant1_5200



11A_Ant2_5200



11A_Ant1_5240



11A_Ant2_5240



11A_Ant1_5745



11A_Ant2_5745



11A_Ant1_5785



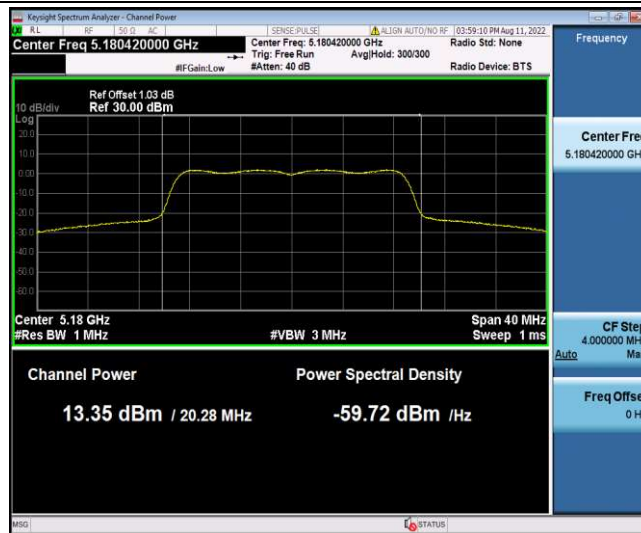
11A_Ant2_5785



11A_Ant1_5825



11A_Ant2_5825



11N20SISO_Ant1_5180



11N20SISO_Ant2_5180



11N20SISO_Ant1_5200



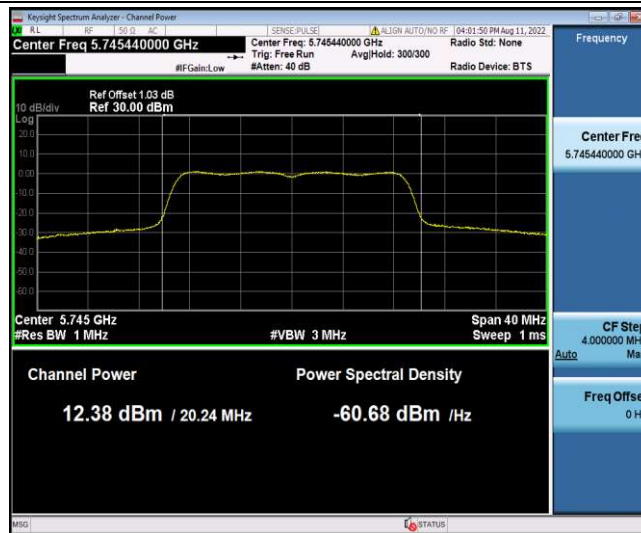
11N20SISO_Ant2_5200



11N20SISO_Ant1_5240



11N20SISO_Ant2_5240



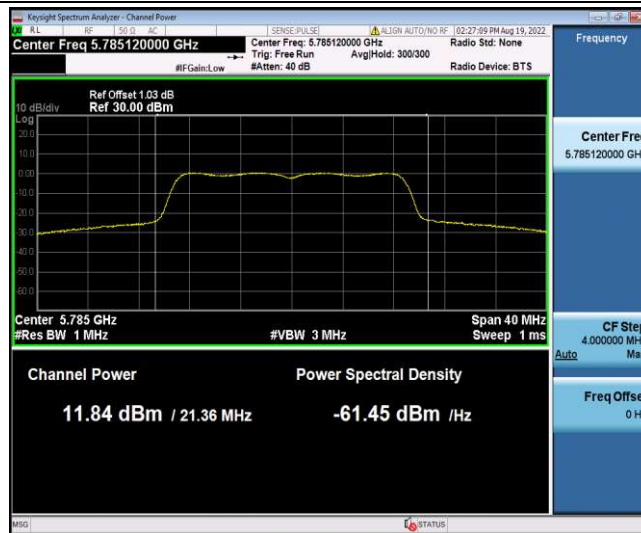
11N20SISO_Ant1_5745



11N20SISO_Ant2_5745



11N20SISO_Ant1_5785



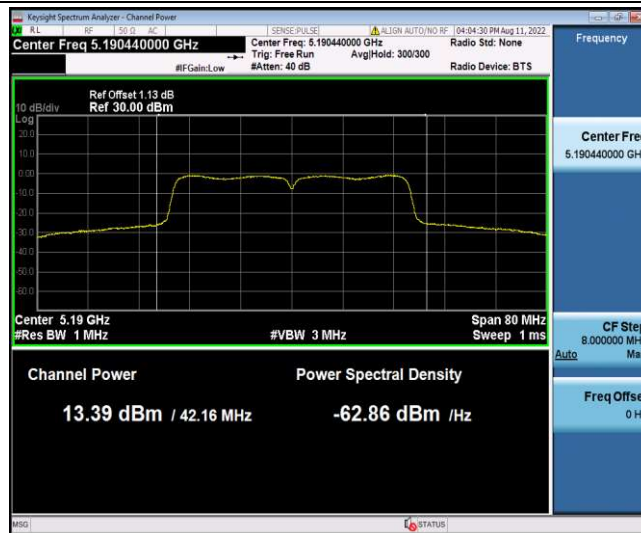
11N20SISO_Ant2_5785



11N20SISO_Ant1_5825



11N20SISO_Ant2_5825



11N40SISO_Ant1_5190



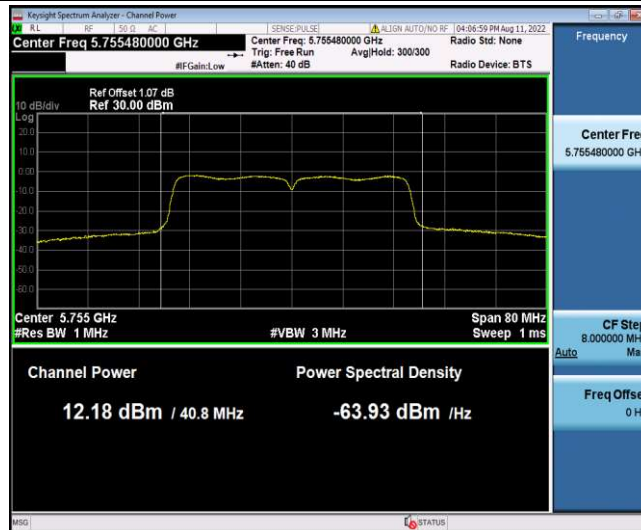
11N40SISO_Ant2_5190



11N40SISO_Ant1_5230



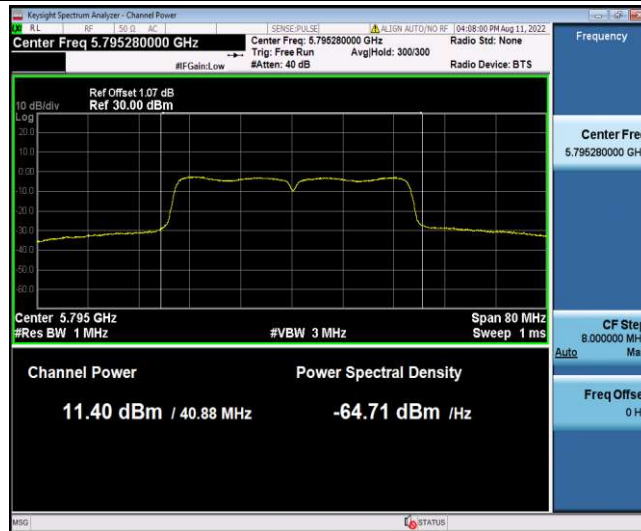
11N40SISO_Ant2_5230



11N40SISO_Ant1_5755



11N40SISO_Ant2_5755



11N40SISO_Ant1_5795



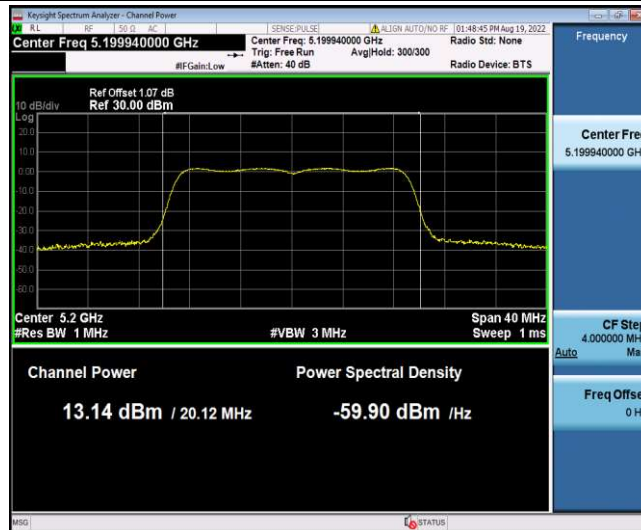
11N40SISO_Ant2_5795



11AC20SISO_Ant1_5180



11AC20SISO_Ant2_5180



11AC20SISO_Ant1_5200



11AC20SISO_Ant2_5200



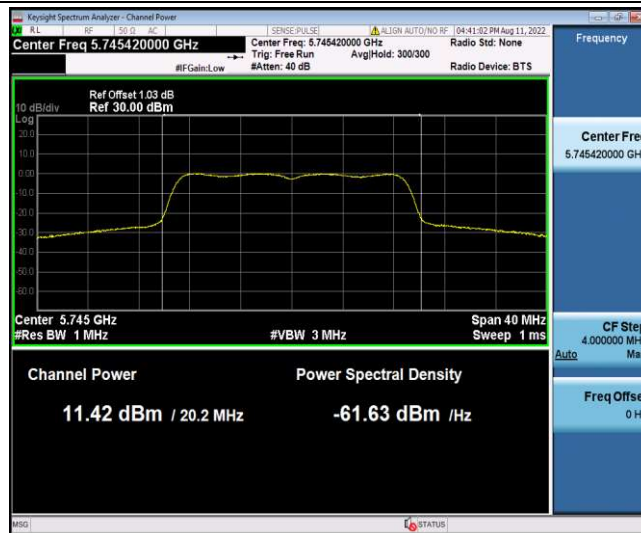
11AC20SISO_Ant1_5240



11AC20SISO_Ant2_5240



11AC20SISO_Ant1_5745



11AC20SISO_Ant2_5745



11AC20SISO_Ant1_5785



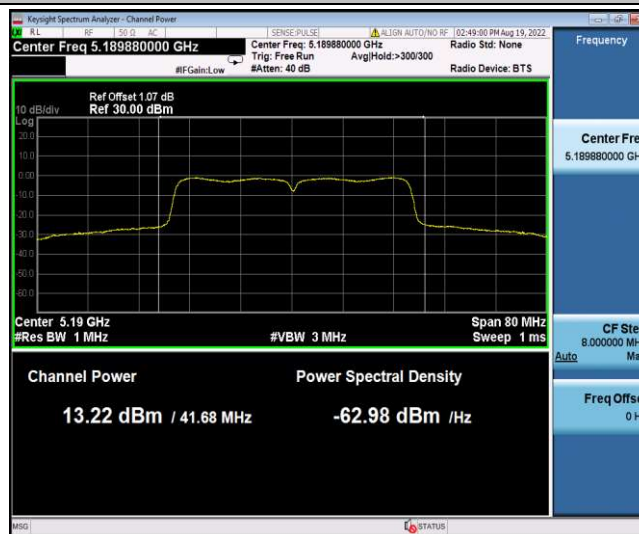
11AC20SISO_Ant2_5785



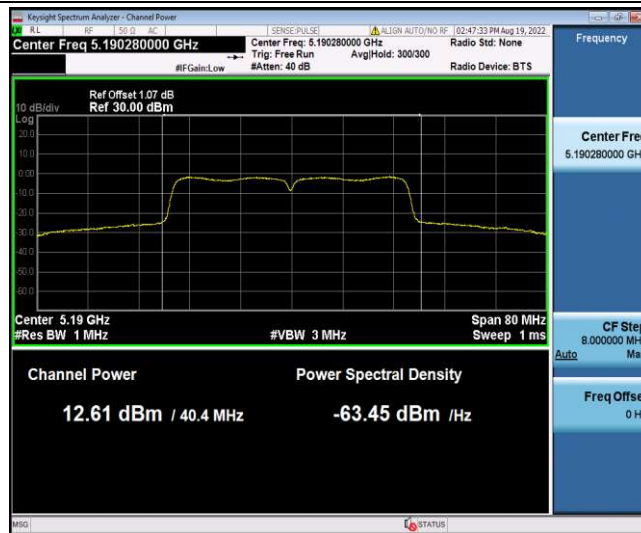
11AC20SISO_Ant1_5825



11AC20SISO_Ant2_5825



11AC40SISO_Ant1_5190



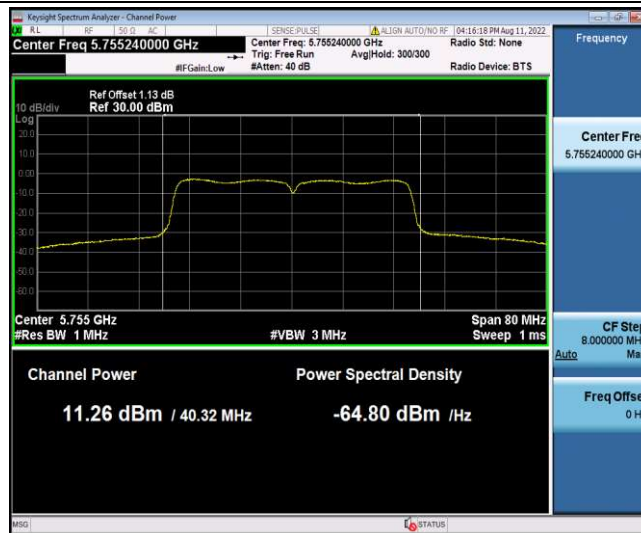
11AC40SISO_Ant2_5190



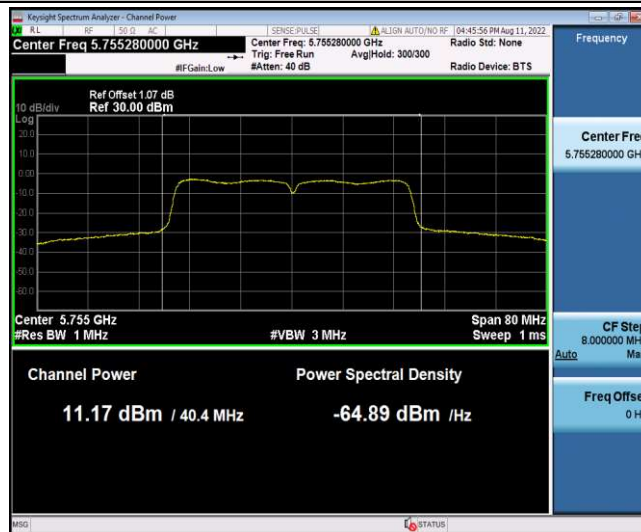
11AC40SISO_Ant1_5230



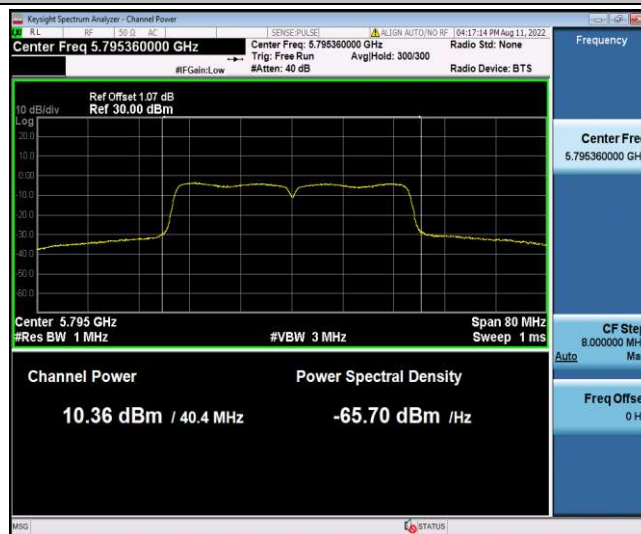
11AC40SISO_Ant2_5230



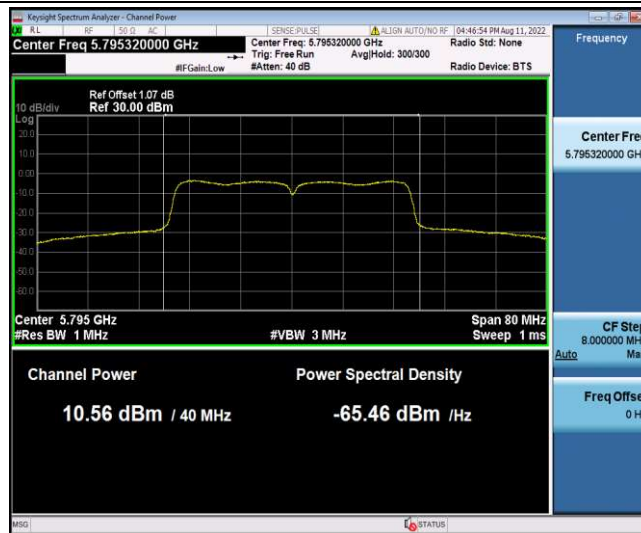
11AC40SISO_Ant1_5755



11AC40SISO_Ant2_5755



11AC40SISO_Ant1_5795



11AC40SISO_Ant2_5795



11AC80SISO_Ant1_5210



11AC80SISO_Ant2_5210



11AC80SISO_Ant1_5775



11AC80SISO_Ant2_5775

Appendix D: Maximum power spectral density

Test Result

TestMode	Antenna	Channel	Result [dBm/MHz]	Limit[dBm/MHz]	Verdict
11A	Ant1	5180	2.61	≤11	PASS
	Ant2	5180	2.45	≤11	PASS
	Ant1	5200	2.62	≤11	PASS
	Ant2	5200	2.83	≤11	PASS
	Ant1	5240	2.83	≤11	PASS
	Ant2	5240	3.09	≤11	PASS
	Ant1	5745	-1.32	≤30	PASS
	Ant2	5745	-1.32	≤30	PASS
	Ant1	5785	-2.29	≤30	PASS
	Ant2	5785	-2.15	≤30	PASS
	Ant1	5825	-2.58	≤30	PASS
	Ant2	5825	-2.16	≤30	PASS
11N20SISO	Ant1	5180	2.38	≤11	PASS
	Ant2	5180	1.56	≤11	PASS
	Ant1	5200	2.59	≤11	PASS
	Ant2	5200	1.86	≤11	PASS
	Ant1	5240	2.64	≤11	PASS
	Ant2	5240	2.21	≤11	PASS
	Ant1	5745	-1.46	≤30	PASS
	Ant2	5745	-1.45	≤30	PASS
	Ant1	5785	-2.38	≤30	PASS
	Ant2	5785	-1.9	≤30	PASS
	Ant1	5825	-2.87	≤30	PASS
	Ant2	5825	-2.36	≤30	PASS
11N40SISO	Ant1	5190	-0.69	≤11	PASS
	Ant2	5190	-1.36	≤11	PASS
	Ant1	5230	-0.23	≤11	PASS
	Ant2	5230	-0.73	≤11	PASS
	Ant1	5755	-4.26	≤30	PASS
	Ant2	5755	-4.63	≤30	PASS
	Ant1	5795	-5.34	≤30	PASS
	Ant2	5795	-5.31	≤30	PASS
11AC20SISO	Ant1	5180	2.26	≤11	PASS
	Ant2	5180	1.65	≤11	PASS
	Ant1	5200	2.47	≤11	PASS
	Ant2	5200	1.85	≤11	PASS
	Ant1	5240	2.77	≤11	PASS
	Ant2	5240	2.13	≤11	PASS

	Ant1	5745	-2.37	≤30	PASS
	Ant2	5745	-2.69	≤30	PASS
	Ant1	5785	-3.39	≤30	PASS
	Ant2	5785	-3.35	≤30	PASS
	Ant1	5825	-3.82	≤30	PASS
	Ant2	5825	-3.6	≤30	PASS
11AC40SISO	Ant1	5190	-0.7	≤11	PASS
	Ant2	5190	-1.25	≤11	PASS
	Ant1	5230	-0.27	≤11	PASS
	Ant2	5230	-0.81	≤11	PASS
	Ant1	5755	-4.95	≤30	PASS
	Ant2	5755	-5.55	≤30	PASS
	Ant1	5795	-6.33	≤30	PASS
	Ant2	5795	-6.12	≤30	PASS
11AC80SISO	Ant1	5210	-3.73	≤11	PASS
	Ant2	5210	-4.53	≤11	PASS
	Ant1	5775	-8.35	≤30	PASS
	Ant2	5775	-8.57	≤30	PASS

Note: 1. The Duty Cycle Factor was compensated in the spectrum analyzer Ref offset.

2. Spectrum analyzer Ref offset=Cable Loss(1dB) + Duty Cycle Factor.

3. Duty Cycle Factor=10Log(1/X), where X is the Duty Cycle.

Test Graphs



11A_Ant1_5180



11A_Ant2_5180



11A_Ant1_5200



11A_Ant2_5200



11A_Ant1_5240



11A_Ant2_5240



11A_Ant1_5745



11A_Ant2_5745



11A_Ant1_5785



11A_Ant2_5785



11A_Ant1_5825



11A_Ant2_5825



11N20SISO_Ant1_5180



11N20SISO_Ant2_5180



11N20SISO_Ant1_5200



11N20SISO_Ant2_5200



11N20SISO_Ant1_5240



11N20SISO_Ant2_5240



11N20SISO_Ant1_5745



11N20SISO_Ant2_5745



11N20SISO_Ant1_5785



11N20SISO_Ant2_5785



11N20SISO_Ant1_5825



11N20SISO_Ant2_5825



11N40SISO_Ant1_5190



11N40SISO_Ant2_5190



11N40SISO_Ant1_5230



11N40SISO_Ant2_5230



11N40SISO_Ant1_5755



11N40SISO_Ant2_5755



11N40SISO_Ant1_5795



11N40SISO_Ant2_5795



11AC20SISO_Ant1_5180



11AC20SISO_Ant2_5180



11AC20SISO_Ant1_5200



11AC20SISO_Ant2_5200



11AC20SISO_Ant1_5240



11AC20SISO_Ant2_5240



11AC20SISO_Ant1_5745



11AC20SISO_Ant2_5745



11AC20SISO_Ant1_5785



11AC20SISO_Ant2_5785