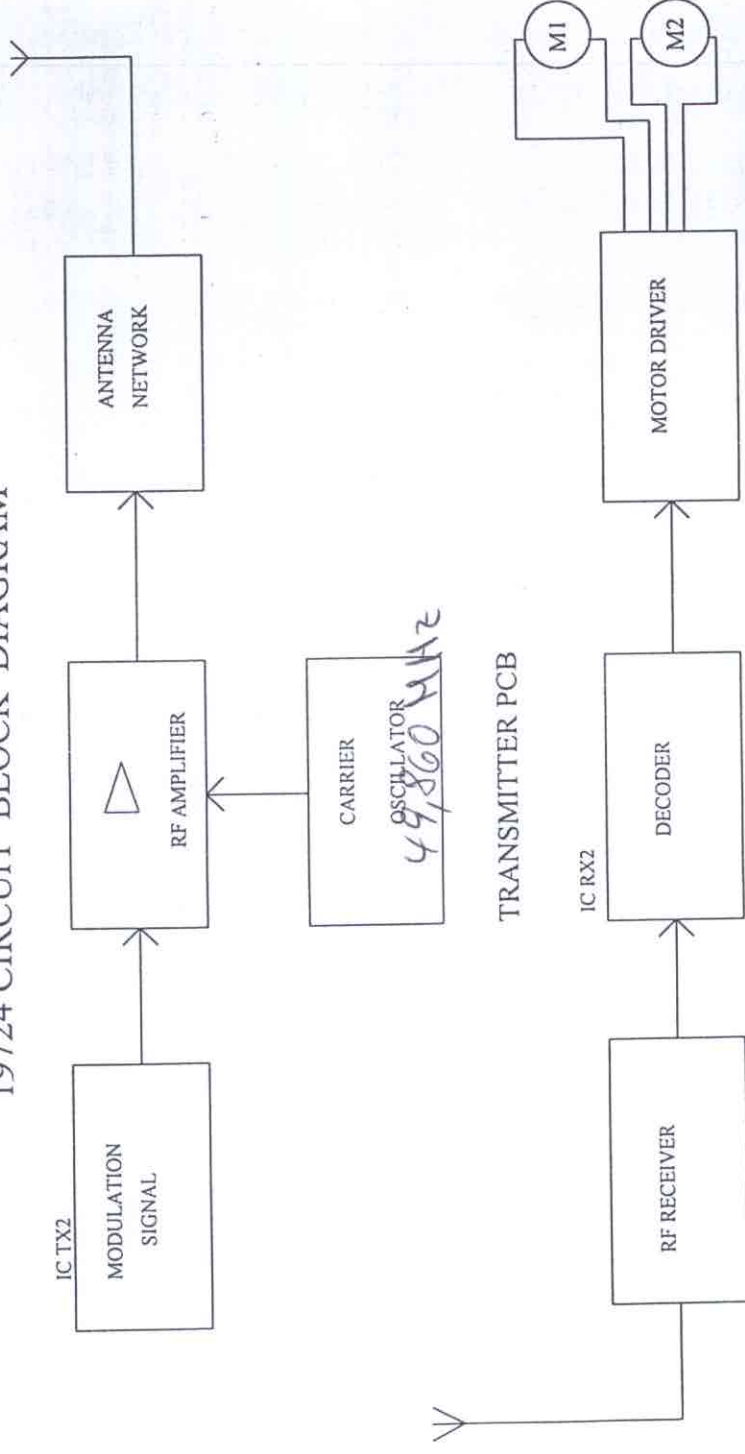


4fMHz FCC conformity

Model 49003

19724 CIRCUIT BLOCK DIAGRAM



Title		Number	Revision
Size	A4	8-Jul-2003	Sheet of 4
Date:	File:	G:\98\data\trv\trv.Dbb	Drawn By: TANG YUAN SONG