

Realtek Bluetooth MP UI User Guide







A. Electrical Characteristics			
Working Frequency	2·4GHz~2·5GHz 4.9GHz~5.85GHz		
S.W.R.	2.4GHz~2.5GHz <2.0 4.9GHz~5.85GHz <2.0		
Antenna Gain	Main port:2dBi Aux port:2dBi		
Antenna Type	PCB		
Efficiency	2.4GHz~2.5GHz >70%		
	4.9GHz~5.85GHz >75%		
Impedance	50 Ohm		
Polarization / Azimuth	Linear / Omni-directional		
B. Mechanical Dimension			
Cable Length Of Main Ant	L:340mm(BLACK)		
Cable Length Of Aux Ant			
C. Material			
Stamping Metal	Copper antenna		
Coaxial Cable	50 Ohm / O.D.1.13mm		
Mini Coaxial Connector	IPEX PLUG		
D. Environmental			
Operation Temperature	-40℃~ +85℃		
Storage Temperature	-40℃~ +85℃		





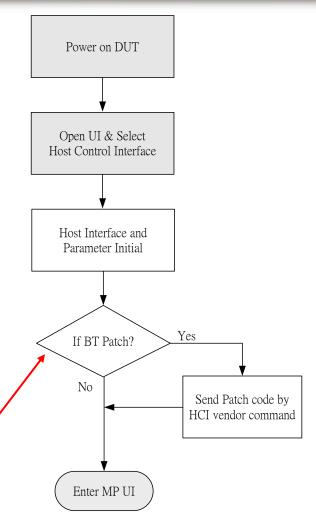
Bluetooth MP Operation Flow







Host Interface: BT USB/UART/PCIe



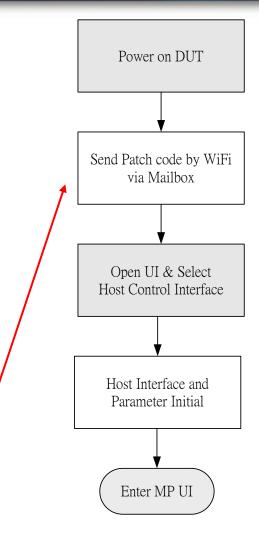
Set "IsFirmwarePatchAtInit" in the HCISetup.ini, 0→Skip Patch, 1→Patch The patch file is "patch.bin" (located the same directory with RTK_BT_MP.exe)







Host Interface: WiFi USB/UART/PCIe (Windows Platform)



The WiFi driver must download BT patch while DUT power on.







Host Interface: Linux Platform

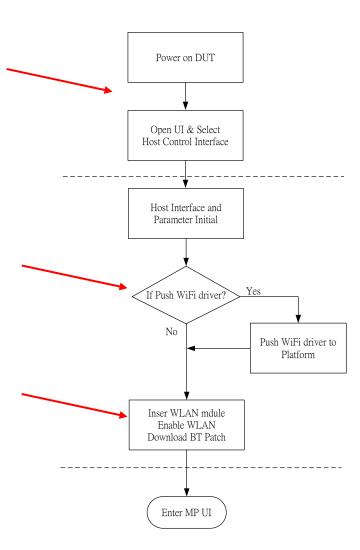
The WiFi in the DUT UI must be turned off after Power on. (no WLAN module exit in the system)

"IsPushMPDriver" in the HCISetup.ini defines: 0→ skip push, 1→push.

The MP driver filename is specified in the "ADBMPDriverFilename" of the HCISetup.ini It must place in the same directory with RTK BT MP.exe.

The push destination is defined in the "ADBMPDriverRoute" of the HCISetup.ini.

The insert WiFi module is specified by the "ADBMPDriverRoute" + "ADBMPDriverFilename" in the HCISetup.ini.









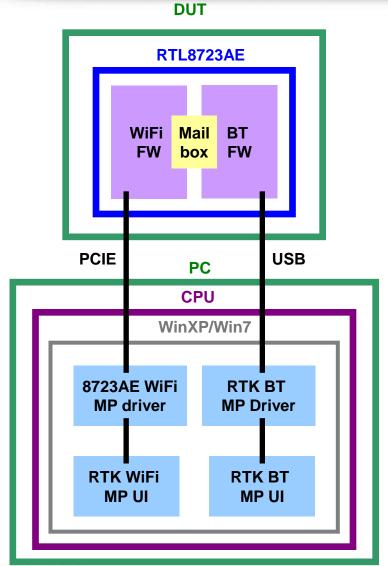
Bluetooth MP UI Initial

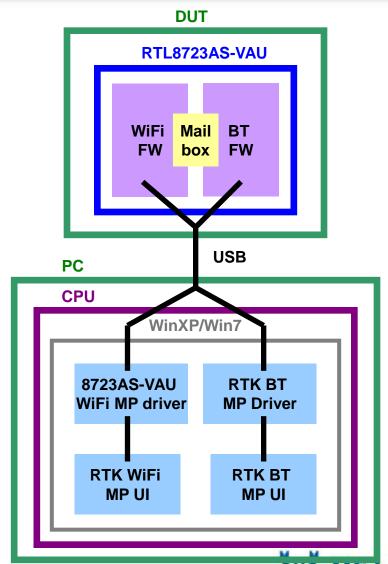






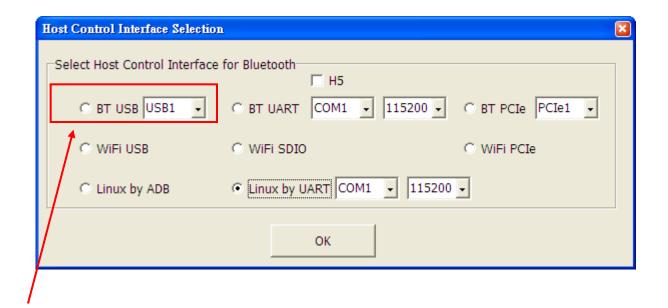
Host Interface: BT USB Port (8723AE,8723AE-VAU)







Host Interface: BT USB Port (8723AE,8723AE-VAU)



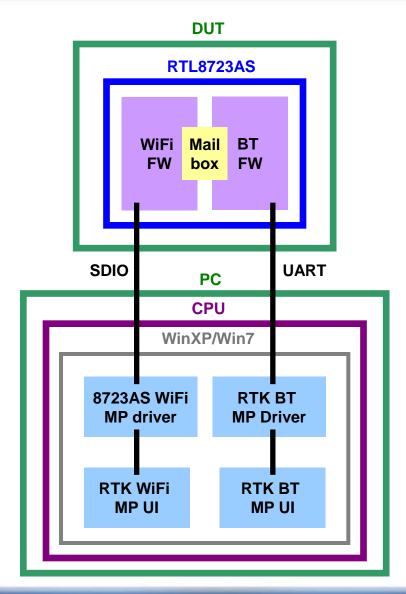
Select "BT USB", Choose USB port number, and Press "OK" button to start the MP.







Host Interface: BT UART Port (8723AS)

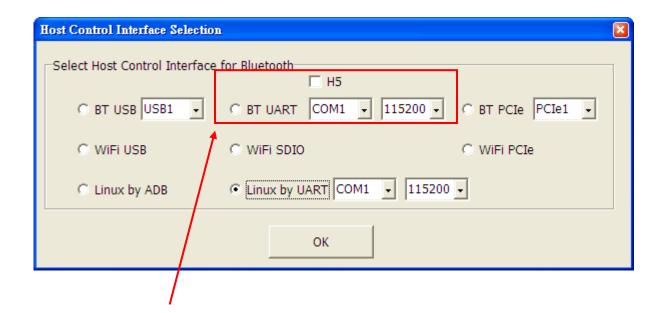








Host Interface: BT UART Port (8723AS)



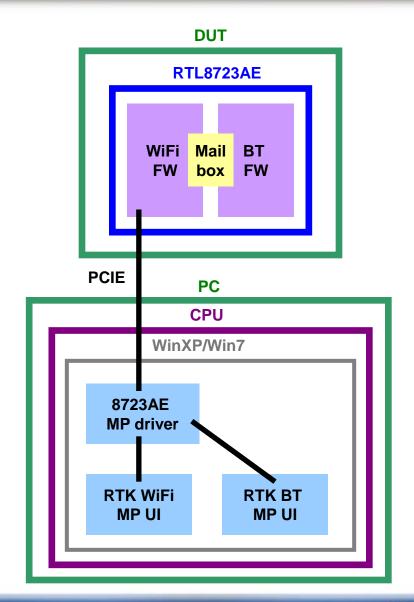
Select "BT UART", Choose COM port number and baud rate (default: 115200), and Press "OK" button to start the MP.

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Host Interface: WiFi PCIe Port (8723AE)

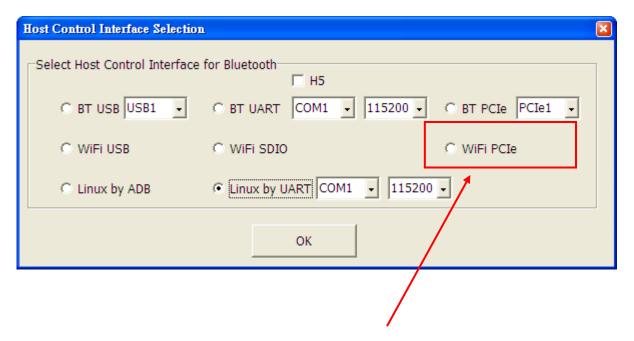








Host Interface: WiFi PCIe Port (8723AE)



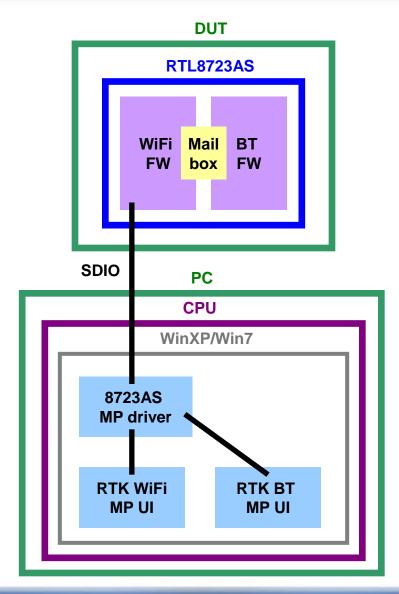
Select "WiFi PCIe", and Press "OK" button to start the MP.







Host Interface: WiFi SDIO Port (8723AS)

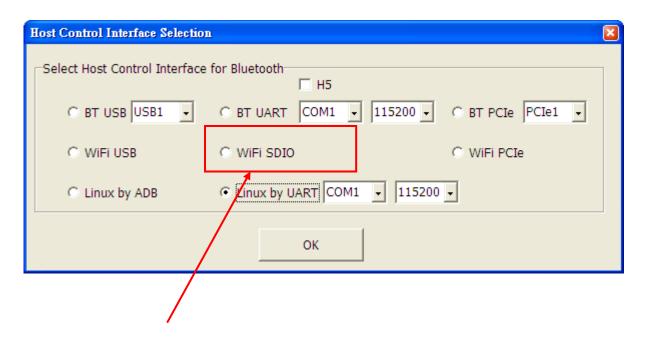








Host Interface: WiFi SDIO Port (8723AS)

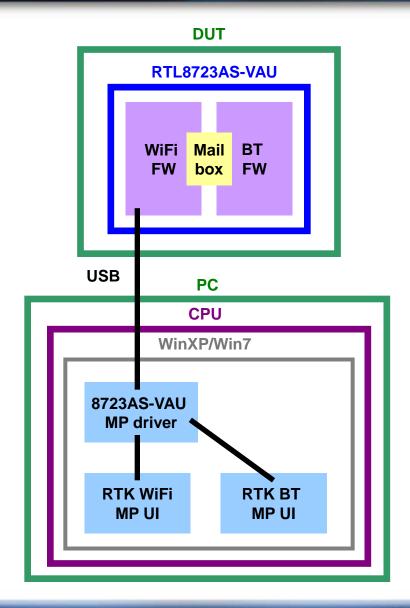


Select "WiFi SDIO", and Press "OK" button to start the MP.





Host Interface: WiFi USB Port (8723AS-VAU)

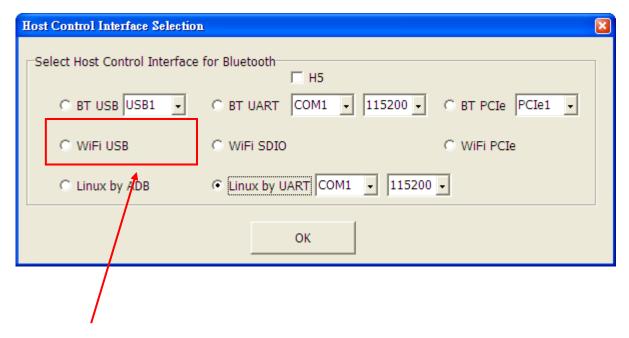








Host Interface: WiFi USB Port (8723AS-VAU)

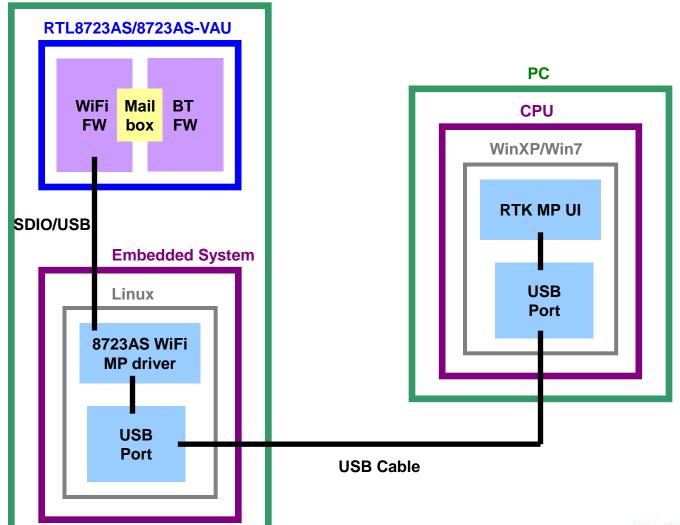


Select "WiFi USB", and Press "OK" button to start the MP.





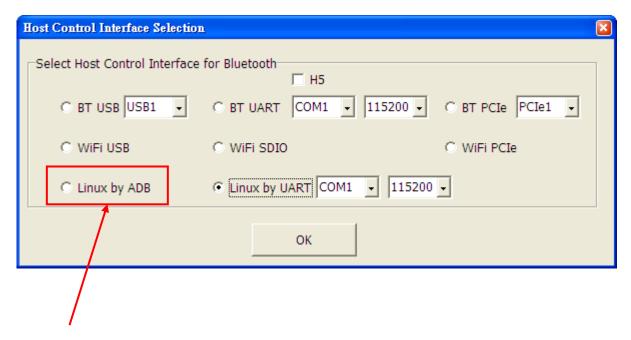
Host Interface: Linux USB Port (8723AS,8723AS-VAU)







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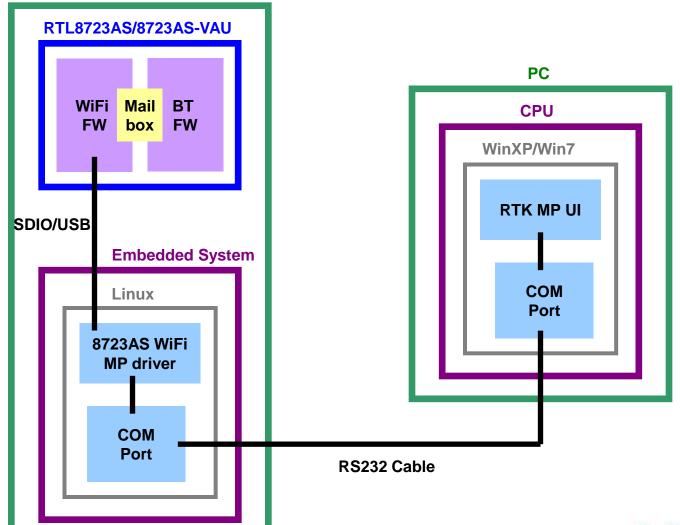


Select "Linux by ADB", and Press "OK" button to start the MP.





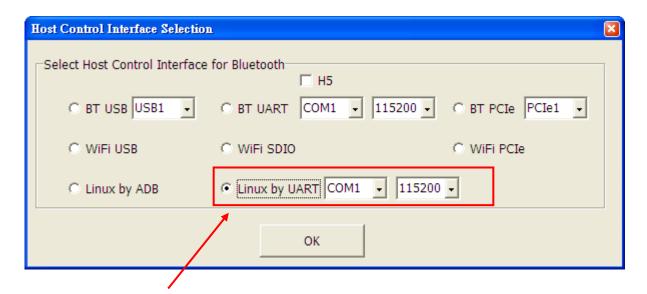
Host Interface: Linux COM Port (8723AS,8723AS-VAU)







Select Host Control Interface



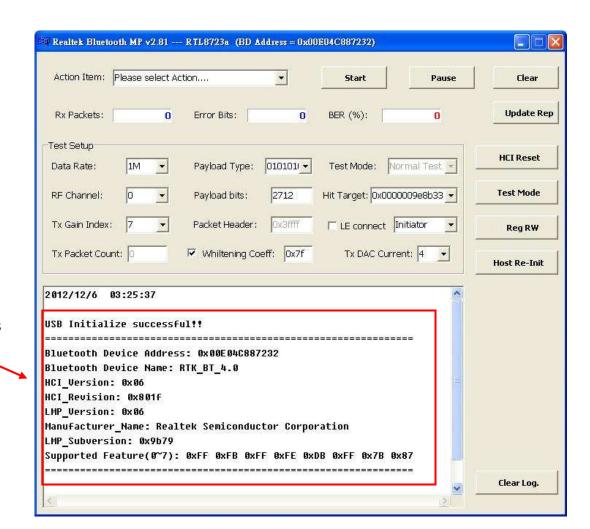
Select "Linux by UART", Choose COM port number and baud rate (default: 115200), and Press "OK" button to start the MP.

Note: It is recommended to press the "OK" button after the system boot completely.





The Main UI (BT Host type)

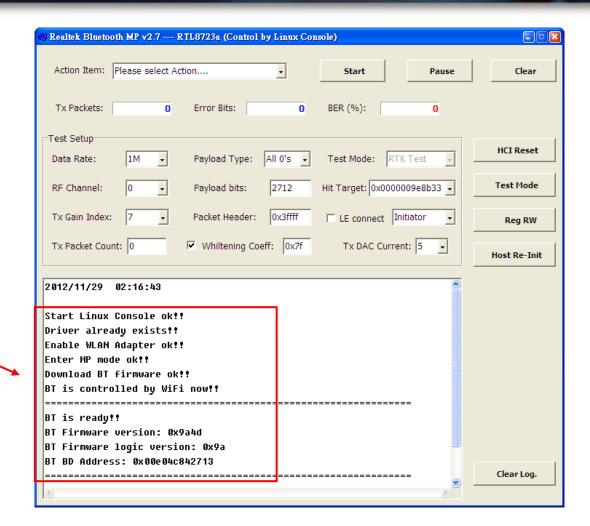


If initial ok, memo show as below





The Main UI (WiFi Host type)



If initial ok, memo show as below

If initial fail, Check those:

- a. RS232/USB cable
- b. UART setup /USB port
- c. BT Module
- d. BT MP driver

Note: If initial fail (ex: enable WLAN adapter fail), it may result from the boot is not completed during MP UI start. You can press "Host Re-Init" button or reopen the MP UI.





Bluetooth DUT Test Mode Setup & Test Procedure

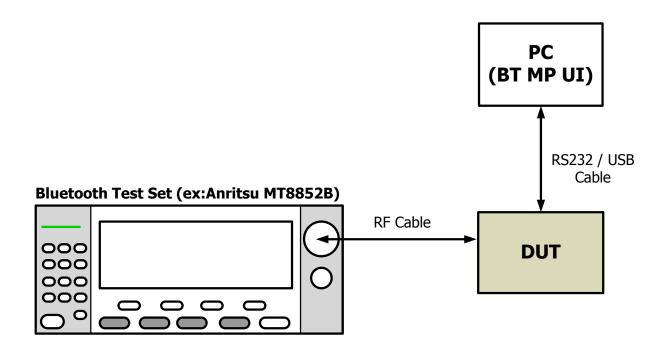






BT DUT Test Mode Test Topology

The BT MP sends BT HCl command to DUT. The DUT enable Inqr/Page scan and enter DUT test mode. The Bluetooth tester established a link with the DUT over the RF channel using the normal Bluetooth protocol.





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UI Setup Step-1: Enter DUT Test Mode

Action Item: Please select Action Start Pause Clear Tx Packets: 0 Error Bits: 0 BER (%): 0 Test Setup Data Rate: 1M Payload Type: All 0's Test Mode: RTKTest RF Channel: 0 Payload bits: 2712 Hit Target: 0x0000009e8b33 RF Channel: 0 Packet Header: 0x3ffff Reg RW Tx Gain Index: 7 Packet Header: 0x3ffff Reg RW Tx Packet Count: 0 Whiltening Coeff: 0x7f Tx DAC Current: 5 Host Re-Init Enter MP mode ok!! Download BT firmware ok!! BT is ready!! BT is ready!! BT firmware version: 0x9a4d BT Firmware logic version: 0x9a BT BD Address: 0x80e0Ac842713 >> HCI Reset successful!! >> Enter DUT Test mode ok!!	Realtek Bluetooth MP v2.7	RTL8723a (Control by Linux Cor	isole)			
Test Setup Data Rate: 1M Payload Type: All 0's Test Mode: RTK Test RF Channel: 0 Payload bits: 2712 Hit Target: 0x0000009e8b33 Test Mode Tx Gain Index: 7 Packet Header: 0x3ffff Tx DAC Current: 5 Test Mode Tx Packet Count: 0 Whiltening Coeff: 0x7f Tx DAC Current: 5 Those Reg RW Host Re-Init Enter MP mode ok!! BT is controlled by WiFi now!! BT is ready!! BT is ready!! BT irmware version: 0x9a4d BT Firmware logic version: 0x9a BT BD Address: 0x09e04c842713 >> HCI Reset successful!! >> Enter DUT Test mode ok!!	Action Item: Please select A	ction	Start Pause	Clear		
Data Rate: 1M Payload Type: All 0's Test Mode: RTKTest RF Channel: 0 Payload bits: 2712 Hit Target: 0x0000009e8b33 Test Mode Tx Gain Index: 7 Packet Header: 0x3ffff Tx DAC Current: 5 Host Reg RW Tx Packet Count: 0 Whiltening Coeff: 0x7f Tx DAC Current: 5 Host Re-Init Enter MP mode ok!! Download BT firmware ok!! BT is controlled by WiFi now!! BT is ready!! BT Firmware version: 8x9a4d BT Firmware logic version: 8x9a BT BD Address: 8x80e04c842713 >> HCI Reset successful!! >> Enter DUT Test mode ok!!	Tx Packets: 0	Error Bits: 0	BER (%): 0			
Data Rate: 1M Payload Type: All 0's Test Mode: RTKTest RF Channel: 0 Payload bits: 2712 Hit Target: 0x0000009e8b33 Tx Gain Index: 7 Packet Header: 0x3ffff	Test Setup					
Tx Gain Index: 7 Packet Header: 0x3ffff	Data Rate: 1M	Payload Type: All 0's	Test Mode: RTK Test	HCI Reset		
Tx Packet Count: Whiltening Coeff: Tx DAC Current: Host Re-Init Enter MP mode ok!! Download BT firmware ok!! BT is controlled by WiFi now!! BT is ready!! BT Firmware version: 0x9a4d BT Firmware logic version: 0x9a BT BD Address: 0x00e04c842713 >> HCI Reset successful!! >> Enter DUT Test mode ok!!	RF Channel: 0	Payload bits: 2712	Hit Target: 0x0000009e8b33	Test Mode		
Enter MP mode ok!! Download BT firmware ok!! BT is controlled by WiFi now!! ==================================	Tx Gain Index: 7	Packet Header: 0x3ffff	☐ LE connect Initiator 및	Reg RW		
Download BT firmware ok!! BT is controlled by WiFi now!! BT is ready!! BT Firmware version: 0x9a4d BT Firmware logic version: 0x9a BT BD Address: 0x00e04c842713	Tx Packet Count: 0	Whiltening Coeff: 0x7f	Tx DAC Current: 5	Host Re-Init		
	Download BT firmware ok!! BT is controlled by WiFi now!!					
	>> Enter DUT Test mode	ok!!	•	Clear Log.		

Press "Test Mode" button
To enter BT DUT test mode.







After UI Setup Step-1

After Bluetooth test set creates a connection with DUT, the BT MP UI will show message as below. The RF test can kick off.

```
>> [HCI Para] -> Connection_Handle = 0x002B
>> [HCI Para] -> BD_ADDR = 0x000272D199C1
>> [HCI Para] -> Link_Type = ACL
>> [HCI Para] -> Encryption = disabled
>> Write Link Policy to 0xf (All On) OK!!
>> Write Automatic Flush Timeout (value = 0x3FF) command succeeded!!
>> Connect results listing....
       BD_ADDR
                     Handle
                              Link_Type
                                          Encryption
                                                        Mode
    0x000272D199C1
                                 ACL
                                           Disable
                                                       Active
                     0x002B
```

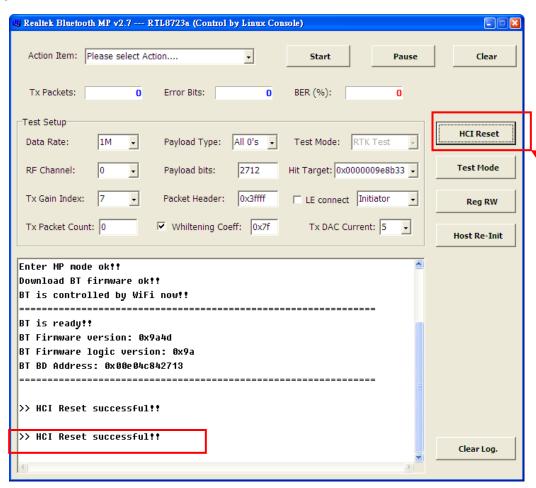






UI Setup Step-2: Exit DUT Test Mode (if required)

The connection can be disconnected by either BT test set or DUT. For DUT end, the connection will lost because of supervision timeout after HCI reset is executed.



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Press "HCU Reset" button To exit BT DUT test mode.







Bluetooth Non-Link Mode Setup & Test Procedure

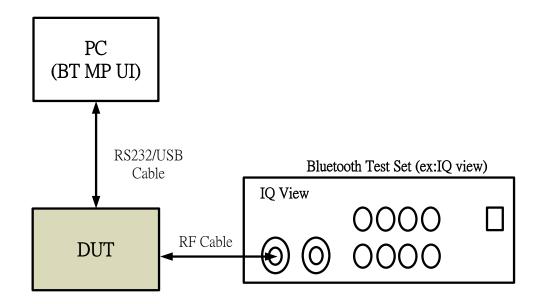






Non-Link Mode Test Topology

For non-link mode test, the tester communicates with DUT over a cable via HCI with the DUT in a special test. The tester doesn't have to establish a protocol link with the DUT. The non-link mode of the 8723 series chip support "Packet-Tx", "Continue-Tx", and "Packet-Rx" for various RF performance test.



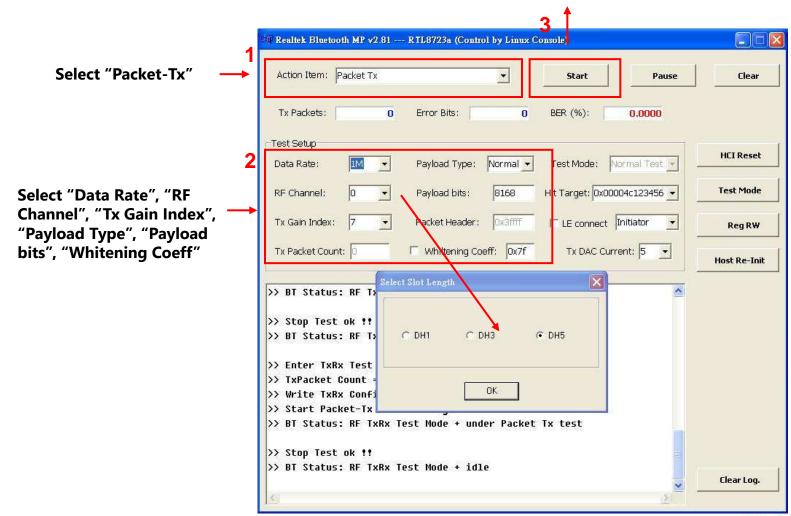






Packet-Tx Setup

Press "Start" button

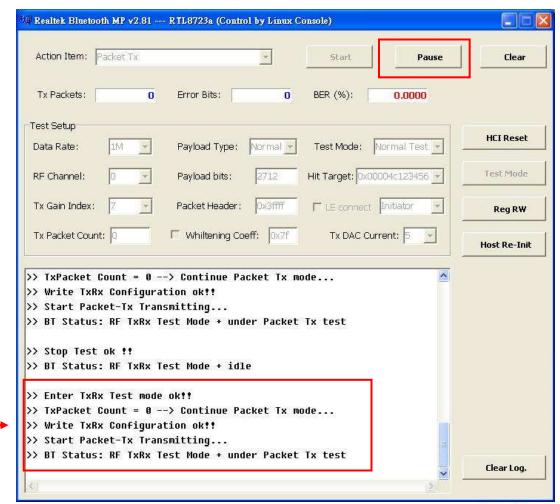








Packet-Tx Run



if "Packet-Tx" ok, the message is shown as memo

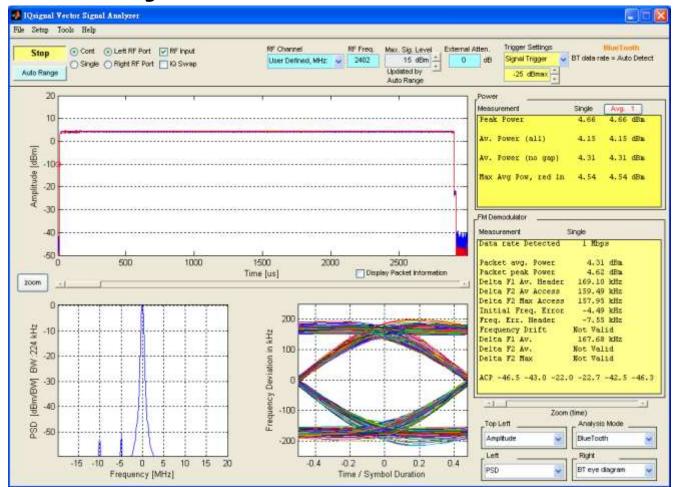
"Packet-Tx" will stop after press "Pause" button





Packet-Tx Measurement form IQ view

Date Rate: 1M/DH5, RF Channel: 0, Payload Length:2712 bits, Payload Type: 11110000, Whitening: Off



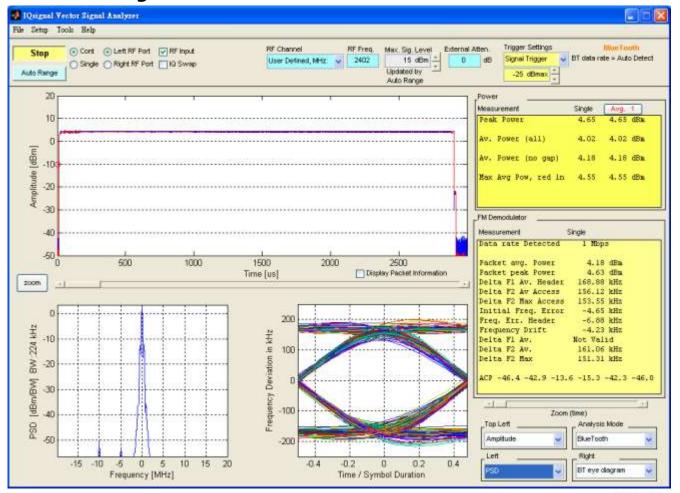






Packet-Tx Measurement form IQ view

Date Rate: 1M/DH5, RF Channel: 0, Payload Length:2712 bits, Payload Type: 10101010, Whitening: Off



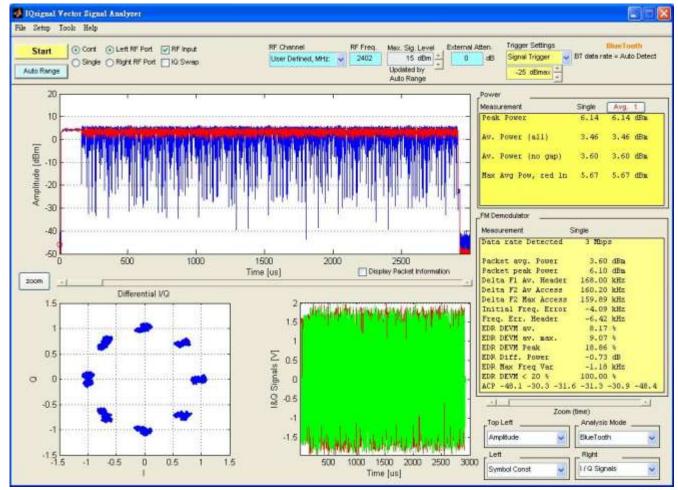






Packet-Tx Measurement form IQ view

Date Rate: 3M/3DH5, RF Channel: 0, Payload Length:8168 bits, Payload Type: Normal, Whitening: On



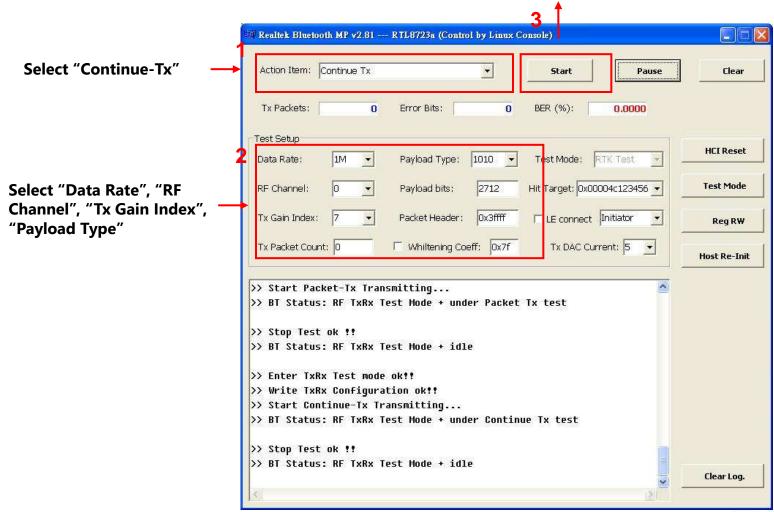






Continue-Tx Setup

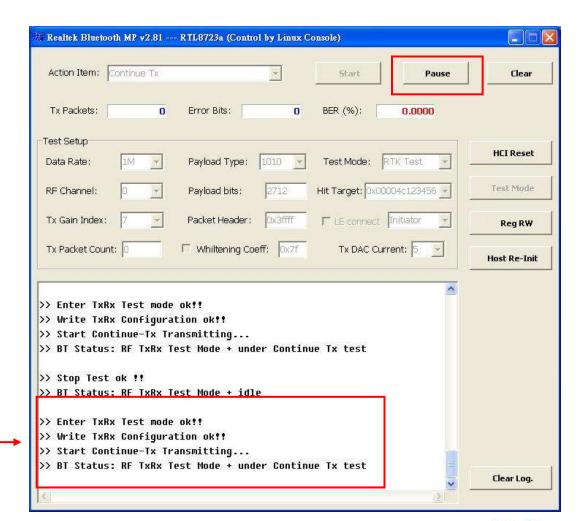
Press "Start" button







Continue-Tx Run



if "Continue-Tx" ok, the message is shown as memo

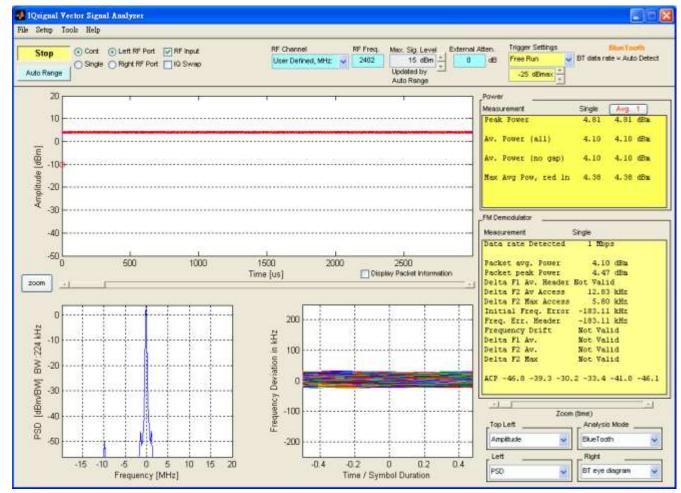
"Continue-Tx" will stop after press "Pause" button





Continue-Tx Measurement form IQ view

"Continue-Tx" is used for Tx power measurement.









Packet-Rx Setup

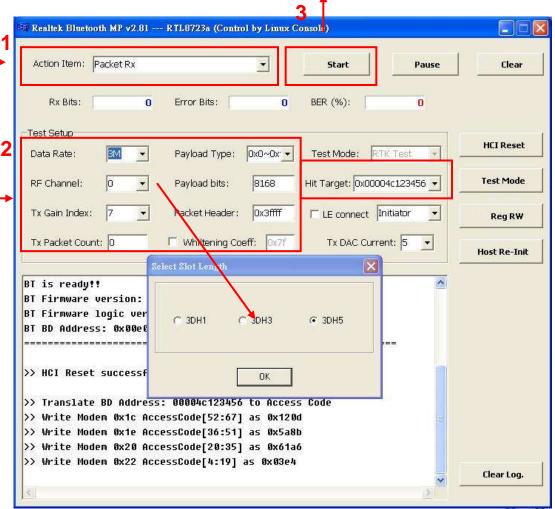
Press "Start" button

Select "Data Rate", "RF Channel", "Tx Gain Index", " "Payload Type", "Payload bits", "Whitening Coeff", "Hit Target"

Select "Packet-Rx"

"Hit Target" is BD address that the BT tester used it to generate the access code of the test pattern.

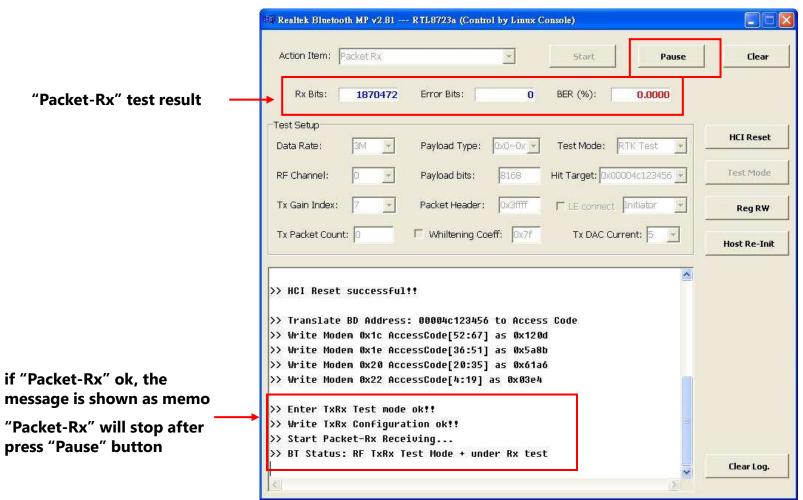
These parameters must meet with the BT tester's pattern.







Packet-Rx Run

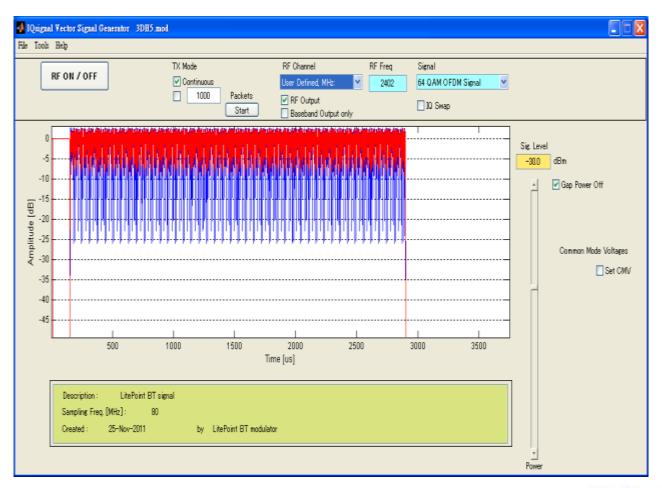






IQ view Vector Signal Generator

Used the IQ view Vector Signal Generator to generate test pattern (*.mod)









Bluetooth BT 4.0 LE Direct Test Mode Setup For RF/PHY Testing

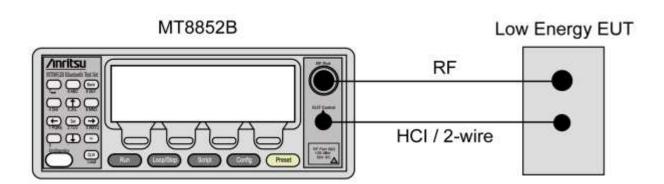






Most BT testers support host control port (USB,UART) that can be as a "upper tester" defined in the direct test mode of BT specifications. The diagram shown as below is the MT8852B test configuration for LE RF/PHY test.

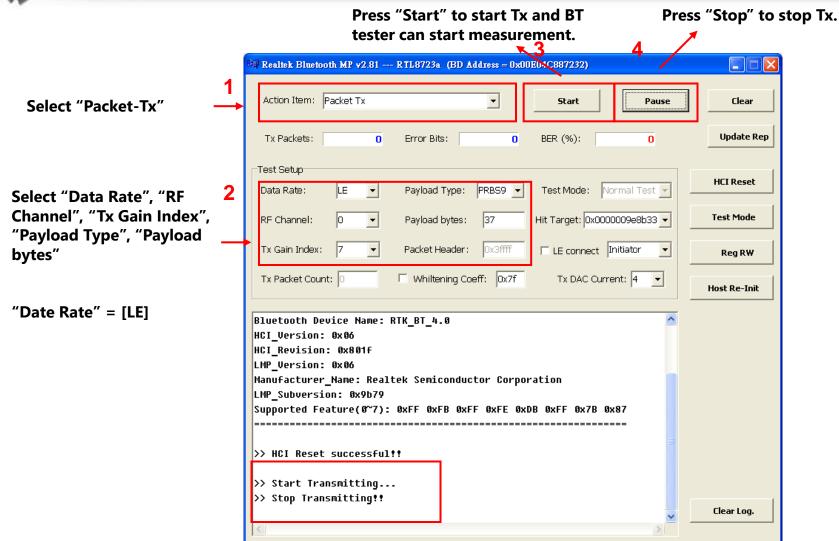
The BT MP also supports Tx and Rx test when the BT tester can not be a "upper tester" (no host control port, ex: IQ view).







Tx Test Setup:







Rx Test Setup:

Press "Start" to start Rx and BT Press "Stop" to stop Rx. tester can start sending packet. Realtek Bluetooth MP v2.81 --- RTL8723a (BD Address = 0x00204C887232) Action Item: Packet Rx Clear Start Pause Select "Packet-Rx" **Update Rep** Rx Bits: 0 Error Bits: 0 BER (%): 0 Test Setup **HCI Reset** Payload Type: PRBS9 -Test Mode: Normal Test -Data Rate: Select "Data Rate". "RF Channel", "Payload Type", Hit Target: 0x0000009e8b33 ▼ **Test Mode** RF Channel: Payload bytes: "Payload bytes" LE connect Initiator Packet Header: Tx Gain Index: Reg RW ☐ Whiltening Coeff: 0x75 Tx DAC Current: 4: ▼ Tx Packet Count: Host Re-Init "Date Rate" = [LE] >> HCI Reset successful!! >> Pseudo_Outer/BER/Report Count Enable >> Translate BD Address: 0000009e8b33 to Access Code >> Write Modem 0x1c AccessCode[52:67] as 0x5e72 >> Write Modem 0x1e AccessCode[36:51] as 0x7334 >> Write Modem 0x20 AccessCode[20:35] as 0x58cc >> Write Modem 0x22 AccessCode[4:19] as 0x475c >> Start Receiving... >> Stop Receiving!! >> 0 packets received Clear Log.



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BT 4.0 LE Setup For FCC AFH Measurement

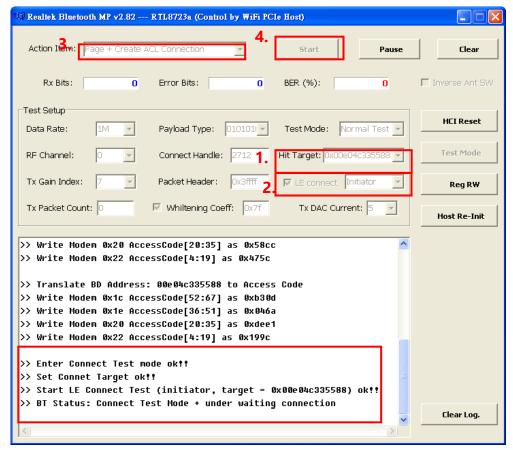






Step 1: Create LE connection (Initiator)

- 1. Checked [LE connect] and select "Initiator".
- 2. Key in the Target BD Address in the [Hit Target] and press "Enter" key
- 3. In the Action Items, select "Page + Create ACL connection"
- 4. press [Start] button









Step 2: Create LE connection (Advertiser)

- 1. Checked [LE connect] and select "Advertiser".
- 2. In the Action Items, select "Page + Create ACL connection"
- 3. press [Start] button

Realtek Bluetooth MP v2.82 RTL8723a (Control by WiFi PCIe Host)				
Action Item: Page + Create ACL Connection	Clear			
Rx Bits: 0 Error Bits: 0 BER (%): 0	☐ Inverse Ant SW			
Test Setup				
Data Rate: 1M Payload Type: 010101 Test Mode: Normal Test	HCI Reset			
RF Channel: Connect Handle: 2712 Hit Target: 0x00e04c335588	Test Mode			
Tx Gain Index: 7 Packet Header: 0x3ffff LE connect Advertiser	Reg RW			
Tx Packet Count: 0 Whiltening Coeff: 0x7f Tx DAC Current: 5	Host Re-Init			
BT is ready!! BT Firmware version: 0x9b79 BT Firmware logic version: 0x00 BT BD Address: 0x446d5731c5ff				
>> BT Status: Connect Test Mode + under waiting connection ClearLog.				







Step 3: Create LE connection

Initiator will create LE connection with Advertiser and AFH start running.

```
>> Set LE Event Mask OK!!

>> Set LE Adv Parameters OK!!

>> Set LE Adv Enable OK!!

>> [HCI Event] -> LE Connection Complete

>> [HCI Para] -> LE Connection completed successfully

>> [HCI Para] -> LE Connection_Handle = 0x0010

>> [HCI Para] -> BD_ADDR = 0x00E04C232218

>> HCI Reset successful!!
```







Setup Antenna Switch (support after v2.82)

Realtek Bluetooth MP v2.82	RTL8723a (Control by Linux Console)		
Action Item: Please select A	ction	Start Pause	Clear
Tx Packets: 0	Error Bits: 0	BER (%): 0	☐ Inverse Ant SW
Test Setup Data Rate: 1M Image: 1M Data Rate: 1M Data R	Payload Type: All 0's 🔻	Test Mode: RTK Test	HCI Reset
RF Channel: 0 ▼	Payload bits: 2712	Hit Target: 0x0000009e8b33 🔻	Test Mode
Tx Gain Index: 7 ▼	Packet Header: 0x3ffff	☐ LE connect Initiator ▼	Reg RW
Tx Packet Count: 0	▼ Whiltening Coeff: 0x7f	Tx DAC Current: 5	Host Re-Init
2013/1/2 10:57:18 Start Linux Console ok!! No MP driver exists!! Insert MP driver ok!! Enable WLAN Adapter ok!! Enter MP mode ok!! Download BT firmware ok!! BT is controlled by WiFi now!! BT is ready!! BT Firmware version: 0x9a4d BT Firmware logic version: 0x9a			
BT BD Address: 0x00e04c842713			Clear Log.
		>	

If Checked: Main → WiFi else Main → BT





FCC Warning Statement

Changes or modifications not expressly approved by the party responsible for complia nce could void the user's authority to operate the equipment. This equipment has been tested and found to comply with the limits for a Class B digi tal device, pursuant to Part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates uses and can radiate radio frequency energy and, if not installed an d used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a par ticular installation. If this equipment does cause harmful interference to radio or televi sion reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measu res: - Reorient or relocate the receiving antenna.

- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

This module is intended for OEM integrator. The OEM integrator is still responsible for the FCC compliance requirement of the end product which integrates this module.

The final end product must be labeled in a visible area with the following" Contains TX FCC ID: 2AC23-WT4XR1210".

The FCC part 15.19 statement below has to also be available on the label: This device complies with Part 15 of FCC rules.

Operation is subject to the following two conditions: (1) this device may not cause harmful interference and

(2) this device must accept any interference received, including interference that may cause undesired operation.

The end user has to be informed that the FCC radio-frequency exposure guidelines for an uncontrolled environment can be satisfied. The end user has to also be informed that any changes or modifications not expressly approved by the manufacturer could void the user's authority to operate this equipment.



