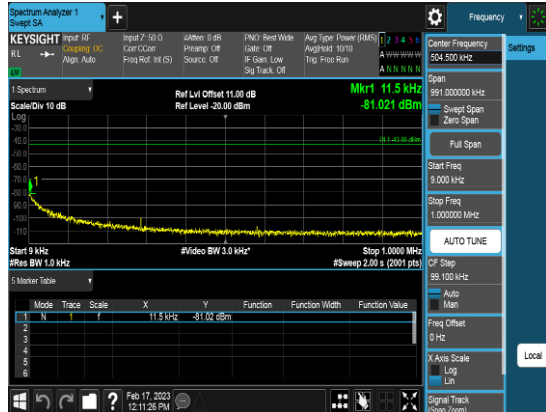


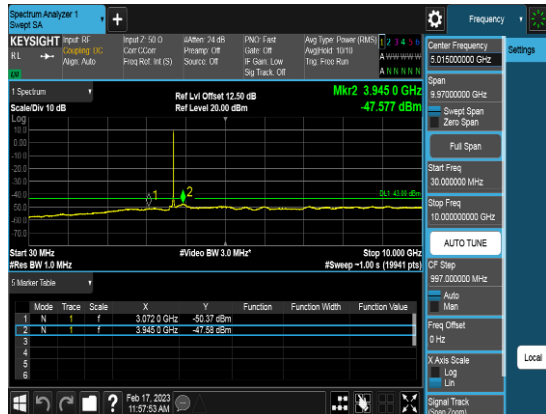
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Port1\_10M\_3694.98MHz\_TM3.1a\_CHH\_CSE0b



Port1\_10M\_3694.98MHz\_TM3.1a\_CHH\_CSE1



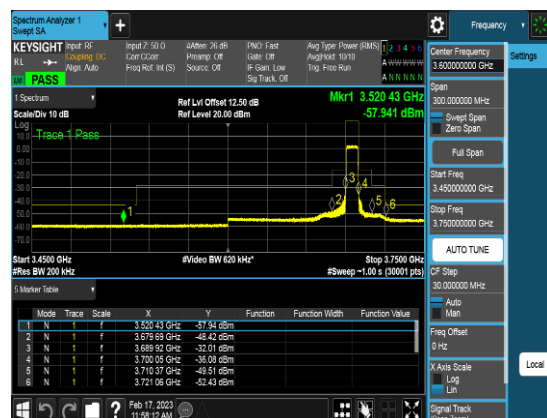
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Port1\_10M\_3694.98MHz\_TM3.1a\_CHH\_CSE3



Port1\_10M\_3694.98MHz\_TM3.1a\_CHH\_CSEpart 96



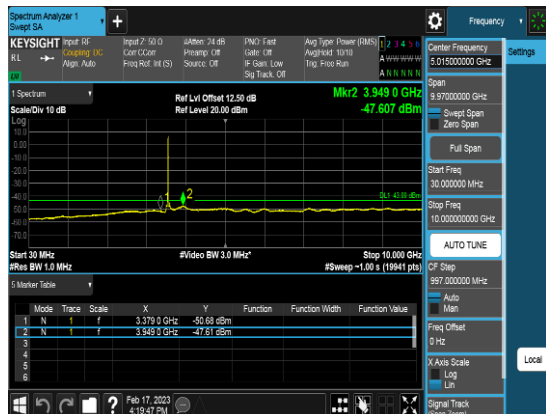
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Port1\_20M\_3560.01MHz\_TM3.1a\_CHL\_CSE1



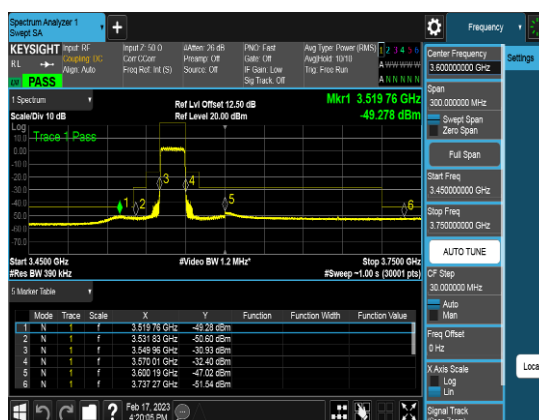
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Port1\_20M\_3560.01MHz\_TM3.1a\_CHL\_CSE3



Port1\_20M\_3560.01MHz\_TM3.1a\_CHL\_CSEpart 96



Port1\_20M\_3624.99MHz\_TM3.1a\_CHM\_CSE0a



Port1\_20M\_3624.99MHz\_TM3.1a\_CHM\_CSE0b



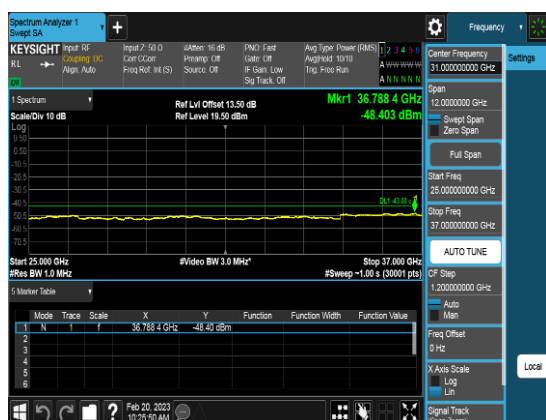
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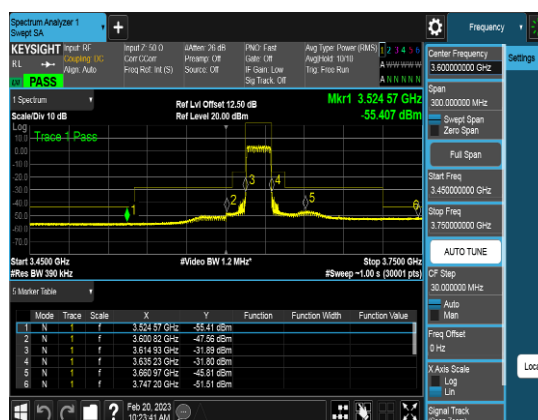
Port1\_20M\_3624.99MHz\_TM3.1a\_CHM\_CSE2



Port1\_20M\_3624.99MHz\_TM3.1a\_CHM\_CSE3

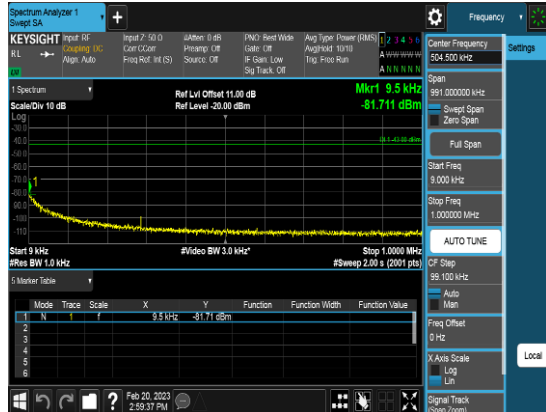


Port1\_20M\_3624.99MHz\_TM3.1a\_CHM\_CSEpar 196





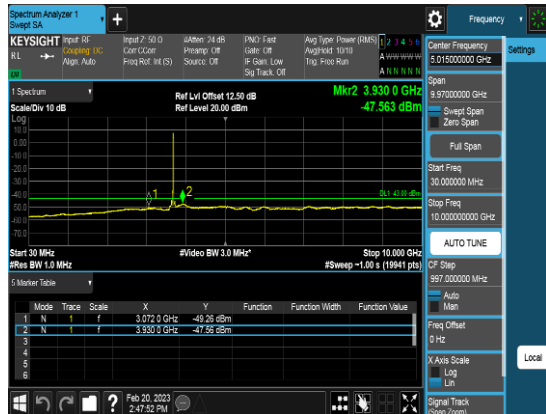
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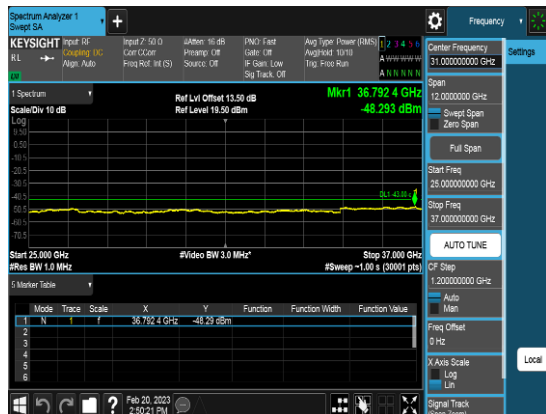
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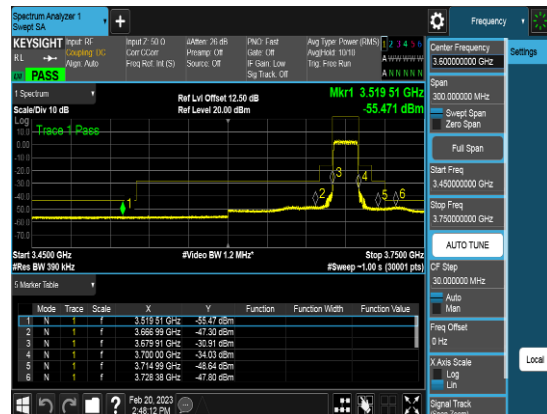
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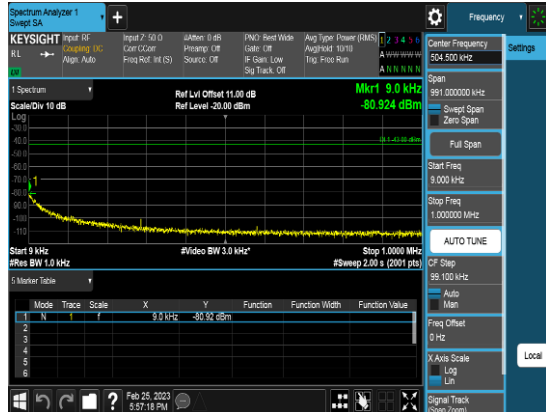
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Port1\_20M\_3690MHz\_TM3.1a\_CHH\_CSEpart96



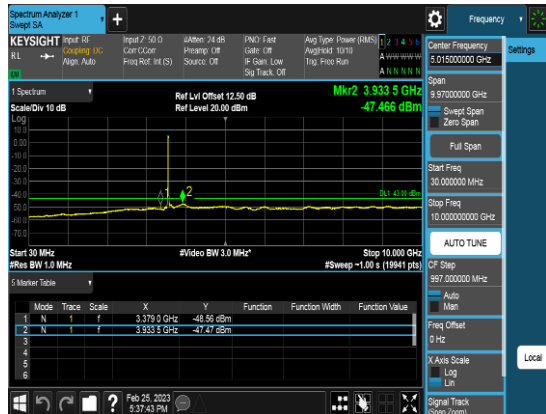
Port1\_30M\_3565.02MHz\_TM3.1a\_CHL\_CSE0a



Port1\_30M\_3565.02MHz\_TM3.1a\_CHL\_CSE0b



Port1\_30M\_3565.02MHz\_TM3.1a\_CHL\_CSE1



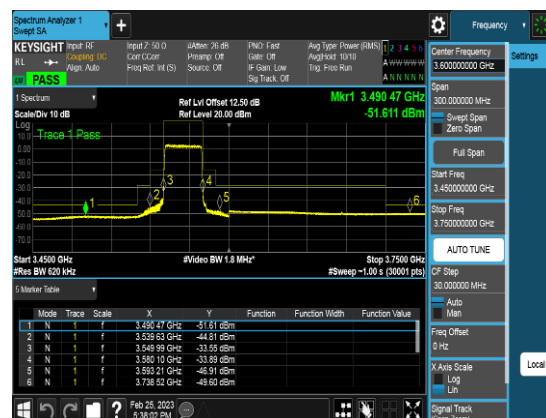
Port1\_30M\_3565.02MHz\_TM3.1a\_CHL\_CSE2



Port1\_30M\_3565.02MHz\_TM3.1a\_CHL\_CSE3



Port1\_30M\_3565.02MHz\_TM3.1a\_CHL\_CSEpart 96



Port1\_30M\_3624.99MHz\_TM3.1a\_CHM\_CSE0a



Port1\_30M\_3624.99MHz\_TM3.1a\_CHM\_CSE0b



Port1\_30M\_3624.99MHz\_TM3.1a\_CHM\_CSE1



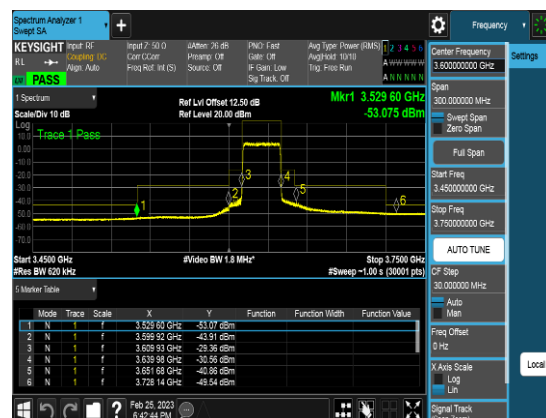
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Port1\_30M\_3624.99MHz\_TM3.1a\_CHM\_CSE3



Port1\_30M\_3624.99MHz\_TM3.1a\_CHM\_CSEpar 196





Port1\_30M\_3684.99MHz\_TM3.1a\_CHH\_CSE0a



Port1\_30M\_3684.99MHz\_TM3.1a\_CHH\_CSE0b



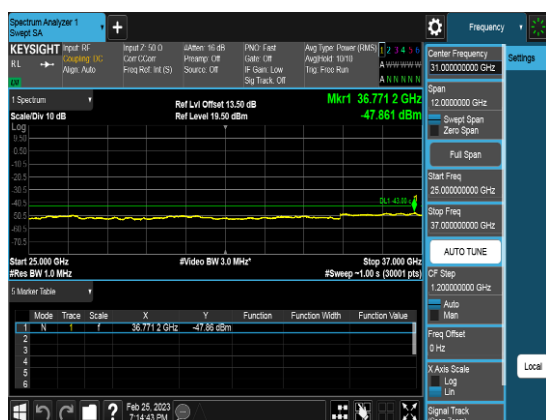
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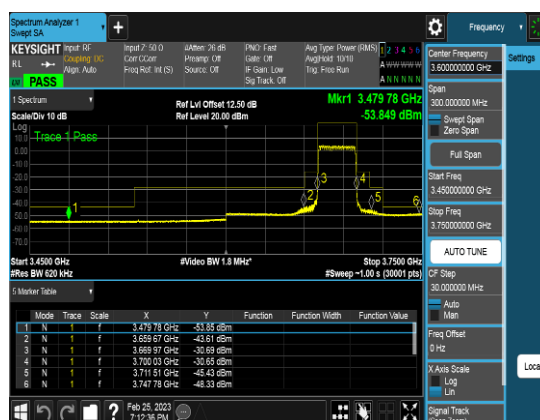
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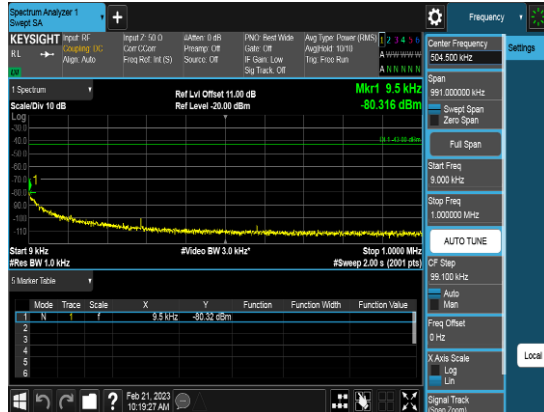
Port1\_30M\_3684.99MHz\_TM3.1a\_CHH\_CSE3



Port1\_30M\_3684.99MHz\_TM3.1a\_CHH\_CSEpart 96



Port1\_40M\_3570MHz\_TM3.1a\_CHL\_CSE0a



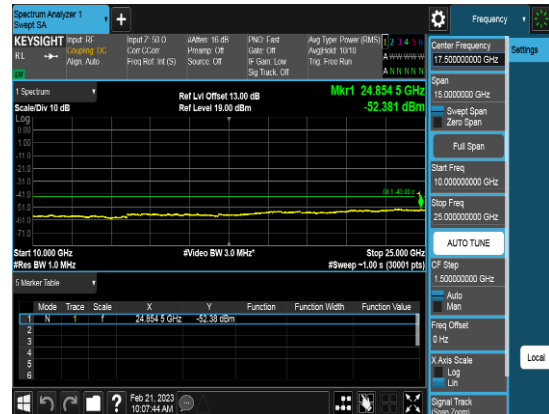
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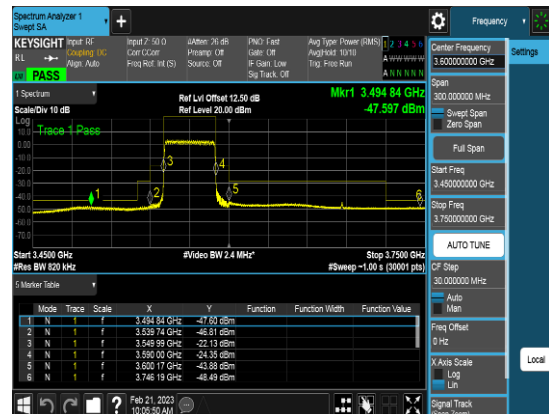
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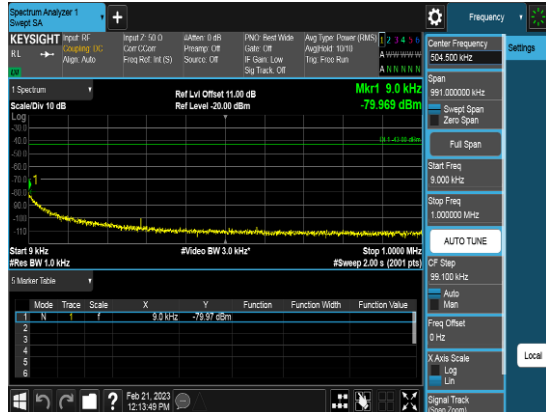


Port1\_40M\_3570MHz\_TM3.1a\_CHL\_CSEpart96





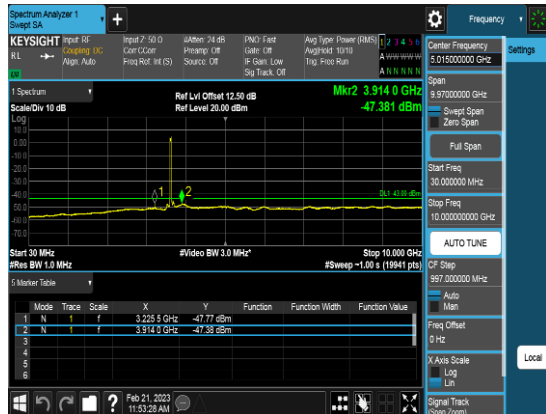
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Port1\_40M\_3624.99MHz\_TM3.1a\_CHM\_CSE1



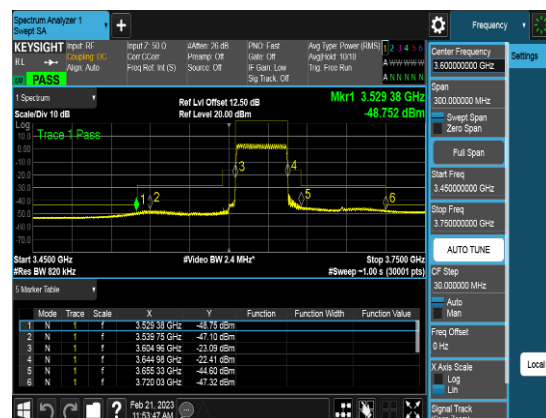
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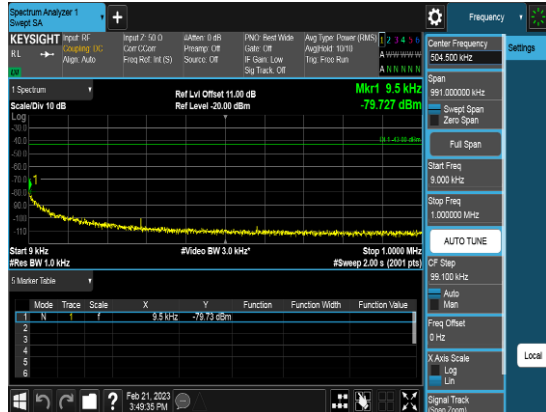
Port1\_40M\_3624.99MHz\_TM3.1a\_CHM\_CSE3



Port1\_40M\_3624.99MHz\_TM3.1a\_CHM\_CSEpar 196



Port1\_40M\_3679.98MHz\_TM3.1a\_CHH\_CSE0a



Port1\_40M\_3679.98MHz\_TM3.1a\_CHH\_CSE0b



Port1\_40M\_3679.98MHz\_TM3.1a\_CHH\_CSE1



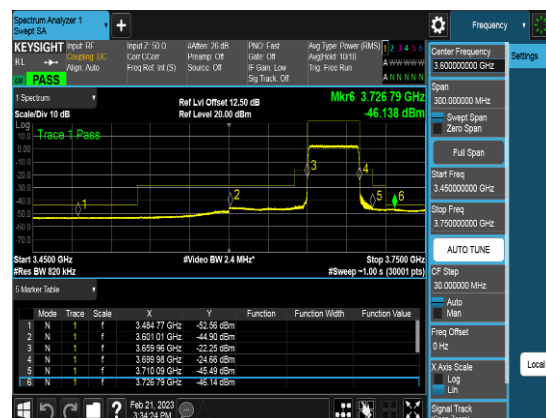
Port1\_40M\_3679.98MHz\_TM3.1a\_CHH\_CSE2



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Port1\_40M\_3679.98MHz\_TM3.1a\_CHH\_CSEpart 96



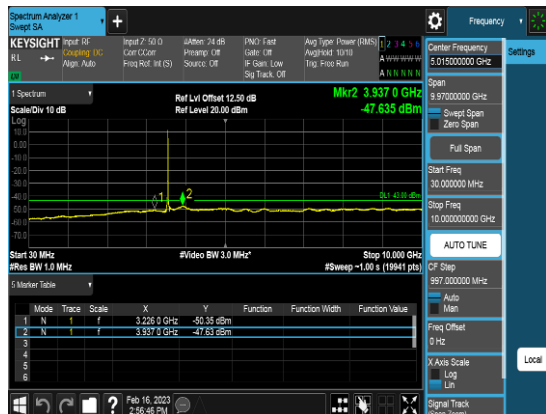
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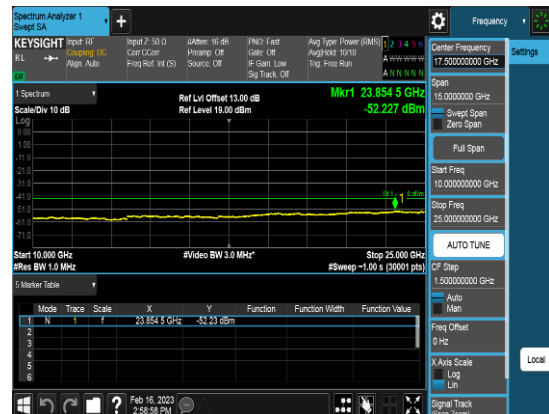
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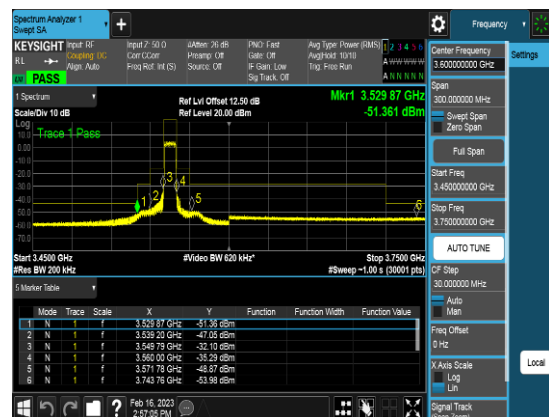
Port2\_10M\_3555MHz\_TM3.1a\_CHL\_CSE2



Port2\_10M\_3555MHz\_TM3.1a\_CHL\_CSE3



Port2\_10M\_3555MHz\_TM3.1a\_CHL\_CSEpart96





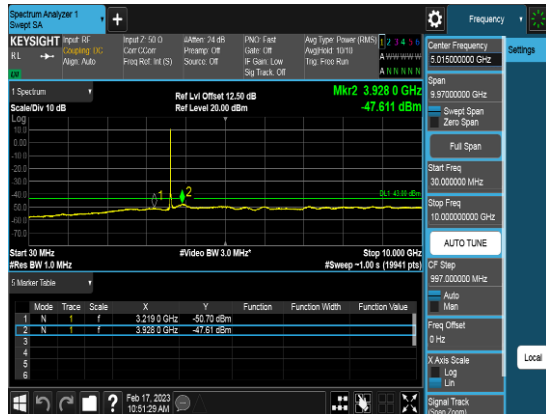
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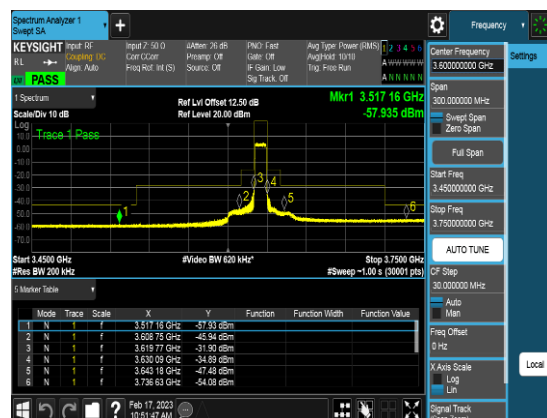
Port2\_10M\_3624.99MHz\_TM3.1a\_CHM\_CSE2



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Port2\_10M\_3624.99MHz\_TM3.1a\_CHM\_CSEpar 196

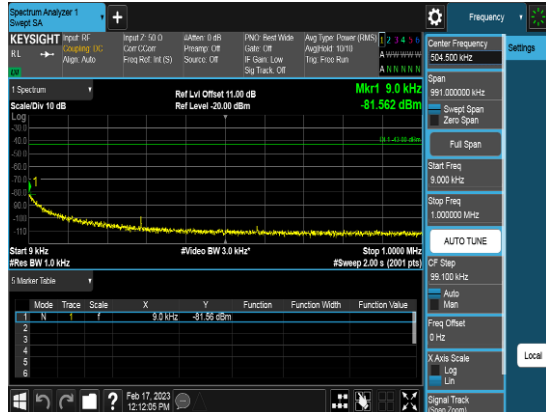




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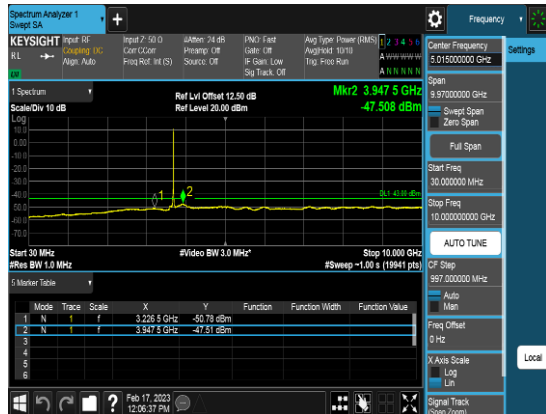
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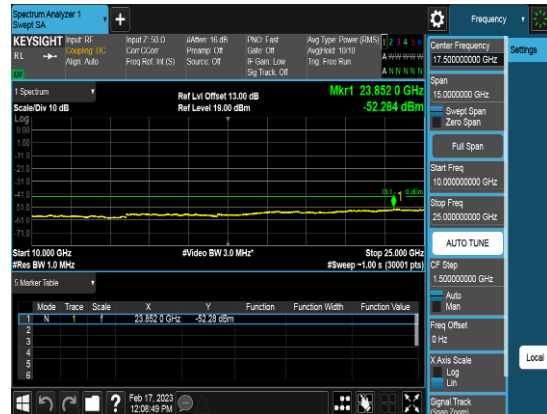
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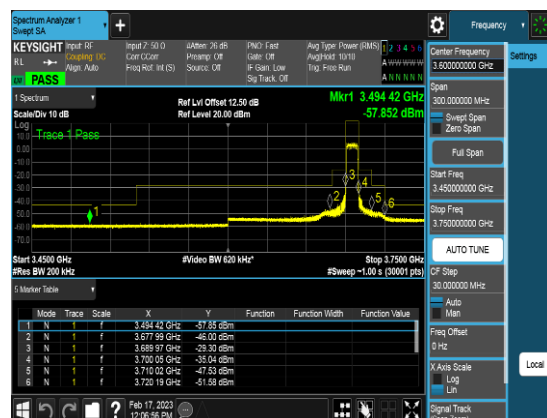
Port2\_10M\_3694.98MHz\_TM3.1a\_CHH\_CSE2



Port2\_10M\_3694.98MHz\_TM3.1a\_CHH\_CSE3



Port2\_10M\_3694.98MHz\_TM3.1a\_CHH\_CSEpart 96



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