AboCom System, Inc.

1F, No.21,R&D Rd.II, SBIP, Hsin-Chu 300, Taiwan, R.O.C.

Federal Communications Commission

Dear Sir.

We, the undersigned, hereby authorize ELECTRONICS TESTING CENTER, TAIWAN to act on our behalf in all matters relating to application for 10.4" TFT LCD Wireless Thin Client (Model:100WT10P, FCC ID No.: MQ4M100WT10P)" authorization, including the signing of all documents relating to these matters. Any and all carried out by ELECTRONICS TESTING CENTER, TAIWAN on our behalf shall have the same effect as acts of our own.

The applicant certifies that in the case of an individual applicant (e.g., corporation), no party to the applicant is subject to a denial of federal benefits, that includes FCC benefits, pursuant to section 5301 of the Anti-drug abuse Act of 1988.21 U.S.A. 862. For a definition of a "party" for these purposes See 47 C.F.R. 1.2002(b).

D _x	Brilly B	y:
By:	Signature)	(Printed)
Title :	Deputy Manager	of pA
Applicant	: AboCom System, Inc.	
Address	: 1F, No.21,R&D Rd.II, SBIP, H Taiwan, R.O.C.	sin-Chu 300,
Tel	: +886-2-89192133	Fax : <u>+886-2-89192130</u>
Date	: Jul. 24, 2003	E-mail: arthur.dai @ ms3, abocom, com, tw

AboCom System, Inc.

1F, No.21,R&D Rd.II, SBIP, Hsin-Chu 300, Taiwan, R.O.C.

FCC ID: MQ4M100WT10P

Federal Communications Commission Authorization and Evaluation Division

Confidentiality Request

Pursuant to Section 0.457 and 0.459 of the Commission's Rules, the Applicant hereby requests confidential treatment of information accompanying this Application as outlined below:

Schematics

Block Diagram

The above materials contain trade secrets and proprietary information not customarily released to the public. The public disclosure of these matters might be harmful to the Applicant and provide unjustified benefits to its competitors.

The Applicant understands that pursuant to Rule 0.457, disclosure of this Application and all accompanying documentation will not be made before the date of the Grant for this application.

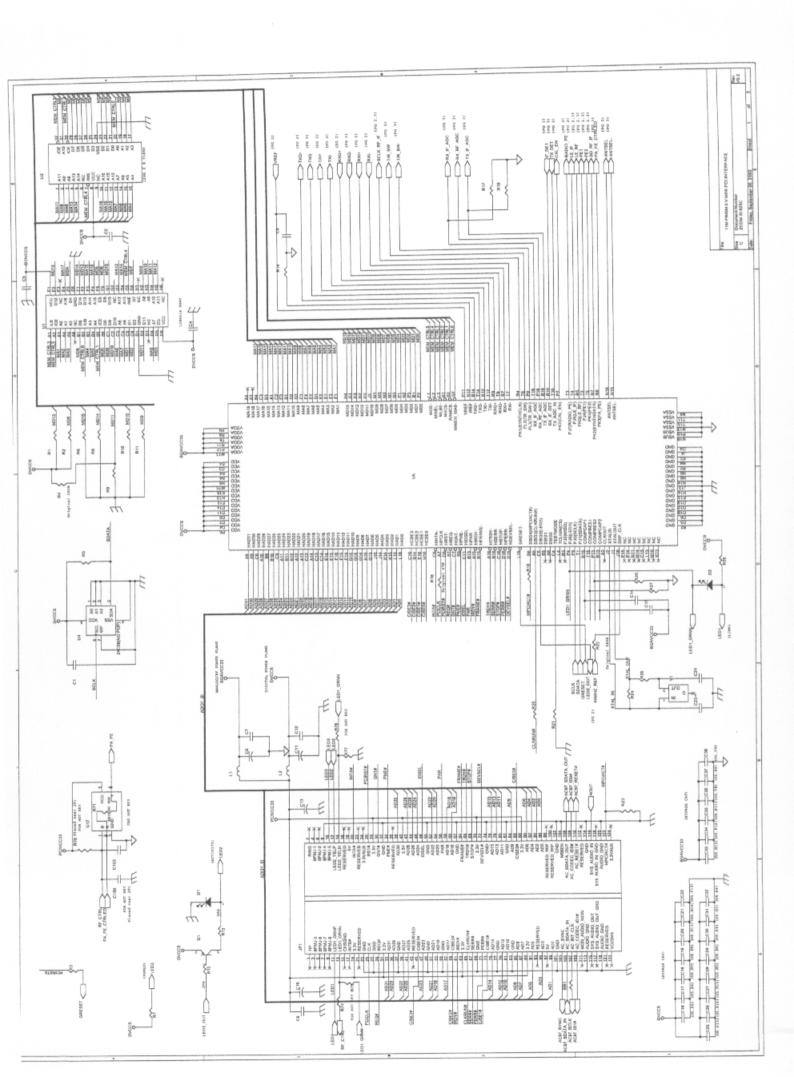
By:(Signature	gnat	By:
Title	:	Deputy Manager of ph
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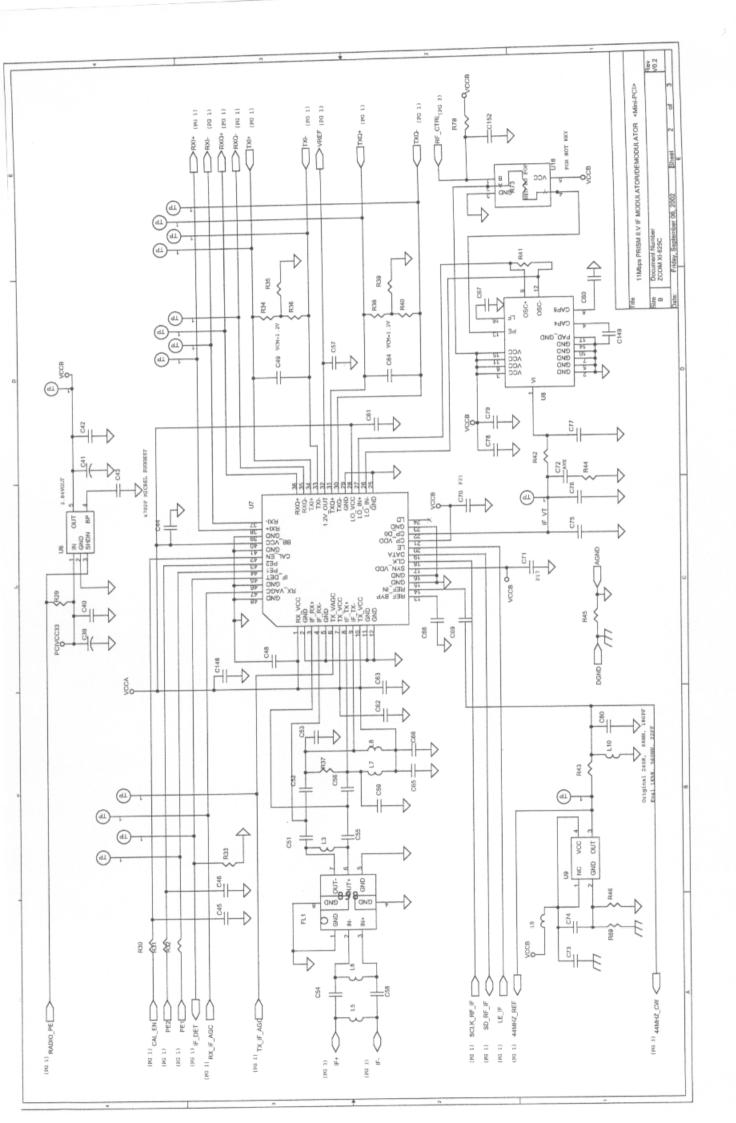
SOSHIN MDR741F ISL3984 RFPA/DET Alpha Ind AS179-92 Alpha Ind AS179-92 UFL-R-SMT SOSHIN MDR742F 44MHz OSC-TXC PLL RF/IF CONV EPCOS B3677 374MHz ISL3685 MVY2074 RF VCO LJPG2745 PLL + 1/QLO 1818181 IF VCO VCC_IN DAC 32,768KHz ISL3874A BBP & MAC AND AND BSI BS616LV2013EC-55 PHY INTERFACE (MDI) SERIAL CONTROL (MMI) MEMORY SRAM PCI/CARD BUS 32 HOST INTERFACE WEP CRO-PROGRAMMI MAC ENGINE FLASH RAM SST39VF010 ON-CHIP RAM ROM

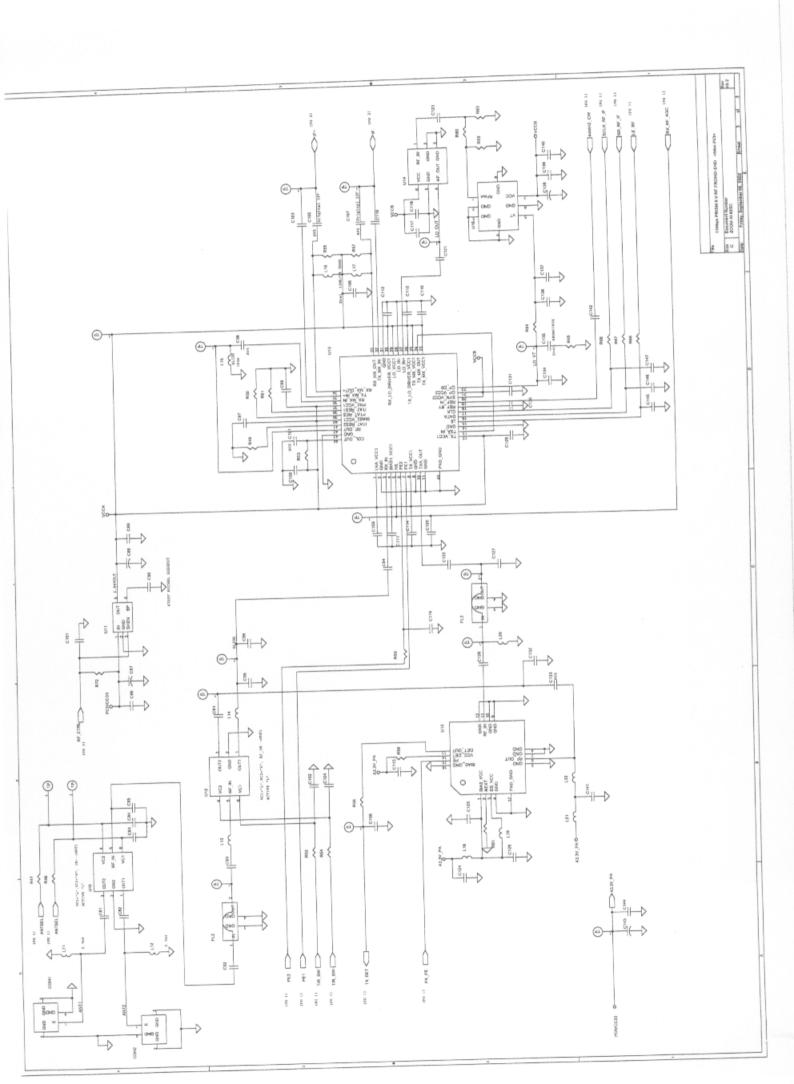
HOST PC INTERFACE

Block Diagram

HIROSE UFL-R-SMT







Operational Description

XI-625 Wireless LAN Mini PCI card, utilizing the ISL3874A integrated Baseband Processor and Medium Access Controller, is optimized for smaller form factors and embedded WLAN applications. It uses PRISM 2.5 solution incorporating sophisticated state-of-the-art silicon germanium (SiGe) and submicro CMOS technology to create a highly integrated, feature-rich WLAN silicon solutions for products operating in the 2.4GHz Industrial Scientific and Medical (ISM) band at the IEEE 802.11b-compliant high-data-rate speed of 11 Megabits-per-second (Mbps).

Key Components:

ISL3984 (2.4GHz Power Amplifier and Detector):

The ISL3984 is a 2.4GHz monolithic SiGe Power Amplifier designed to operate in the ISM band. It delivers 18dBm (Typ) output power for the typical DSSS signal. The ISL3984 is housed in a 16 lead MLFP package.

ISL3685 (2.4GHz RF/IF Converter and Synthesizer):

The ISL3685 is a monolithic SiGe half duplex RF/IF transceiver designed to operate in the 2.4GHz ISM band. The receive chain features a low noise, gain selectable amplifier (LNA) followed by a down-converter mixer. An up-converter mixer and a high performance preamplifier compose the transmit chain. The remaining circuitry comprises a high frequency Phase Locked Loop (PLL) synthesizer with a three sire programmable interface for local oscillator applications. The ISL3685 is housed in a 44 lead MLFP package.

HFA3783 (I/Q Modulator/Demodulator and Synthesizer):

The HFA3783 is a highly integrated and fully differential SiGe baseband converter for half duplex wireless applications. It features all the necessary blocks for quadrature modulation and demodulation of "I" and "Q" baseband signals. The device operates at low LO levels from an external VCO with a PLL reference signal up to 50MHz. The HFA3783 is housed in a thin 48 lead LQFP package.

ISL3874A (Wireless LAN Integrated Medium Access Controller with Baseband Processor with PCI):

The Intersil ISL3874A Wireless LAN Integrated Medium Access Controller with Integrated Baseband Processor is part of PRISM 2.4GHz radio chip set. It has on-board A/Ds and D/A for analog I and Q inputs and outputs, for which the HFA3783 IF QMODEM is recommended. Differential phase shift keying modulation schemes DBPSK and DQPSK, with data scrambling capability, and available along with Complementary Code Keying to provide a variety of data rates. Both Receive and Transmit AGC functions with 7-bit AGC control obtain maximum performance in the analog portions of the transceiver.

ISL3183 (748MHz VCO)

The VCO source is used for IF to BB conversion.

MDR741F & MDR742F (RF Band Pass Filter):

They are used to reject the spurious out of the pass band.

AS179-92 (RF Switch):

There are two pieces in XI-625. One is used to switch the transmit and receive chains, the other is for antenna switch.

- Modulation duty cycles: 33%
- PIFA Metal Antenna
- For Spread Spectrum devices, please provide the following information:

- Bit Rate: 1, 2, 5.5, 11 Mbps - Chipping Rate: 11 Mbps

- Data Rate: 11 Mbps

- Spreading Rate: 11 Mbps