

Rhein Tech Laboratories
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Client: Alinco, Inc
Model: DR-435TMkII
Standards: FCC 15.121/IC RSS-215
Report #: 2003108
Date: June 25, 2003

APPENDIX C: ATTESTATION LETTER

Please refer to the following page.



ALINCO

INCORPORATED

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Osaka 530-0004, Japan Fax : 06(4797)2156 Phone : 06(4797)2134

6/6/2003

Federal Communications Commissions

RE:PH3DR435TMkII / 800MHz analog cellular telephone band blocking

Dear Sir or Madam.

This is to declare that the device in application PH3DR435TMkII has been blocked for any and all access of 824.000 to 849.9975MHz and 869.000 to 894.9975MHz.

The device uses double super heterodyne as a receiver circuit and frequency is generated by a PLL synthesizer circuitry. The first local oscillation frequencies are determined by the N value data of the CPU. The receiver's 380.850 - 455.845MHz range is determined by N-value data of 350.000 - 424.995MHz, while the receiver's 425.000 - 511.995MHz range is determined by 394.150 - 481.145MHz data, and such values are not able to be changed by any means.

There are also two band-pass filters used to filter out the unwanted bands. The CPU used in this device, our parts code XA0851, vender's code M38267M8L272GP is exclusively programmed and burned for this US export model. ALINCO, Inc exports solely this version to the US market, and this CPU can't be modified by any means to receive the declared cellular frequencies. Moreover, the entire circuitry of this device is not designed to cover the cellular frequencies anyway.

To my best of knowledge being informed by the chief-engineer in charge of PH3DR435TMkII, above declared is true.

Sincerely

Kazuhiro Kusuvara

Vice-Chief, Production Section

Electronics Div., Alinco, Inc.