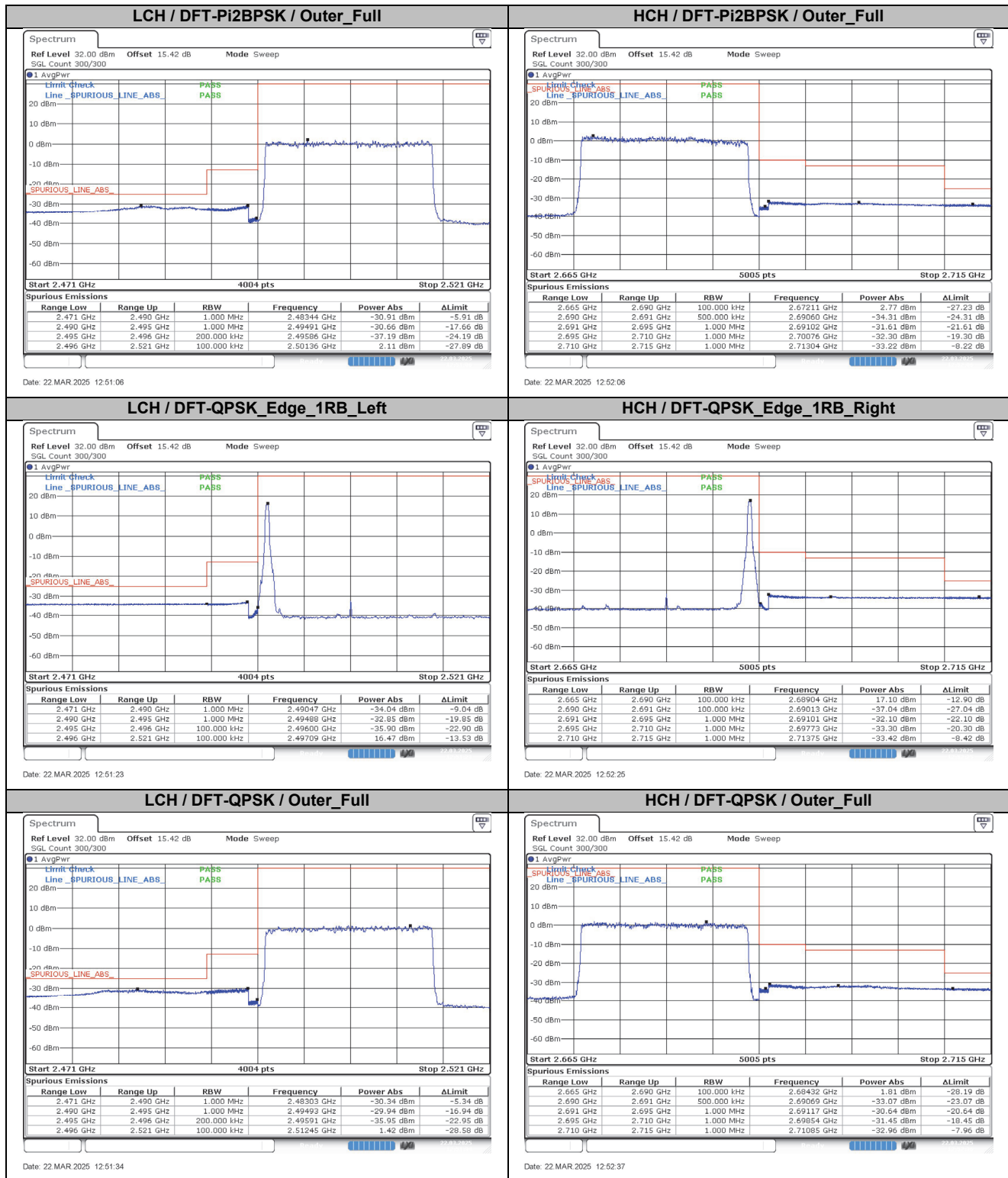
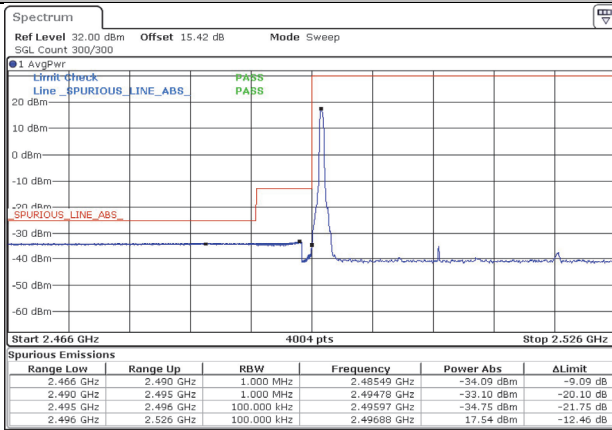




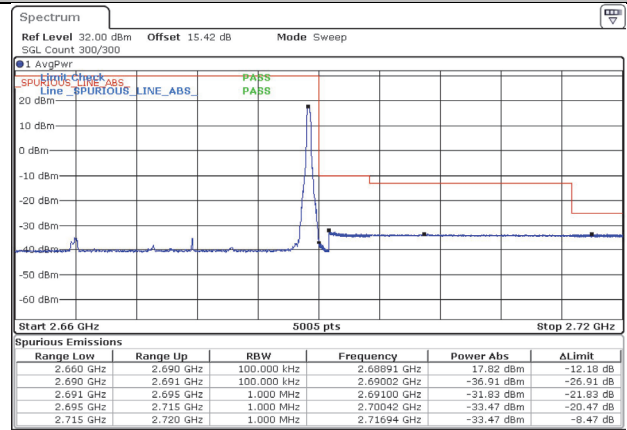
DC_66A_n41A / 30KHz / 25MHz

LCH / DFT-Pi2BPSK_Edge_1RB_Left



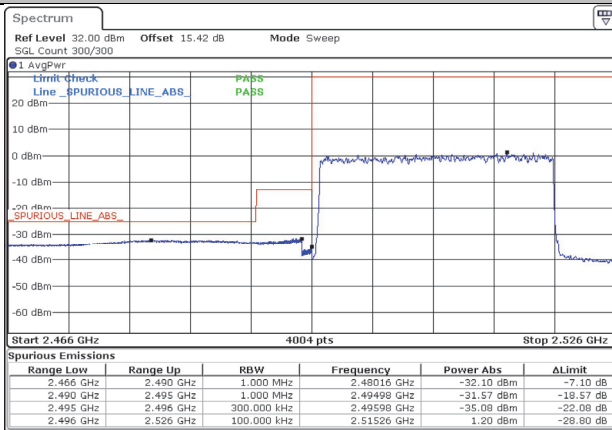
Date: 22 MAR 2025 12:53:11

HCH / DFT-Pi2BPSK_Edge_1RB_Right



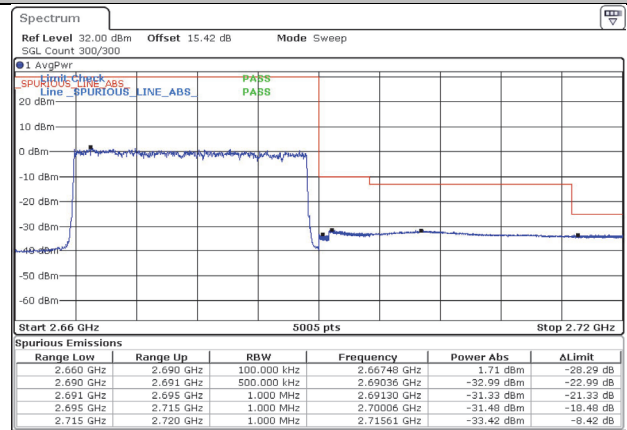
Date: 22 MAR 2025 12:54:29

LCH / DFT-Pi2BPSK / Outer_Full



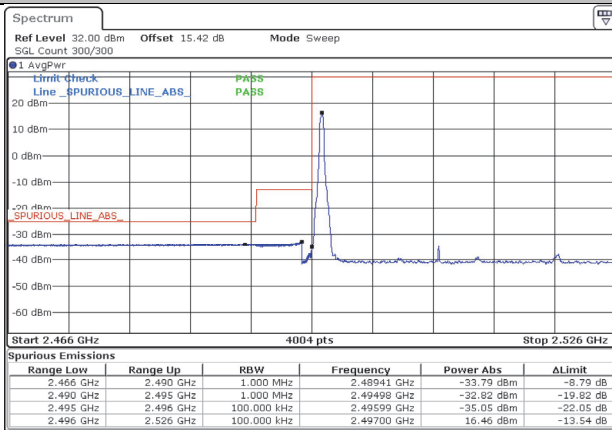
Date: 22 MAR 2025 12:53:41

HCH / DFT-Pi2BPSK / Outer_Full



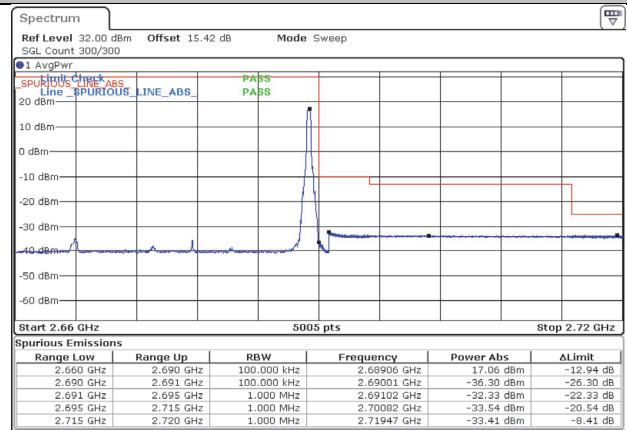
Date: 22 MAR 2025 12:54:41

LCH / DFT-QPSK_Edge_1RB_Left



Date: 22 MAR 2025 12:53:58

HCH / DFT-QPSK_Edge_1RB_Right



Date: 22 MAR 2025 12:55:00

