

Appendix D.4: Maximum conducted output power

Test Result

Test Mode	Antenna	Freq (MHz)	Channel Power [dBm]	Duty Cycle [%]	DC Factor [dBm]	Result [dBm]	Limit [dBm]	Verdict
11A	Ant1	5180	12.35	97.89	0.09	12.44	≤23.98	PASS
		5200	12.90	97.89	0.09	12.99	≤23.98	PASS
		5240	12.40	97.20	0.12	12.52	≤23.98	PASS
11N20SI SO	Ant1	5180	11.45	97.90	0.09	11.54	≤23.98	PASS
		5200	11.92	97.89	0.09	12.01	≤23.98	PASS
		5240	11.44	97.20	0.12	11.56	≤23.98	PASS
11N40SI SO	Ant1	5190	9.56	94.20	0.26	9.82	≤23.98	PASS
		5230	9.71	95.59	0.20	9.91	≤23.98	PASS
11AC20S ISO	Ant1	5180	10.91	97.04	0.13	11.04	≤23.98	PASS
		5200	11.59	97.04	0.13	11.72	≤23.98	PASS
		5240	10.70	97.04	0.13	10.83	≤23.98	PASS
11AC40S ISO	Ant1	5190	10.35	94.20	0.26	10.61	≤23.98	PASS
		5230	9.58	94.20	0.26	9.84	≤23.98	PASS
11AC80S ISO	Ant1	5210	6.41	91.67	0.38	6.79	≤23.98	PASS
11AX20SI SO	Ant1	5180	10.09	97.14	0.13	10.22	≤23.98	PASS
		5200	10.55	96.19	0.17	10.72	≤23.98	PASS
		5240	9.92	97.14	0.13	10.05	≤23.98	PASS
11AX40SI SO	Ant1	5190	10.43	94.64	0.24	10.67	≤23.98	PASS
		5230	9.79	94.64	0.24	10.03	≤23.98	PASS
11AX80SI SO	Ant1	5210	7.31	50.37	2.98	10.29	≤23.98	PASS

Note:

1. The Duty Cycle Factor is compensated in the graph.
2. The Duty Cycle Factor and RBW Factor is compensated in the data.

Appendix D.5: Maximum power spectral density

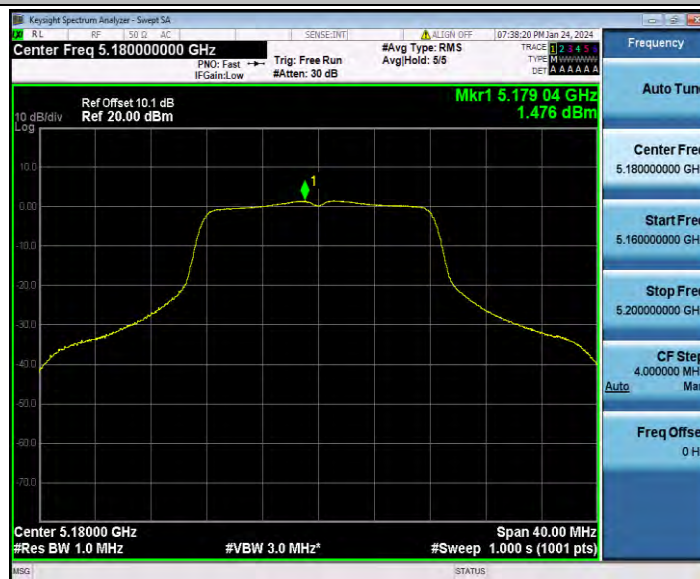
Test Result

TestMode	Antenna	Freq(MHz)	Result [dBm/MHz]	Limit[dBm/MHz]	Verdict
11A	Ant1	5180	1.48	≤11.00	PASS
		5200	2.05	≤11.00	PASS
		5240	1.84	≤11.00	PASS
11N20SISO	Ant1	5180	0.50	≤11.00	PASS
		5200	1.06	≤11.00	PASS
		5240	0.89	≤11.00	PASS
11N40SISO	Ant1	5190	-2.50	≤11.00	PASS
		5230	-3.25	≤11.00	PASS
11AC20SISO	Ant1	5180	-0.09	≤11.00	PASS
		5200	0.46	≤11.00	PASS
		5240	-0.12	≤11.00	PASS
11AC40SISO	Ant1	5190	-2.49	≤11.00	PASS
		5230	-3.29	≤11.00	PASS
11AC80SISO	Ant1	5210	-9.42	≤11.00	PASS
11AX20SISO	Ant1	5180	-1.46	≤11.00	PASS
		5200	-0.95	≤11.00	PASS
		5240	-1.46	≤11.00	PASS
11AX40SISO	Ant1	5190	-2.85	≤11.00	PASS
		5230	-3.54	≤11.00	PASS
11AX80SISO	Ant1	5210	-6.29	≤11.00	PASS

Note: 1.The Result and Limit Unit is dBm/500 kHz in the band 5.725–5.85 GHz.
 2.The Duty Cycle Factor and RBW Factor is compensated in the graph.

Test Graphs

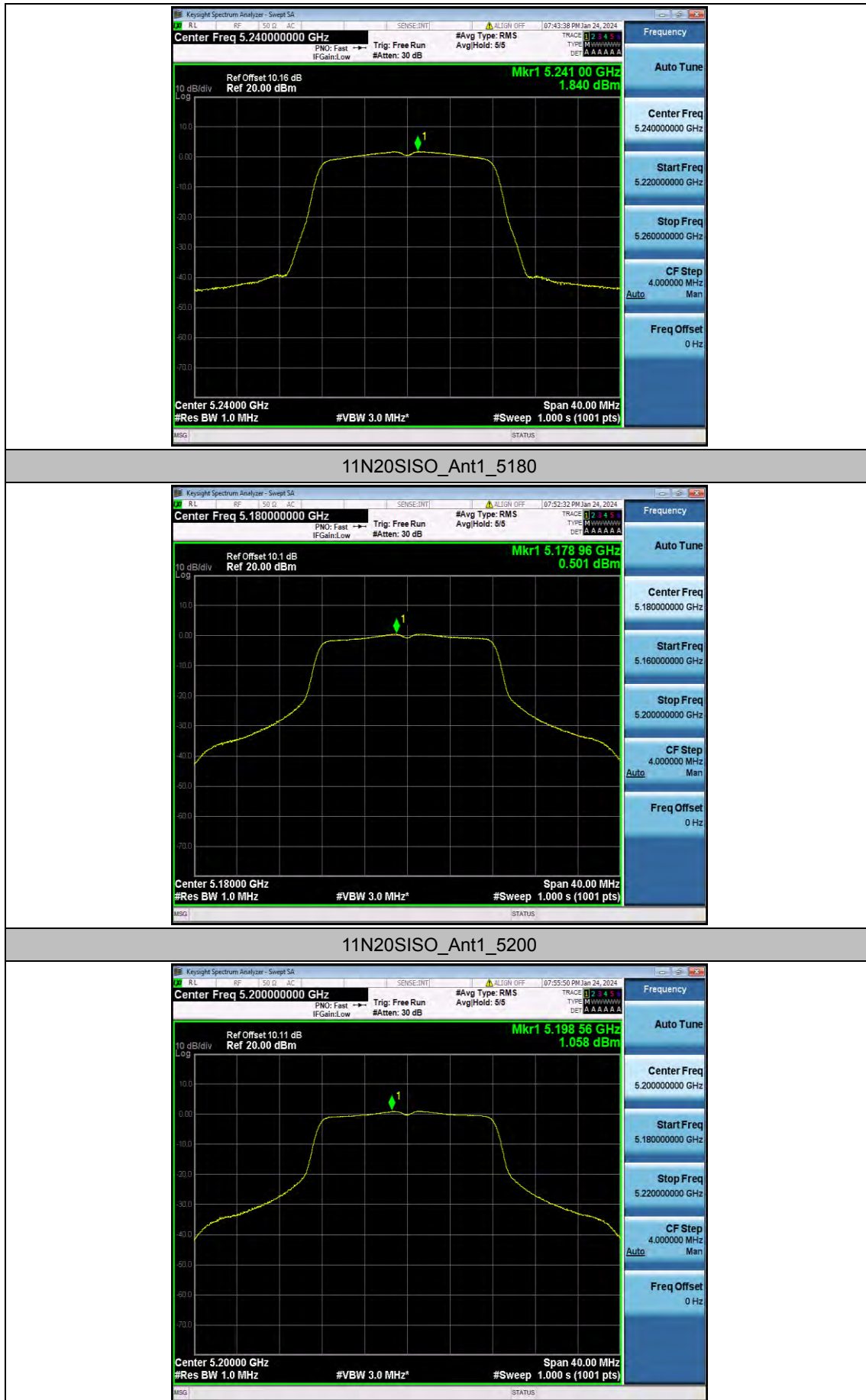
11A_Ant1_5180



11A_Ant1_5200



11A_Ant1_5240



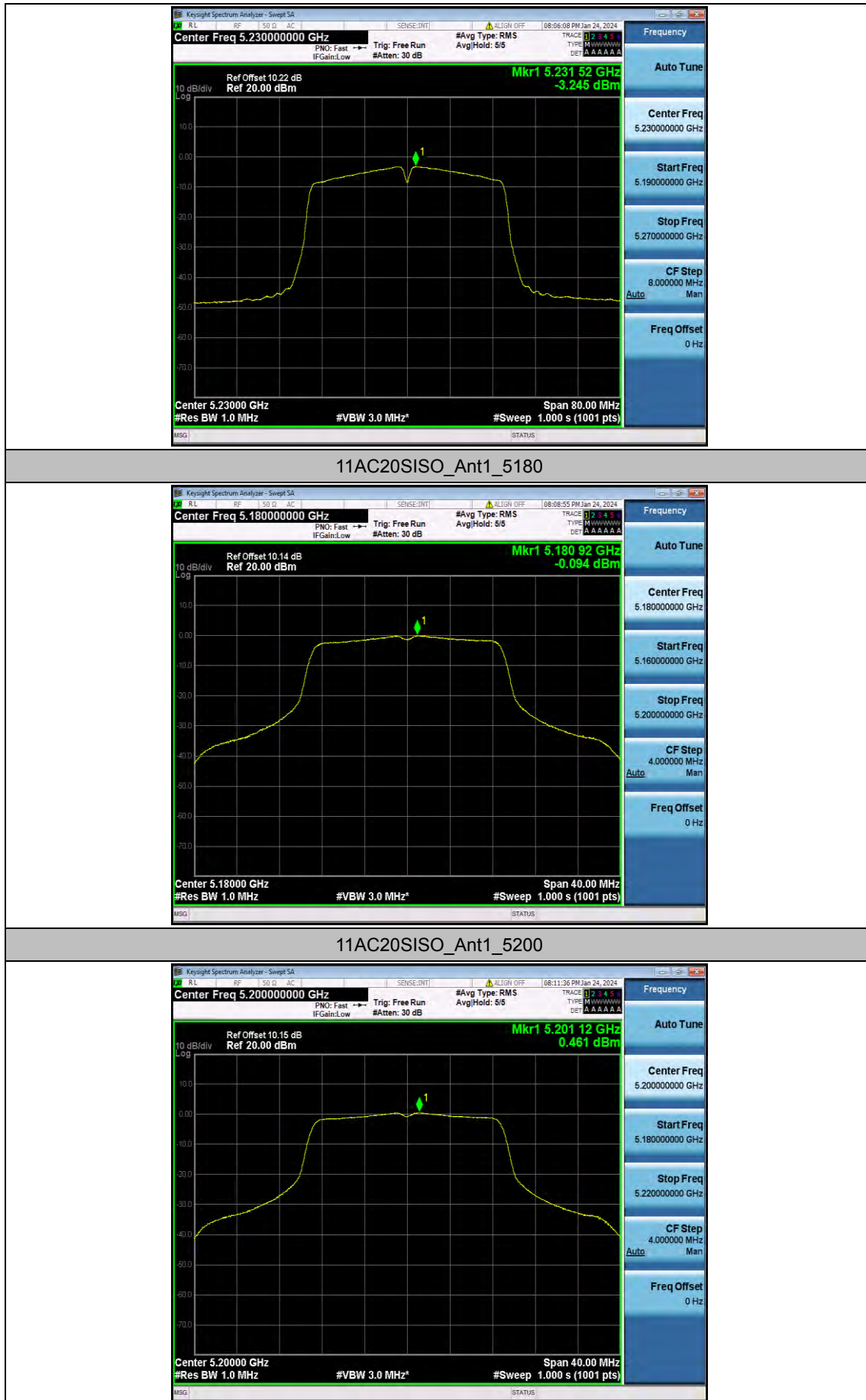
11N20SISO_Ant1_5240



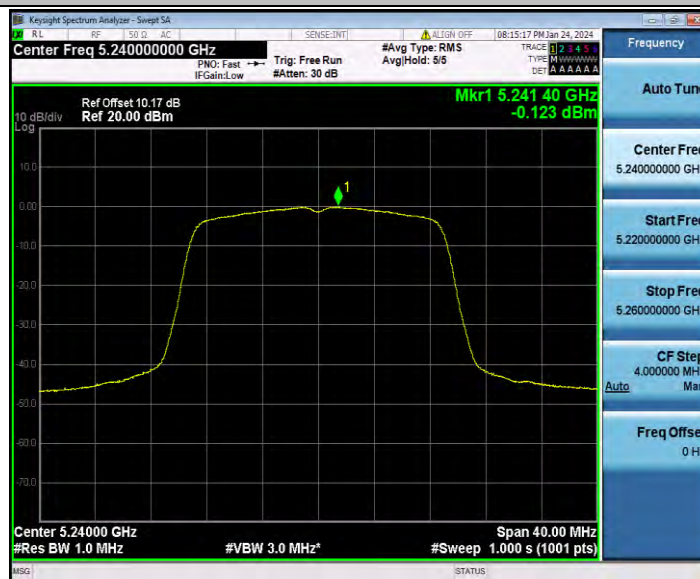
11N40SISO_Ant1_5190



11N40SISO_Ant1_5230



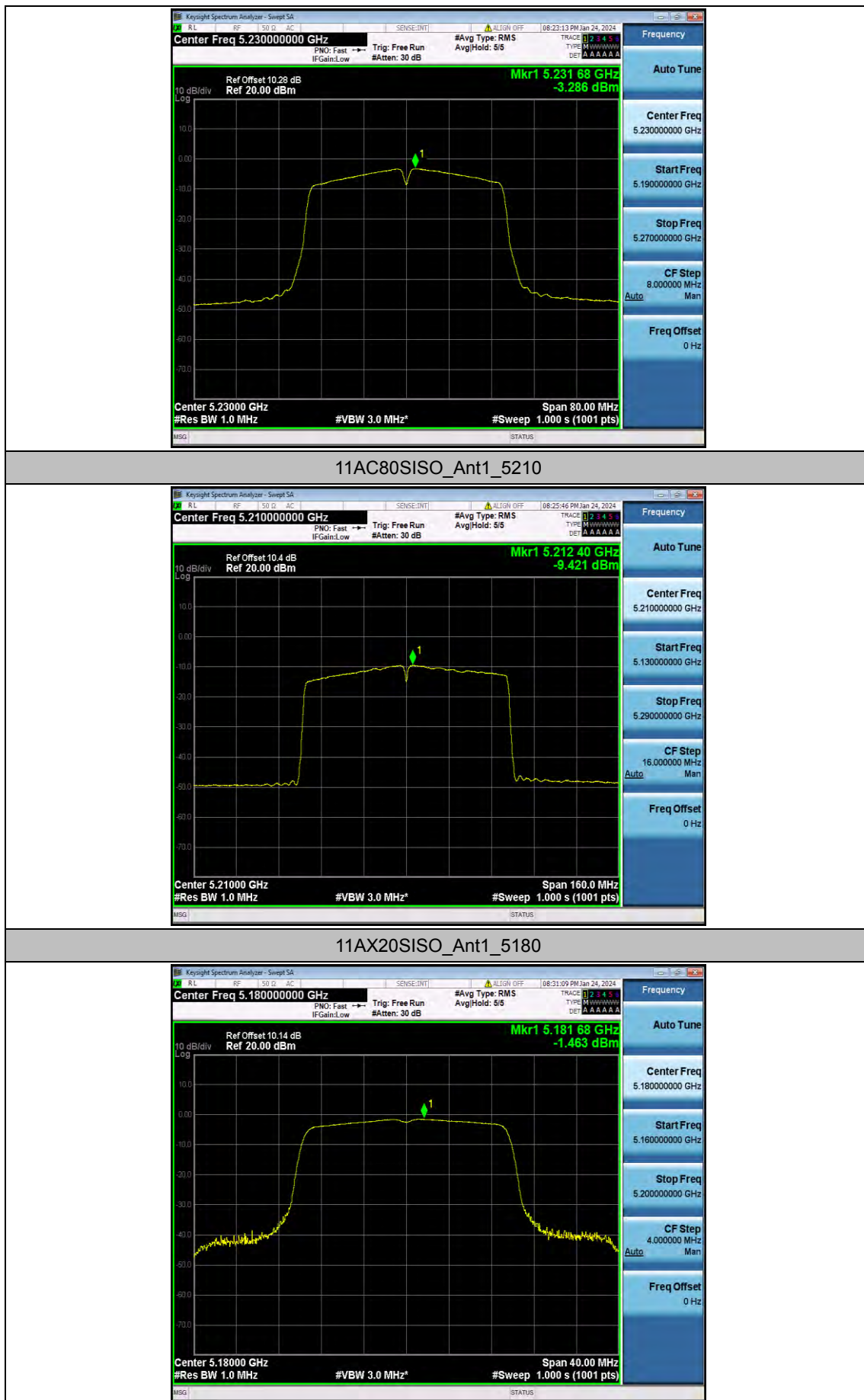
11AC20SISO_Ant1_5240



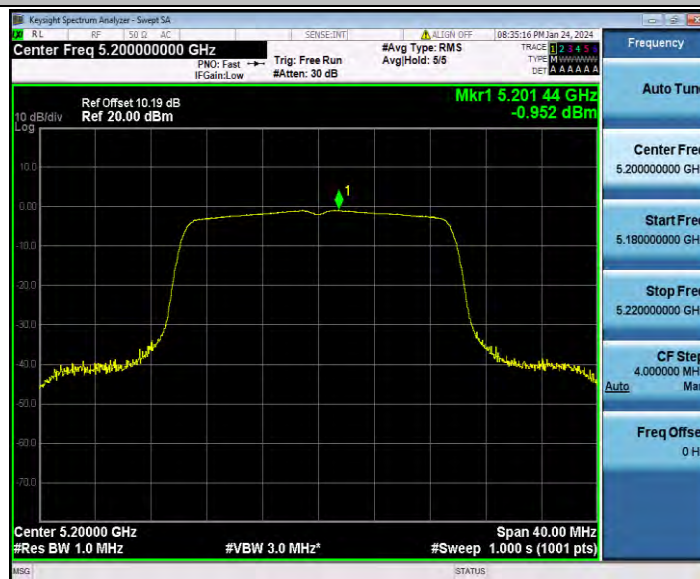
11AC40SISO_Ant1_5190



11AC40SISO_Ant1_5230



11AX20SISO_Ant1_5200



11AX20SISO_Ant1_5240



11AX40SISO_Ant1_5190



11AX40SISO_Ant1_5230



11AX80SISO_Ant1_5210

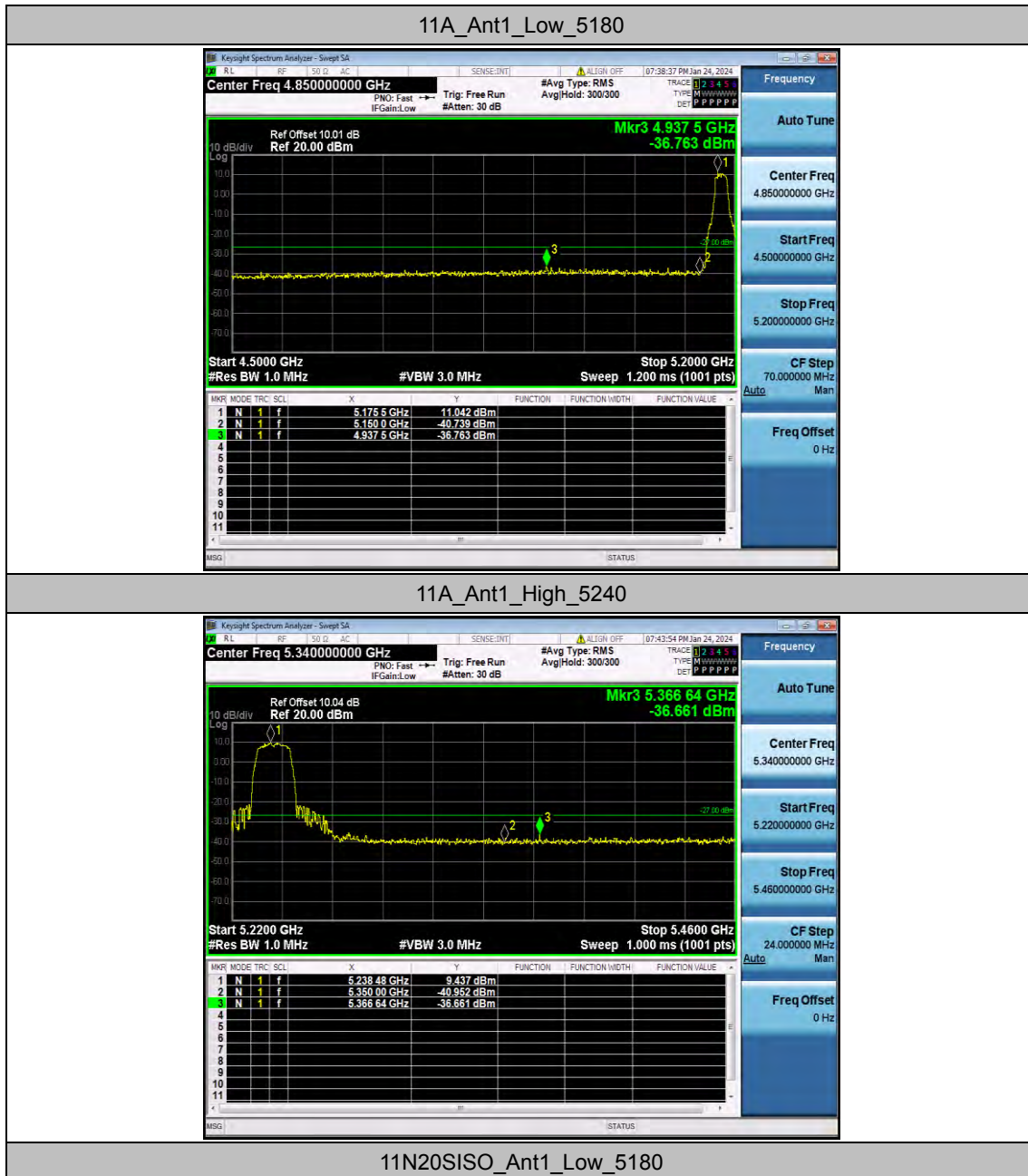


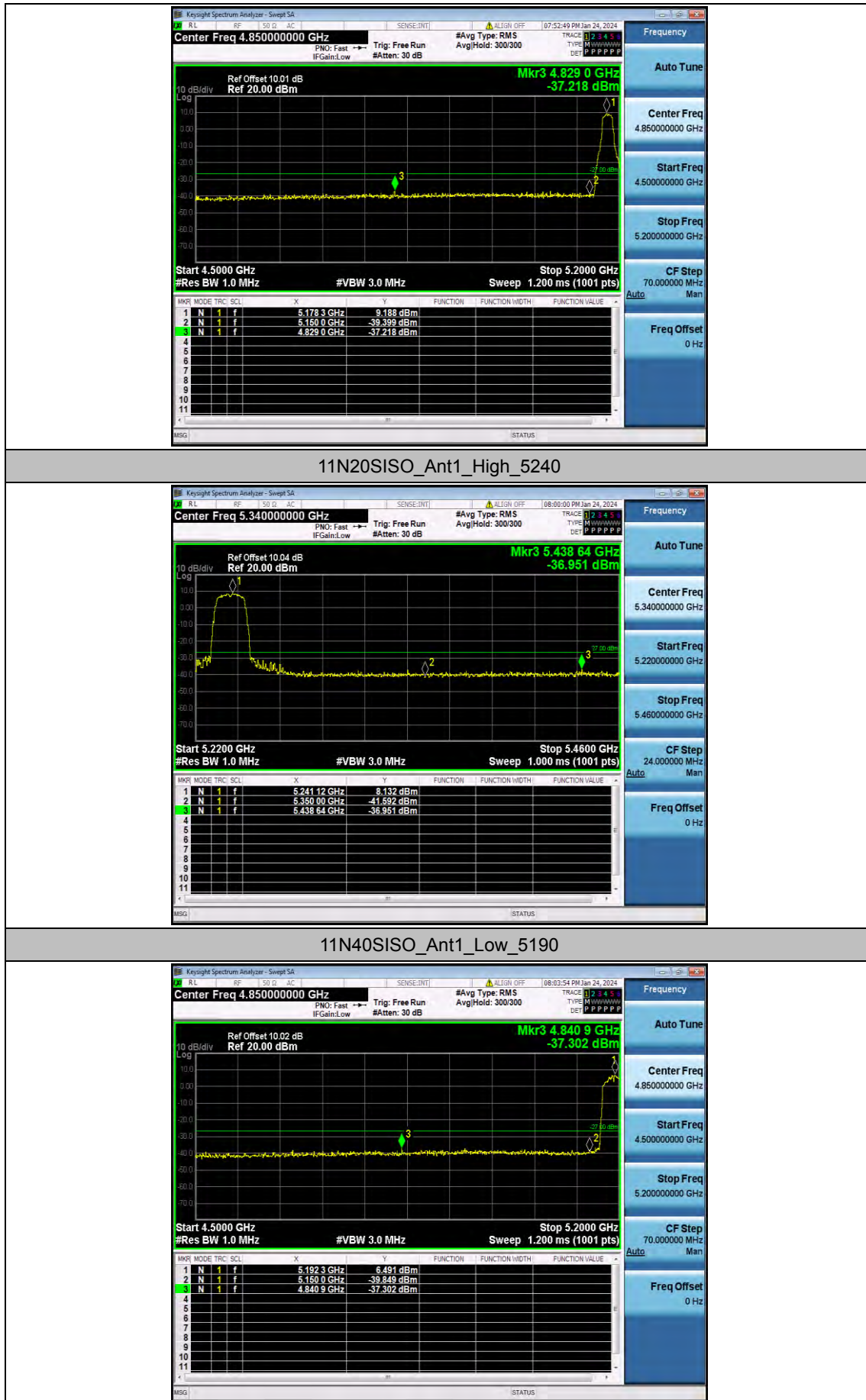
Appendix D.6: Band edge measurements

Test Result B1

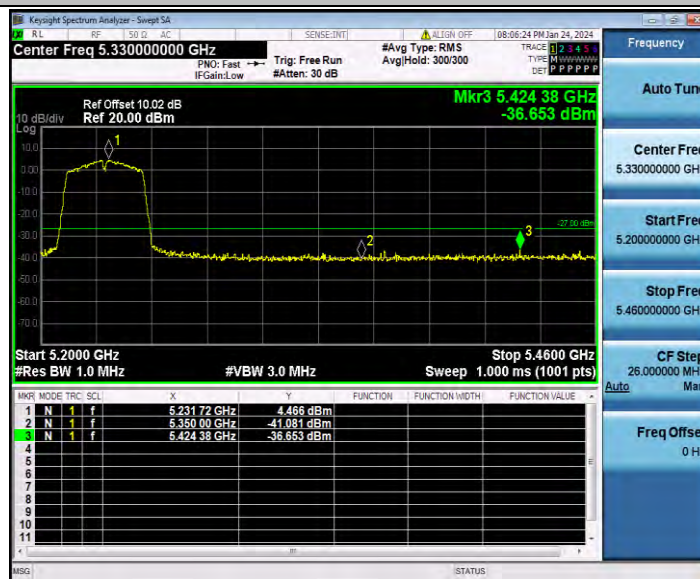
TestMode	Antenna	ChName	Freq(MHz)	Result[dBm]	Limit[dBm]	Verdict
11A	Ant1	Low	5180	-36.76	≤ -27	PASS
		High	5240	-36.66	≤ -27	PASS
11N20SISO	Ant1	Low	5180	-37.22	≤ -27	PASS
		High	5240	-36.95	≤ -27	PASS
11N40SISO	Ant1	Low	5190	-37.3	≤ -27	PASS
		High	5230	-36.65	≤ -27	PASS
11AC20SISO	Ant1	Low	5180	-37.55	≤ -27	PASS
		High	5240	-37.72	≤ -27	PASS
11AC40SISO	Ant1	Low	5190	-37.02	≤ -27	PASS
		High	5230	-37.38	≤ -27	PASS
11AC80SISO	Ant1	Low	5210	-36.61	≤ -27	PASS
		High	5210	-37.35	≤ -27	PASS
11AX20SISO	Ant1	Low	5180	-36.49	≤ -27	PASS
		High	5240	-37.57	≤ -27	PASS
11AX40SISO	Ant1	Low	5190	-37.22	≤ -27	PASS
		High	5230	-37.53	≤ -27	PASS
11AX80SISO	Ant1	Low	5210	-34.66	≤ -27	PASS
		High	5210	-37.72	≤ -27	PASS

Test Graphs B1

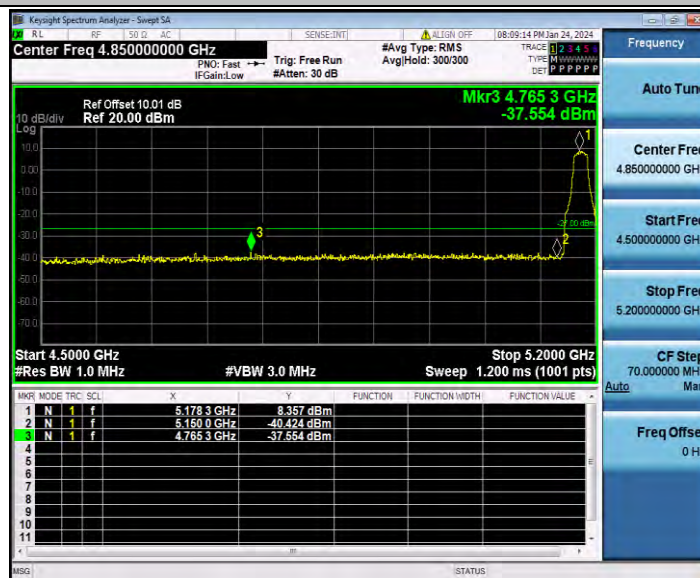




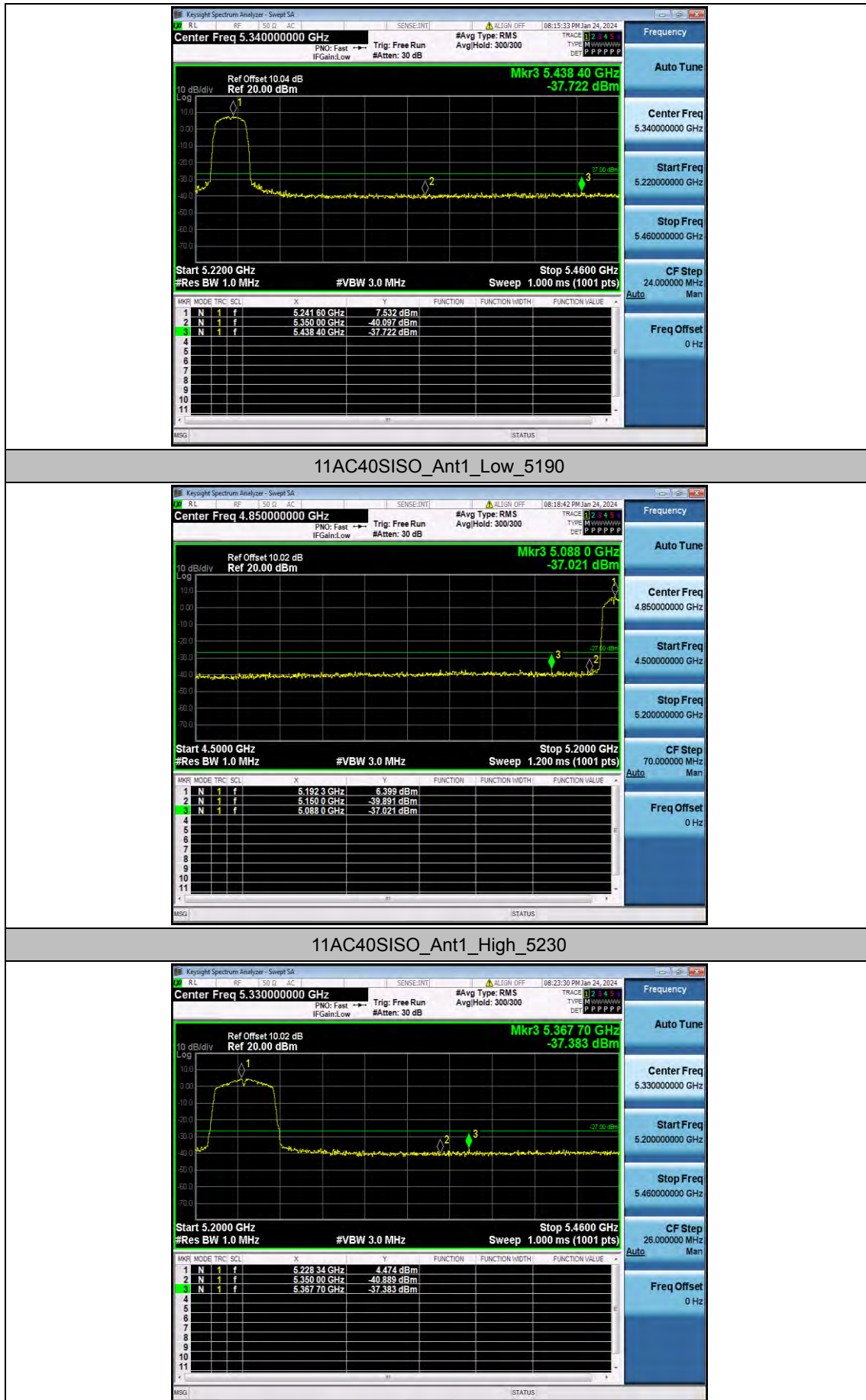
11N40SISO_Ant1_High_5230



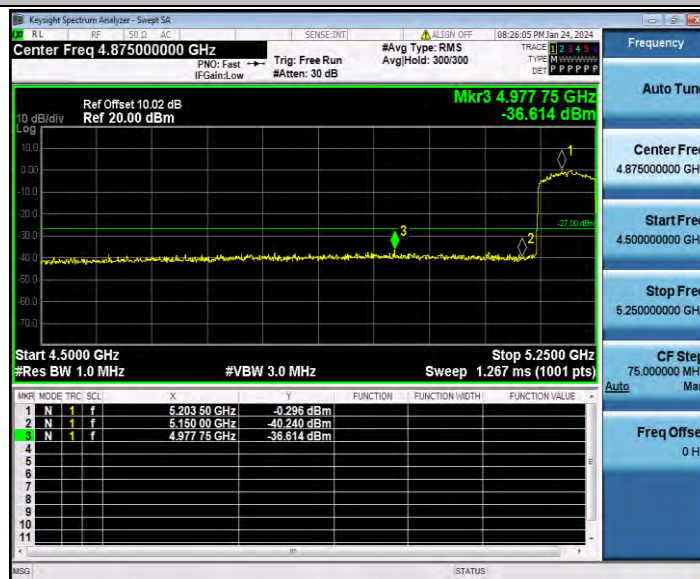
11AC20SISO_Ant1_Low_5180



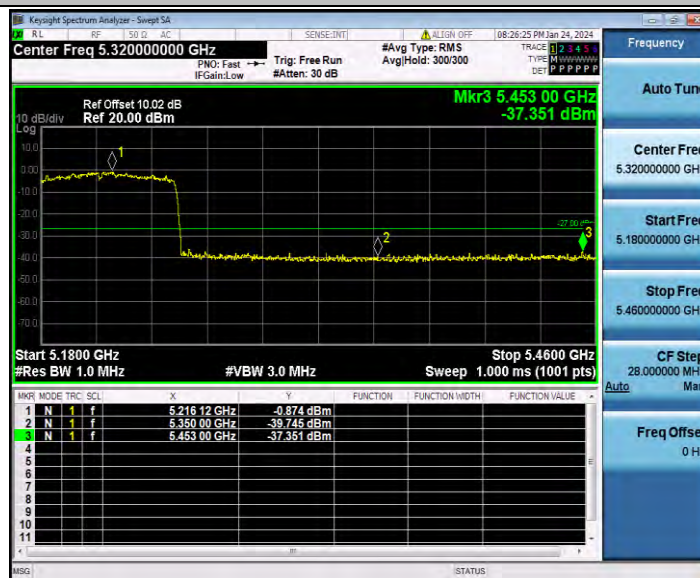
11AC20SISO_Ant1_High_5240



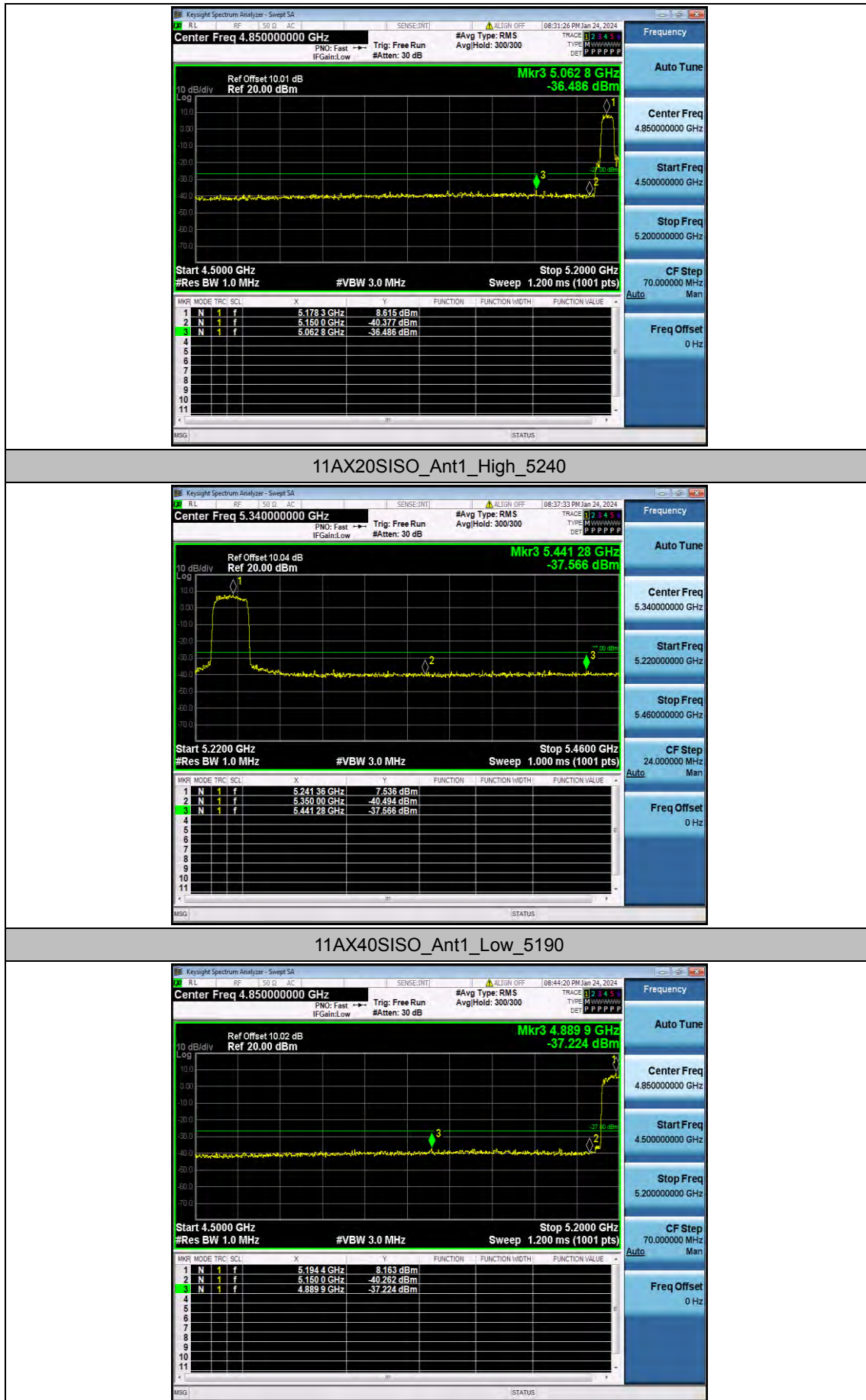
11AC80SISO_Ant1_Low_5210



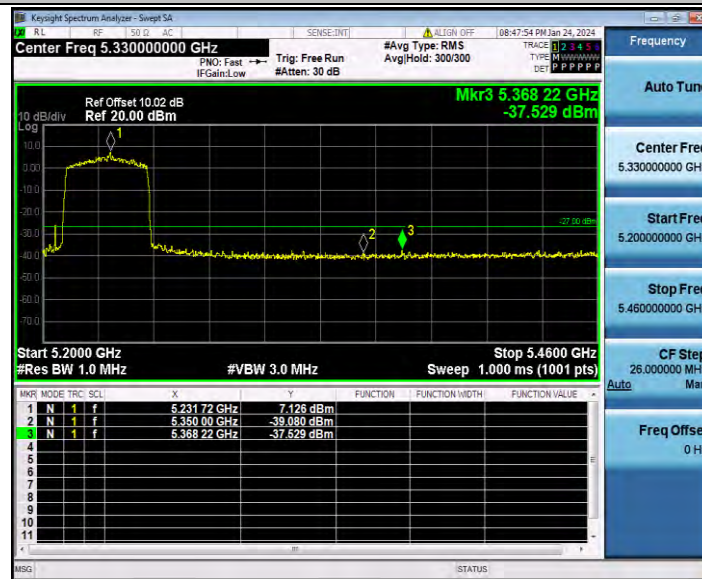
11AC80SISO_Ant1_High_5210



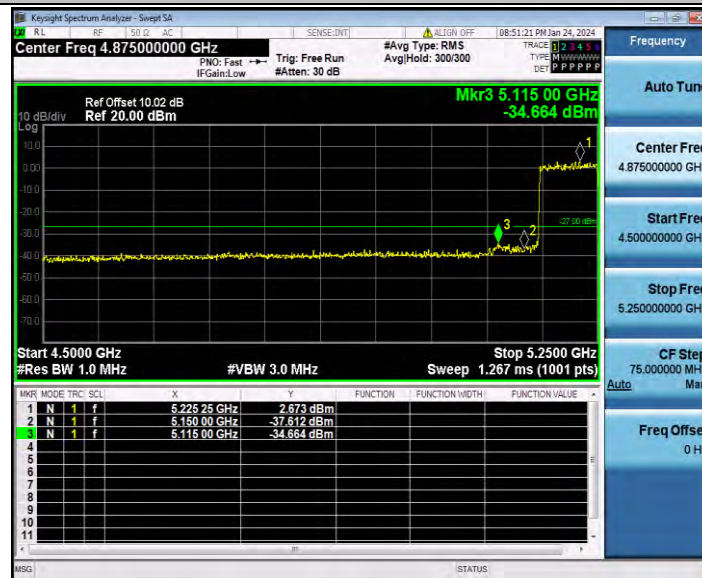
11AX20SISO_Ant1_Low_5180



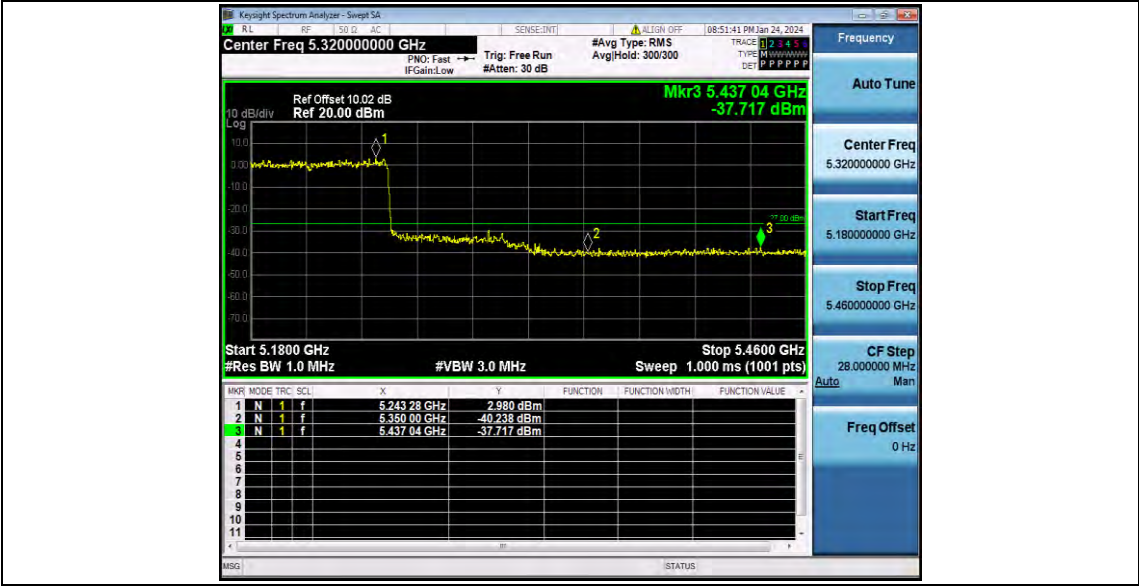
11AX40SISO_Ant1_High_5230



11AX80SISO_Ant1_Low_5210



11AX80SISO_Ant1_High_5210



Appendix D.7: Conducted Spurious Emission

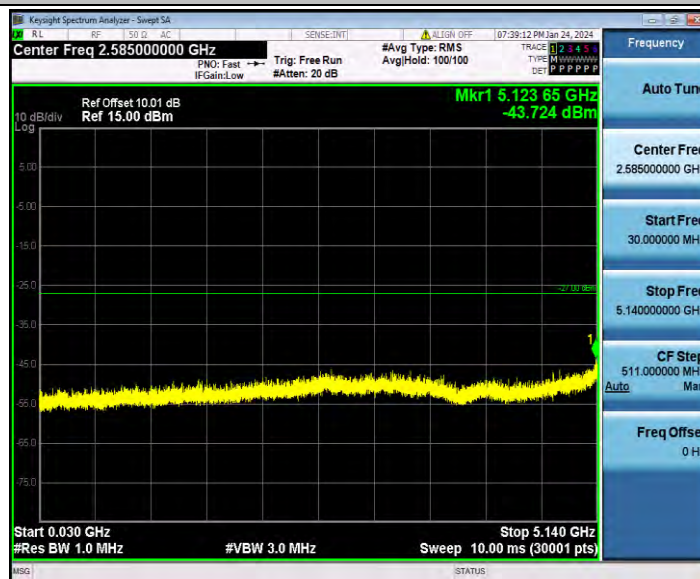
Test Result

TestMode	Antenna	Freq(MHz)	FreqRange [MHz]	Max. Fre [MHz]	Max. Level [dBm]	Limit [dBm]	Verdict
11A	Ant1	5180	30~5140	5123.65	-43.72	≤-27	PASS
			5360~40000	10358.84	-29.63	≤-27	PASS
		5200	30~5140	5137.27	-45.33	≤-27	PASS
			5360~40000	10399.23	-31.39	≤-27	PASS
		5240	30~5140	4958.6	-45.95	≤-27	PASS
			5360~40000	10479.3	-34.01	≤-27	PASS
11N20SISO	Ant1	5180	30~5140	5125.01	-44.74	≤-27	PASS
			5360~40000	10361.73	-31.29	≤-27	PASS
		5200	30~5140	5133.87	-44.72	≤-27	PASS
			5360~40000	10396.35	-32.5	≤-27	PASS
		5240	30~5140	2439.02	-45.03	≤-27	PASS
			5360~40000	10481.47	-36.32	≤-27	PASS
11N40SISO	Ant1	5190	30~5140	5107.13	-45.55	≤-27	PASS
			5360~40000	23521.01	-36.48	≤-27	PASS
		5230	30~5140	2431.7	-44.99	≤-27	PASS
			5360~40000	26060.1	-37.43	≤-27	PASS
11AC20SISO	Ant1	5180	30~5140	2429.49	-43.62	≤-27	PASS
			5360~40000	10360.28	-30.08	≤-27	PASS
		5200	30~5140	1742.87	-44.3	≤-27	PASS
			5360~40000	10397.79	-32.74	≤-27	PASS
		5240	30~5140	2638.14	-46.19	≤-27	PASS
			5360~40000	10480.75	-33.71	≤-27	PASS
11AC40SISO	Ant1	5190	30~5140	5122.97	-44.23	≤-27	PASS
			5360~40000	10376.87	-36.52	≤-27	PASS
		5230	30~5140	5023.32	-46.47	≤-27	PASS
			5360~40000	24232.97	-37.9	≤-27	PASS
11AC80SISO	Ant1	5210	30~5140	5115.81	-44.32	≤-27	PASS
			5360~40000	24236.57	-36.63	≤-27	PASS
11AX20SISO	Ant1	5180	30~5140	1743.55	-37.43	≤-27	PASS
			5360~40000	10359.56	-33.06	≤-27	PASS
		5200	30~5140	1744.41	-36.17	≤-27	PASS
			5360~40000	10394.91	-34.43	≤-27	PASS
		5240	30~5140	5069.14	-45.24	≤-27	PASS
			5360~40000	24244.51	-38.08	≤-27	PASS
11AX40SISO	Ant1	5190	30~5140	5095.2	-43.74	≤-27	PASS
			5360~40000	10384.09	-36.22	≤-27	PASS

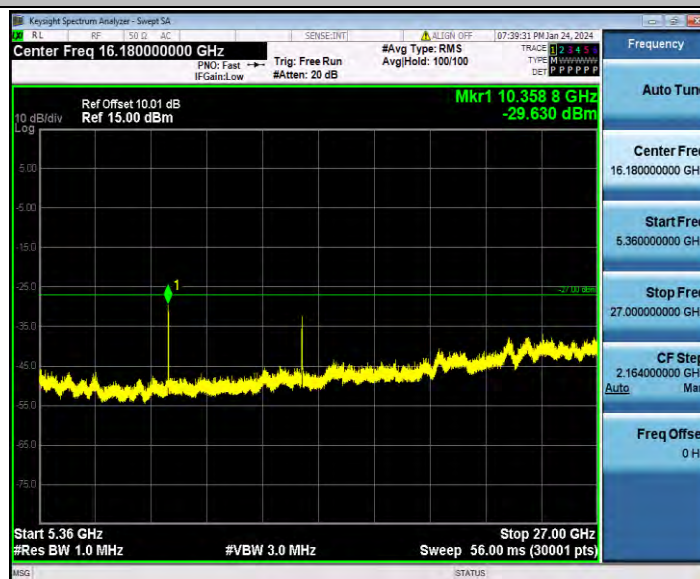
		5230	30~5140	3380.12	-45.95	≤ -27	PASS
			5360~40000	26770.62	-37.73	≤ -27	PASS
11AX80SISO	Ant1	5210	30~5140	5112.92	-37.57	≤ -27	PASS
			5360~40000	24275.52	-37.37	≤ -27	PASS

Test Graphs

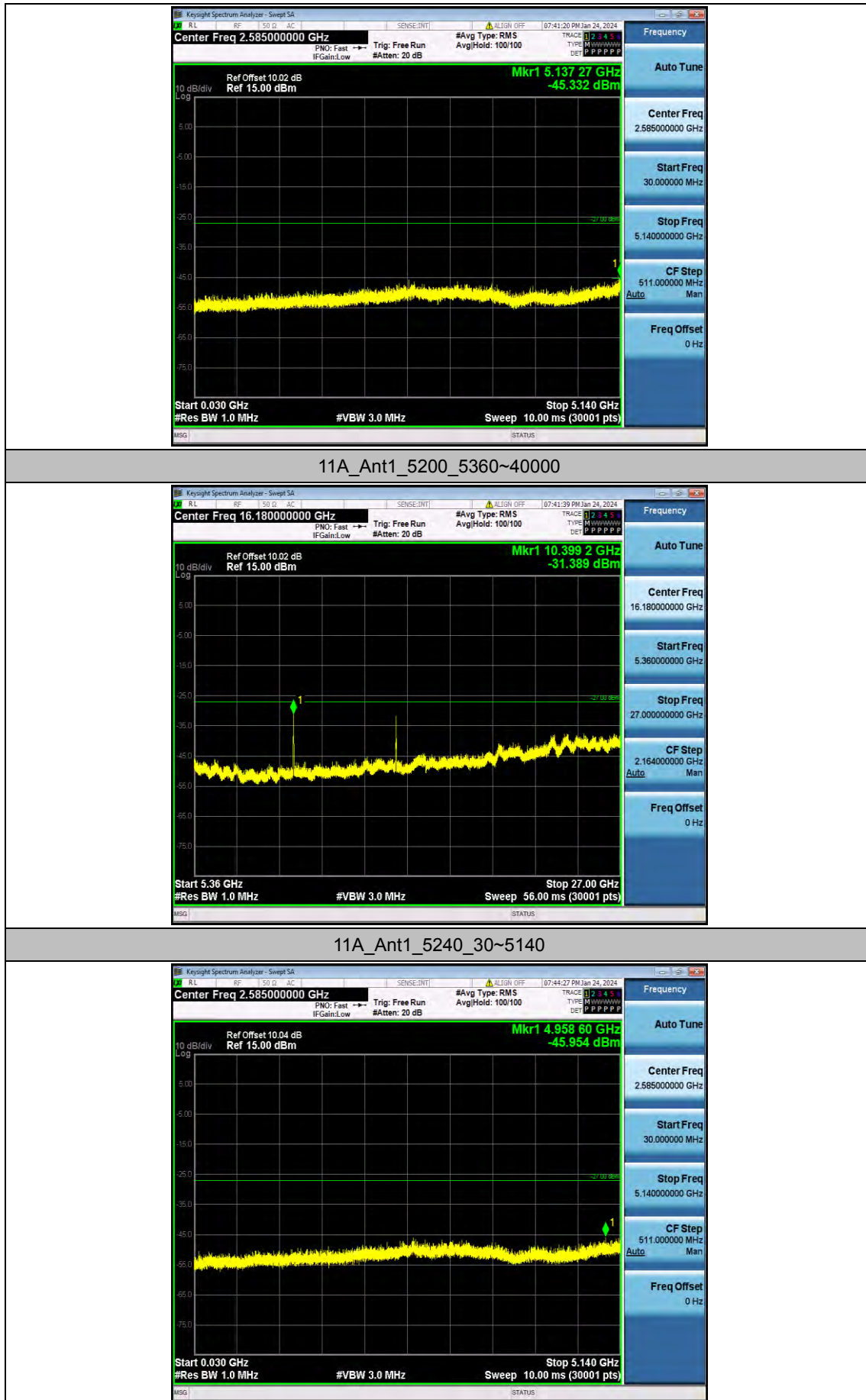
11A_Ant1_5180_30~5140



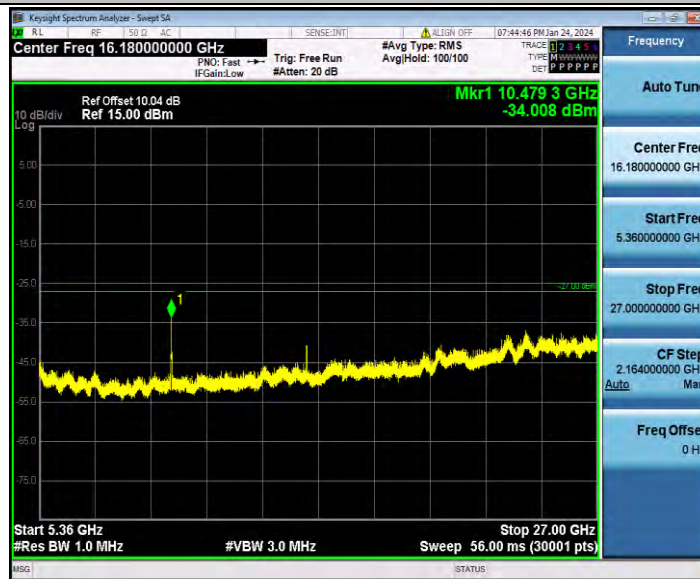
11A_Ant1_5180_5360~40000



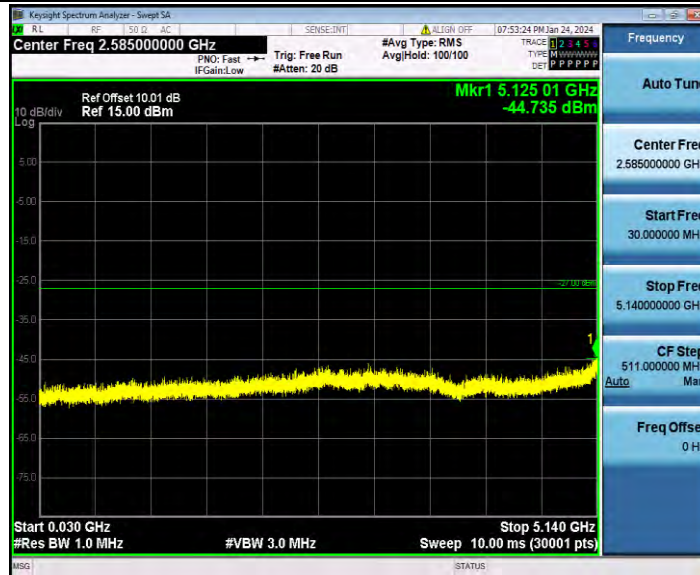
11A_Ant1_5200_30~5140



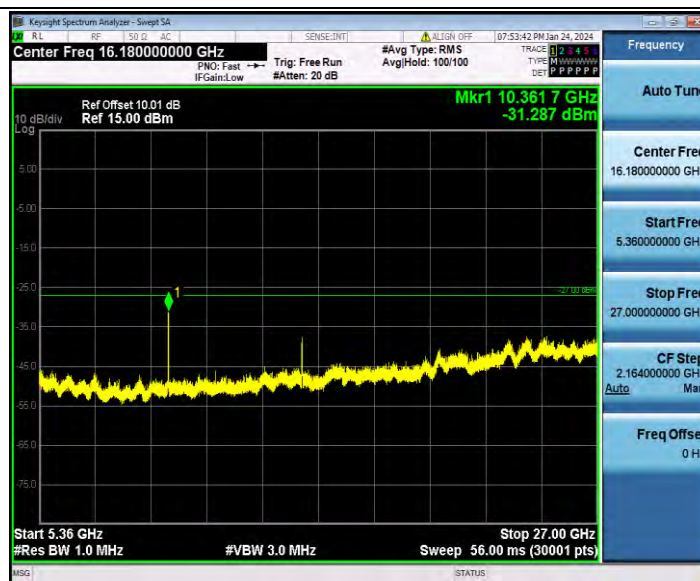
11A_Ant1_5240_5360~40000



11N20SISO_Ant1_5180_30~5140



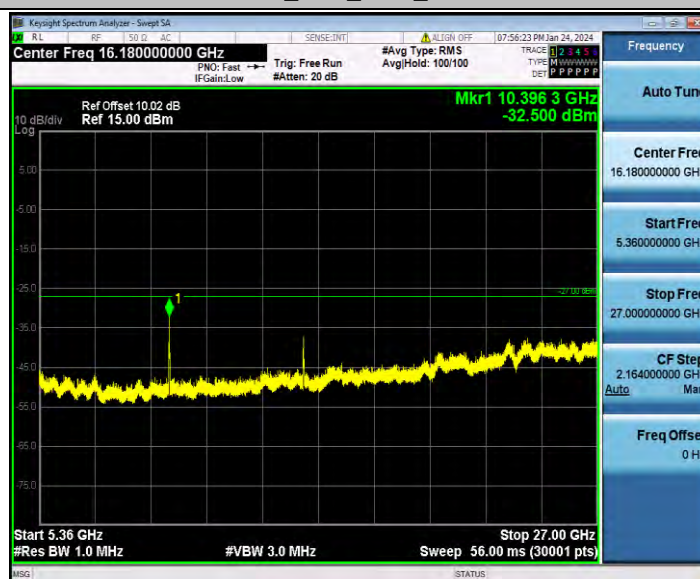
11N20SISO_Ant1_5180_5360~40000



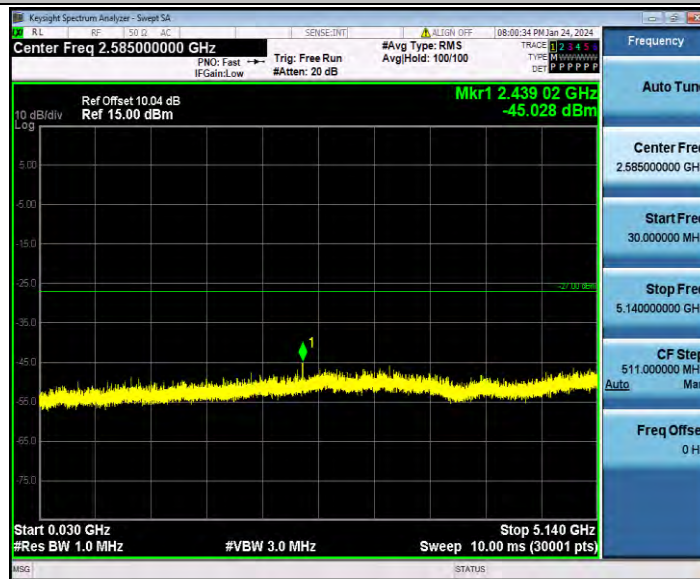
11N20SISO_Ant1_5200_30~5140



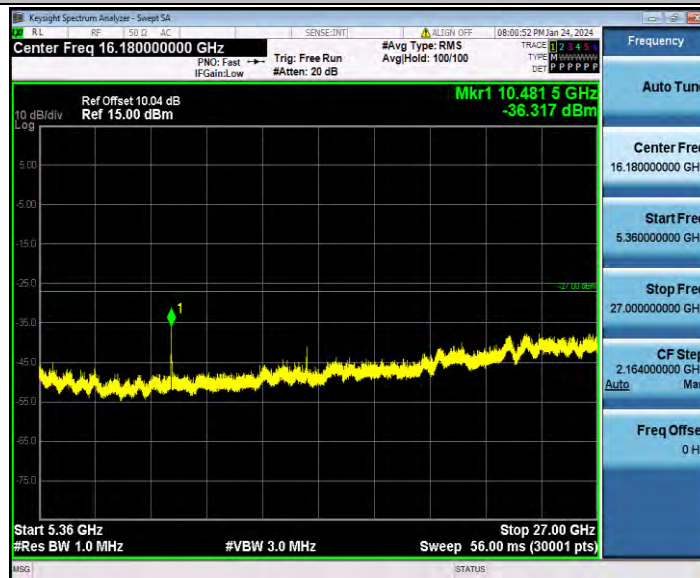
11N20SISO_Ant1_5200_5360~40000



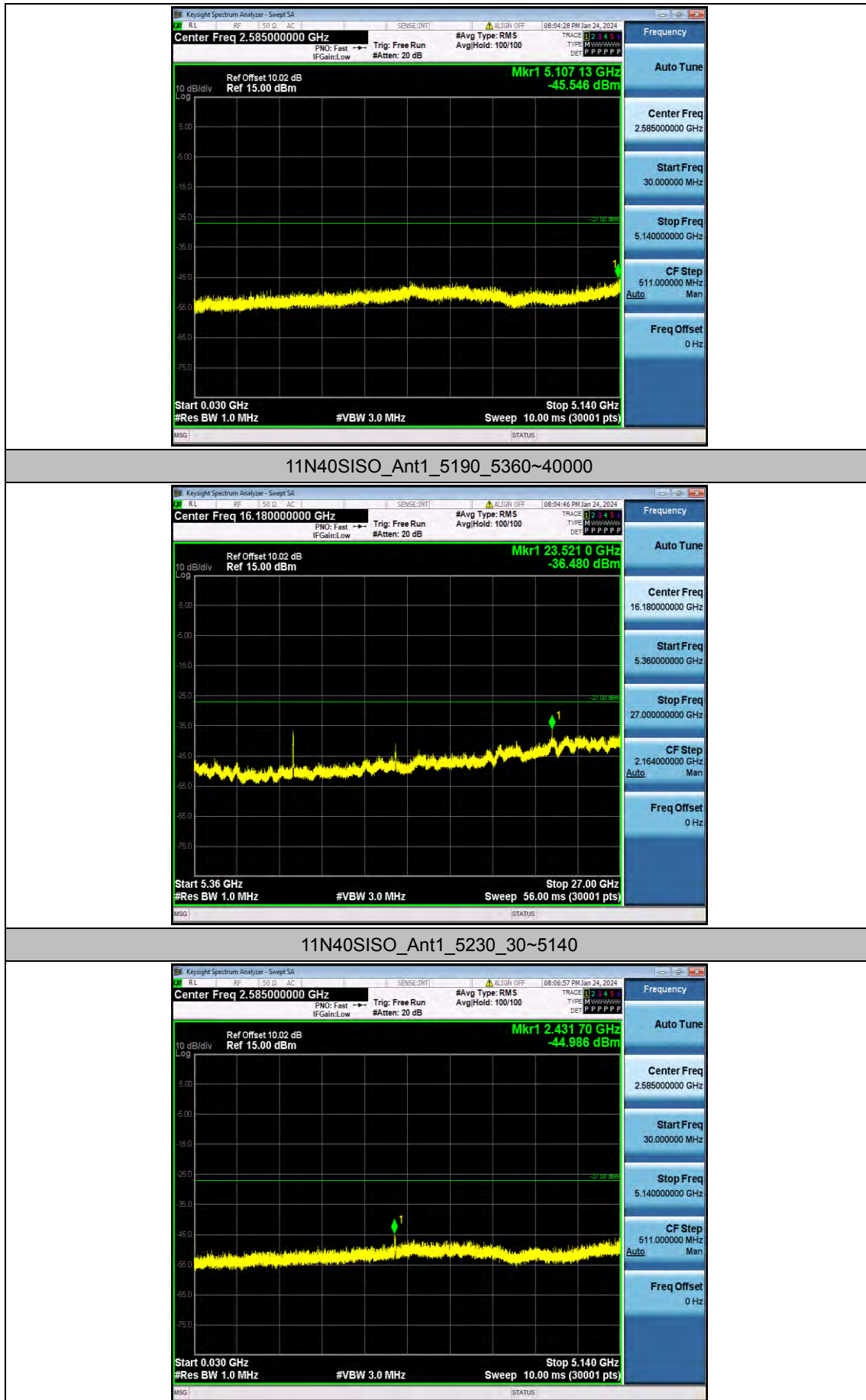
11N20SISO_Ant1_5240_30~5140



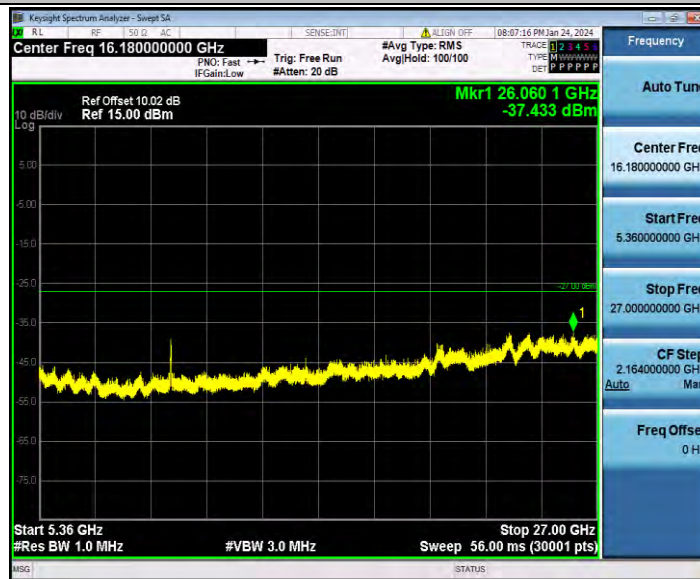
11N20SISO_Ant1_5240_5360~40000



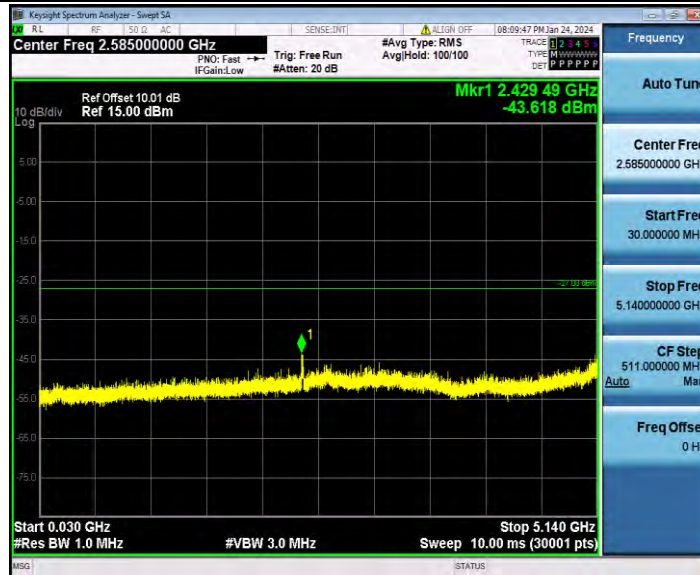
11N40SISO_Ant1_5190_30~5140



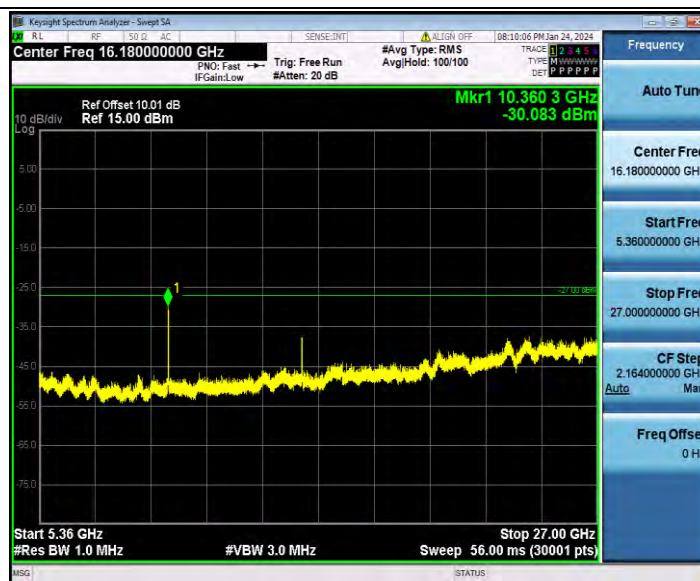
11N40SISO_Ant1_5230_5360~40000



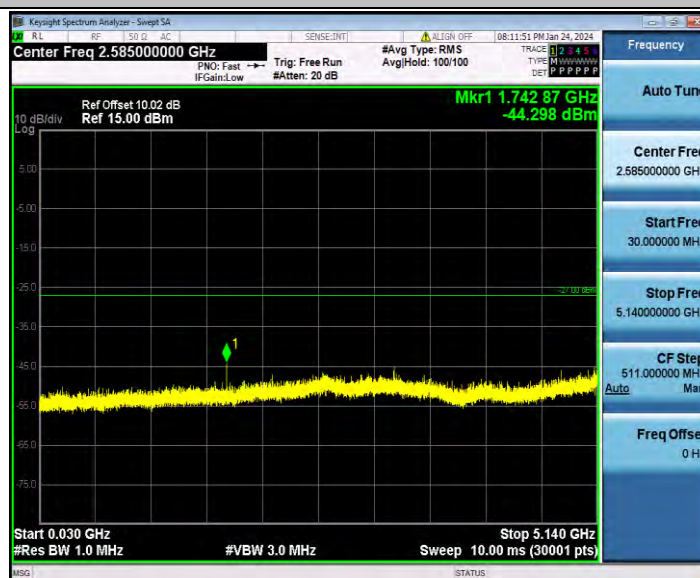
11AC20SISO_Ant1_5180_30~5140



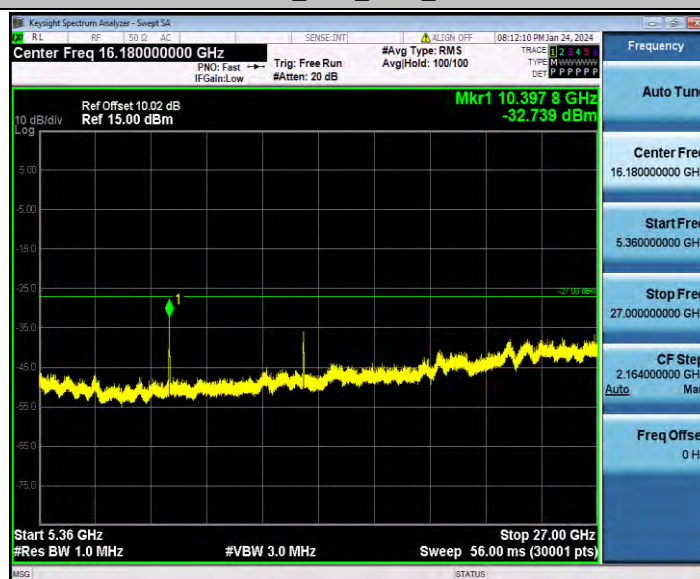
11AC20SISO_Ant1_5180_5360~40000



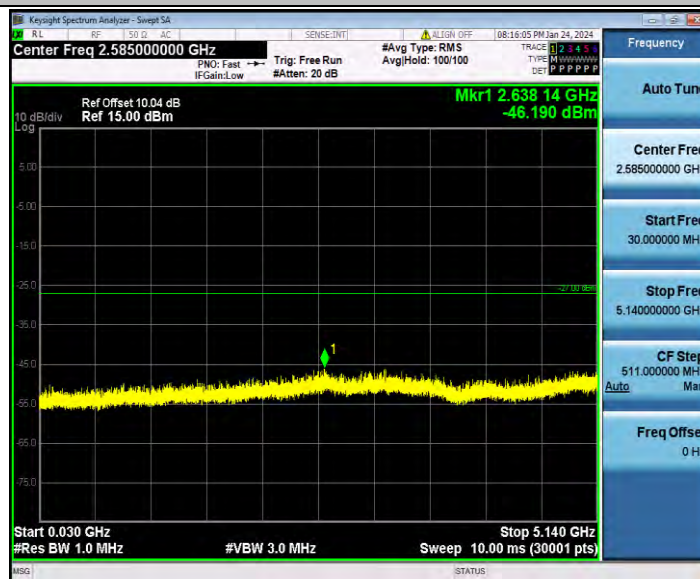
11AC20SISO_Ant1_5200_30~5140



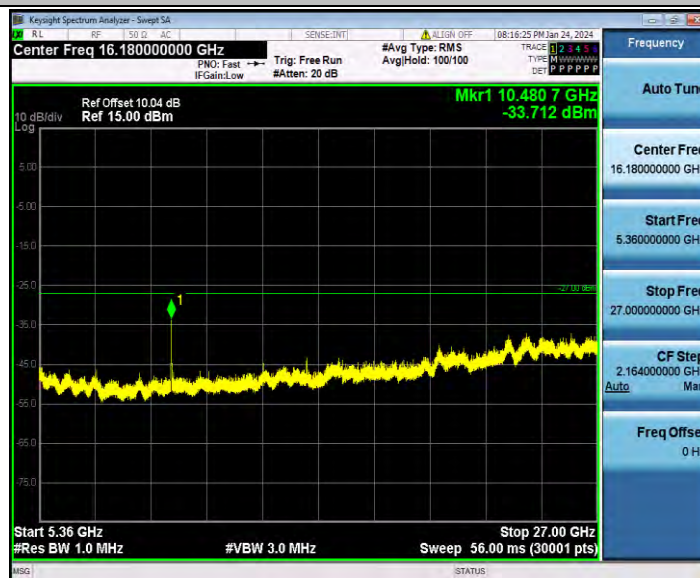
11AC20SISO_Ant1_5200_5360~40000



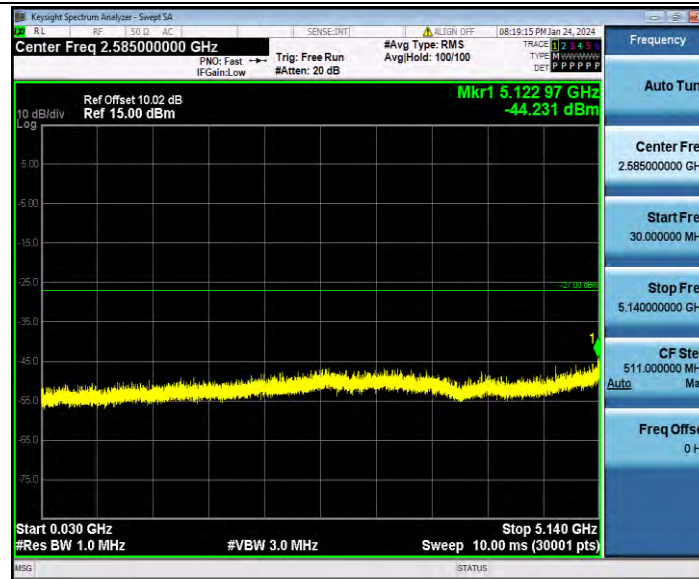
11AC20SISO_Ant1_5240_30~5140



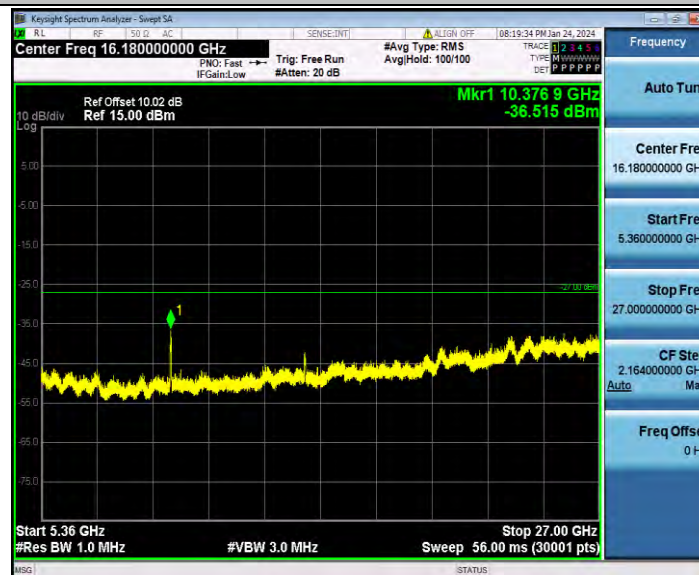
11AC20SISO_Ant1_5240_5360~40000



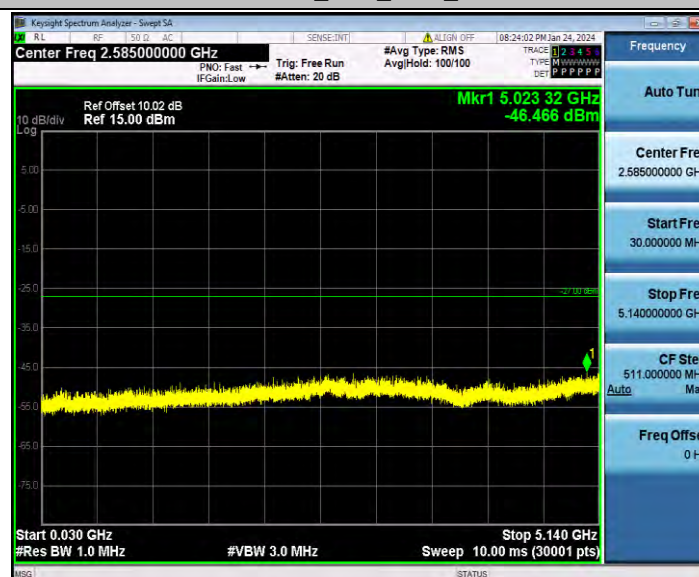
11AC40SISO_Ant1_5190_30~5140



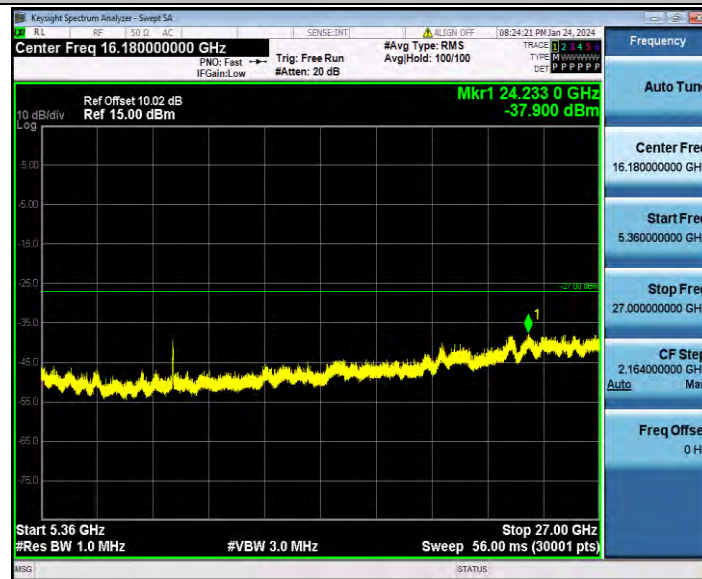
11AC40SISO_Ant1_5190_5360~40000



11AC40SISO_Ant1_5230_30~5140



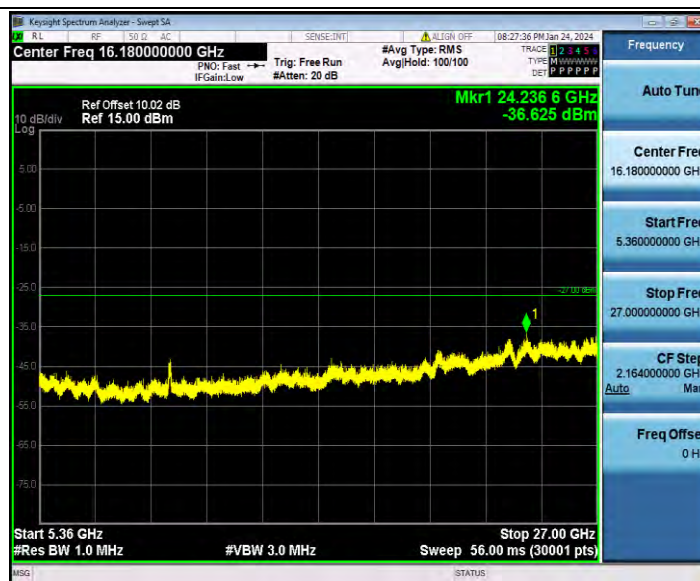
11AC40SISO_Ant1_5230_5360~40000



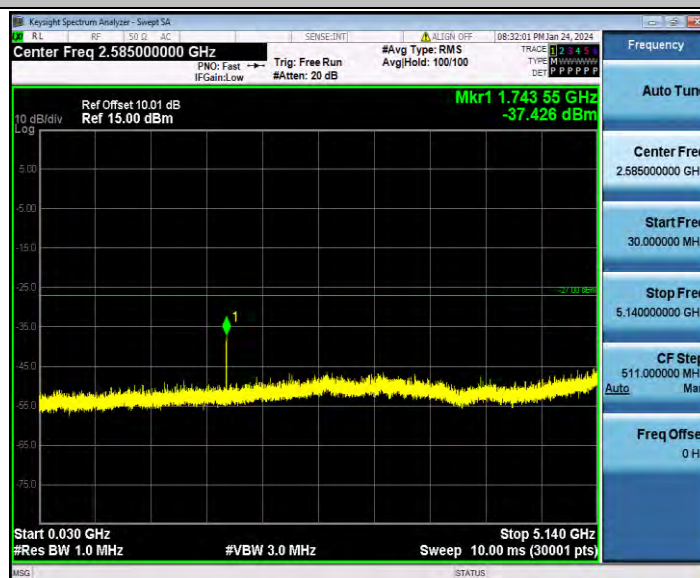
11AC80SISO_Ant1_5210_30~5140



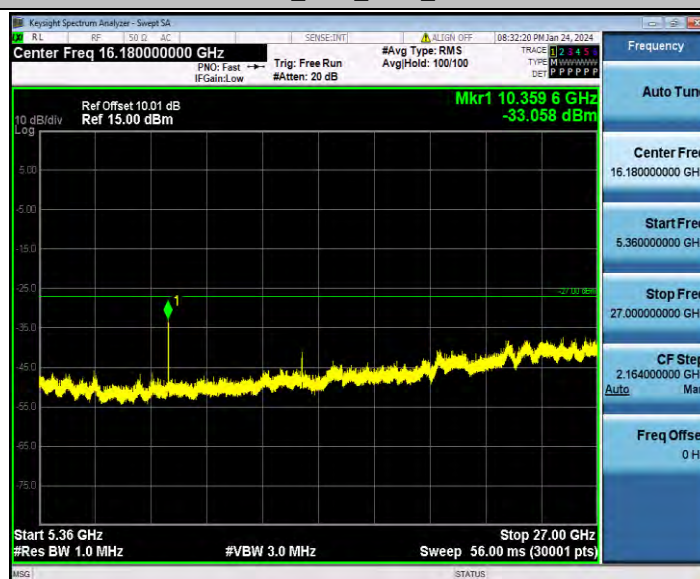
11AC80SISO_Ant1_5210_5360~40000



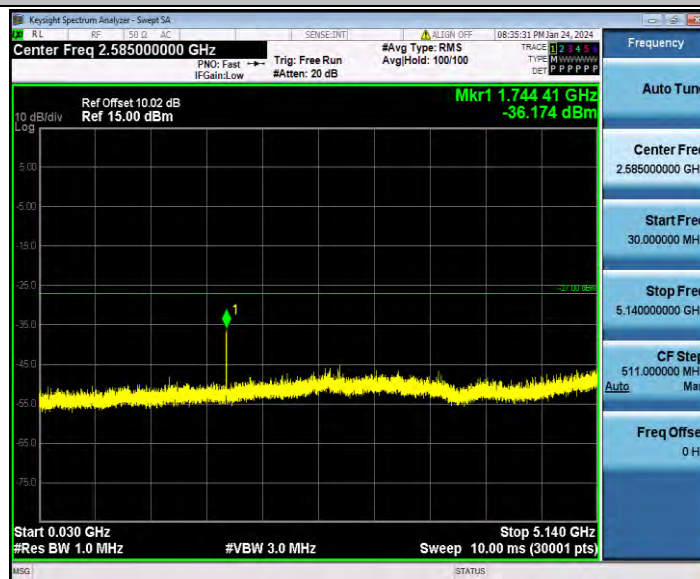
11AX20SISO_Ant1_5180_30~5140



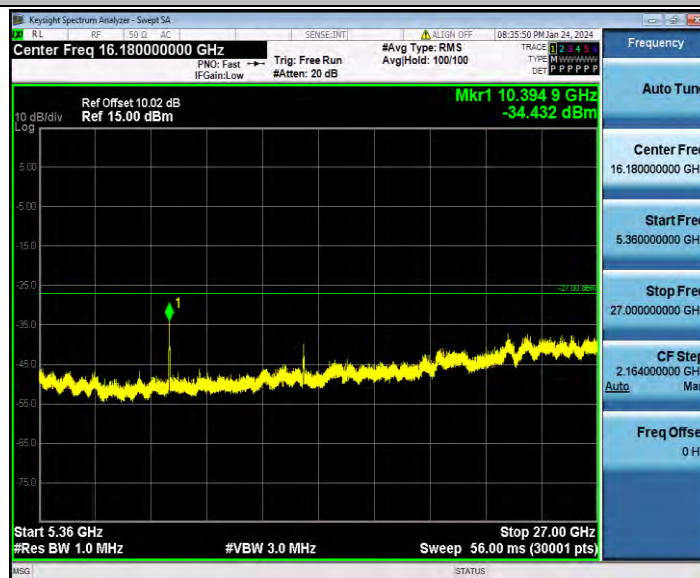
11AX20SISO_Ant1_5180_5360~40000



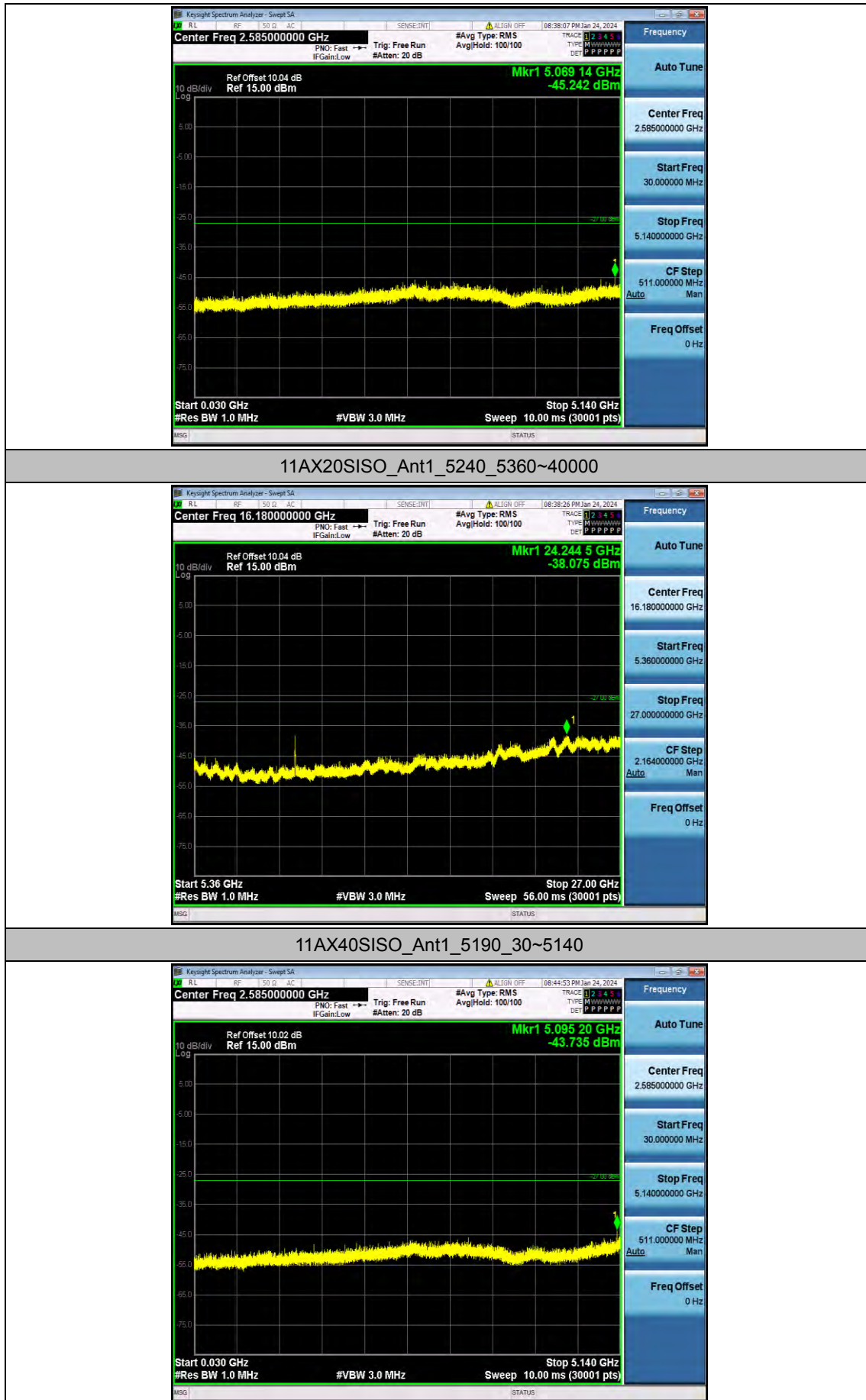
11AX20SISO_Ant1_5200_30~5140



11AX20SISO_Ant1_5200_5360~40000



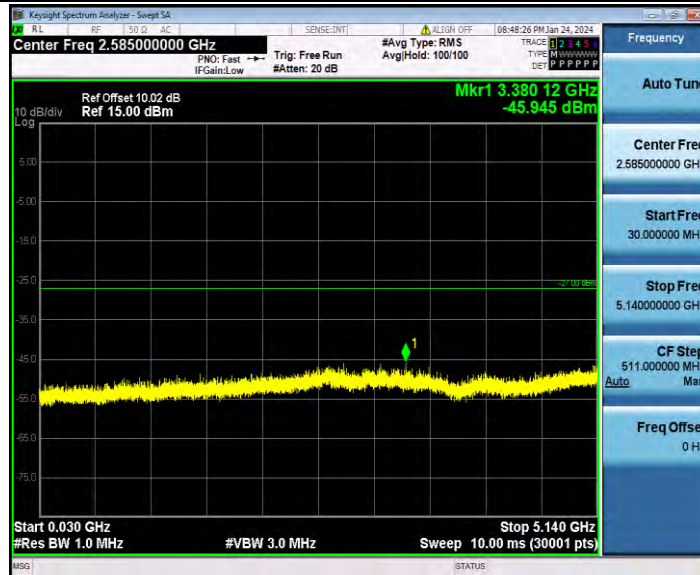
11AX20SISO_Ant1_5240_30~5140



11AX40SISO_Ant1_5190_5360~40000



11AX40SISO_Ant1_5230_30~5140



11AX40SISO_Ant1_5230_5360~40000

