

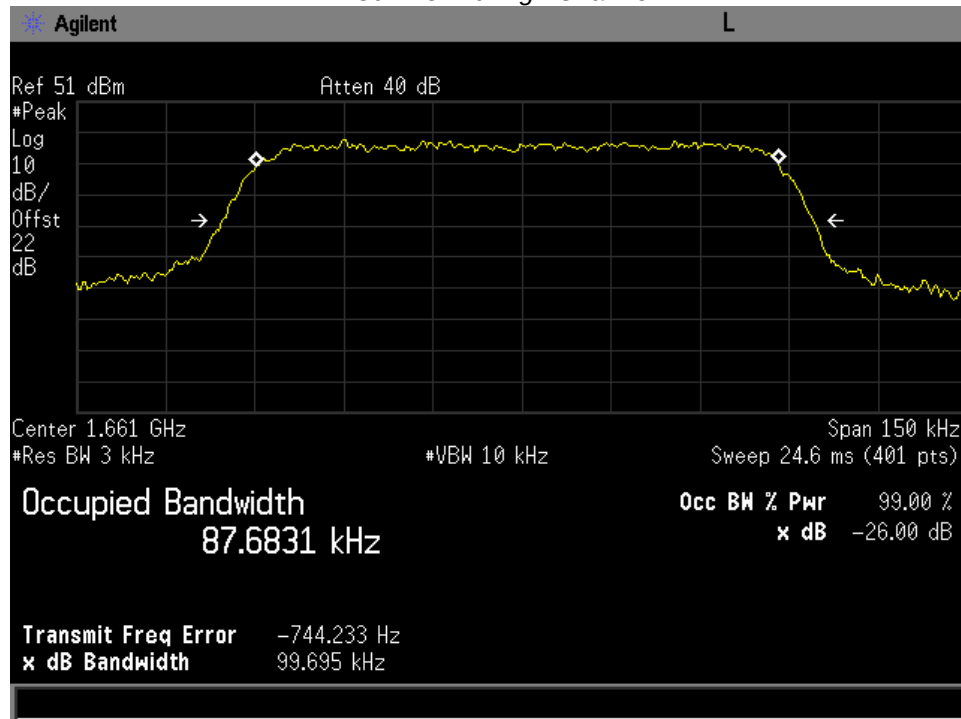


Annex A

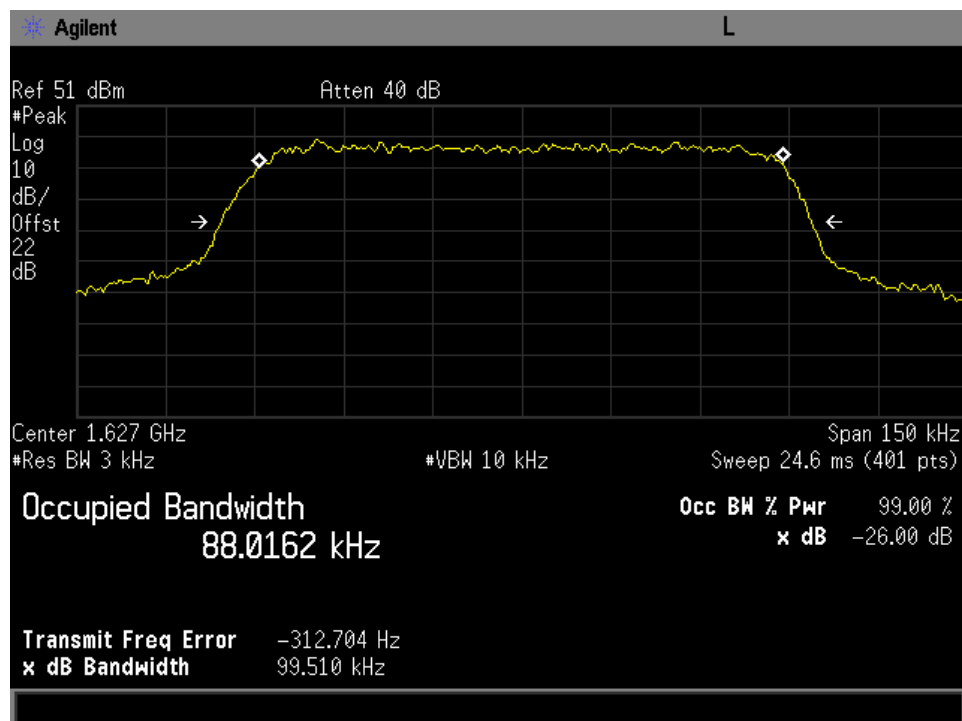
Occupied Bandwidth



FR80T2.5X16 High Channel

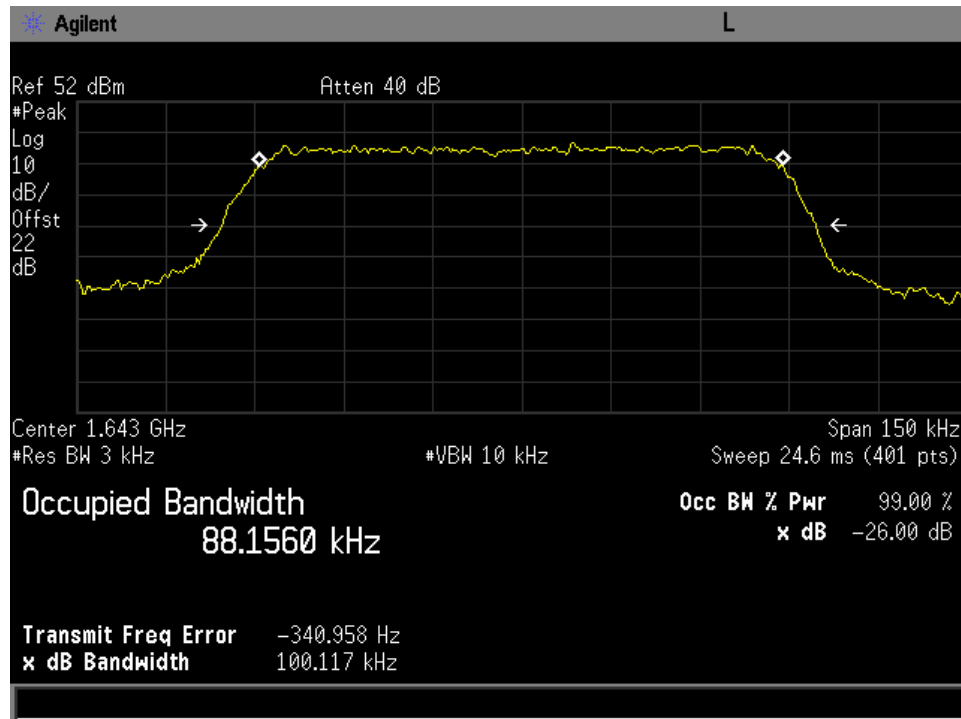


FR80T2.5X16 Low Channel

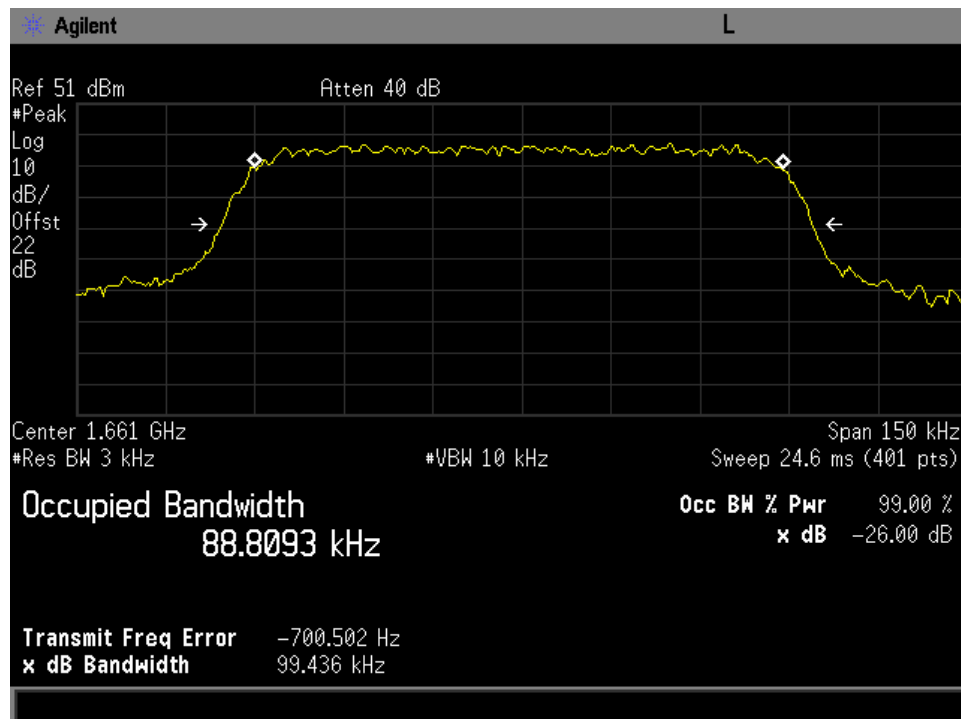




FR80T2.5X16 Mid Channel

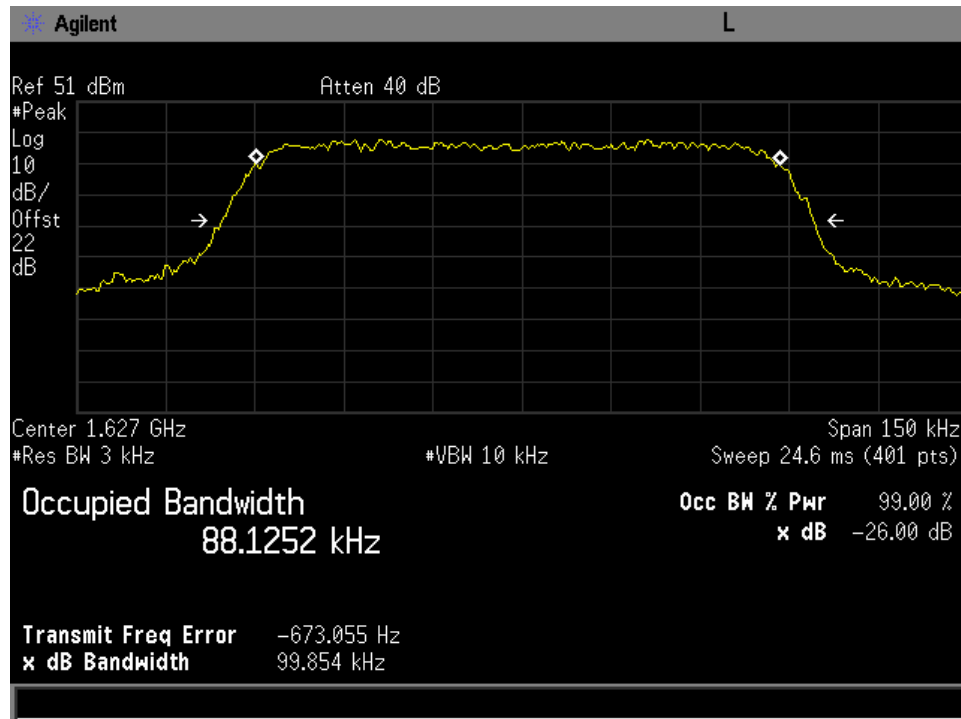


FR80T2.5X32 High Channel

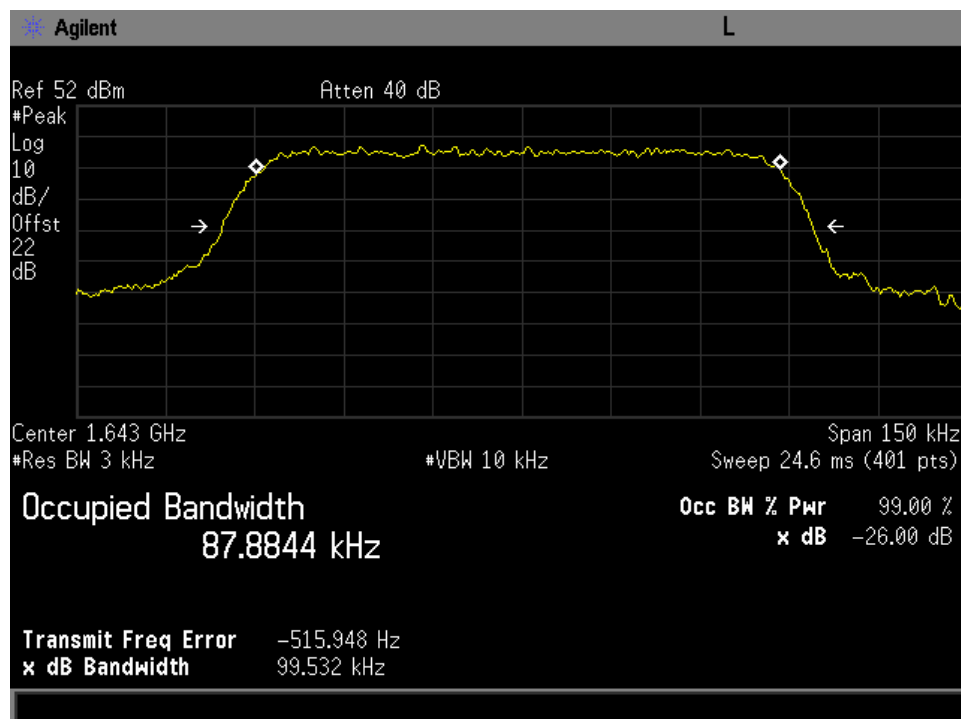




FR80T2.5X32 Low Channel

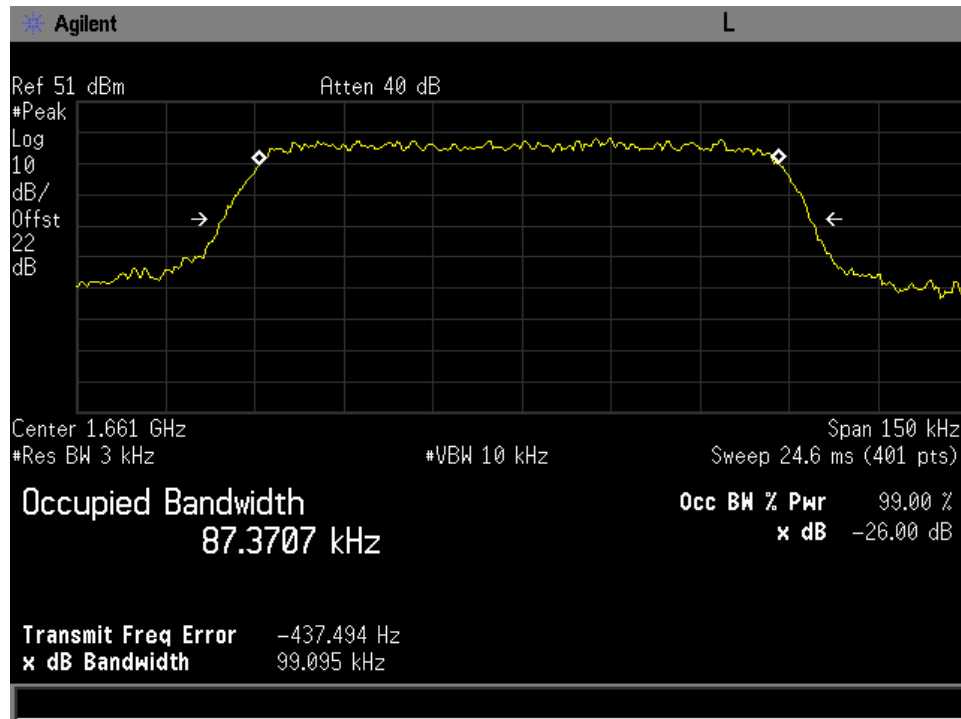


FR80T2.5X32 Mid Channel

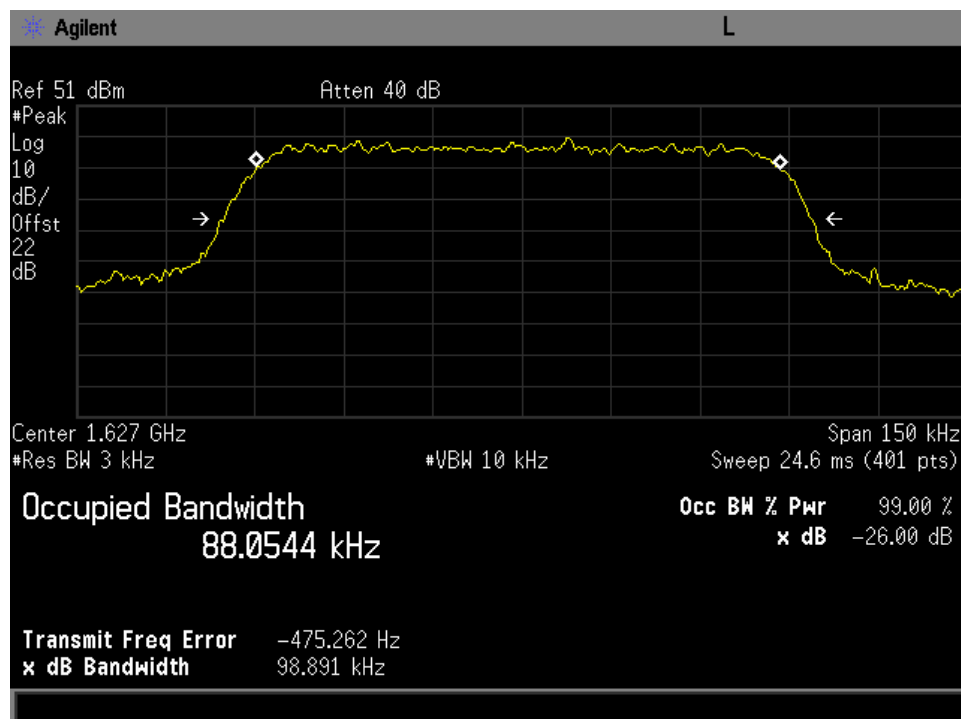




FR80T2.5X64 High Channel

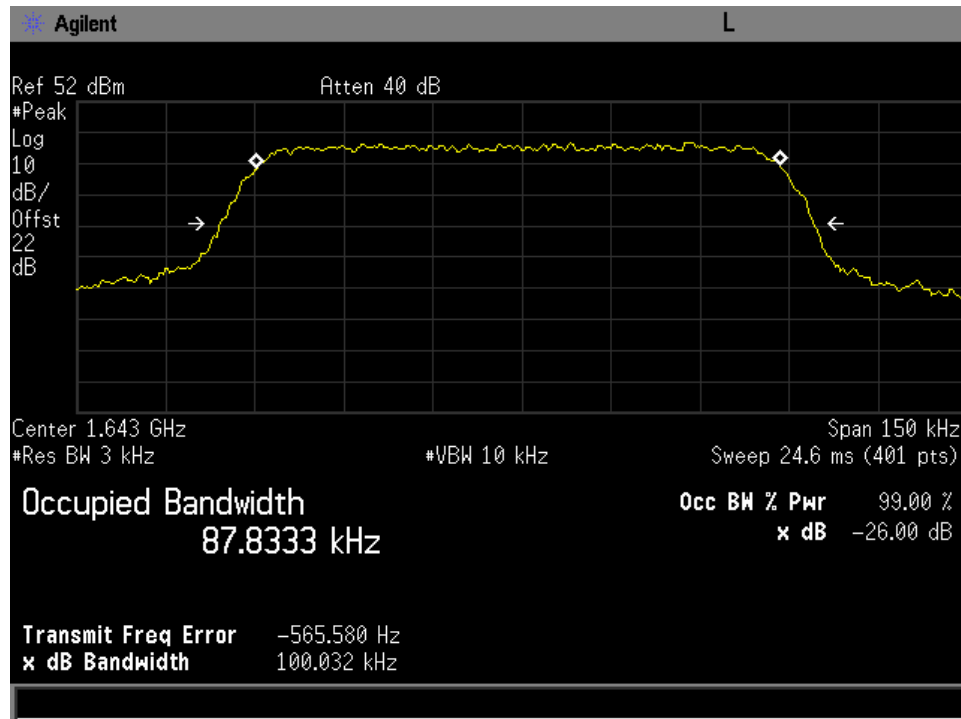


FR80T2.5X64 Low Channel

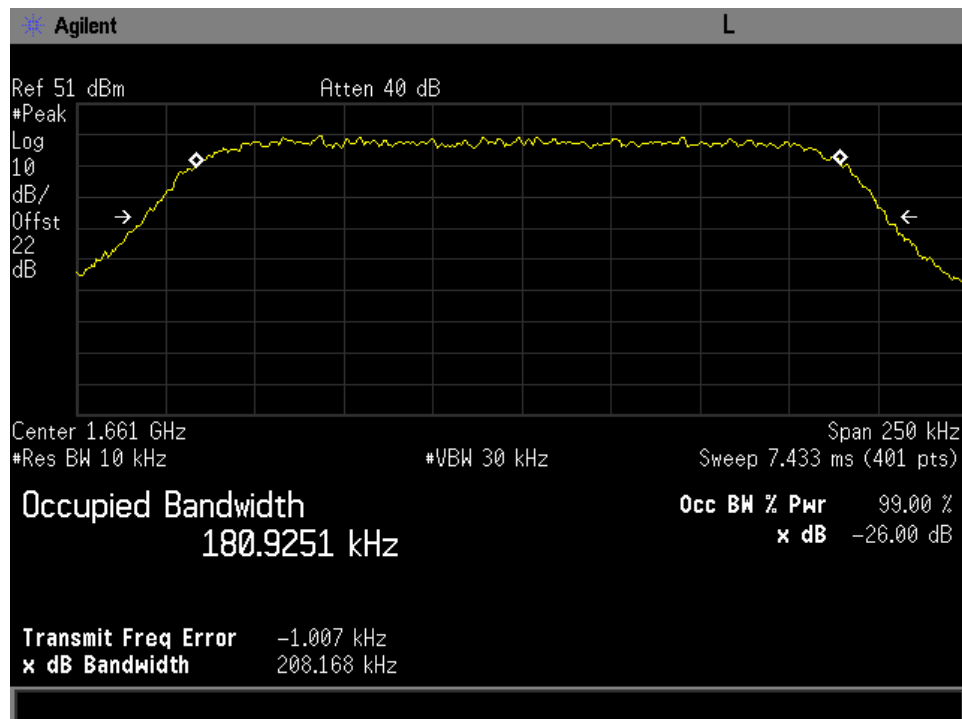




FR80T2.5X64 Mid Channel

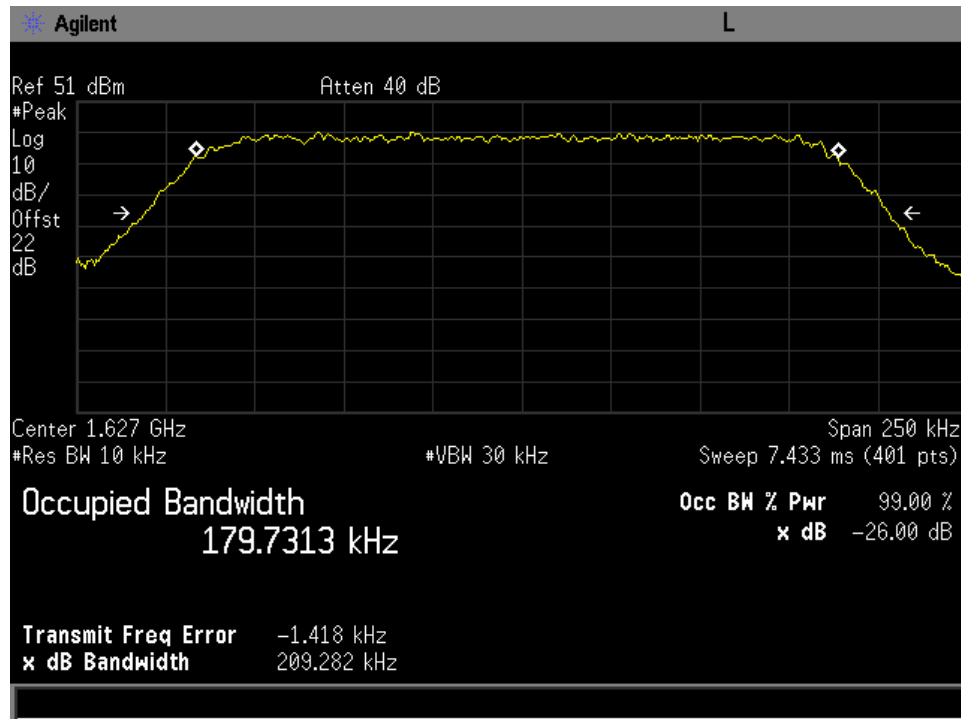


FR80T5X16 High Channel

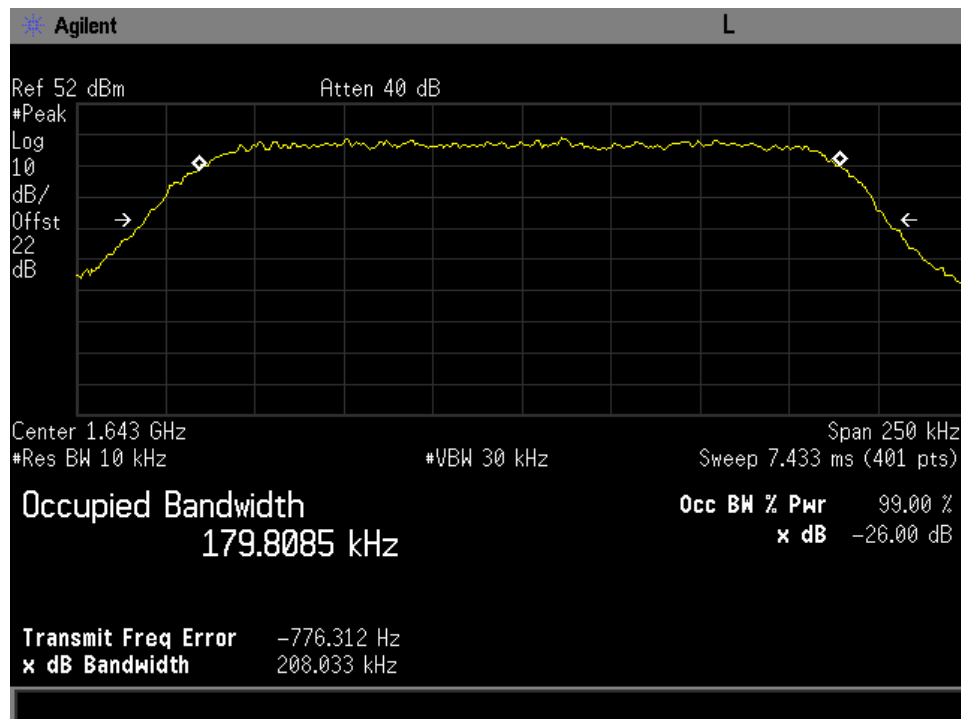




FR80T5X16 Low Channel

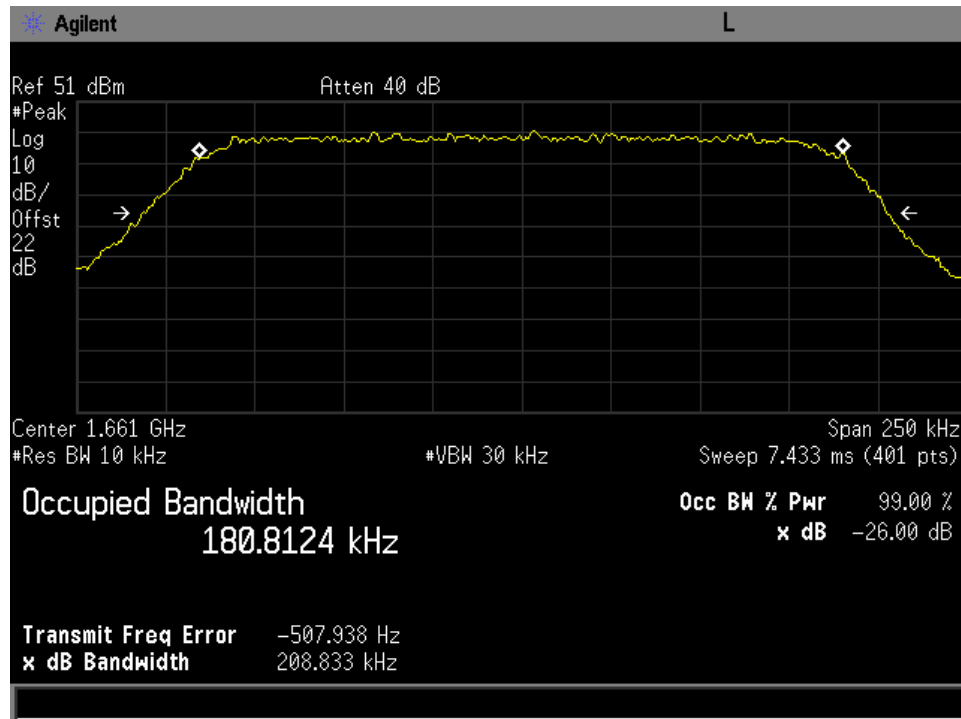


FR80T5X16 Mid Channel

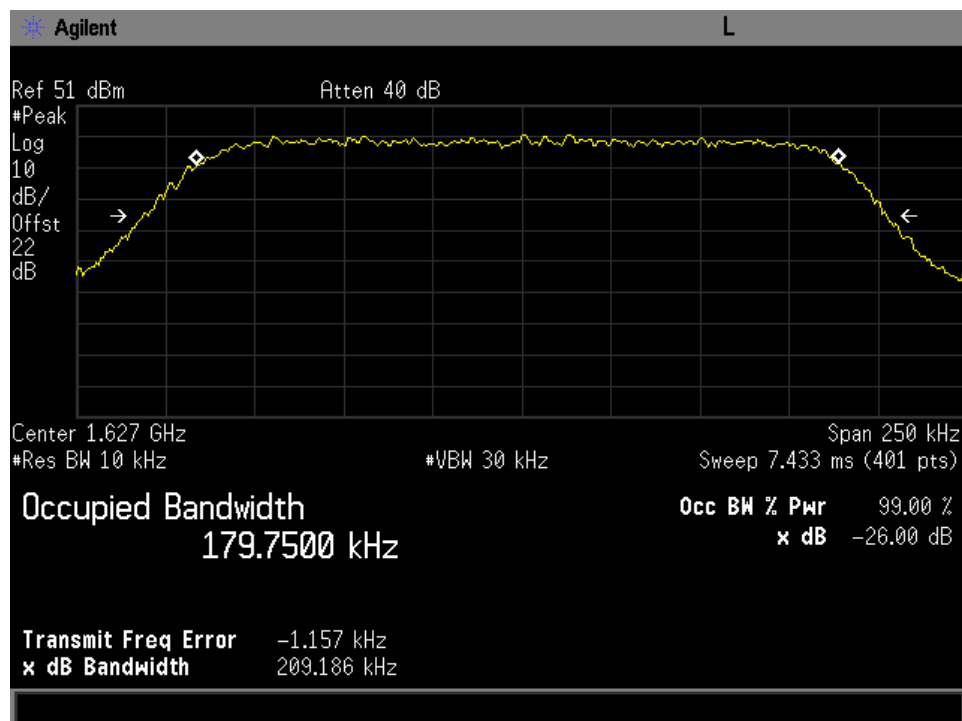




FR80T5X32 High Channel

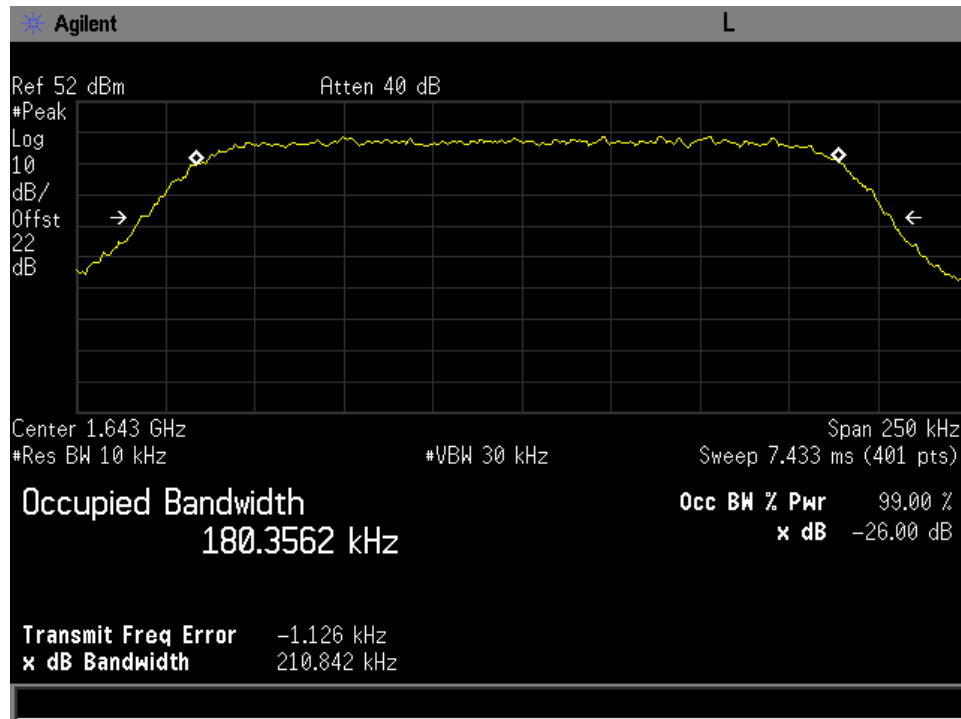


FR80T5X32 Low Channel

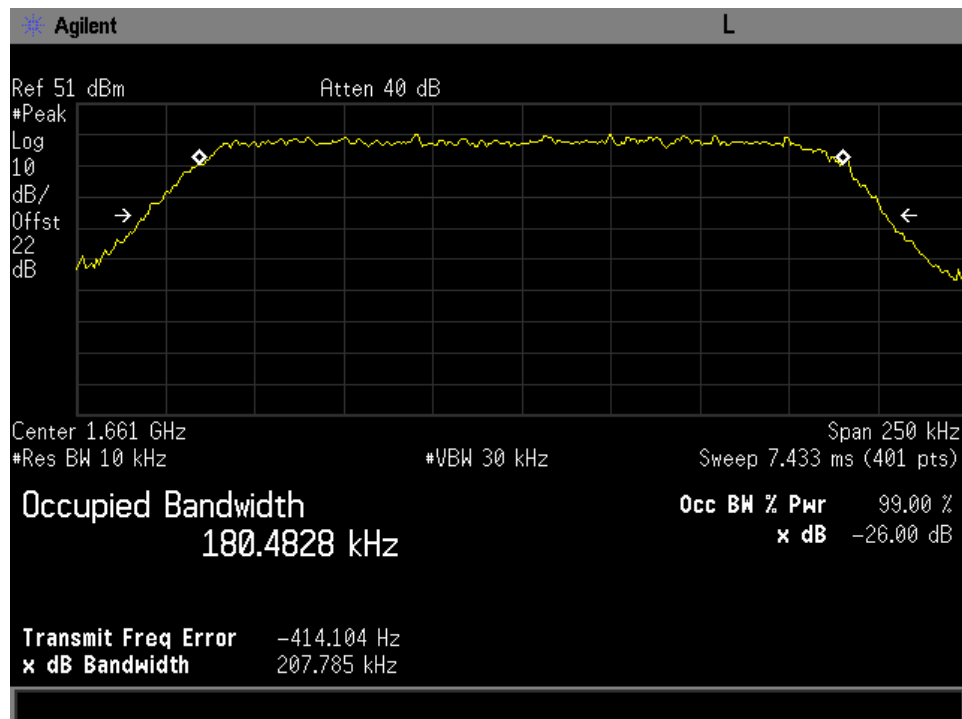




FR80T5X32 Mid Channel

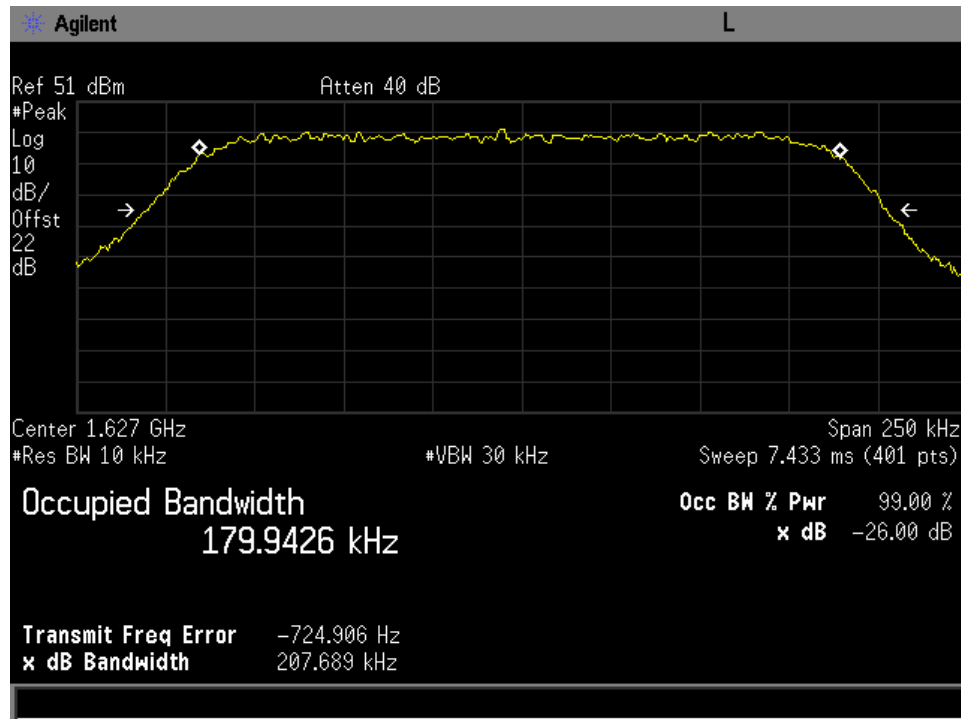


FR80T5X64 High Channel

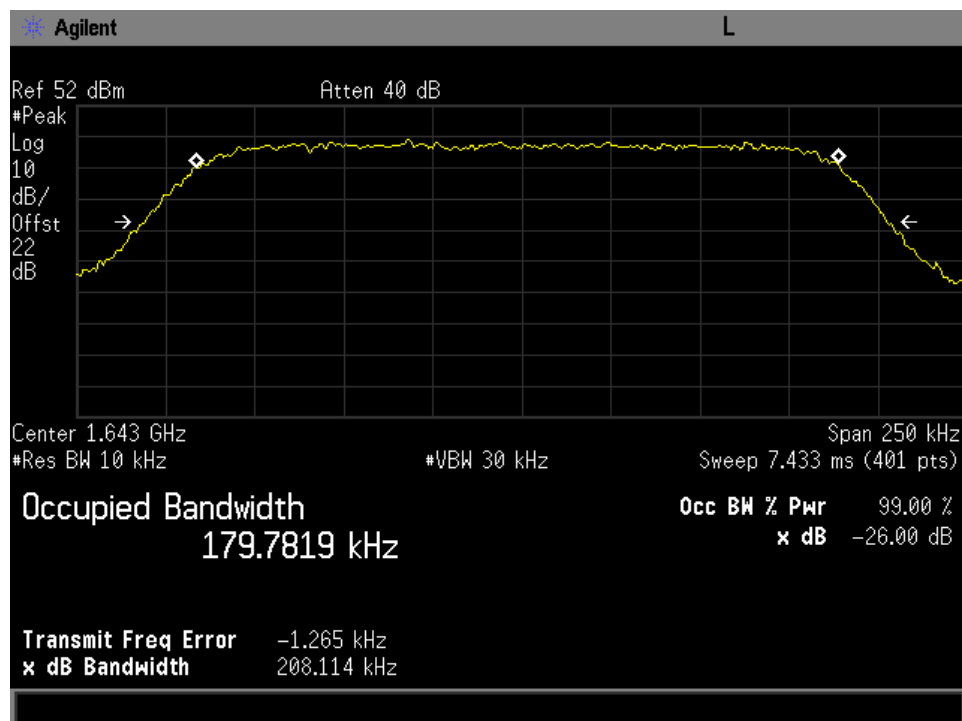




FR80T5X64 Low Channel

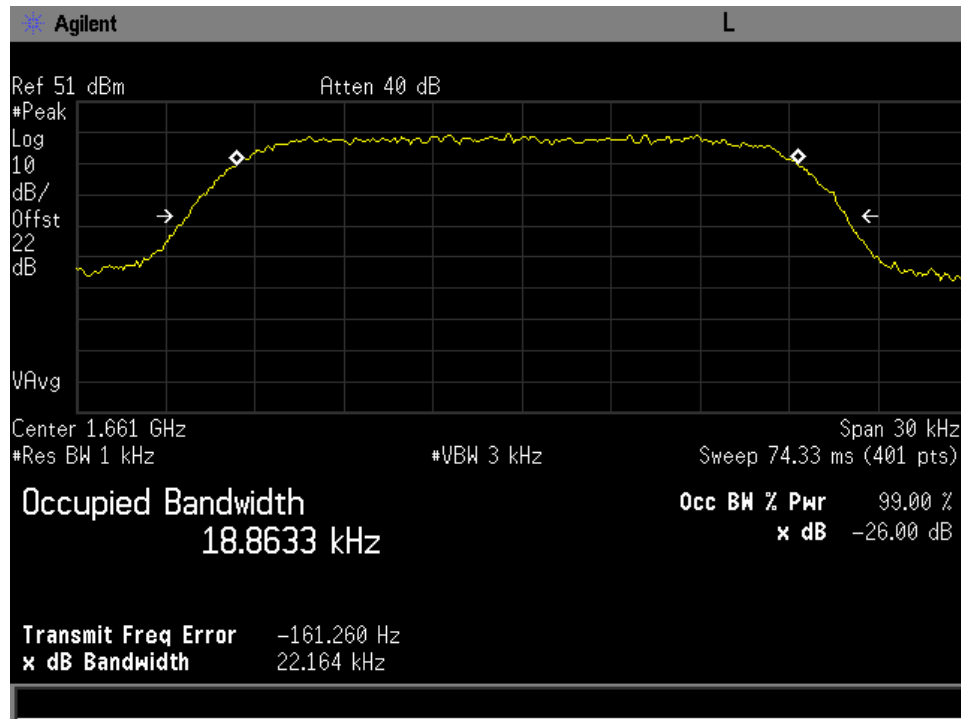


FR80T5X64 Mid Channel

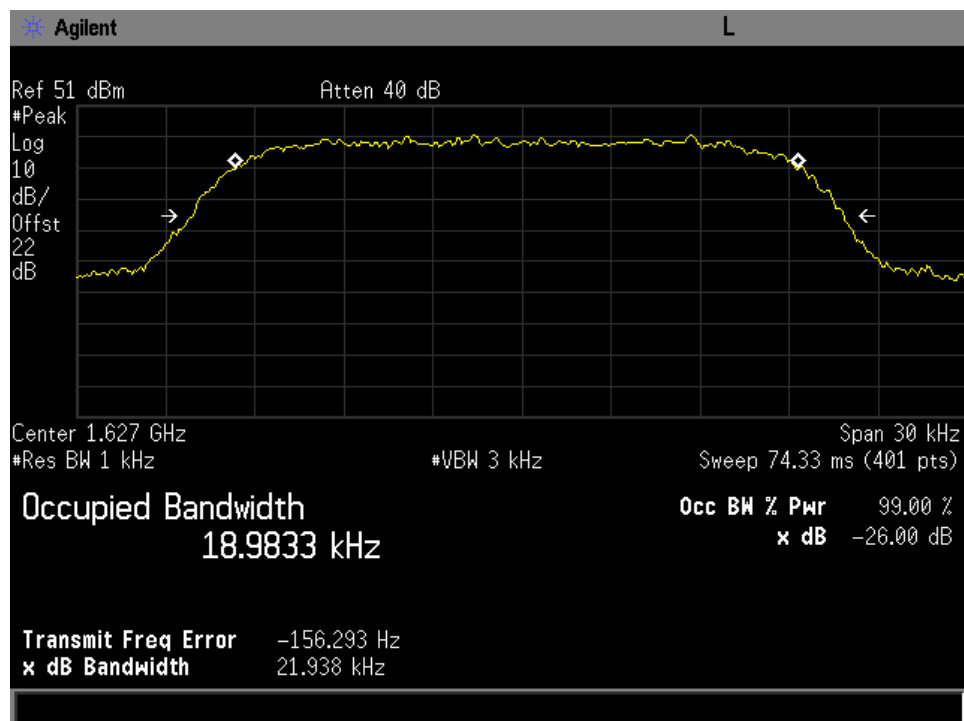




R5T0.5QD High Channel

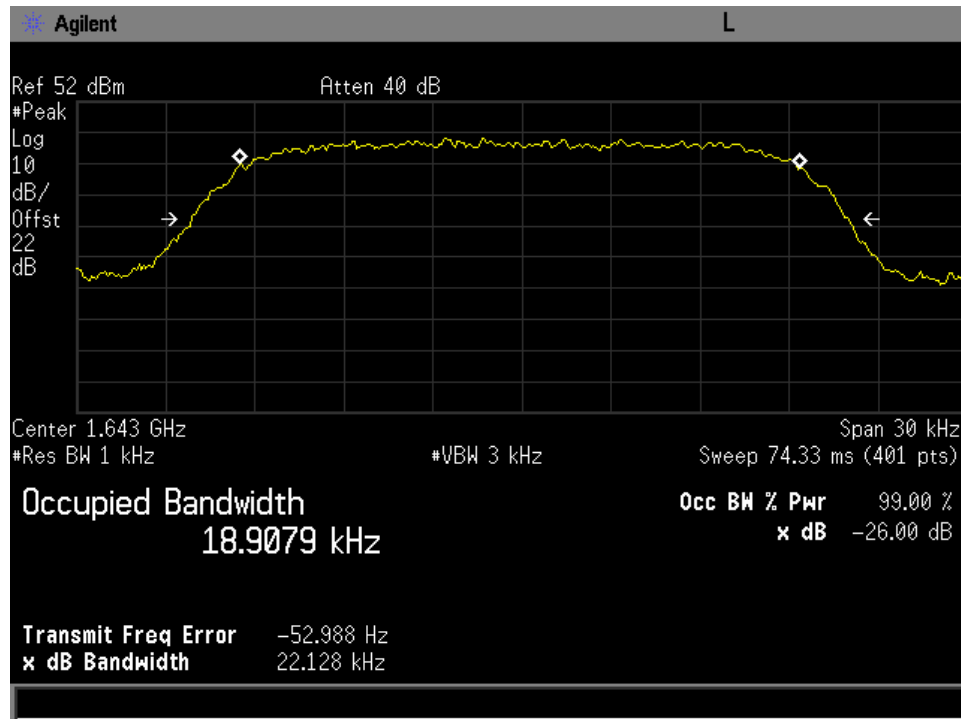


R5T0.5QD Low Channel

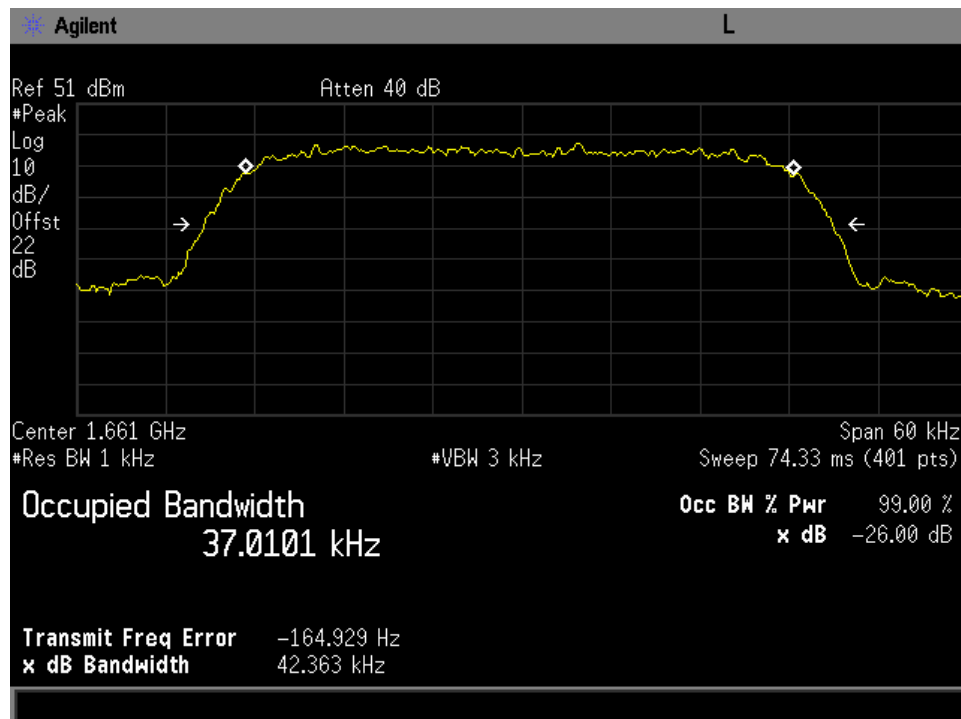




R5T0.5QD Mid Channel

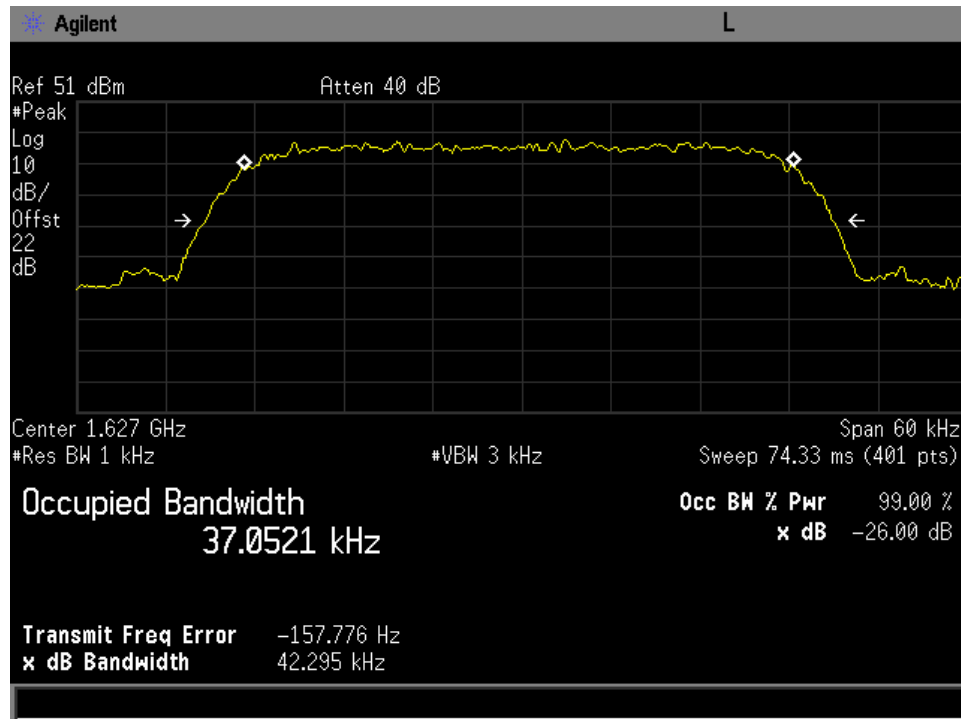


R5T1QD High Channel

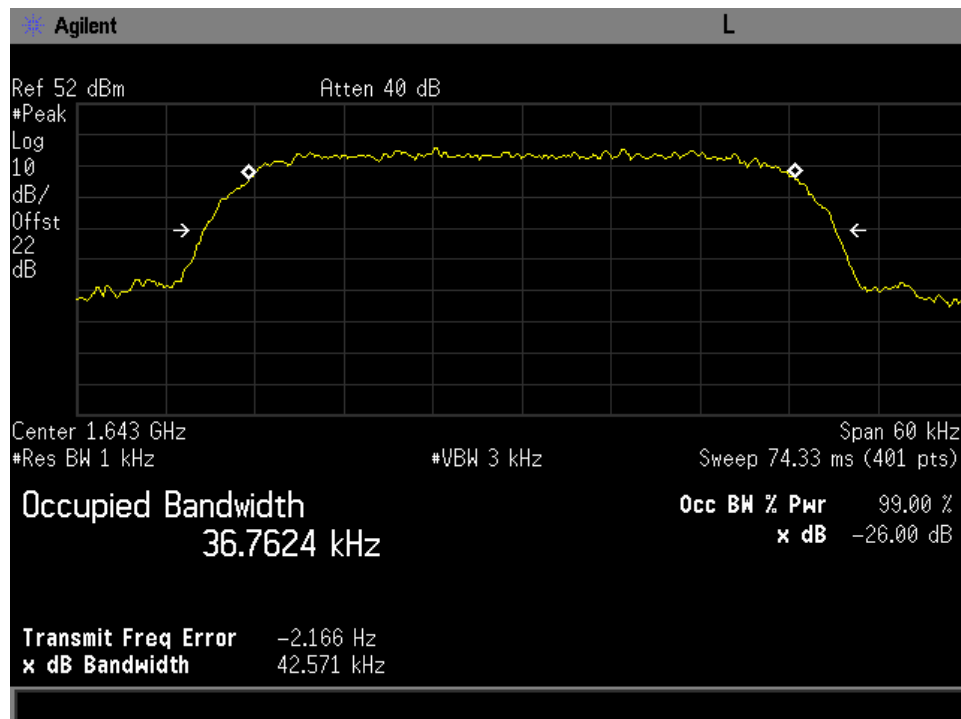




R5T1QD Low Channel

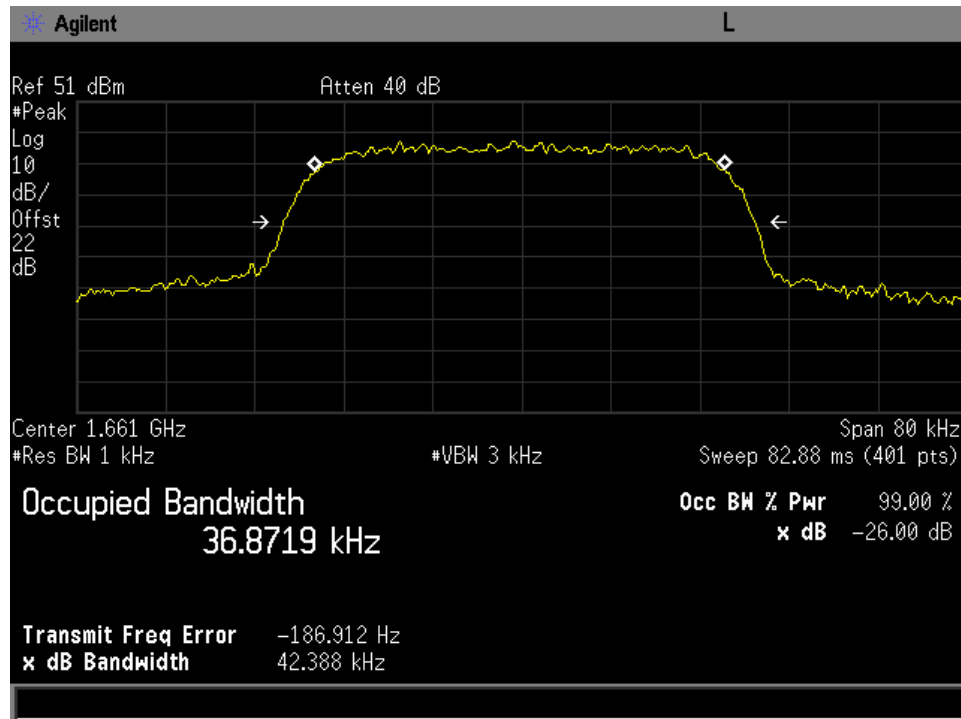


R5T1QD Mid Channel

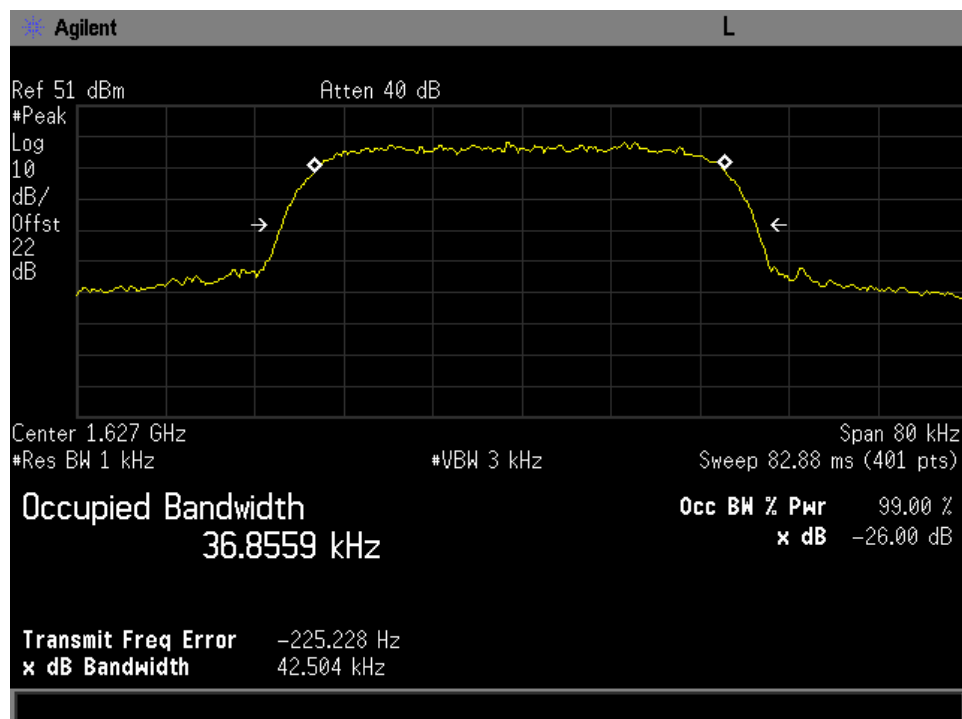




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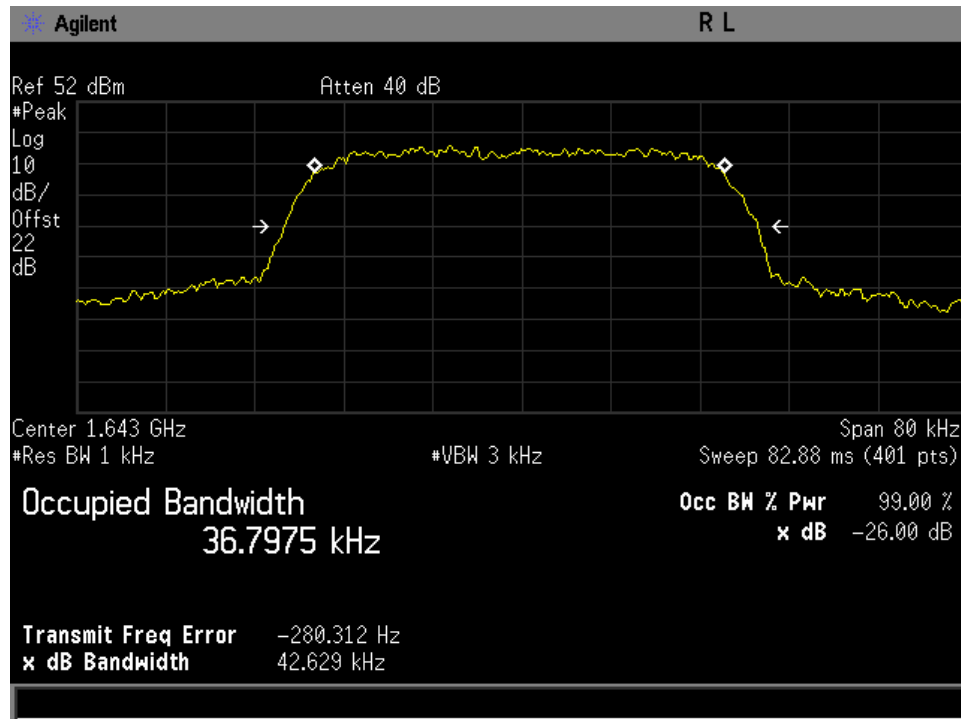


R5T1XD Low Channel

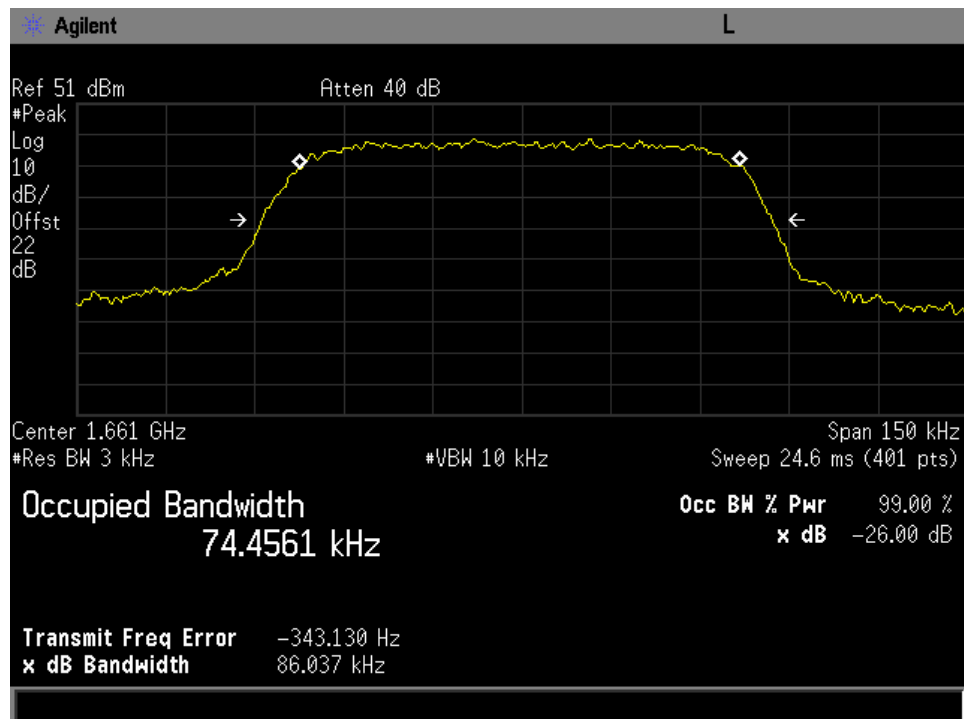




R5T1XD Mid Channel

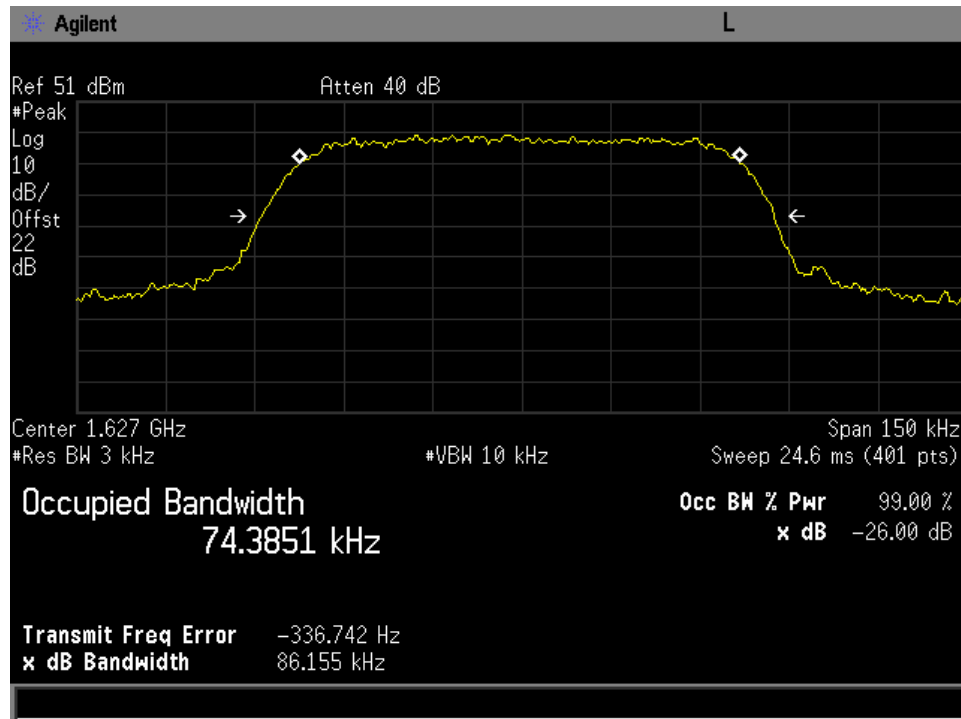


R5T2QD High Channel

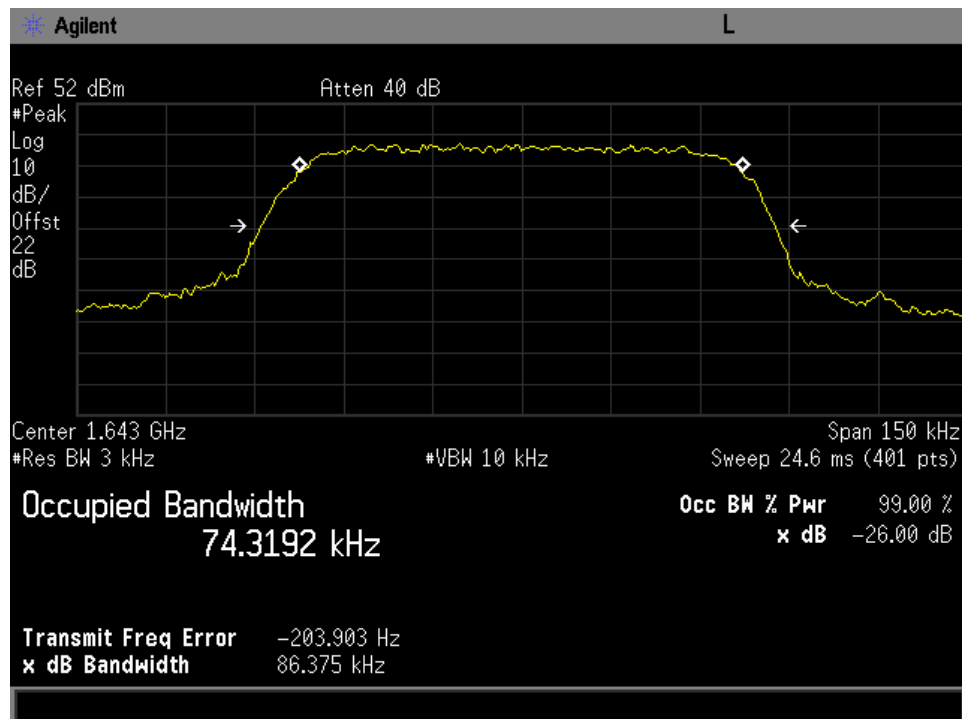




R5T2QD Low Channel

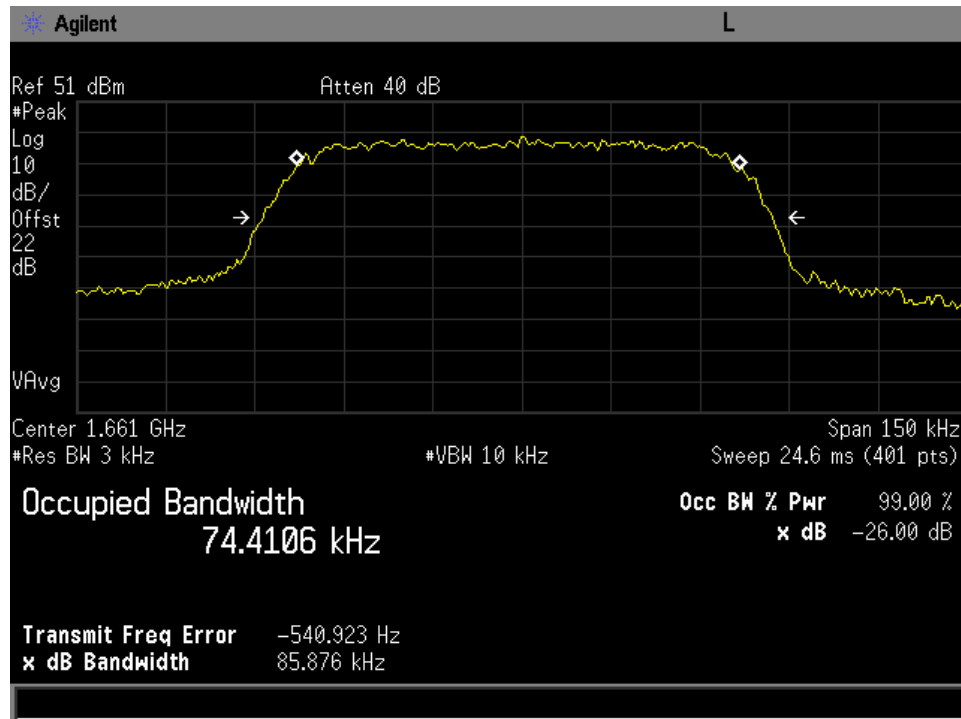


R5T2QD Mid Channel

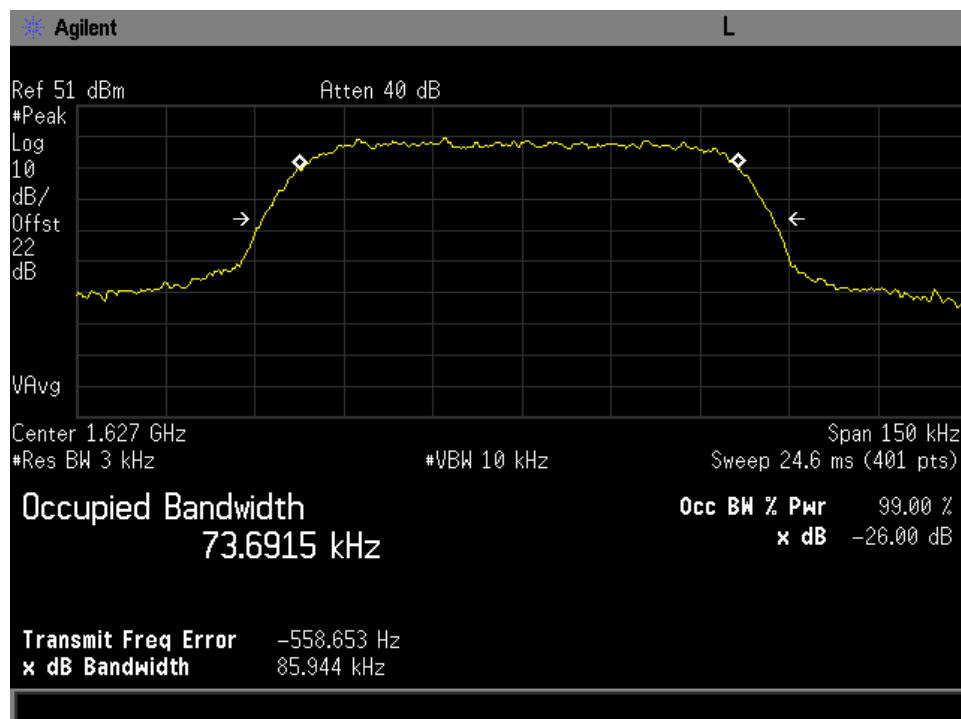




R5T2XD High Ch

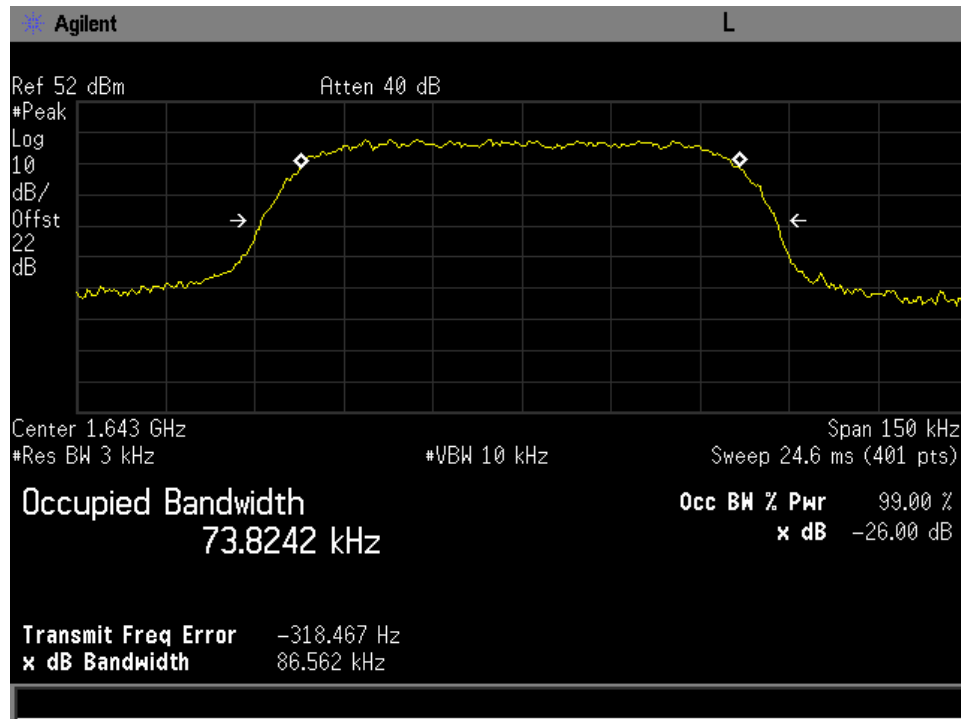


R5T2XD Low Ch

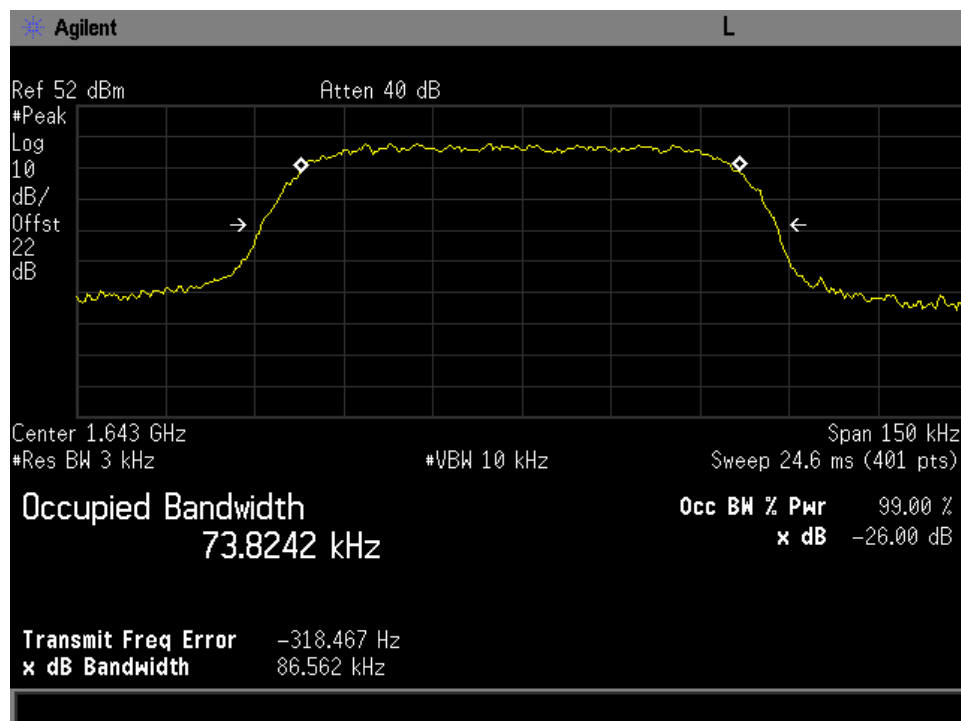




R5T2XD Mid Ch (2)

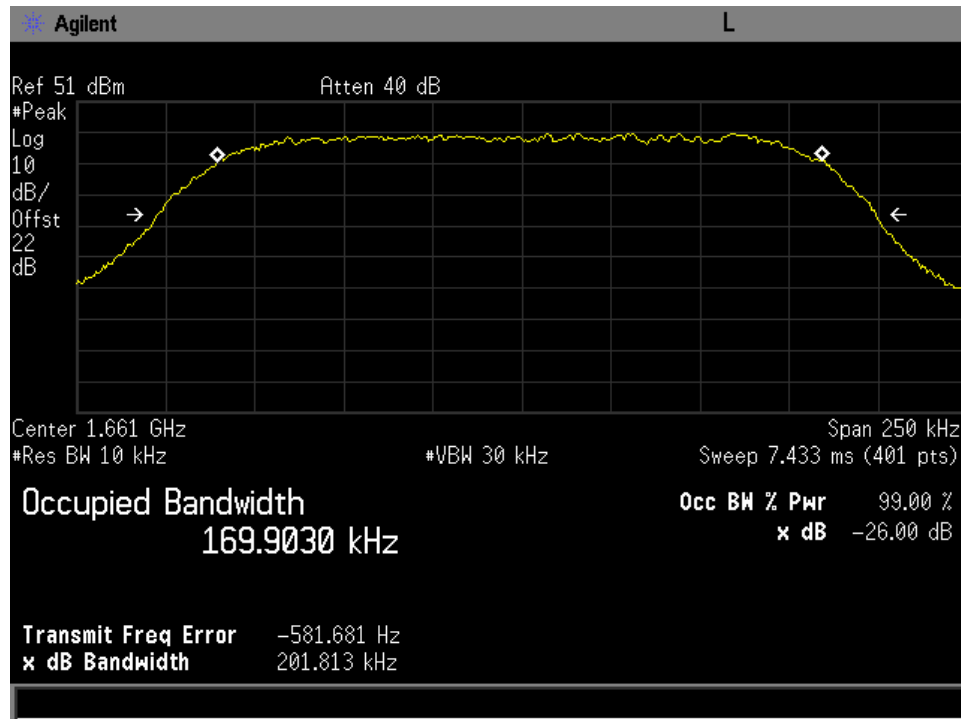


R5T2XD Mid Ch

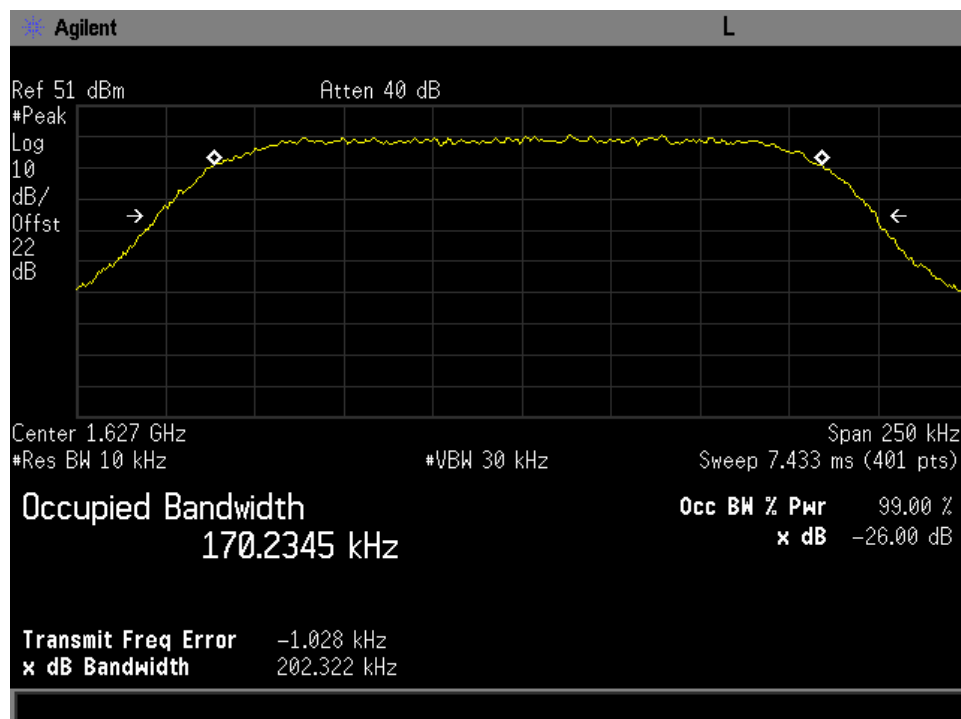




R5T4.5QD High Channel

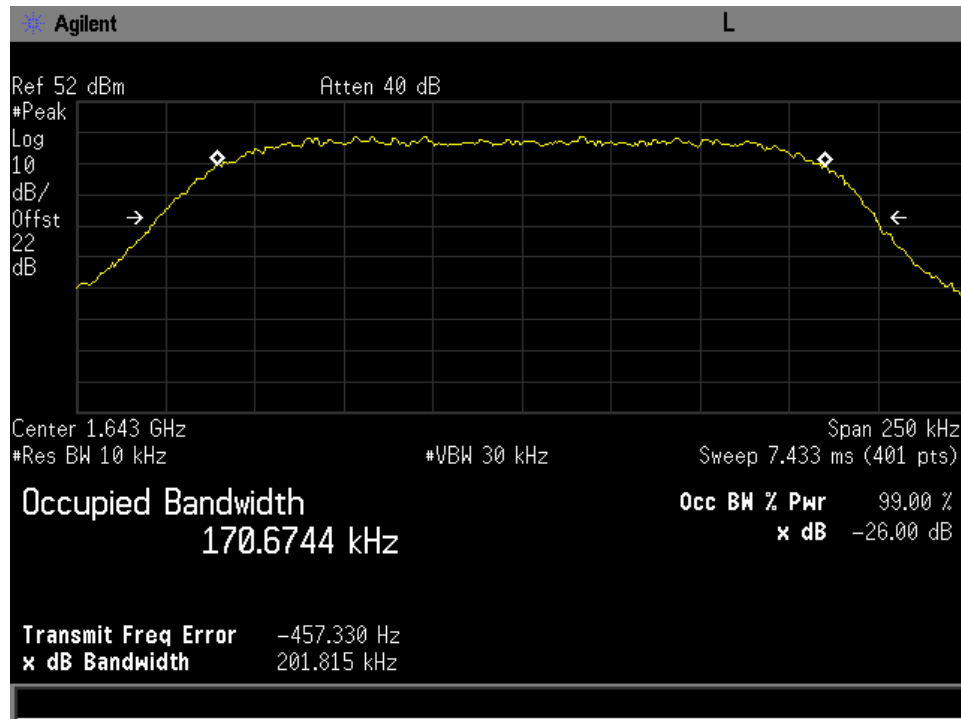


R5T4.5QD Low Channel

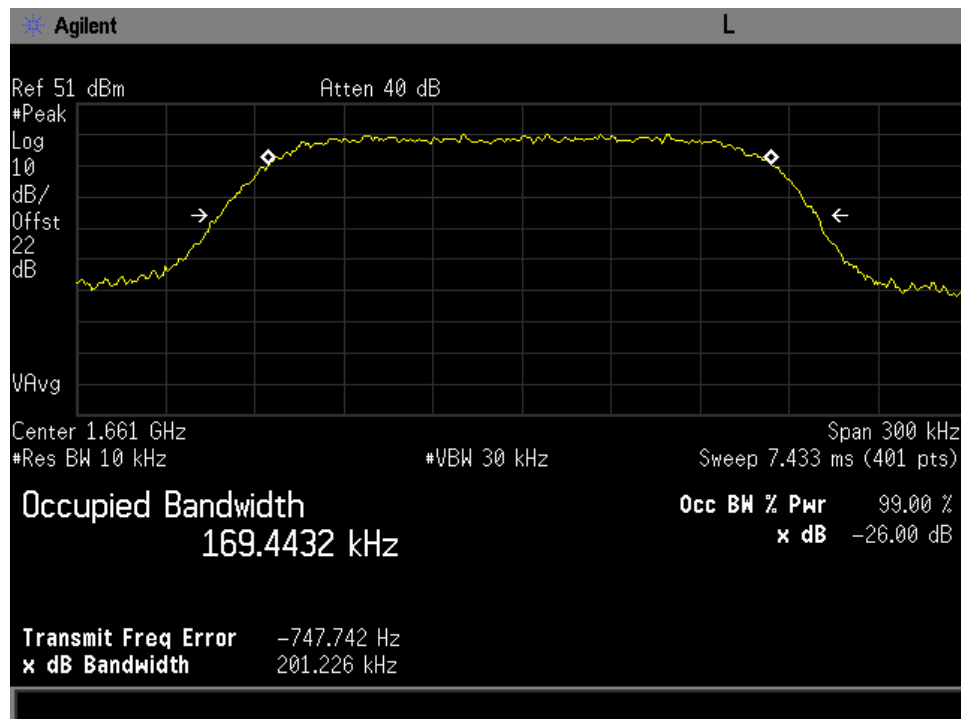




R5T4.5QD Mid Channel

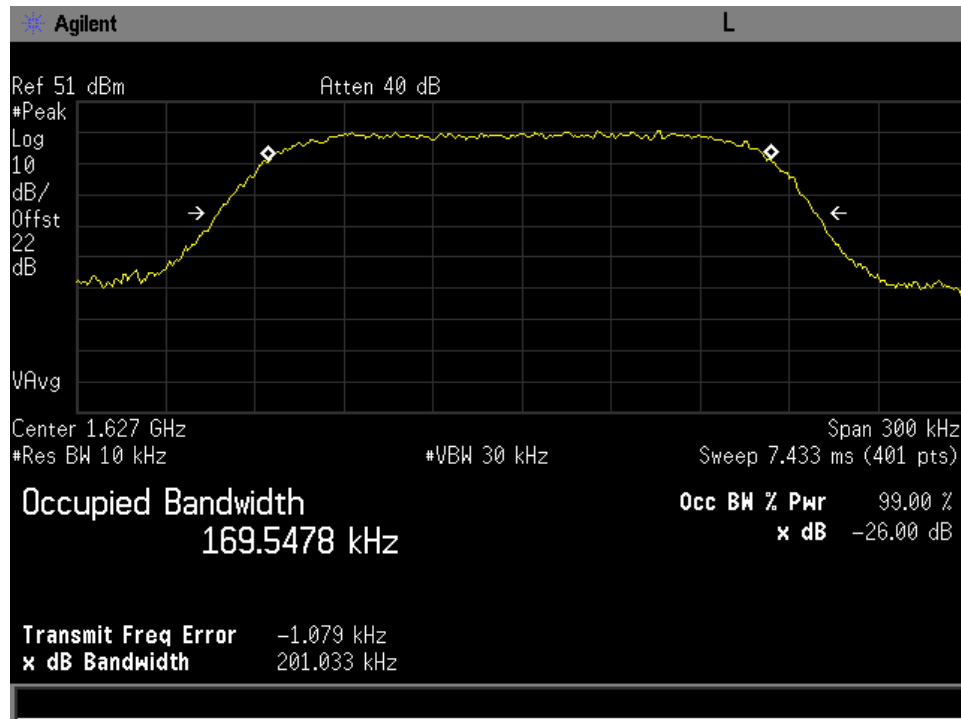


R5T4.5XD High Channel





R5T4.5XD Low Channel



R5T4.5XD Mid Channel

