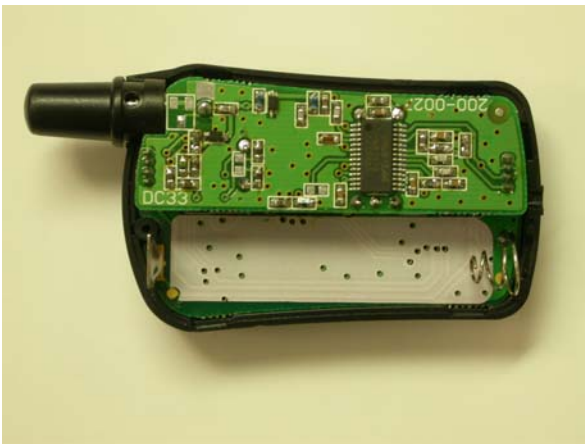


## MAIN PCB AND RF DAUGHTERBOARD



## MAIN PCB TOP AND BOTTOM



## DAUGHTERBOARD TOP AND BOTTOM

