

CIRCUIT DESCRIPTION

EXHIBIT 2-2

CFS8DL5809EN-1

The CFS8DL5809EN-1 is basically a 're-packaging' of the CFS8DL5809EN. The TX ON and MESSAGE DATA are generated by ENCODER ASIC U1. The OCS. Q2 (etc.) is turned on and off by SWITCH Q1 (etc.) The OCS. drives the PA. Q3, which is MODULATED BY Q4 and Q5. C17 & C18 couple the energy into ANT1 & ANT2. XL1 is the OCS.'s. FDE.