WT-01E User Manual

Extreme / Open / Small / Easy

Specification Version 1.0 2019/01/13



FCC Requirement(FCC warning - Module)

This device complies with Part 15 of the FCC Rules. Operation is subject to the following two conditions:

- (1) this device may not cause harmful interference, and
- (2) this device must accept any interference received, including interference that may cause undesired operation.

Any changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

Please notice that if the FCC identification number is not visible when the module is installed inside another device, then the outside of the device into which the module is installed must also display a label referring to the enclosed module. This exterior label can use wording such as the following: "Contains FCC ID: 2AFOS-WT-01E" any similar wording that expresses the same meaning may be used.

This equipment complies with FCC radiation exposure limits set forth for an uncontrolled environment. This equipment should be installed and operated with a minimum distance of 20cm between the radiator & your body. This transmitter must not be co-located or operating in conjunction with any other antenna or transmitter.

The module is limited to OEM installation ONLY.

The OEM integrator is responsible for ensuring that the end-user has no manual instruction to remove or install module.

The module is limited to installation in mobile application.

A separate approval is required for all other operating configurations, including portable configurations with respect to Part 2.1093 and difference antenna configurations.

There is requirement that the grantee provide guidance to the host manufacturer for compliance with Part 15B requirements.

The OEM integrator is responsible for ensuring that the end-user has no manual instructions to rem-ove or install module.

The module is limited to installation in mobile or fixed application.



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Note

As the product upgrade or other reasons, this manual may change. Shenzhen Wireless-Tag Technology Co., Ltd has right to modify the contents of this manual without any notice or warning. This manual is only as a guide, Wireless-Tag Technology Co., Ltd Spareno effort to provide accurate information in this manual, but the Wireless-Tag can't guarantee manual there is no problem, all statements in this manual, information and suggestions do not constitute any guarantee of express or implication.

CE marking:

Use the WT-01E in the environment with the temperature between -40°C and 85°C. For the following equipment: WT-01ERED is in compliance with the essential requirements and other relevant provisions of Directive (RED) 2014/53/EU.

Hereby, WIRELESS-TAG TECHNOLOGY CO., LIMITED declares that this WiFi module is in compliance with essential requirements and other relevant provisions of Directive (RED) 2014/53/EU,A copy of the declaration of conformity can be found at www.wireless-tag.com.



Amendment record

Version	Changed by	Time	Reason	Details
V1.0	Louie	2019.01.13	Original	



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1.Overview

WT-01E Wi-Fi module is a low consumption, high performance Wi-Fi network control module designed by Wireless-Tag. It can meet the IoT application requirements in smart power grids, building automation, security and protection, smart home, remote health care etc.

The module's core processor ESP8285 integrates an enhanced version of Tensilica's L106 Diamond series 32-bit processor with smaller package size and 16 bit compact mode, main frequency support 80 MHz and 160 MHz, support RTOS, integrated Wi-Fi MAC / BB / RF / PA / LNA, on-board IPEX connector antenna or Spring antenna.

The module supports standard IEEE802.11 b / g / n protocol, a complete TCP / IP protocol stack.it can be used to host the application or to offload Wi-Fi networking functions from another application processor.

2. Main Features

- SMD-18 package for easy welding
- IPEX connector or Spring Antenna
- Operating Voltage: 3.3V
- Operating Temperature: -20-85°C
- CPU Tensilica L106
 - o RAM 50KB (Available)
 - The built-in 1 MB flash

System

- o 802.11 b/g/n
- Integrated Tensilica L106 ultra-low power 32-bitmicro MCU, with 16-bit RSIC. The CPU clock speed is 80MHz. It can also reach a maximum value of 160MHz.
- O WIFI 2.4 GHz, support WEP/WPA-PSK/WPA2-PSK
- Ultra-Small 18mm*17mm*2.8mm(±0.2mm)
- o Integrated 10 bit high precision ADC
- o Integrated TCP/IP Stack
- o Integrated TR switch, balun, LNA, Power amplifier and matching network
- Deep sleep current<20uA, Power down leakage current < 5uA
- Standby power consumption<1.0mW (DTIM3)
- UART baud rate up to 4Mbps
- Support AT remote upgrades and cloud OTA upgrade
- Support STA/AP/STA+AP operation modes
- o FCC/CE/RoHs



3. Hardware Specifications

3.1 System Diagram

3.2Pin Description

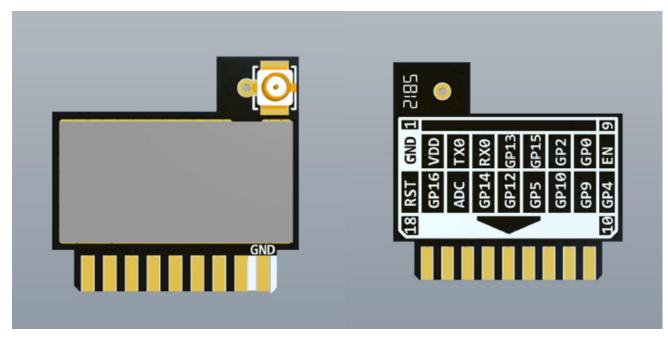


Figure-2 Physical Map

Table 1 Pin Definition and Description

Pin	Name	Description
1	GND	GND
2	VDD	Module power supply pin, Voltage 3.0V ~ 3.6V
3	TX	UARTO_TXD; GPIO1
4	RX	UARTO_RXD; GPIO3
5	IO13	GPIO13; HSPI_MOSI; UARTO_CTS
6	IO15	GPIO15; MTDO; HSPICS; UARTO_RTS
7	102	GPIO2; UART1_TXD
8	100	GPIO0;HSPI_MISO;I2SI_DATA
9	EN	Chip Enabled Pin, Active High
10	104	GPIO4
11	109	GPIO9
12	IO10	GPIO10
13	105	GPIO5;IR_R



14	IO12	GPIO12; HSPI_MISO
15	IO14	GPIO14; HSPI_CLK
16	ADC	Detecting chip VDD3P3 supply voltage or ADC pin input voltage (not available at the same time). Input voltage range 0~1V, the value range is 0~1024.
17	IO16	Connect with RST pin to wake up Deep Sleep
18	RST	Reset Pin, Active Low

Note:

Table-2 Pin Mode

Mode	EN	RST	TXD0	IO15	100	102
UARTDownl oad Mode	High	High	High	Low	Low	High
Flash Boot Mode	High	High	High	Low	High	High

Table-3 Interface Description

Name	Pin	Function Description
HSPI Interface	IO12(MISO),IO13(MOSI),I O14(CLK),IO15(CS)	Can connect external SPI Flash, display and MCU etc.
PWM Interface	IO12(R),IO15(G),IO13(B)	The official demo provides4-channel PWM (user can expand to 8-channel), can be used to control lights, buzzers, relays and motors, etc.
IR Interface	IO14(IR_T), IO5(IR_R)	The functionality of Infrared remote control interface can be implemented via software programming. NEC coding, modulation, and demodulation are used by this interface. The frequency of modulated carrier signal is 38KHz.
ADC Interface	ADC	ESP8285EX Integrated 10-bit precision SARADC. ADC_IN interface is used to test the power supply voltage of VDD3P3(Pin 3 and Pin 4), as well as the input voltage of TOUT (Pin 6). It can be used in sensors application.
I2C Interface	IO14(SCL), IO2(SDA)	Can connect to external sensor and display, etc.
UART Interface	UARTO: TX0(U0TXD),RX0(U0RXD), IO15(RTS),IO13(CTS) UART1: IO2(TX0)	Devices with UART interfaces can be connected Download: U0TXD+U0RXD or GPIO2+U0RXD Communication: (UART0):U0TXD,U0RXD,MTDO(U0RTS),MTCK(U0CTS) Debug: UART1_TXD(GPIO2)Can be used to print debugging information By default, UART0 will output some printed information when the device is powered on and is booting up. If this issue exerts influence on some specific applications, users can exchange the inner pins of UART when initializing, that is to say, exchange U0TXD, U0RXD with U0RTS, U0CTS.



3.3Electrical Characteristic

3.3.1Maximum Ratings

Table- 4. Maximum Ratings

Ratings	Condition	Value	Unit
Storage Temperature	/	-40 to 85	°C
Maximum Soldering Temperature	/	260	°C
Supply Voltage	IPC/JEDEC J-STD-020	+2.7 to +3.6	V

3.3.2Recommended Operating Environment

Table -5 Recommended Operating Environment

Working Environment	Name	Min Value	Typical Values	Max Value	Unit
Operating Temperature	/	-20	20	85	°C
Supply Voltage	VDD	2.7	3.3	3.6	V

3.3.3Digital Port Characteristics

Table -6 Digital Port Characteristics

Port	Typical Values	Min Value	Max Value	Unit
Input low logic level	VIL	-0.3	0.25VDD	V
Input high logic level	VIH	0.75vdd	VDD+0.3	V
Output low logic level	VOL	N	0.1VDD	V
Output high logic level	VOL	0.8VDD	N	V

3.4Power Consumption

3.4.1Operating Power Consumption

Table -7 Operating Power Consumption

Mode	Standard	tandard Speed Rate		Unit
	11b	11	170	
Tx	11g	54	140	mA
	11n	MCS7	120	
Rx	All rates		56	mA

Note: RX mode data packet length is 1024 bytes;



3.4.2Standby Power Consumption

The following current consumption is based on 3.3V supply and a voltage stabilizer, in 25°C ambient temperature. Values are measured at antenna port without SAW filter. All the transmission measurements values are based on 90% duty cycle, continuous transmission mode.

Mode	Status	Typical Value						
	Modem Sleep	20mA						
Chandbu	Light Sleep		2mA					
Standby	Deep Sleep	20uA						
	Off	0.5uA						
Power Save Mode	DTIM period	Current Cons. (mA)	T1 (ms)	T2 (ms)	Tbeacon (ms)	T3 (ms)		
(2.4G) (Low Power Listen	DTIM 1	1.2	2.01	0.36	0.99	0.39		
disabled) ¹	DTIM 3	0.9	1.99	0.32	1.06	0.41		

Table -8 Standby Power Consumption

- ①: Modem-Sleep requires the CPU to be working, as in PWM or I2S applications. According to802.11 standards (like U-APSD), it saves power to shut down the Wi-Fi Modem circuit whilemaintaining a Wi-Fi connection with no data transmission. E.g. in DTIM3, to maintain a sleep 300mswake 3ms cycle to receive AP's Beacon packages, the current is about 20mA.
- ②: During Light-Sleep, the CPU may be suspended in applications like Wi-Fi switch. Without datatransmission, the Wi-Fi Modem circuit can be turned off and CPU suspended to save poweraccording to the 802.11 standard (U-APSD). E.g. in DTIM3, to maintain a sleep 300ms-wake 3mscycle to receive AP's Beacon packages, the current is about 2mA.

3.5RF Characteristics

3.5.1RF Configuration and General Specifications of Wireless LAN

Table-9 RF Configuration and General Specifications of Wireless LAN

Items	Specifications			
Country/Domain Code	Reserved			
	11b	2.412-2.472	GHz	
Center Frequency	11g	2.412-2.472	GHz	
	11n HT20	2.412-2.472	GHz	
	11b	1, 2, 5.5, 11	Mbps	
Rate	11g	6, 9, 12, 18, 24, 36, 48, 54	Mbps	
	11n 1stream	MCS0, 1, 2, 3, 4, 5, 6, 7	Mbps	
Modulation type	11b	DSSS	_	



11g/n	OFDM	_
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3.5.2 RF Tx Characteristics

Table-10 Emission Characteristics

Mark	Parameters	Condition	Min Value	Typical Value	Max Value	Unit	
Ftx	Input Frequency		2.412	-	2.484	GHz	
Pout	Output Power						
		PA output power in 11b mode	19.5	20.5	21.5	dBm	
		PA output power at 72.2 Mbps	15.5	16.5	17.5	dBm	

3.5.3RF Rx Characteristics

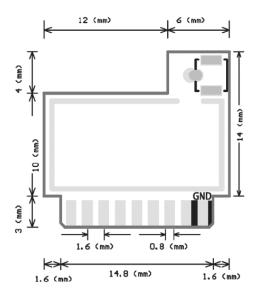
Table-11RF Receiving Characteristics

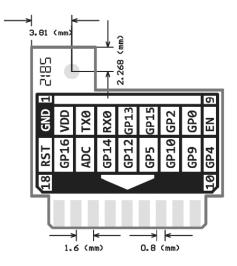
Mark	Parameters	Condition	Min Value	Typical Value	Max Value	Unit
Frx	Input Frequency	_	2.412	-	2.484	GHz
	Sensitivity					
Srf	DSSS	1 Mbps	_	-90	_	dBm
		11 Mbps	_	-85	_	dBm
	OFDM	6 Mbps	_	-88	_	dBm
		54 Mbps	_	-70	_	dBm
	HT20	MCS7	_	-67	_	dBm

4. Application Specification

4.1Module Size







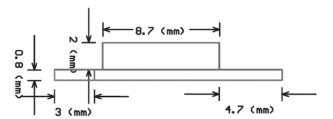
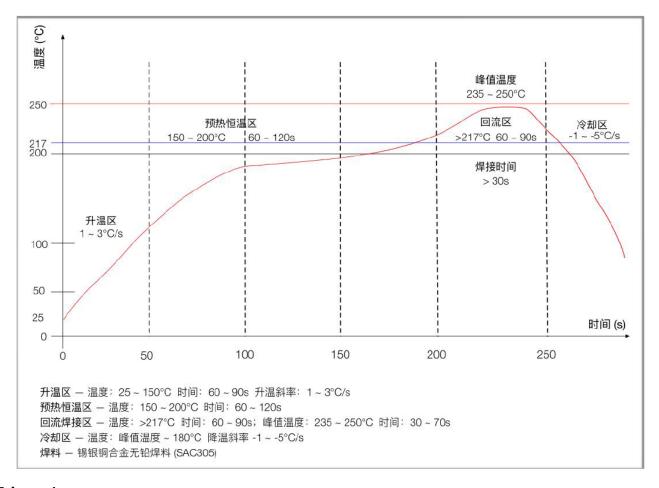


Figure -4 Module Size



4.2 Reflow Profile



4.3 Schematics