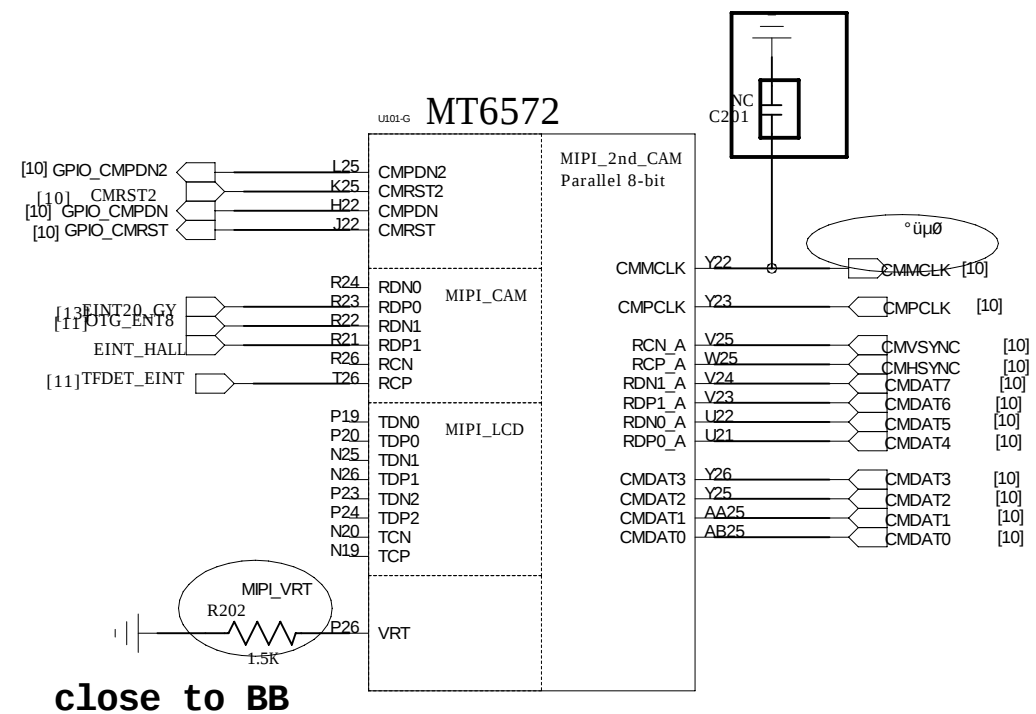
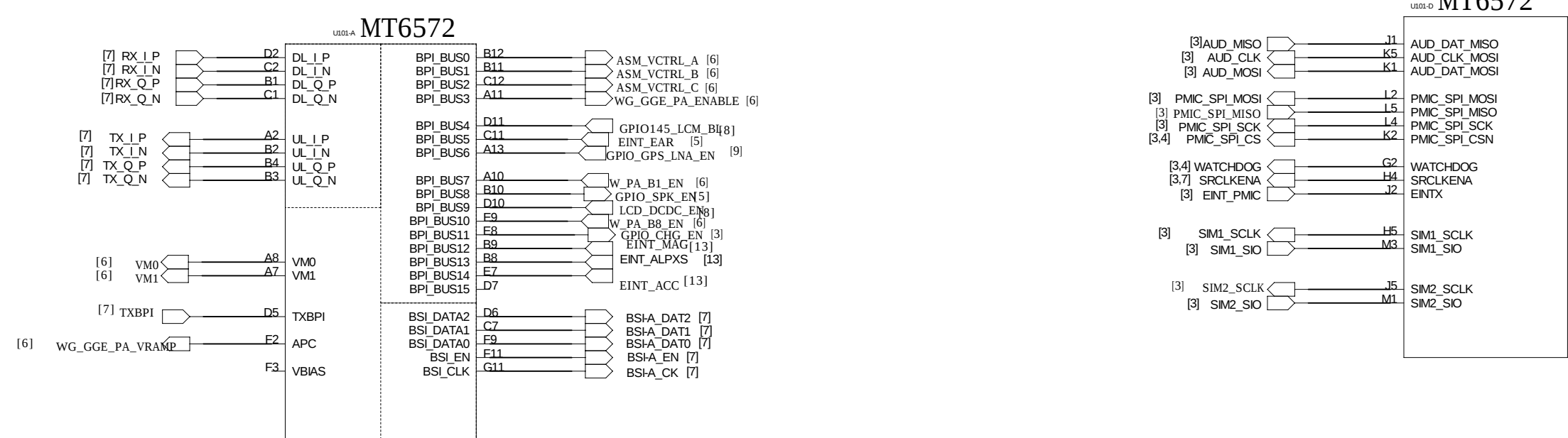
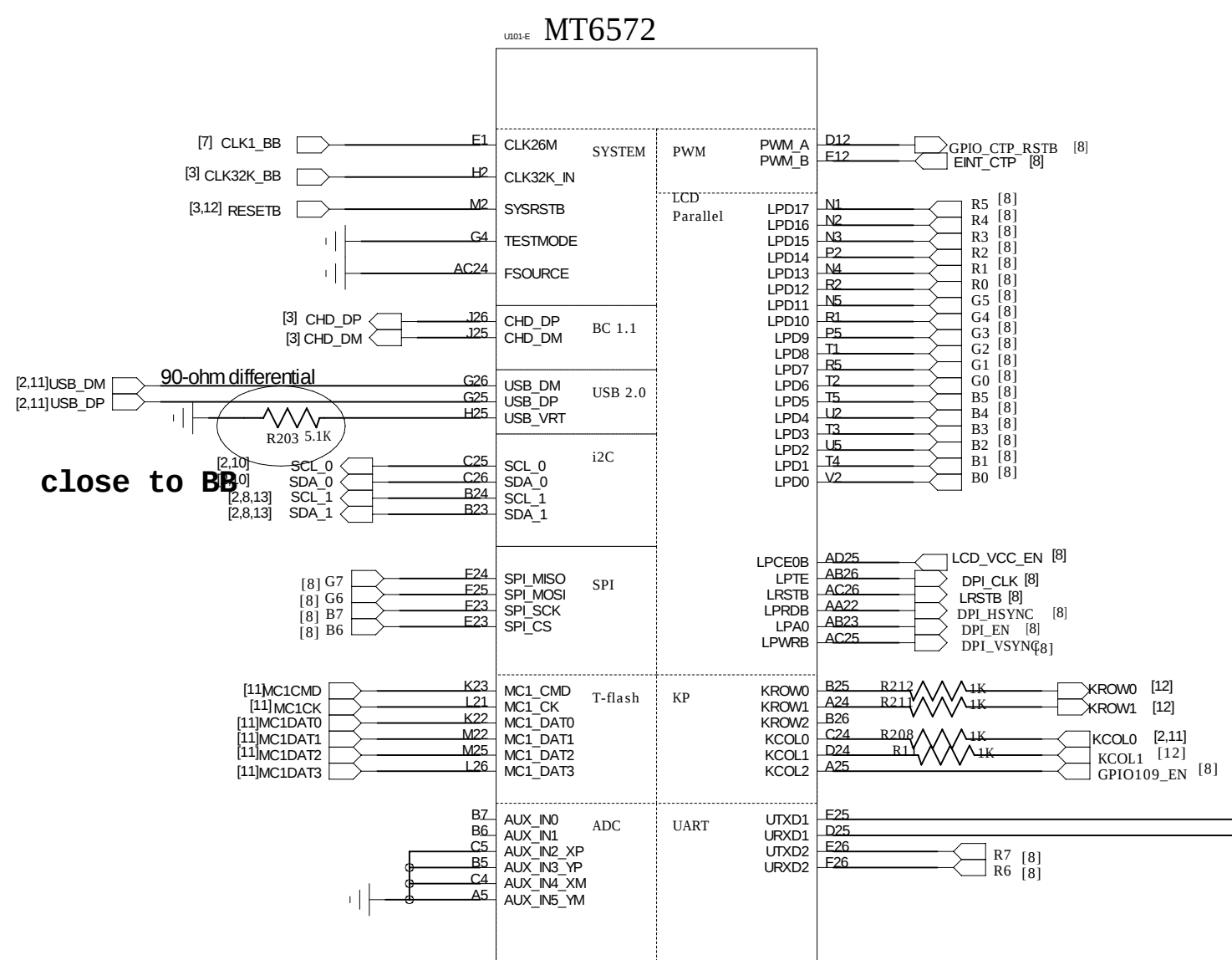




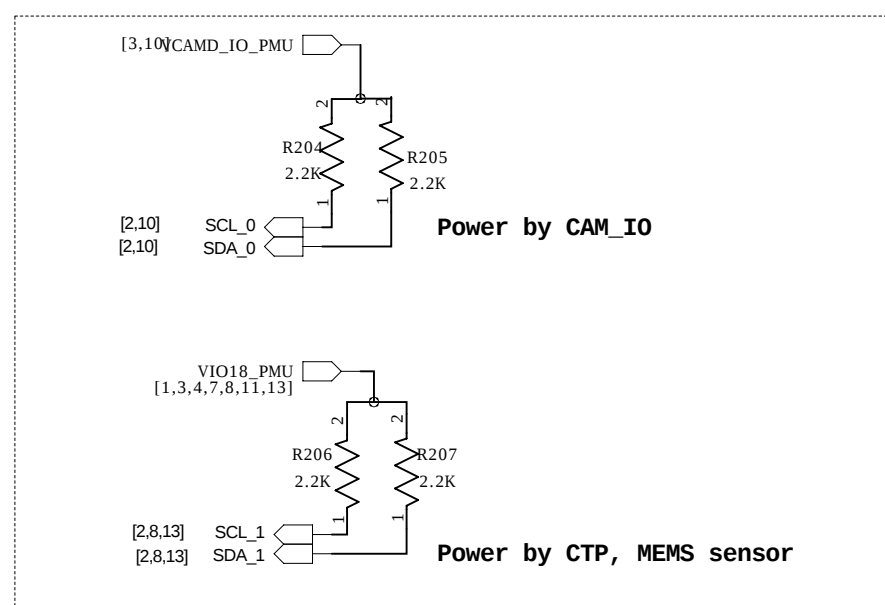
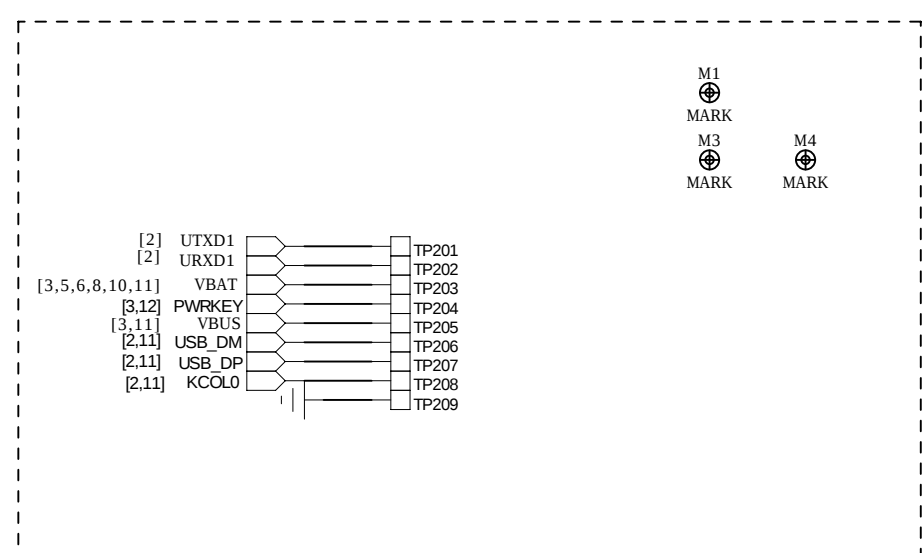
Based on your system level design, if better desense performance is needed on your system, please refer to desense performance enhance proposal

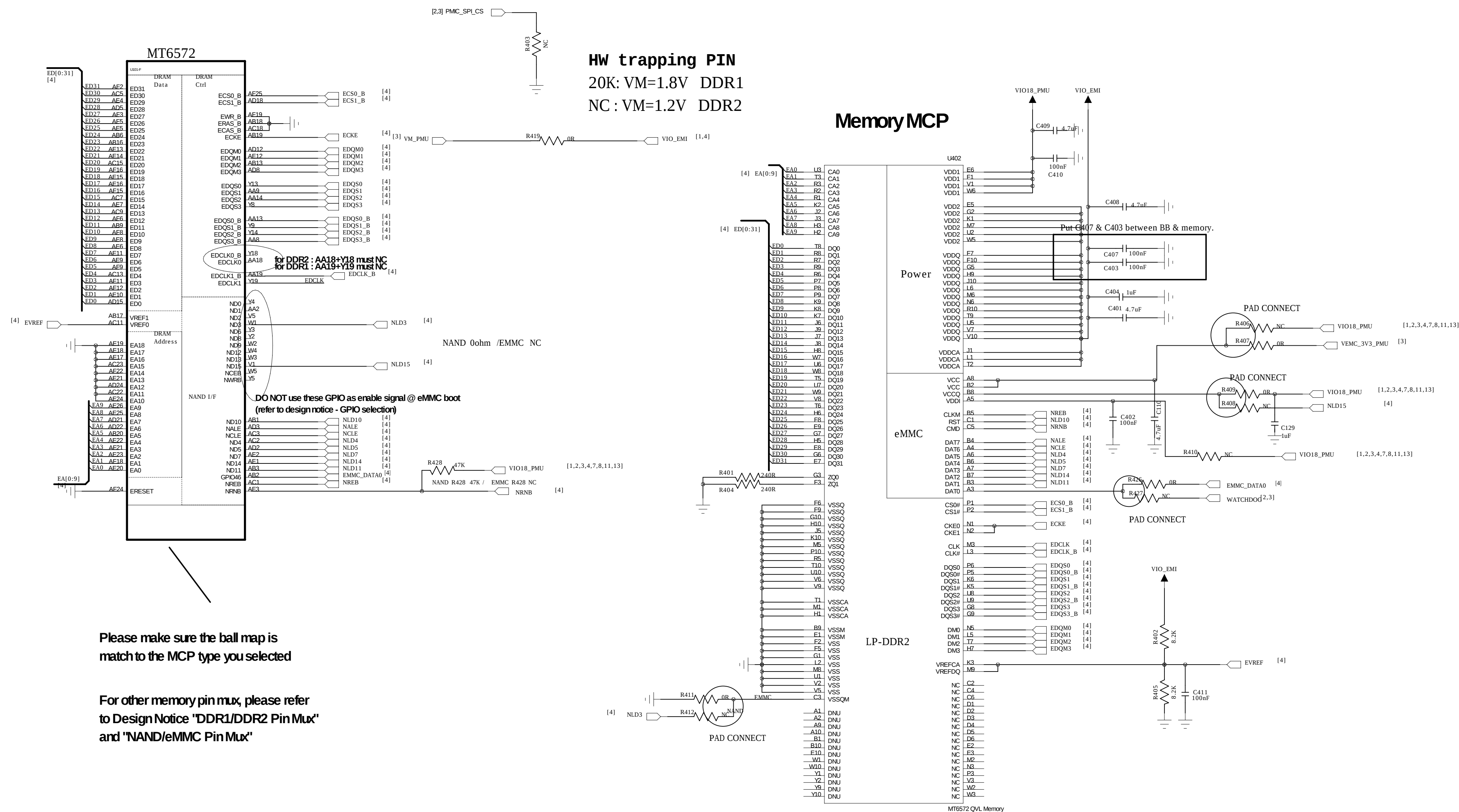


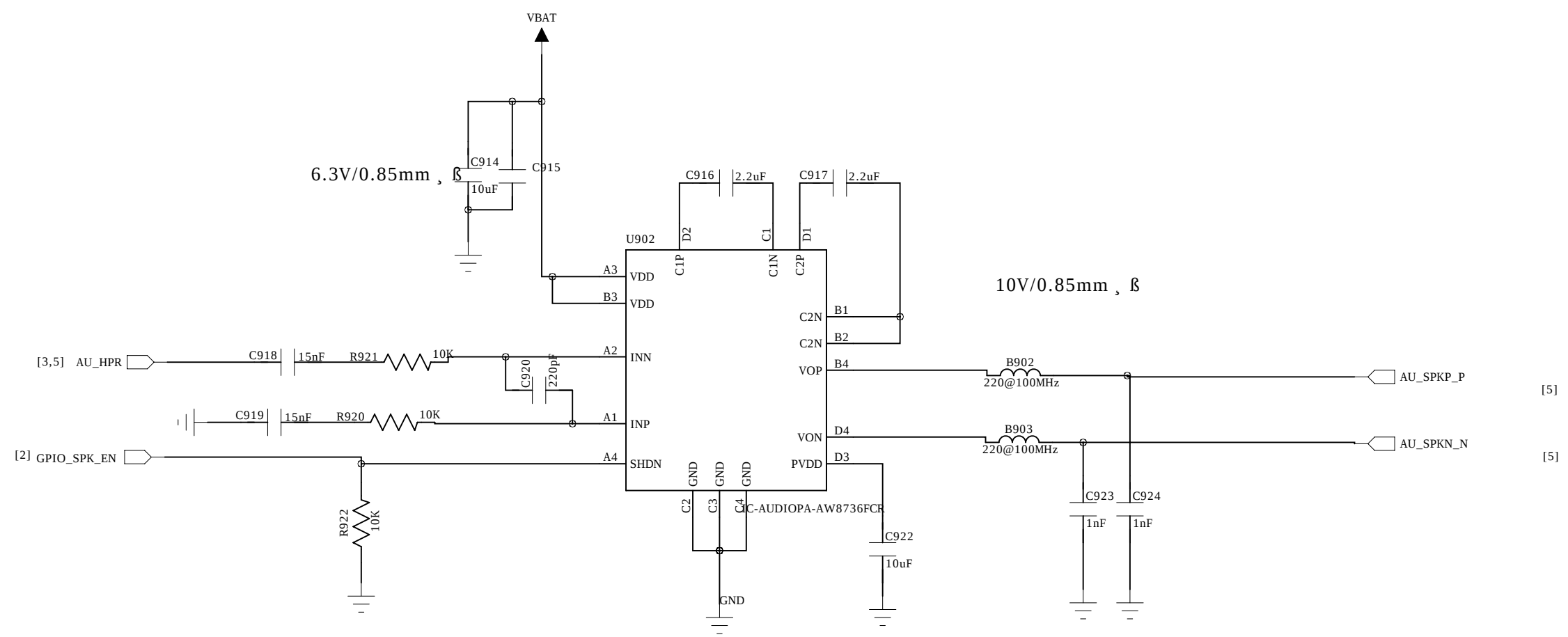
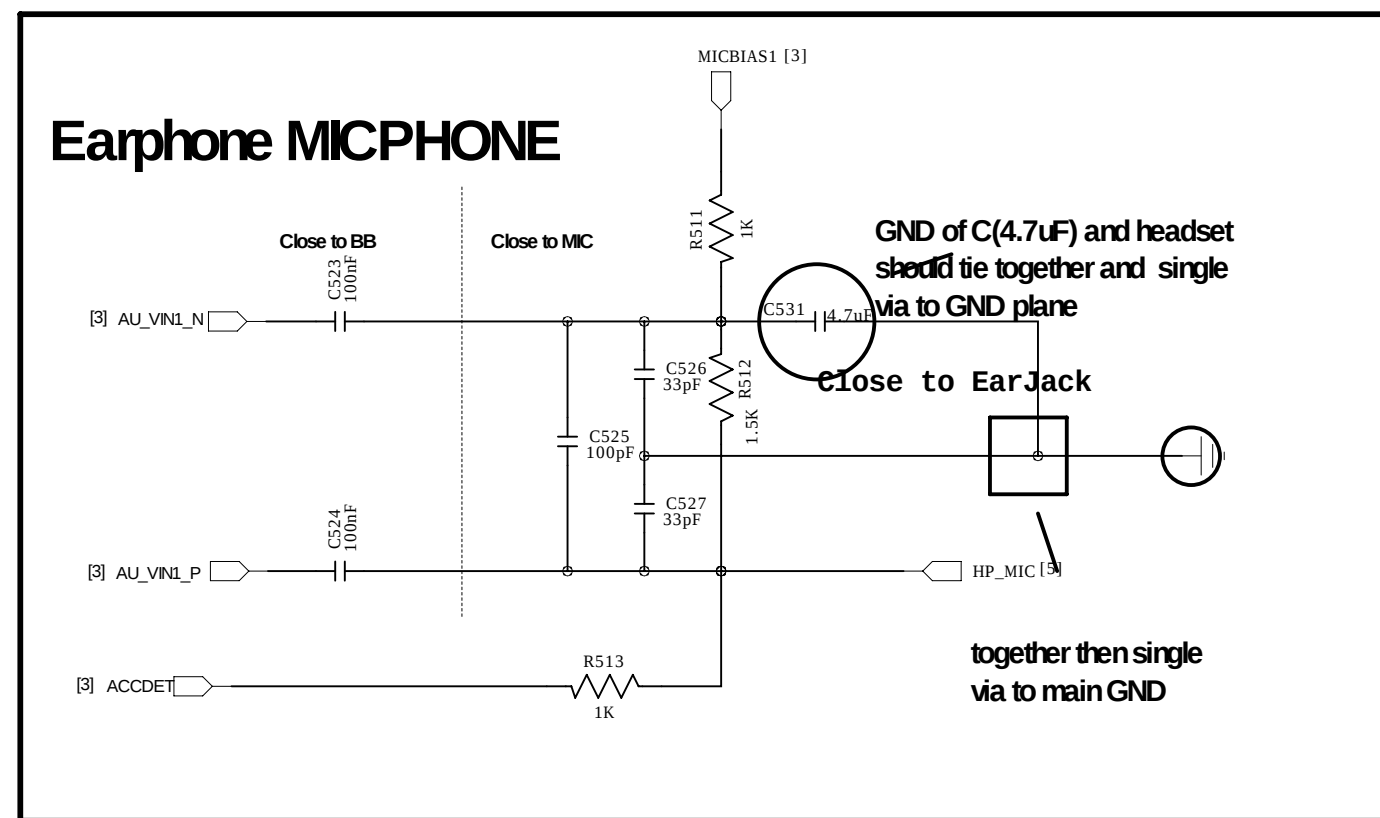
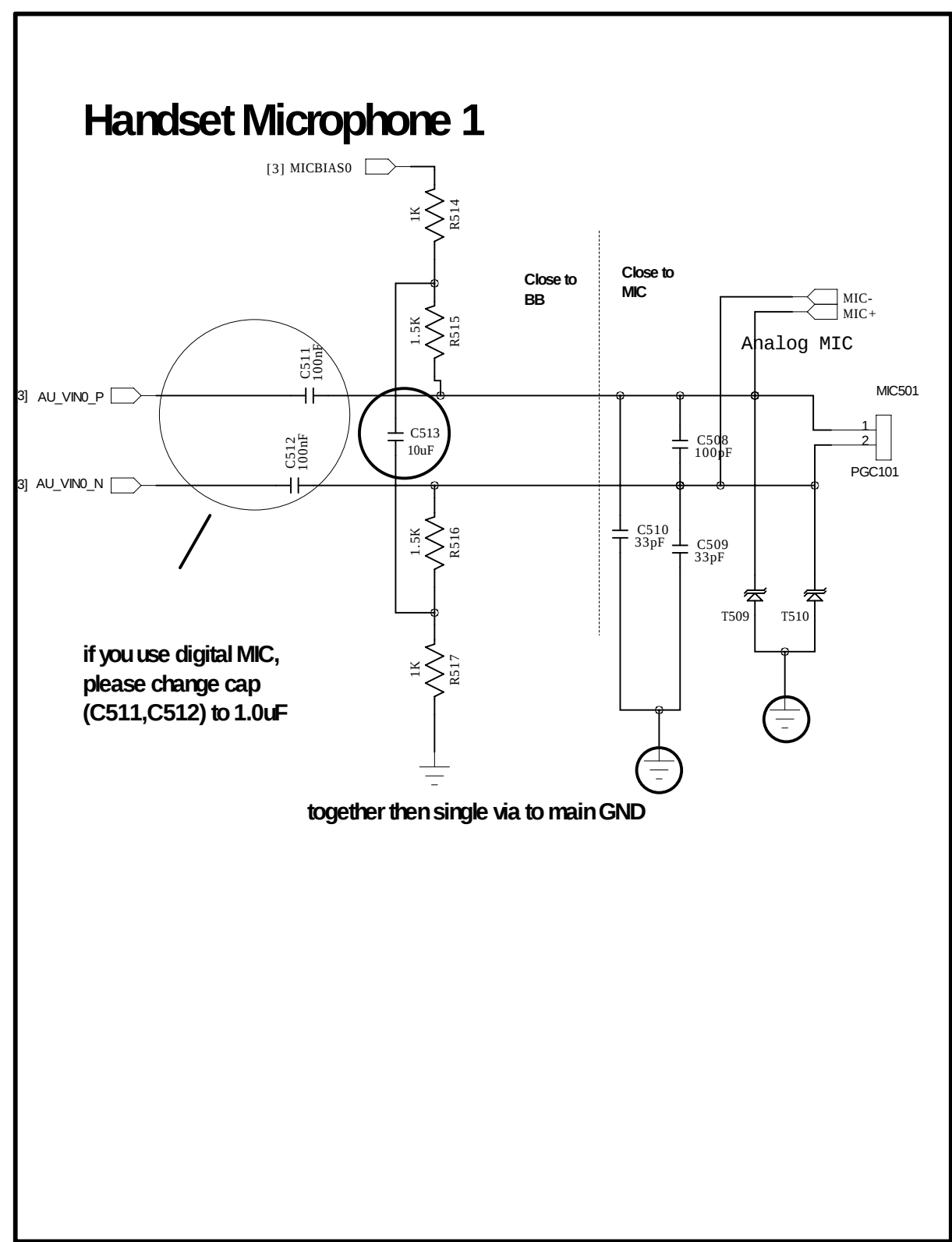
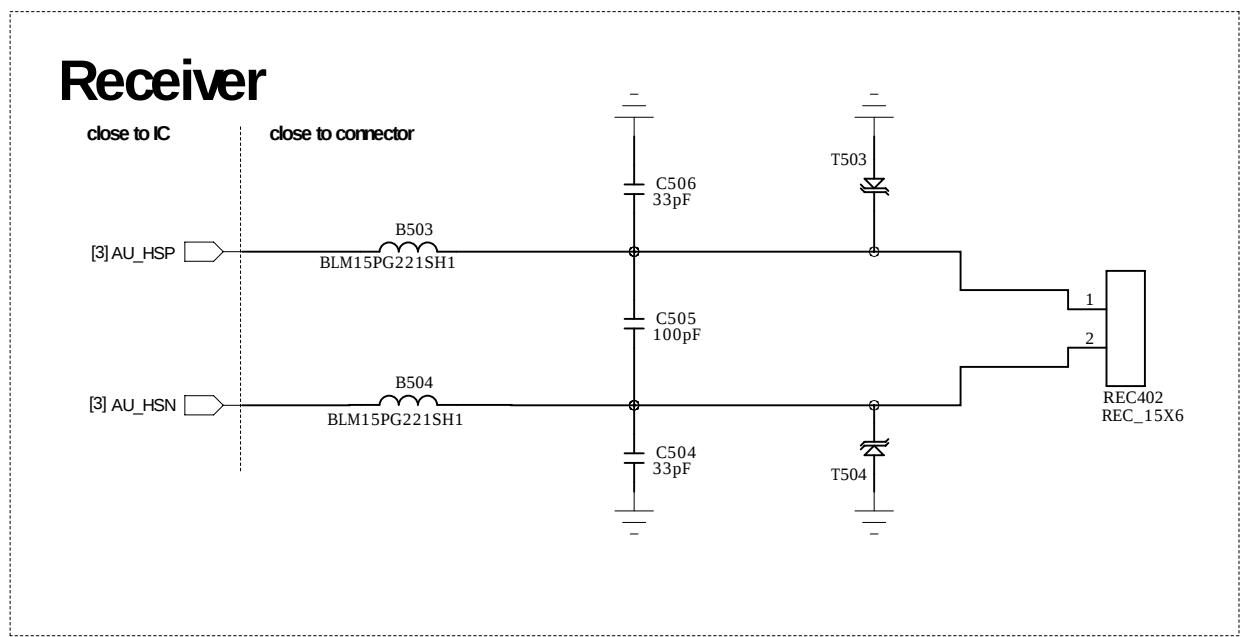
MT6572 support JTAG from below :

1. KP (recommand)
2. MC1
3. CAM

for JTAG-pin out from MC1/CAM, refer to HW design notice





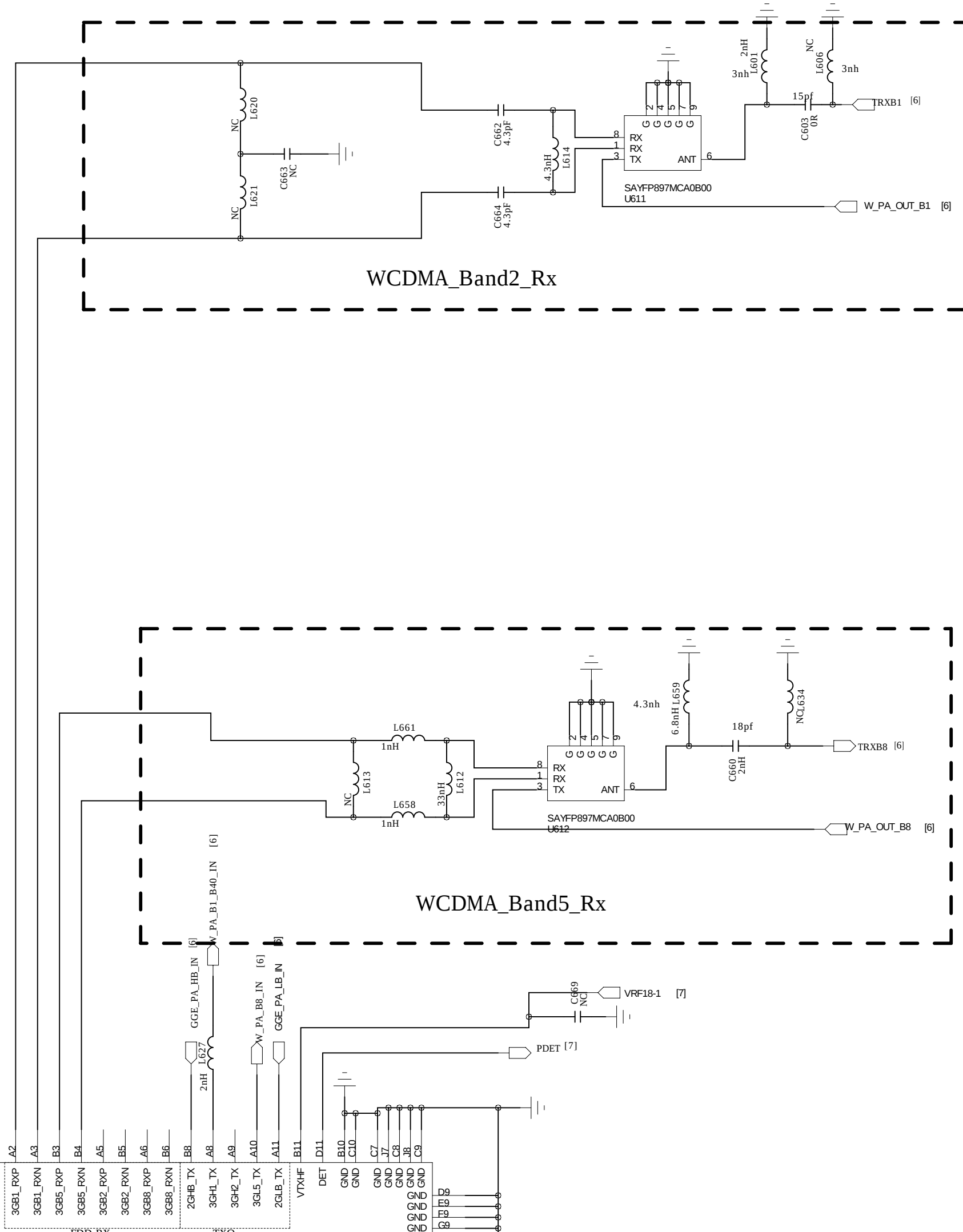
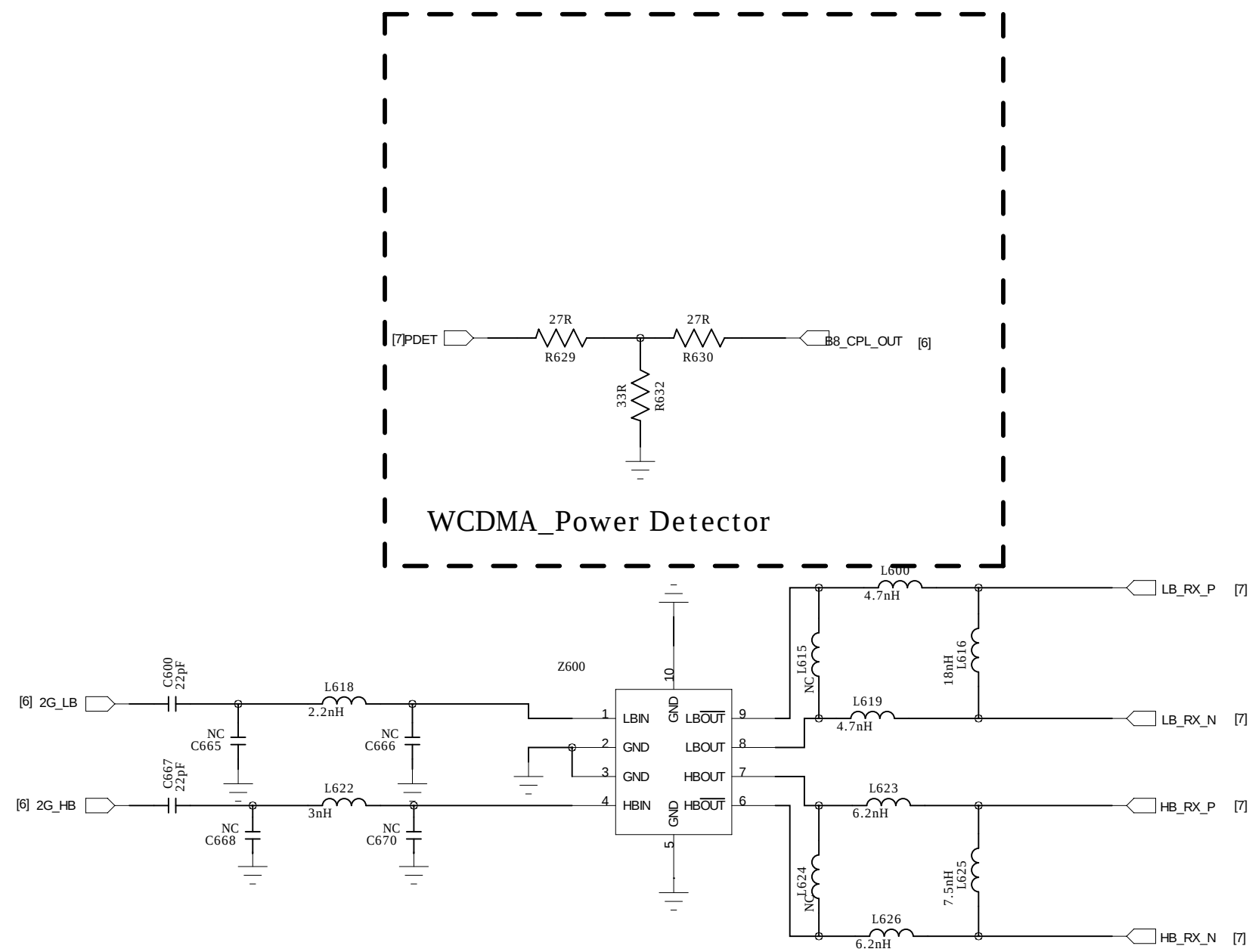
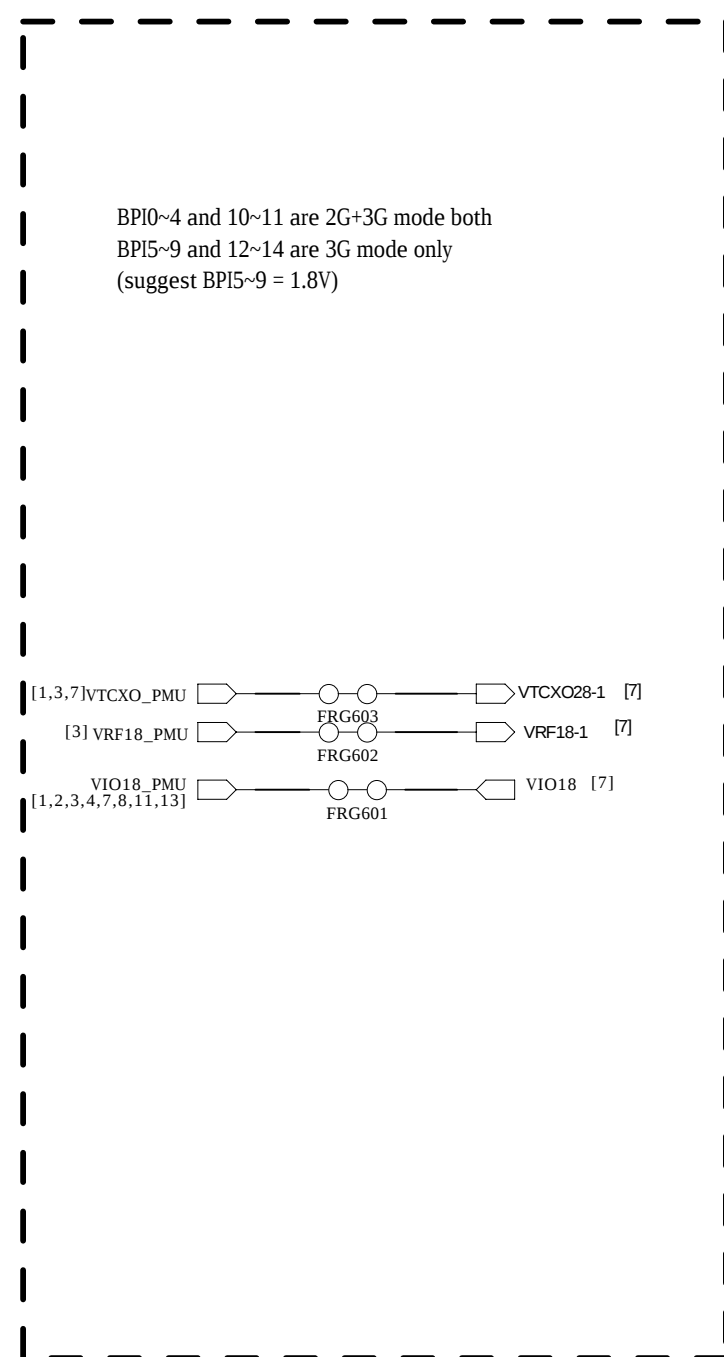


Earphone Audio

close to IC

close to connector

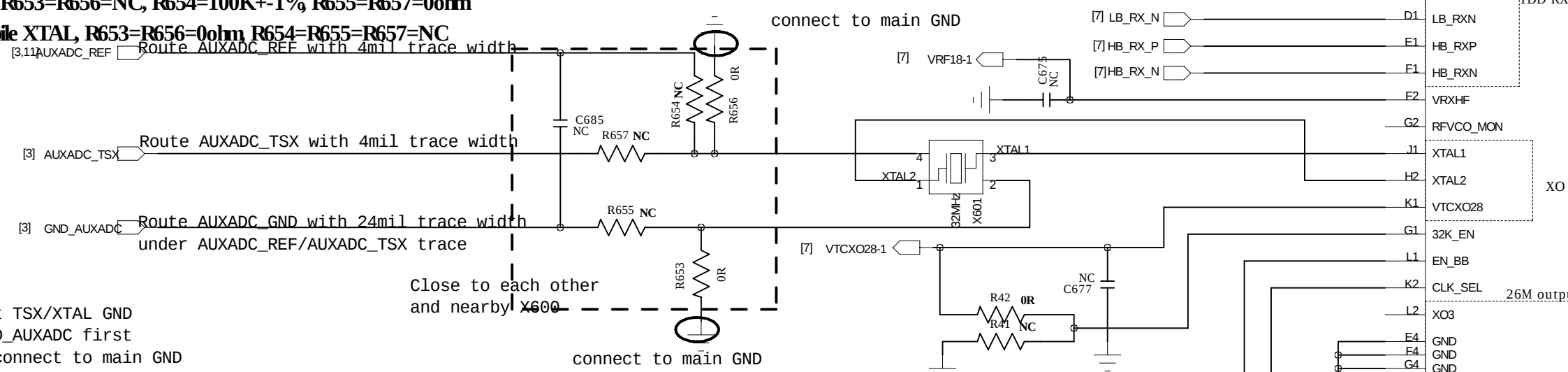
Reserve bead+C footprint for FM performance tuning



Two Application Circuit Conditions,

1.TSX Circuit : X600=TSX, R653=R656=NC, R654=100K+-1%, R655=R657=0ohm

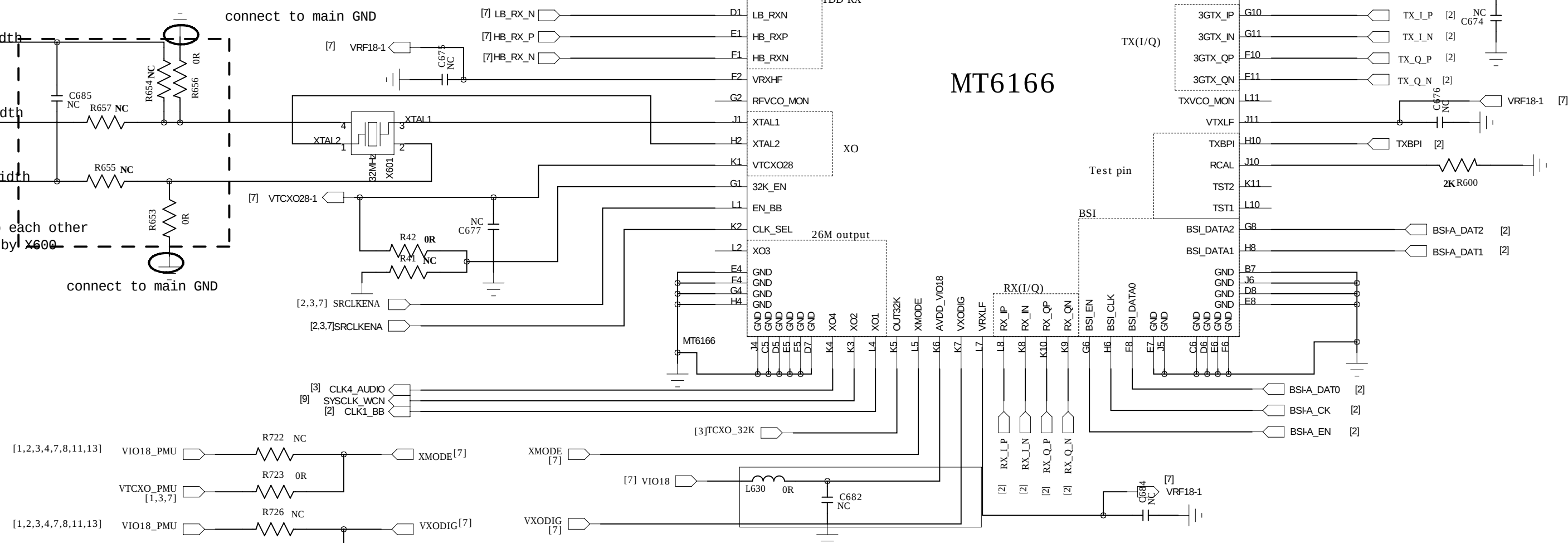
2.XTAL Circuit :X600=Mobile XTAL, R653=R656=0ohm R654=R655=R657=NC



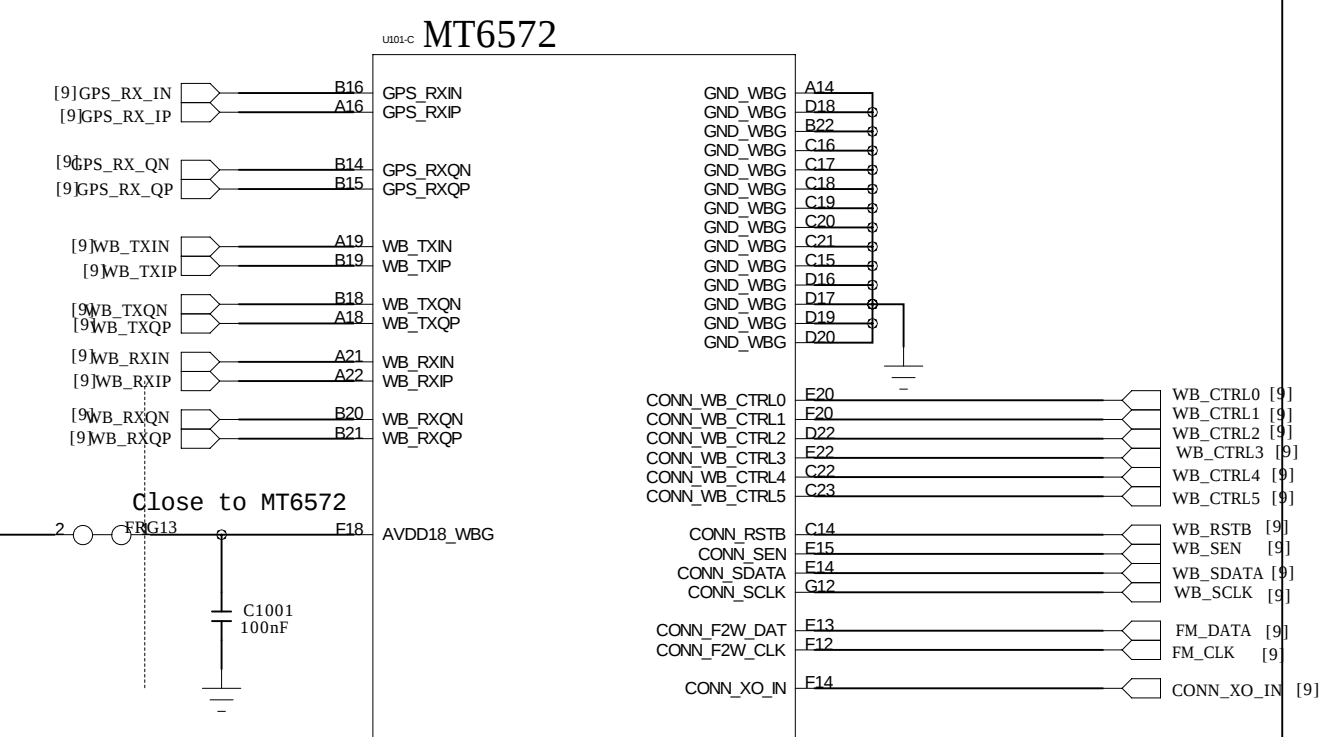
ute AUXADC_REF/AUXADC_TSX as differential trace with well GND shielding

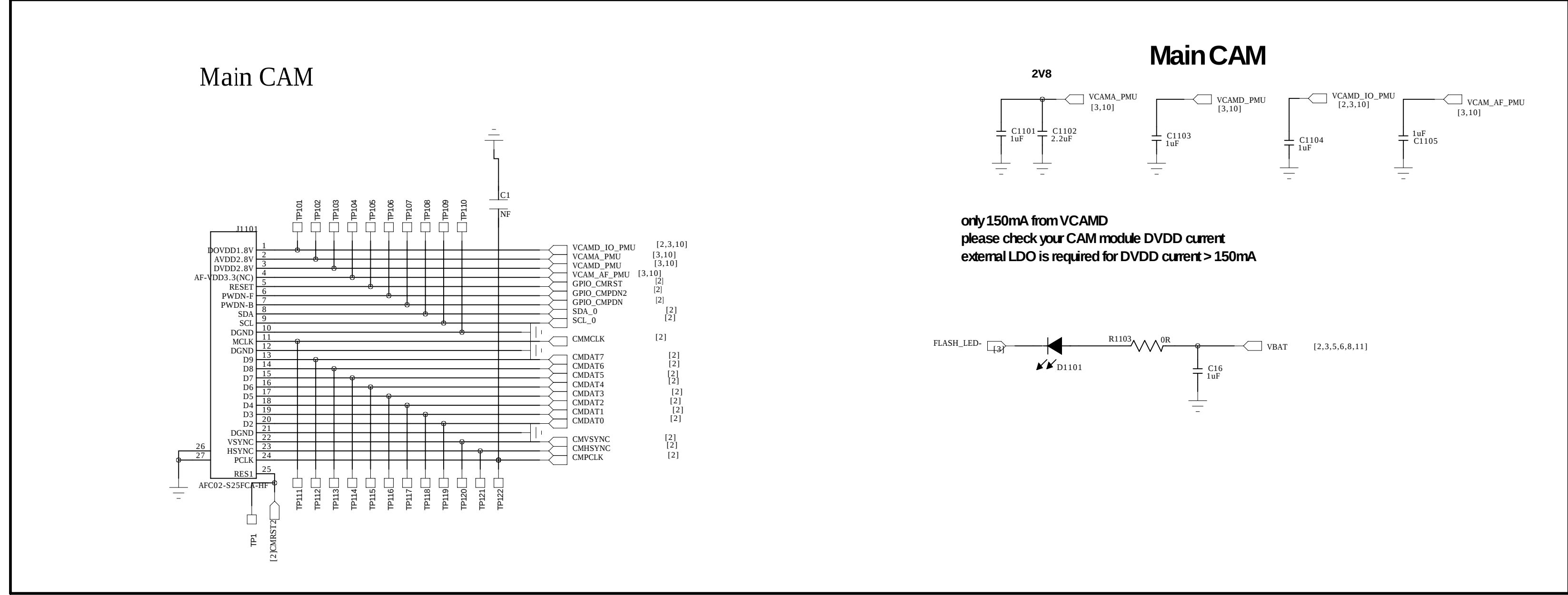
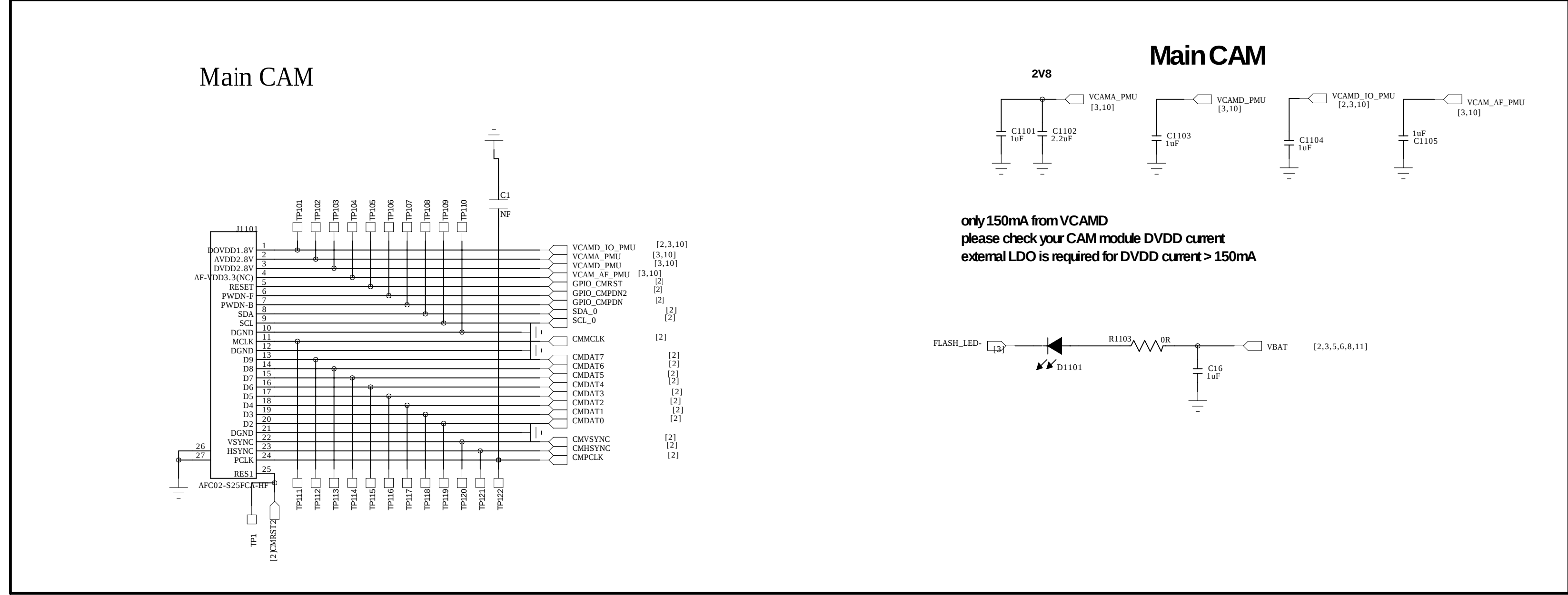
1 route AUXADC_GND with 24mil trace width under

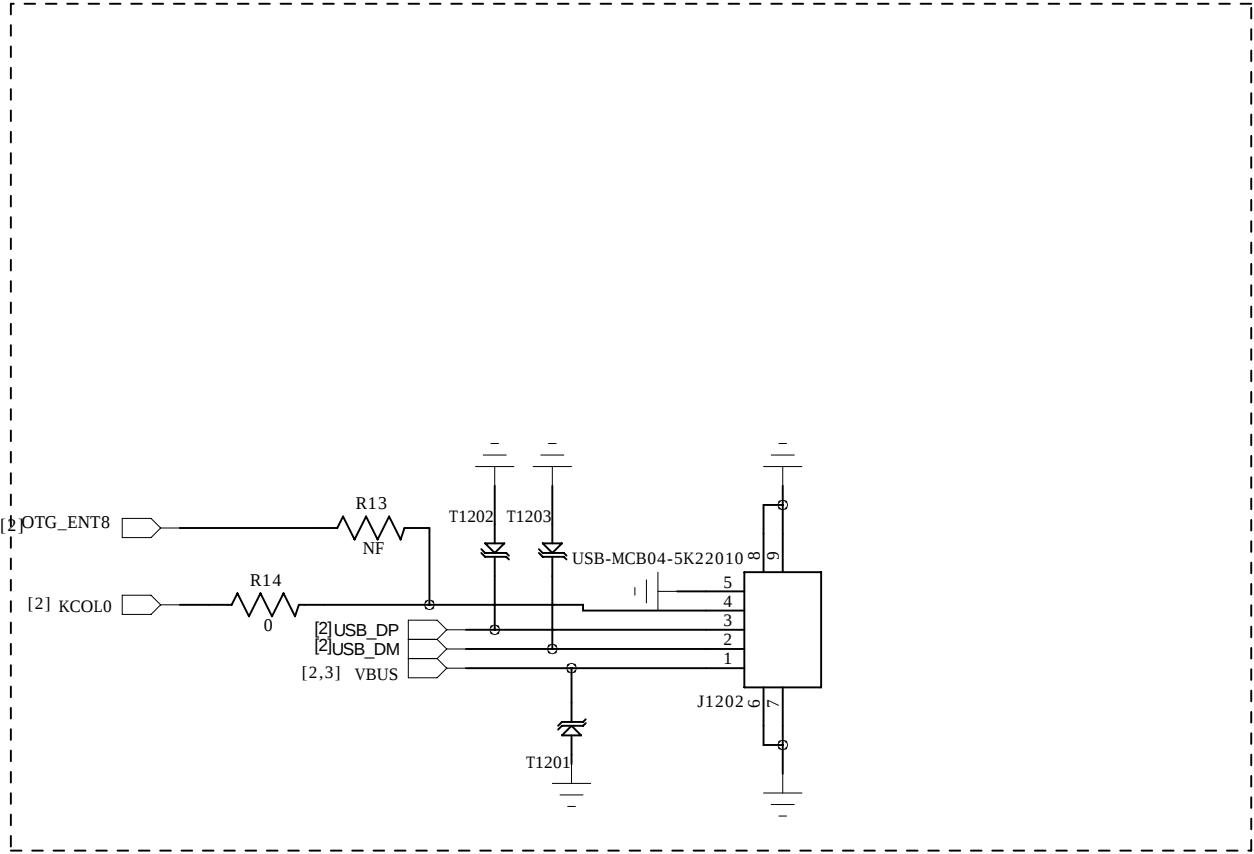
TXADC_TSX/AUXADC_REF trace to provide return current path.



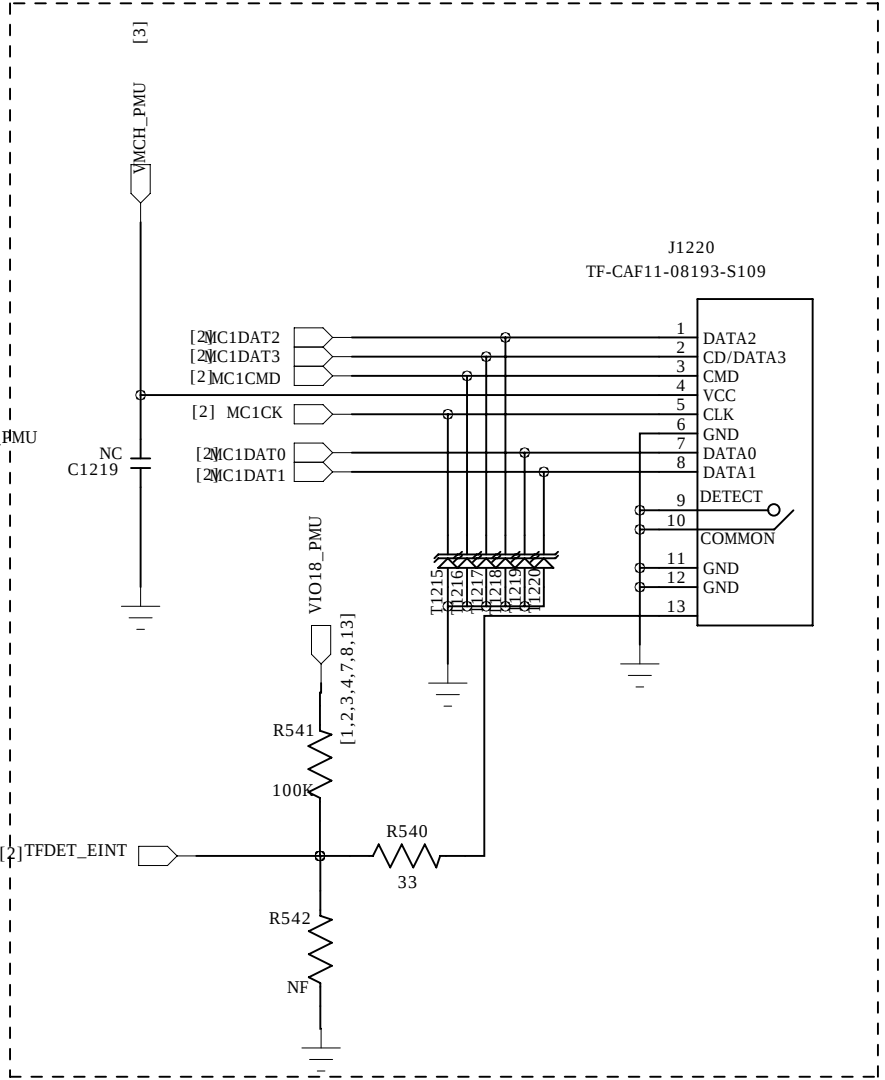
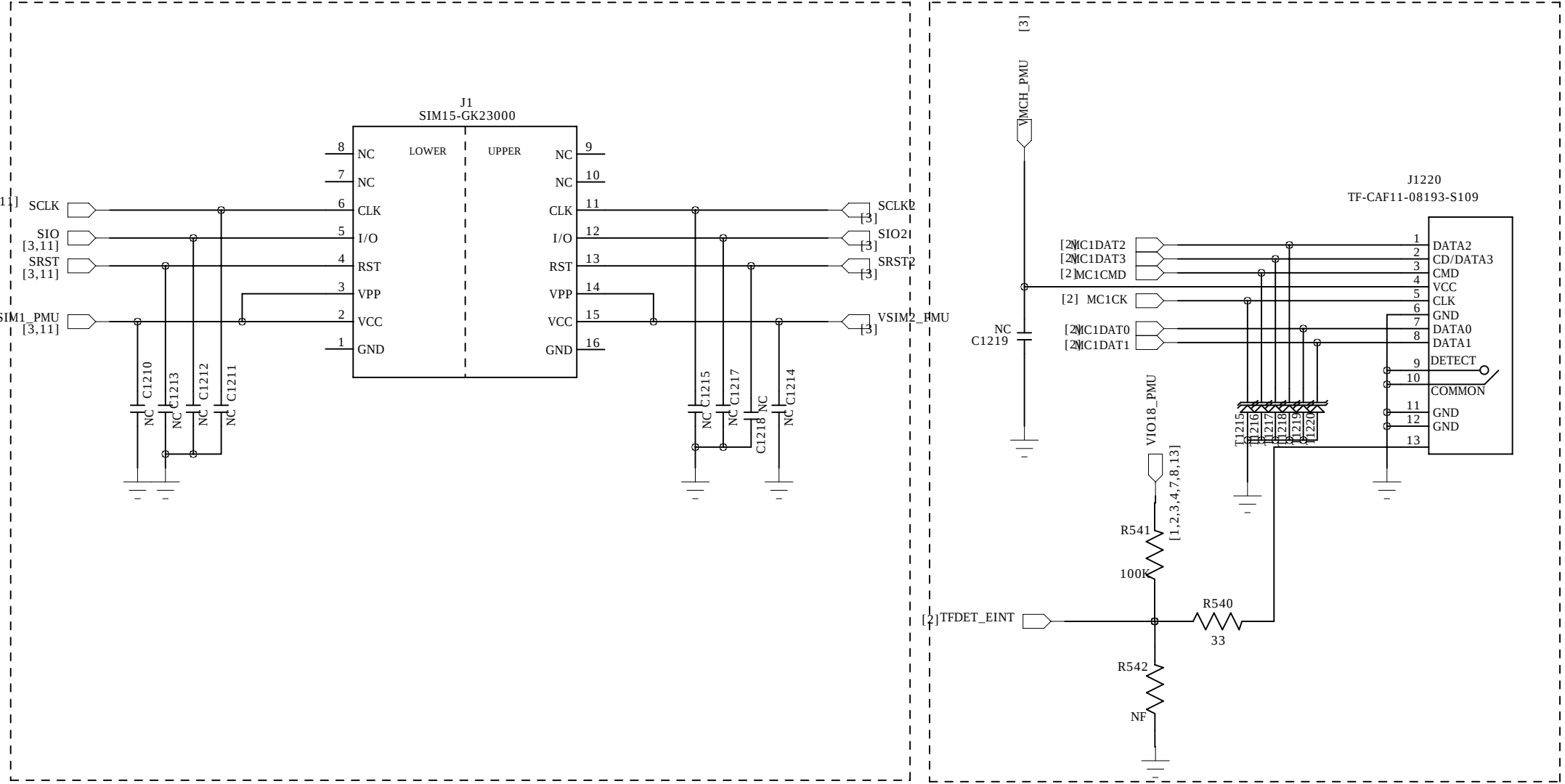
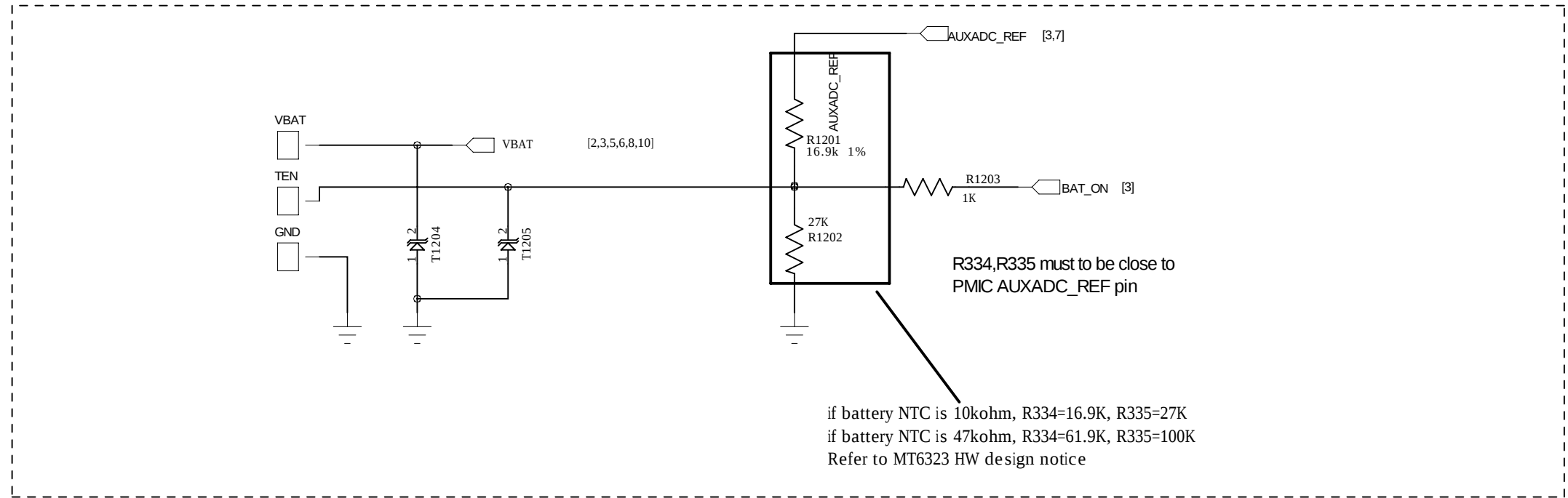
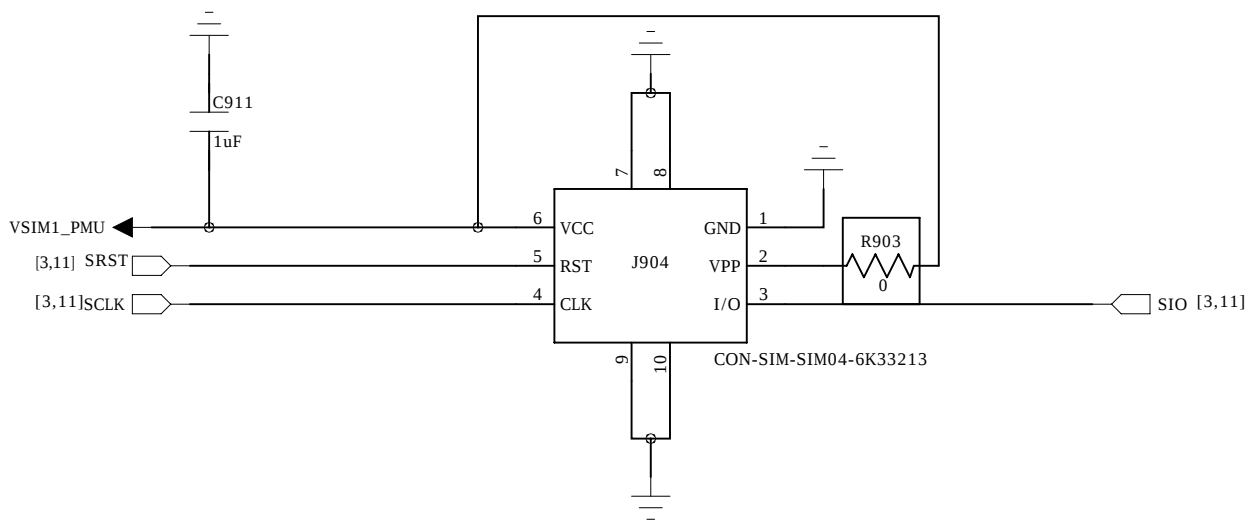
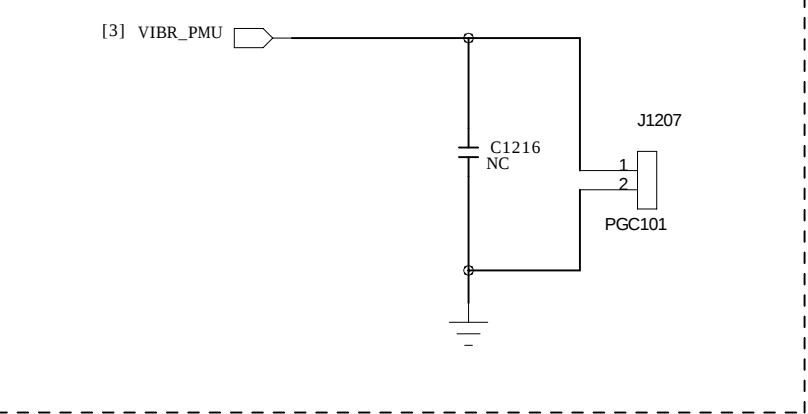
Reserved LC filter

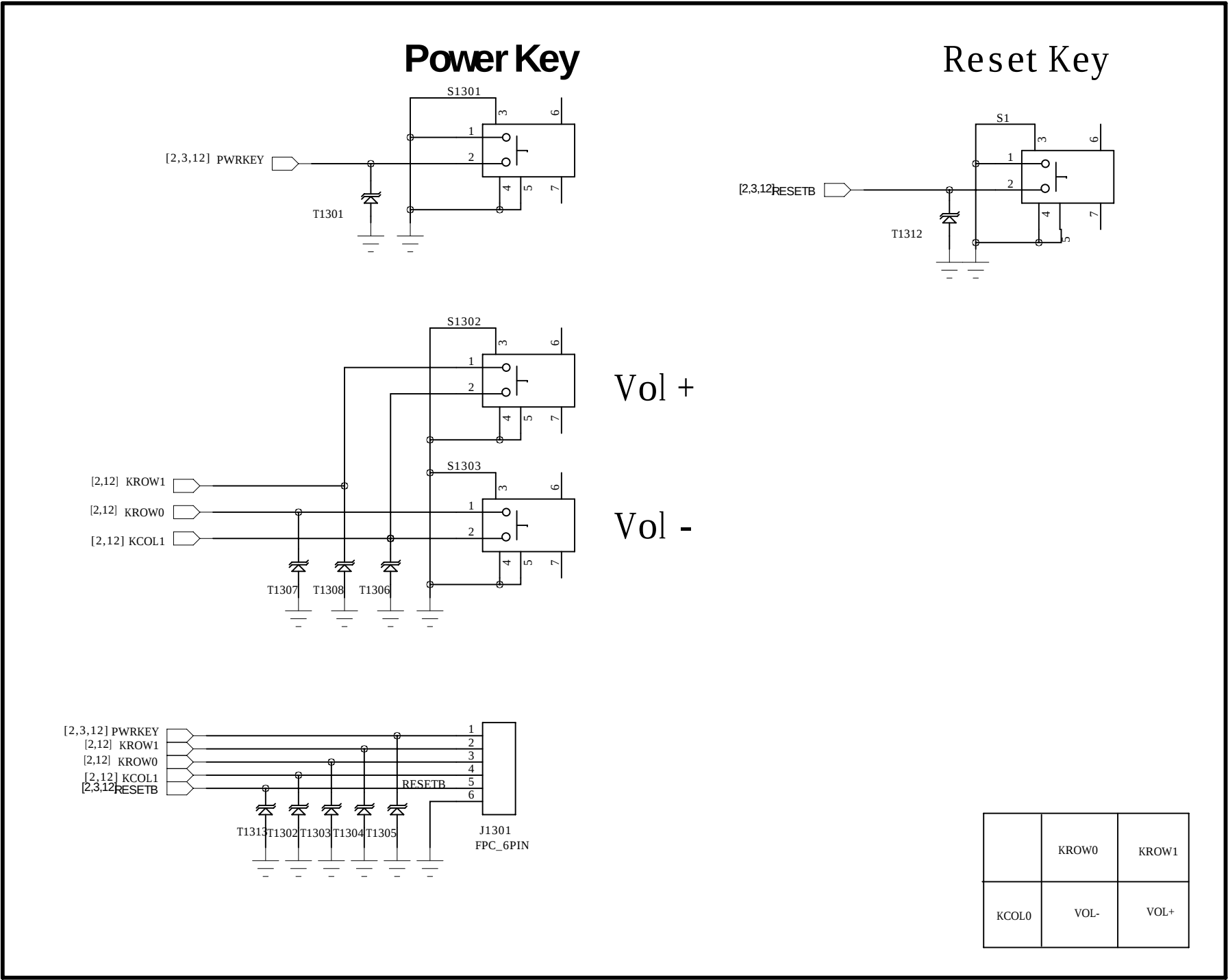






Vibra





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