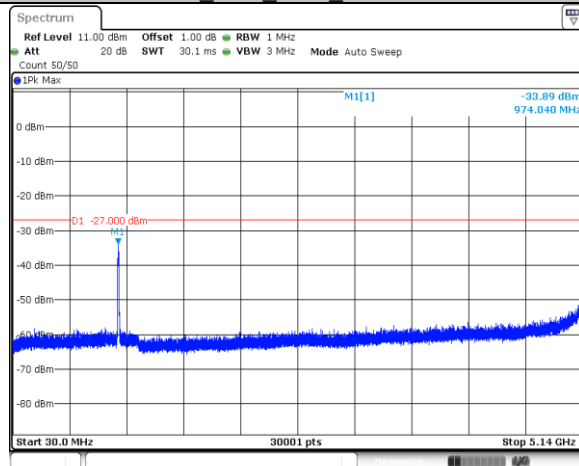
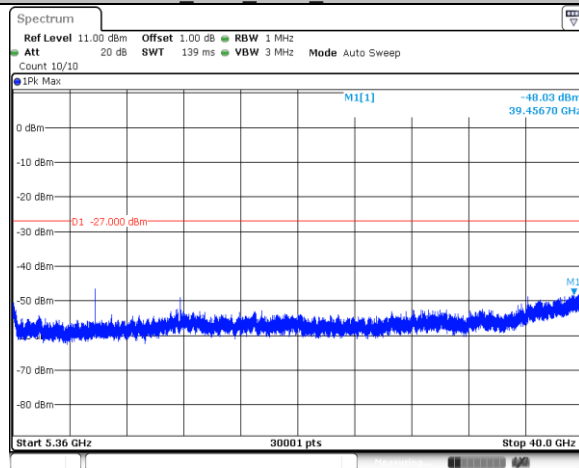


11A_Ant2_5180_30~5140



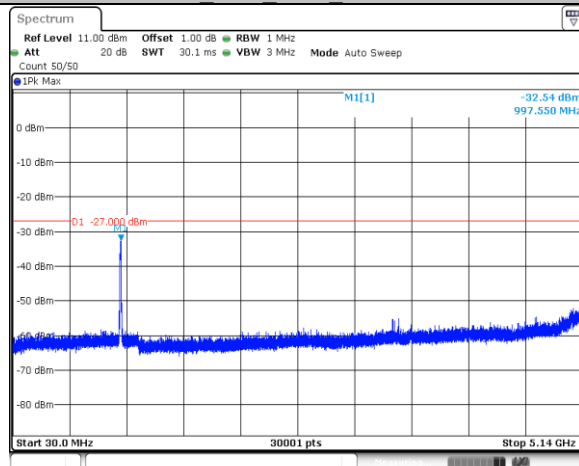
Date: 2 NOV 2023 18:06:11

11A_Ant2_5180_5360~40000



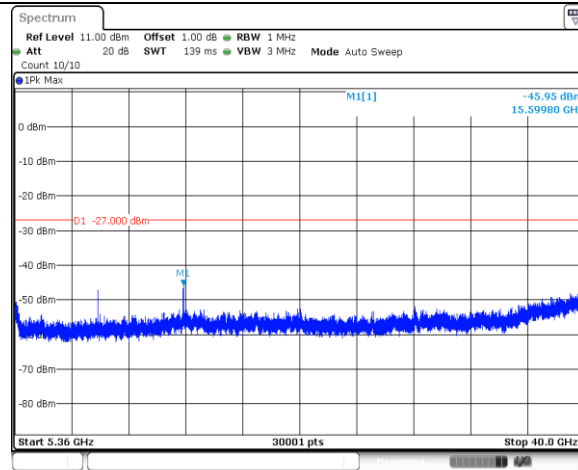
Date: 2 NOV 2023 18:06:16

11A_Ant2_5200_30~5140



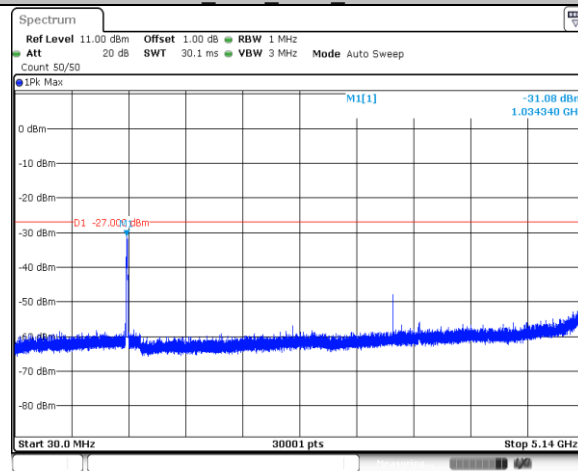
Date: 2 NOV 2023 18:08:45

11A_Ant2_5200_5360~40000



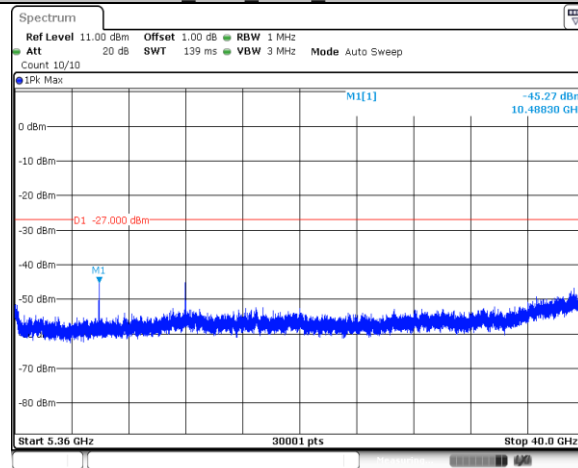
Date: 2 NOV 2023 18:08:50

11A Ant2 5240 30-5140



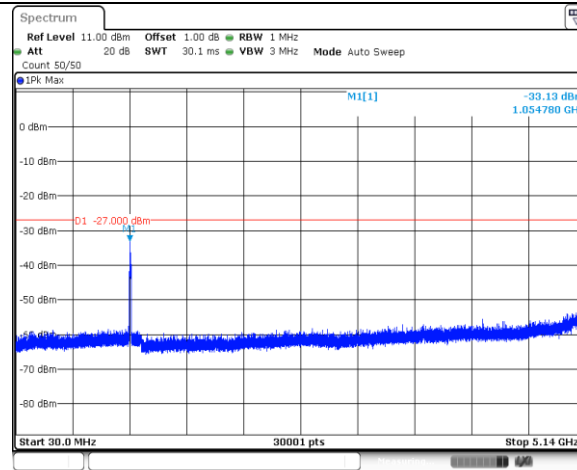
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11A Ant2 5240 5360-40000



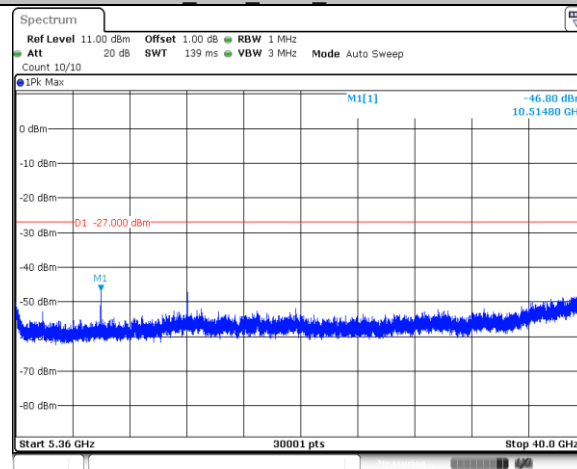
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11A Ant2 5260 30-5140



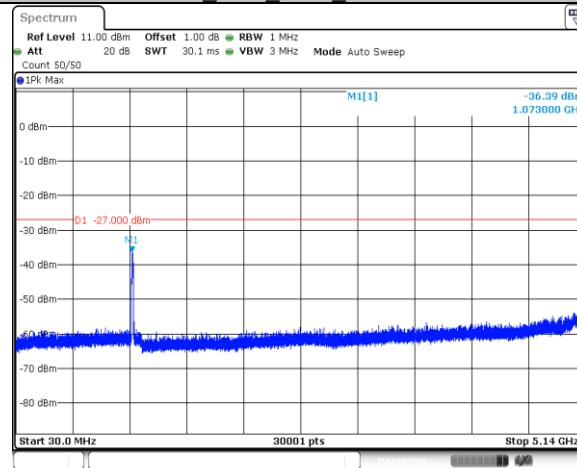
Date: 2 NOV 2023 18:12:37

11A_Ant2_5260_5360~40000



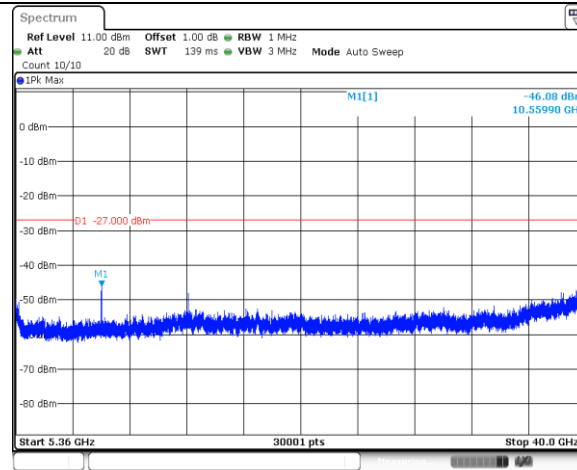
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11A_Ant2_5280_30~5140



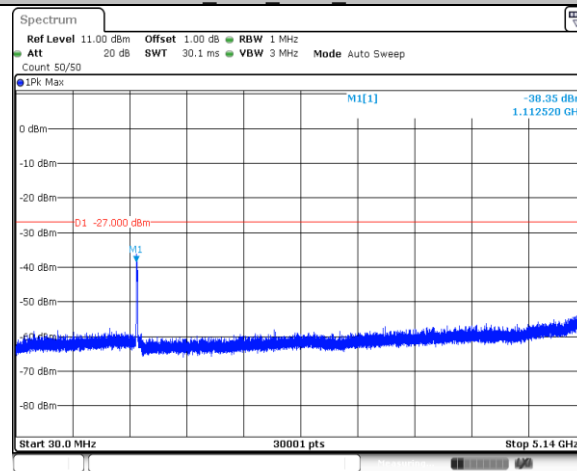
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11A_Ant2_5280_5360~40000



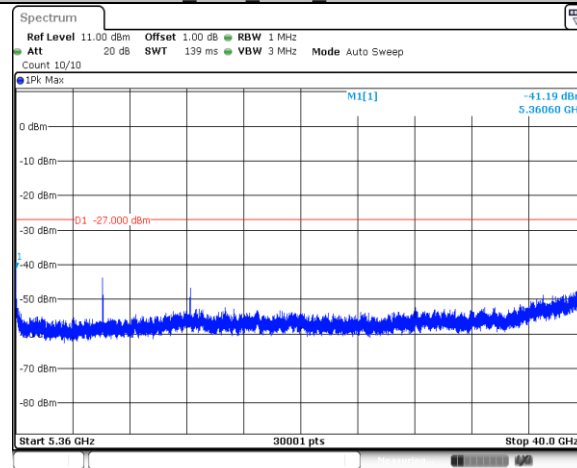
Date: 2 NOV 2023 18:16:04

11A Ant2 5320 30-5140



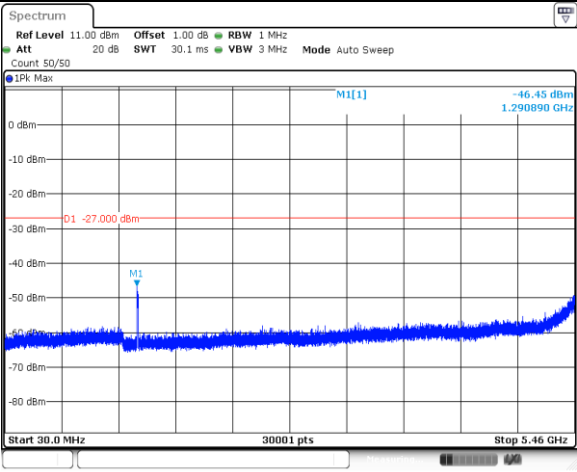
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11A Ant2 5320 5360-40000

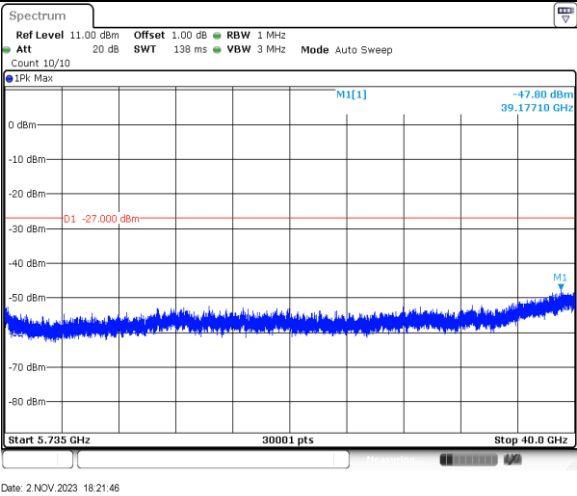


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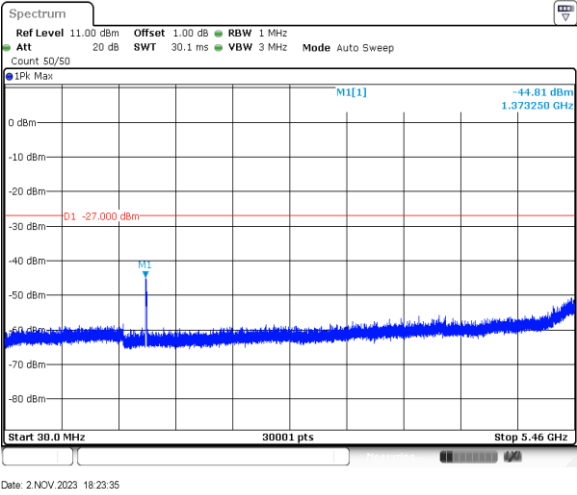
11A Ant2 5500 30-5460



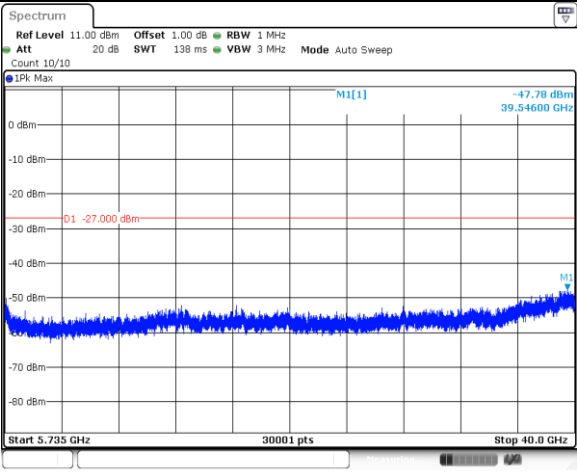
11A_Ant2_5500_5735~40000



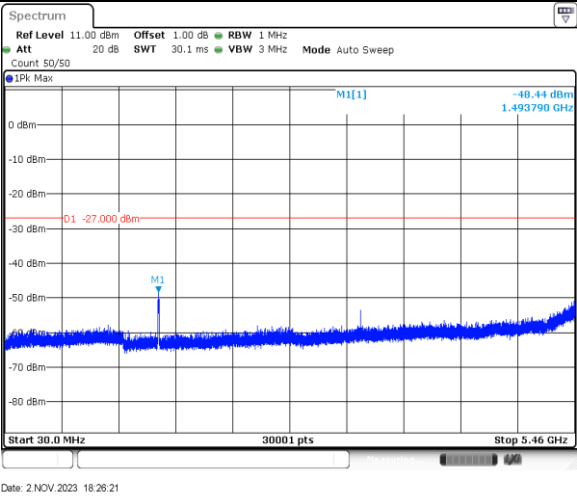
11A_Ant2_5580_30~5460



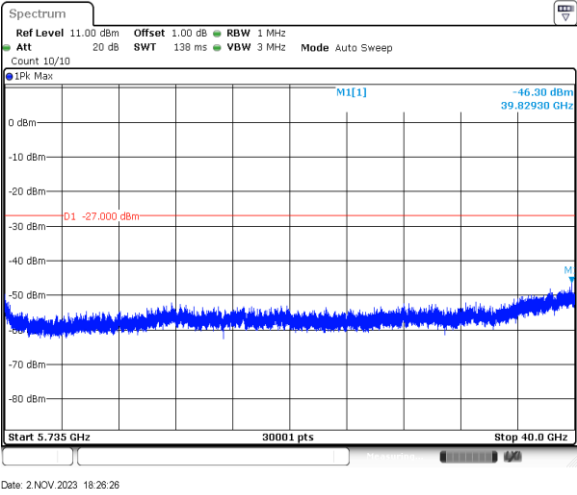
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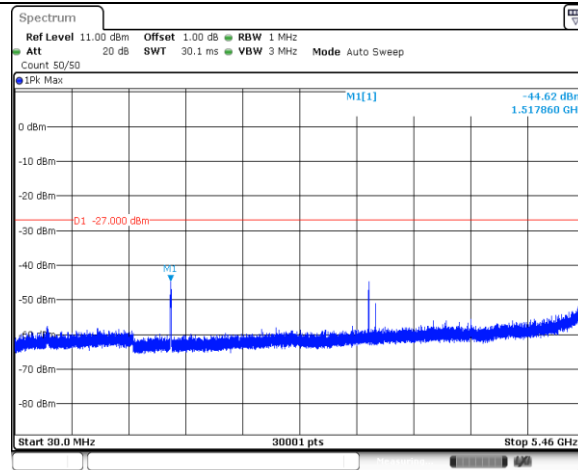
11A Ant2 5700 30-5460



11A Ant2 5700 5735-40000

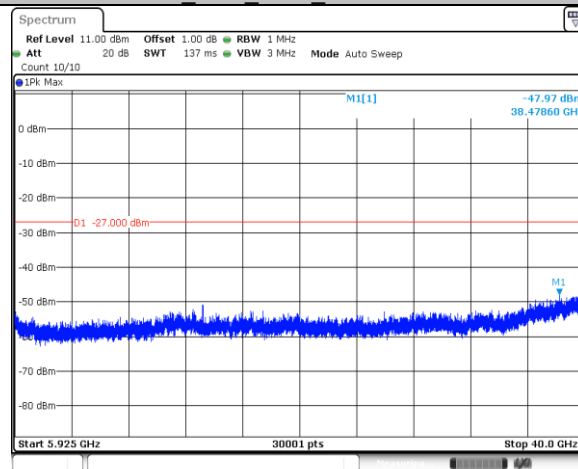


11A Ant2 5720 30-5460



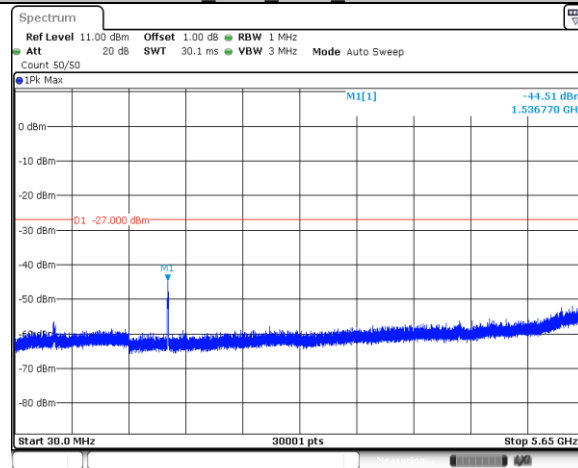
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11A Ant2 5720_5925~40000



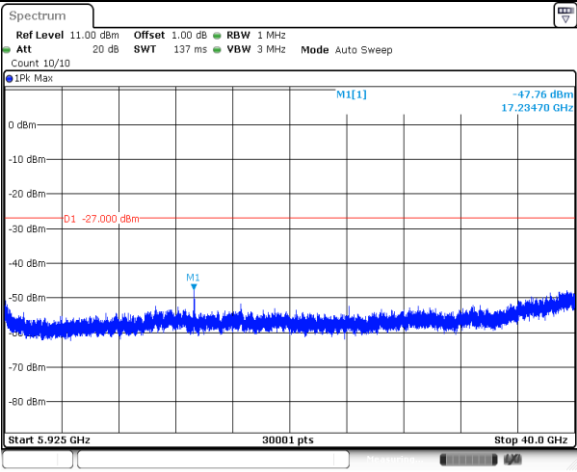
Date: 2 NOV 2023 20:06:49

11A Ant2 5745_30~5650

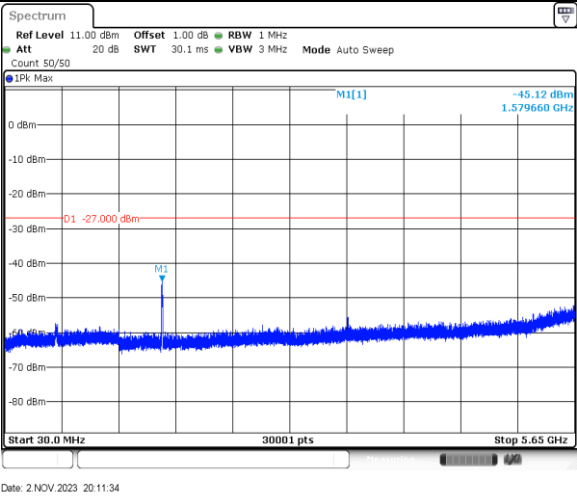


Date: 2 NOV 2023 20:09:20

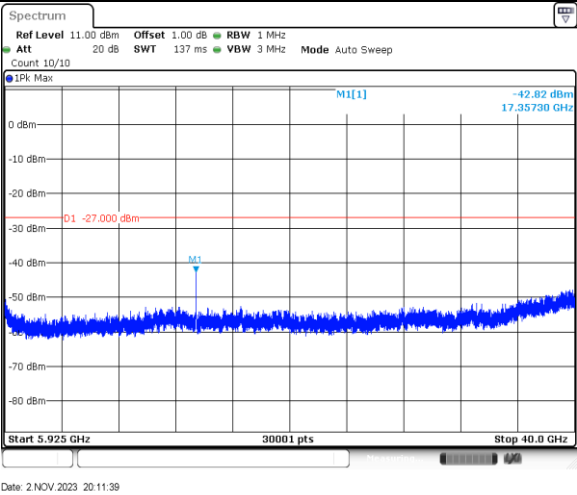
11A Ant2 5745_5925~40000



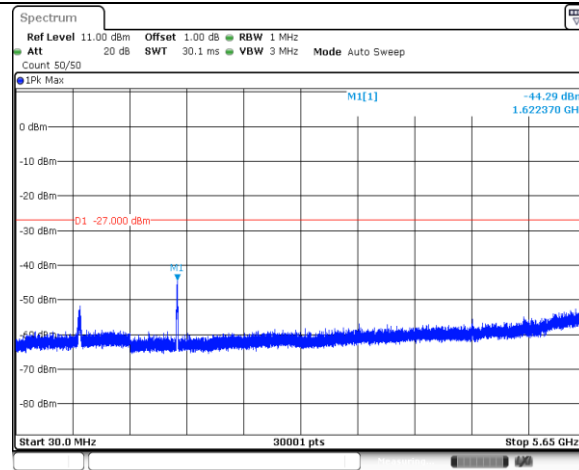
11A Ant2 5785 30-5650



11A Ant2 5785 5925-40000

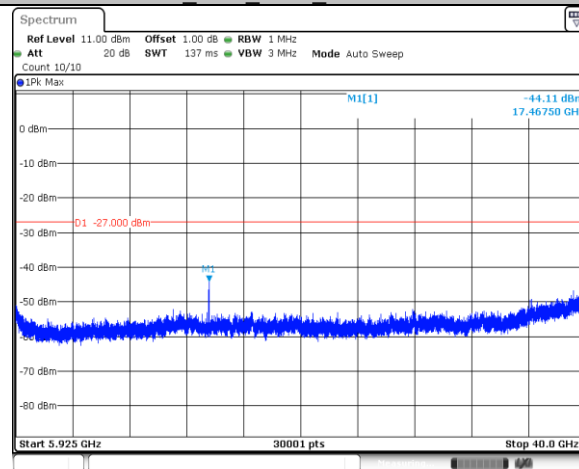


11A Ant2 5825 30-5650



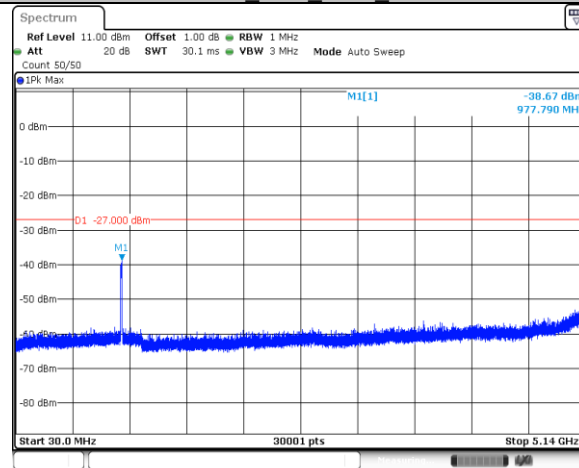
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11A Ant2 5825 5925~40000



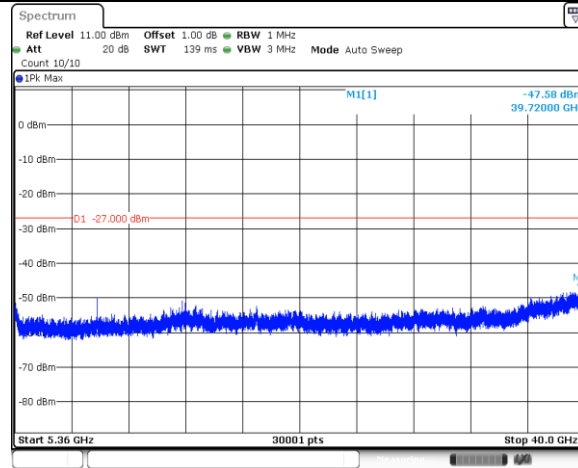
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11N20SISO Ant2 5180 30~5140



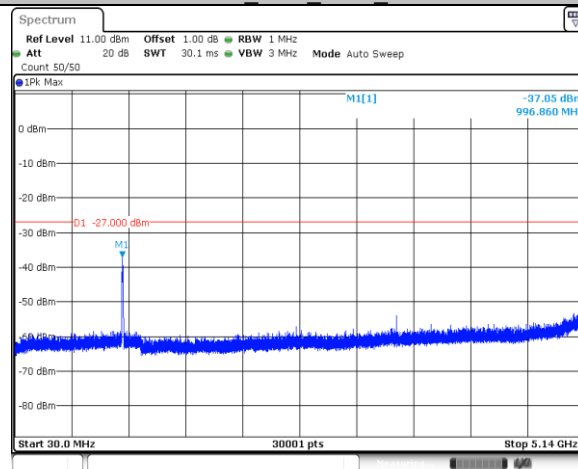
Date: 2 NOV 2023 20:16:28

11N20SISO Ant2 5180 5360~40000



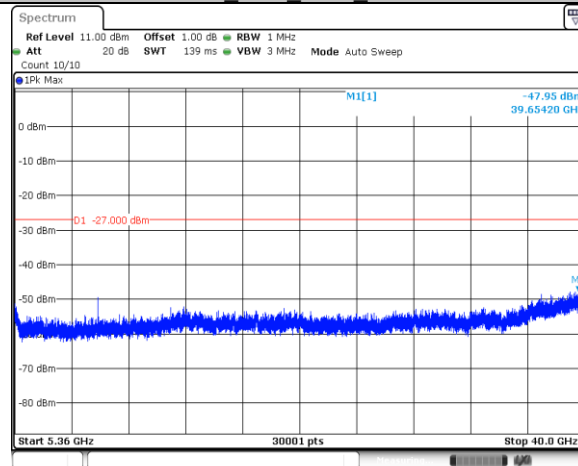
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11N20SISO Ant2 5200 30~5140



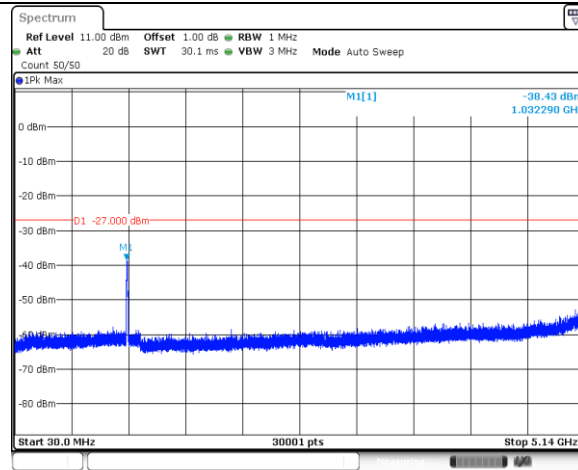
Date: 2 NOV 2023 20:19:07

11N20SISO Ant2 5200 5360~40000



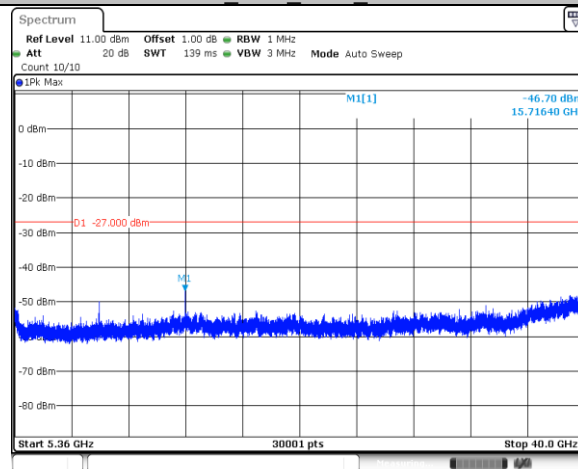
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11N20SISO Ant2 5240 30~5140



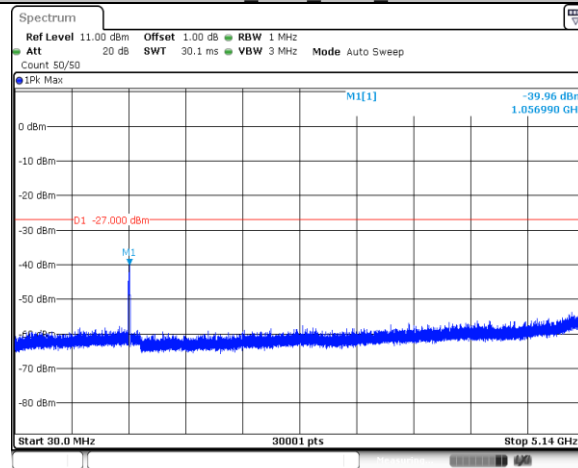
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11N20SISO Ant2 5240 5360-40000



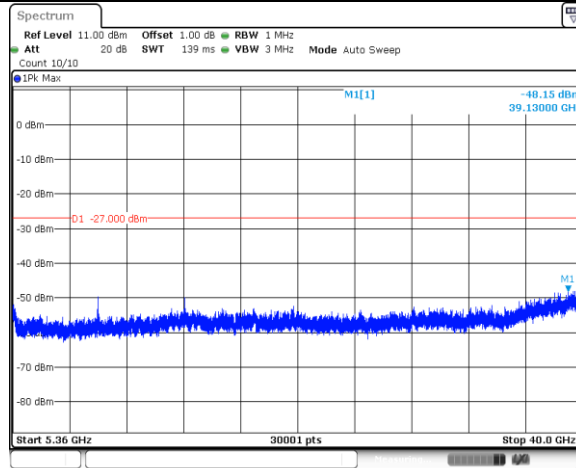
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11N20SISO Ant2 5260 30-5140



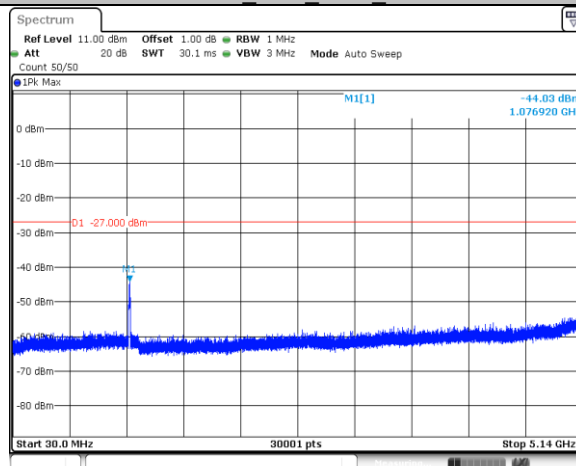
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11N20SISO Ant2 5260 5360-40000



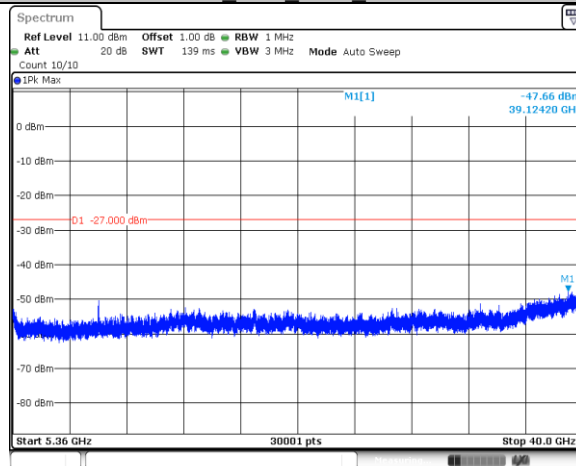
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11N20SISO Ant2 5280 30~5140



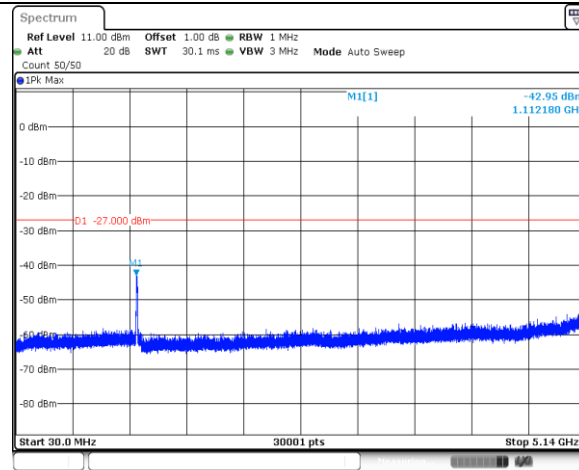
Date: 2 NOV 2023 20:25:31

11N20SISO Ant2 5280 5360~40000



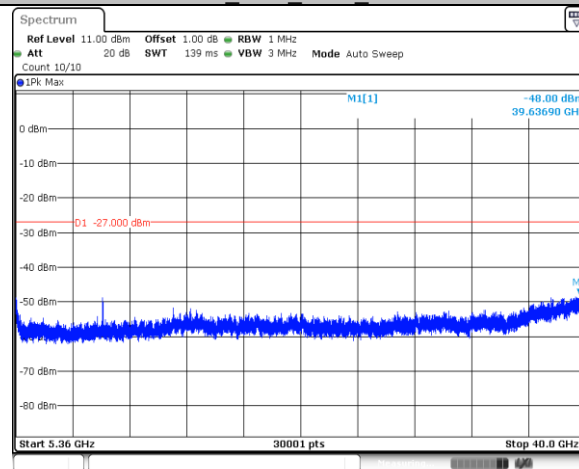
Date: 2 NOV 2023 20:25:36

11N20SISO Ant2 5320 30~5140



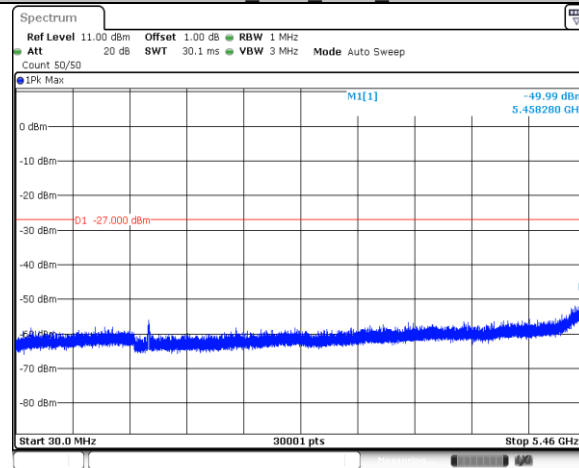
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11N20SISO Ant2 5320 5360-40000



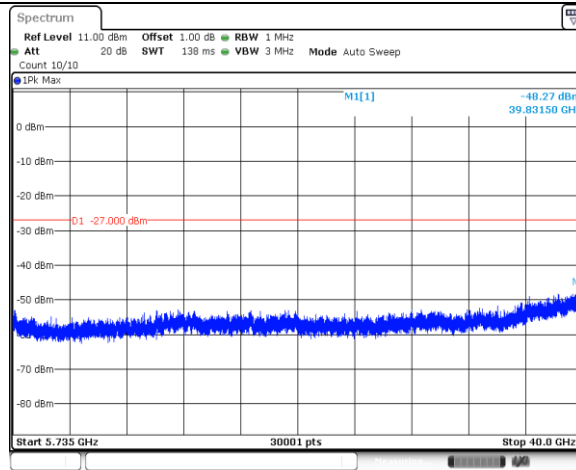
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11N20SISO Ant2 5500 30-5460



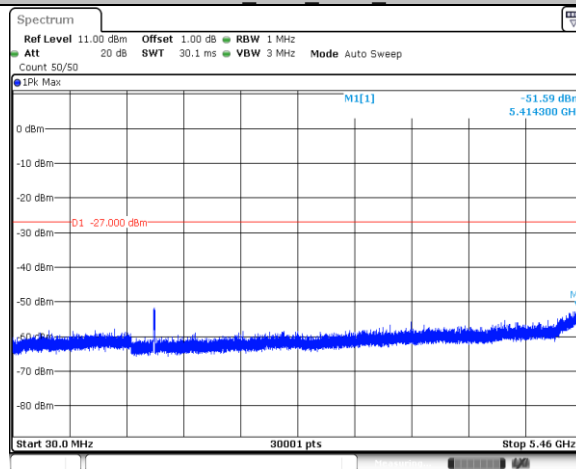
Date: 2 NOV 2023 21:05:30

11N20SISO Ant2 5500 5735-40000



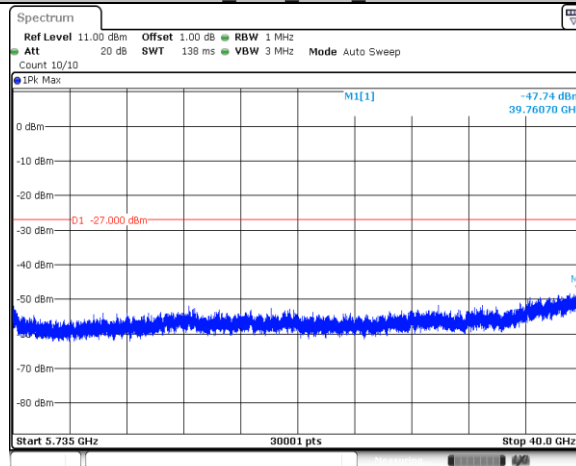
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11N20SISO Ant2 5580 30~5460



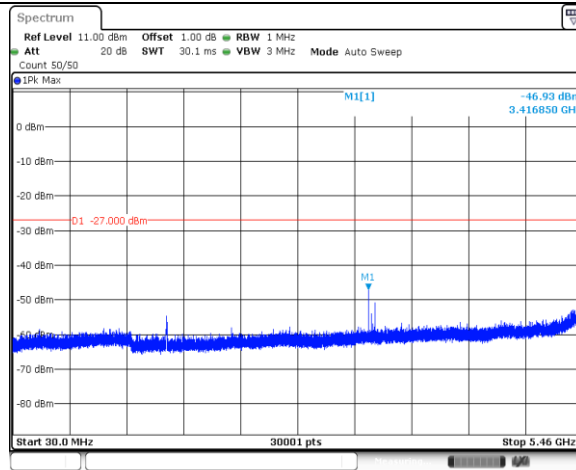
Date: 2 NOV 2023 21:07:27

11N20SISO Ant2 5580 5735~40000



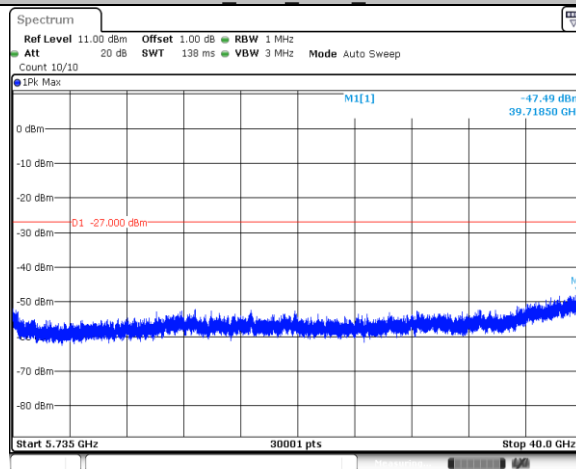
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11N20SISO Ant2 5700 30~5460



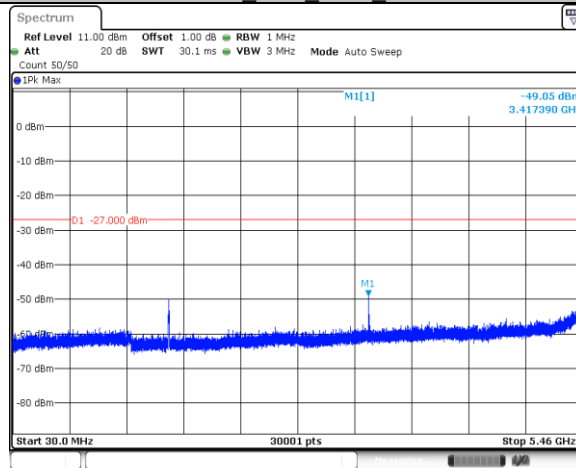
Date: 2 NOV 2023 21:09:35

11N20SISO Ant2 5700 5735-40000



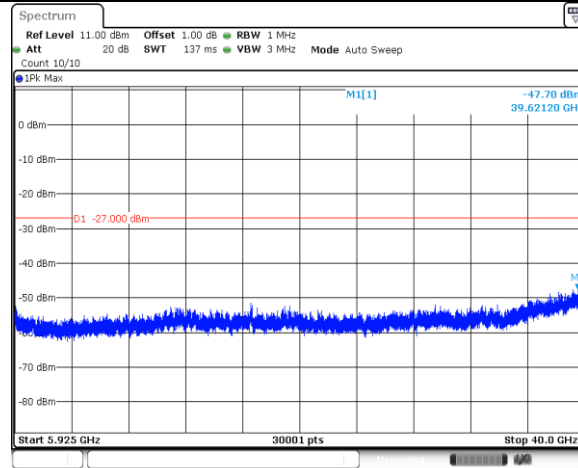
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11N20SISO Ant2 5720 30-5460



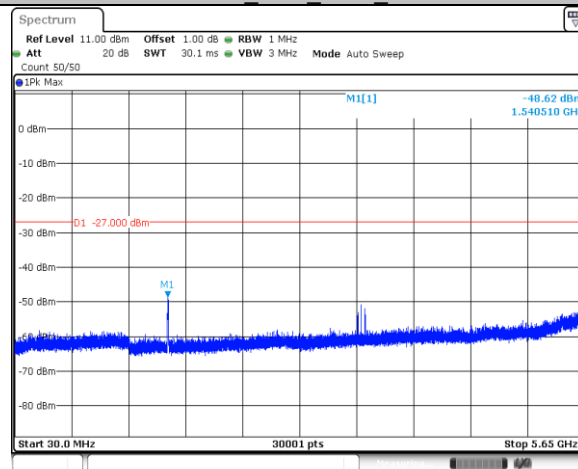
Date: 2 NOV 2023 21:12:24

11N20SISO Ant2 5720 5925-40000



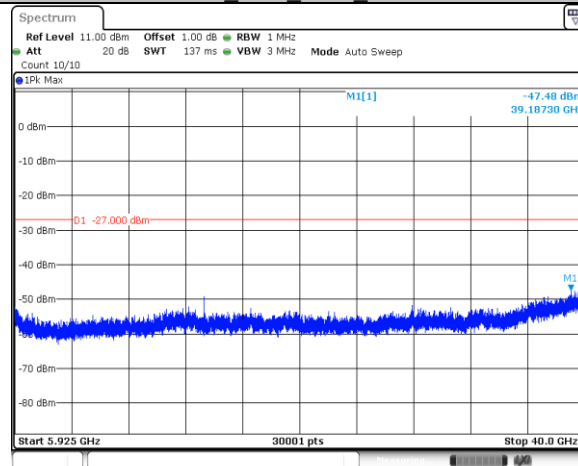
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11N20SISO Ant2 5745 30~5650



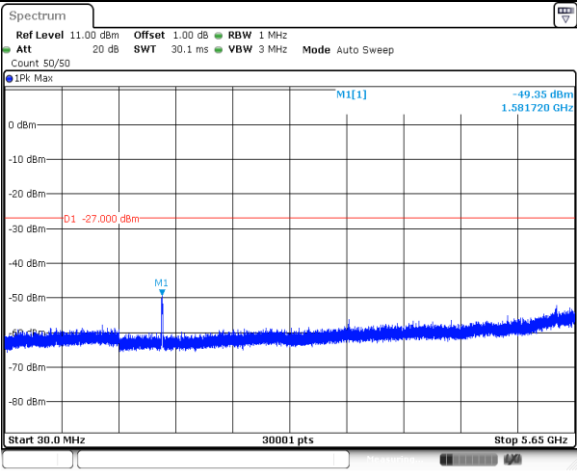
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11N20SISO Ant2 5745 5925~40000

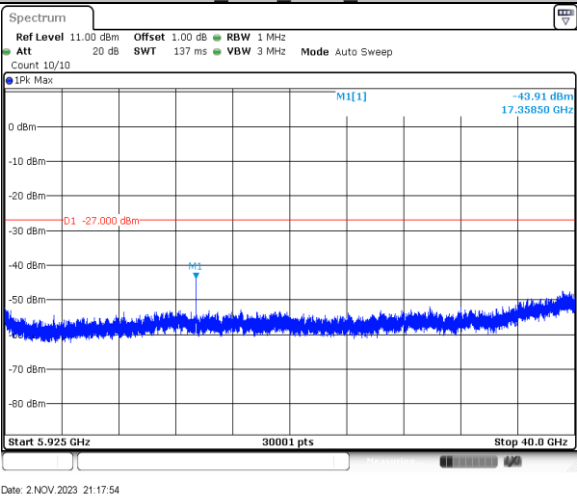


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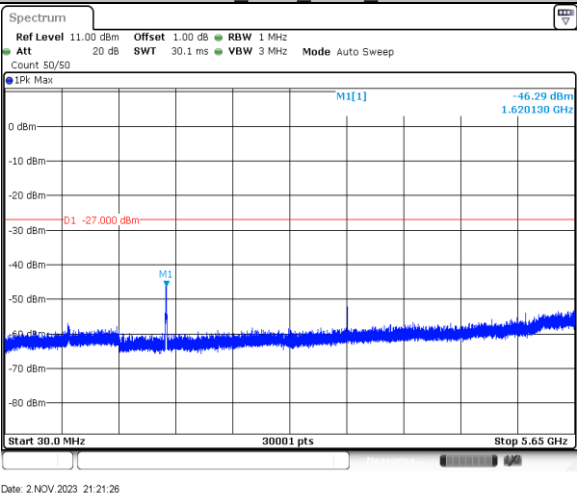
11N20SISO Ant2 5785 30~5650



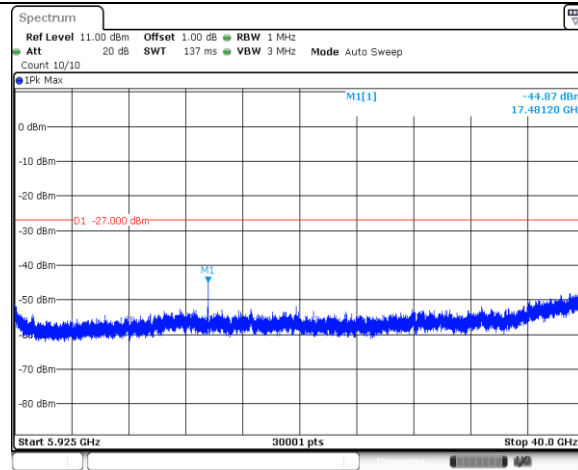
11N20SISO Ant2 5785 5925~40000



11N20SISO Ant2 5825 30~5650

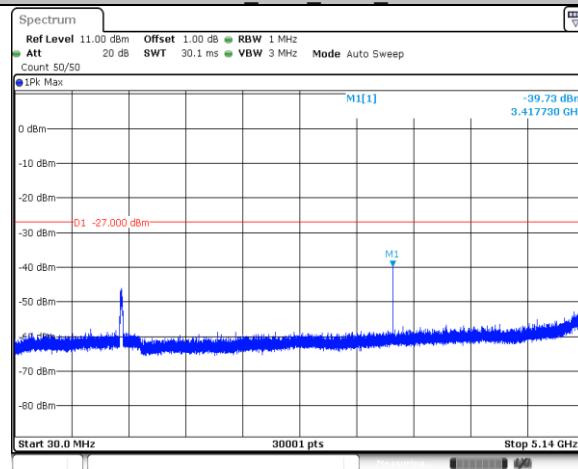


11N20SISO Ant2 5825 5925~40000



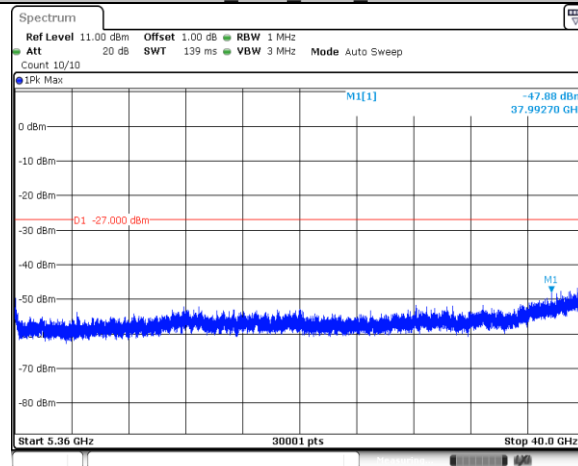
Date: 2 NOV 2023 21:21:31

11N40SISO Ant2 5190 30~5140



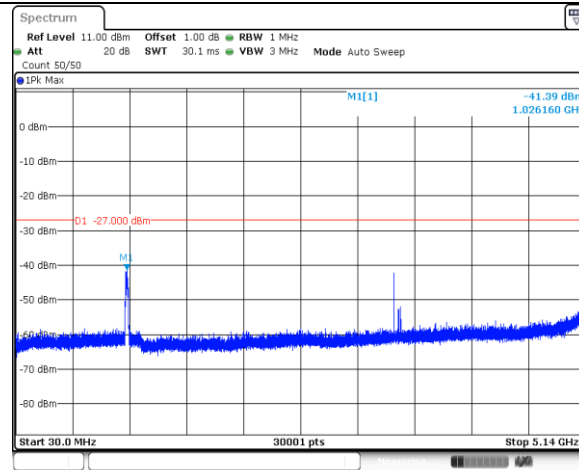
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11N40SISO Ant2 5190 5360~40000



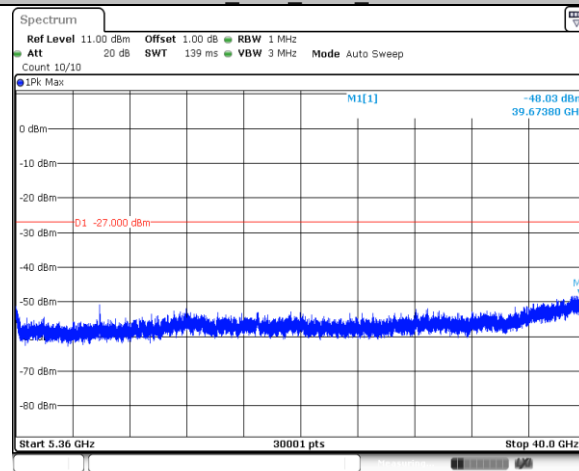
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11N40SISO Ant2 5230 30~5140



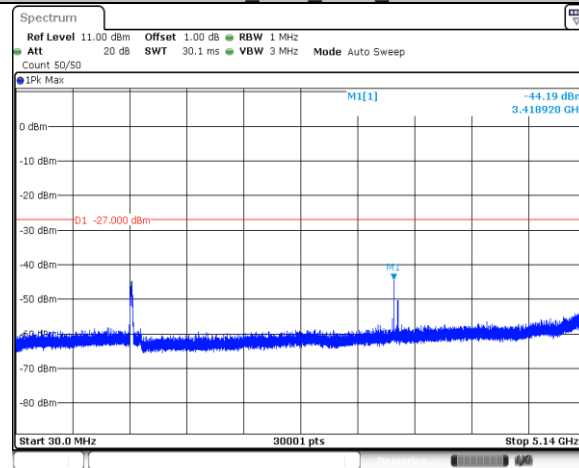
Date: 2 NOV 2023 21:25:55

11N40SISO Ant2 5230 5360-40000



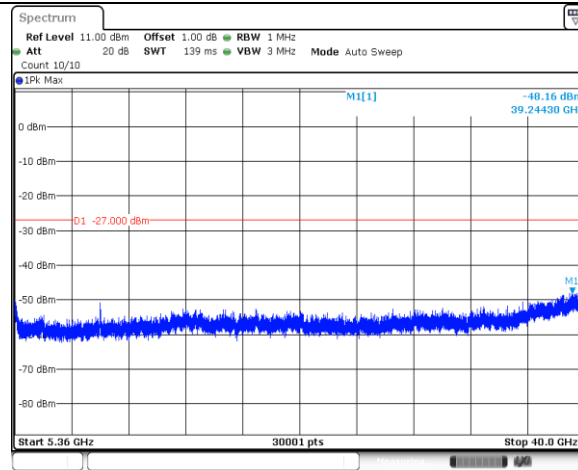
Date: 2 NOV 2023 21:26:00

11N40SISO Ant2 5270 30-5140



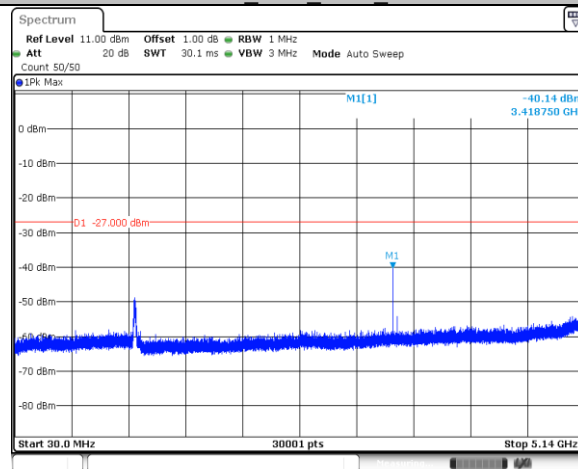
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11N40SISO Ant2 5270 5360-40000



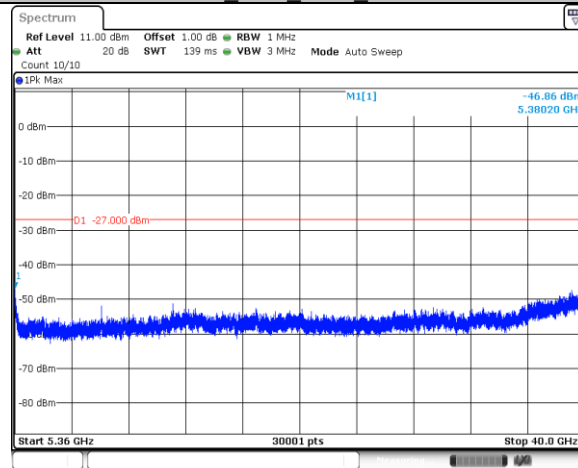
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11N40SISO Ant2 5310 30~5140



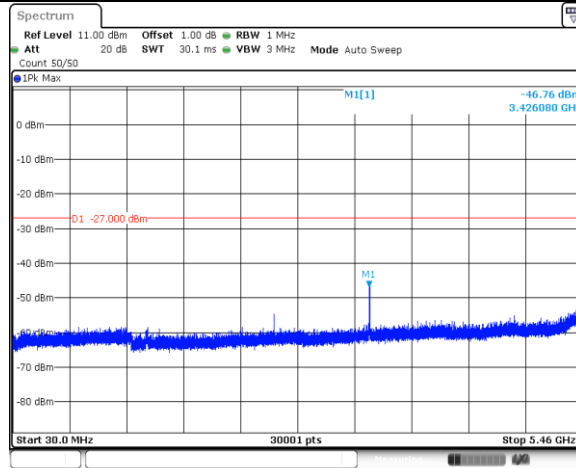
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11N40SISO Ant2 5310 5360~40000



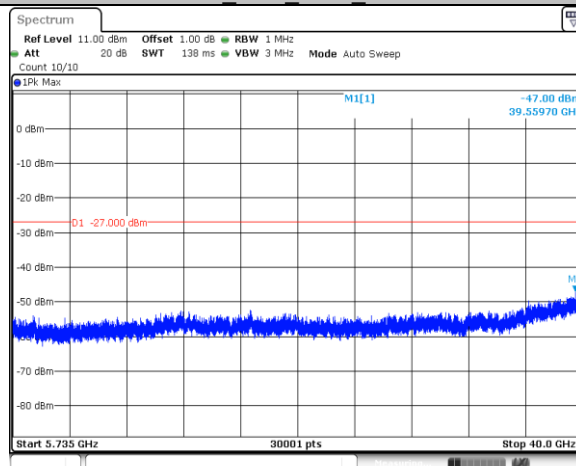
Date: 2 NOV 2023 21:31:22

11N40SISO Ant2 5510 30~5460



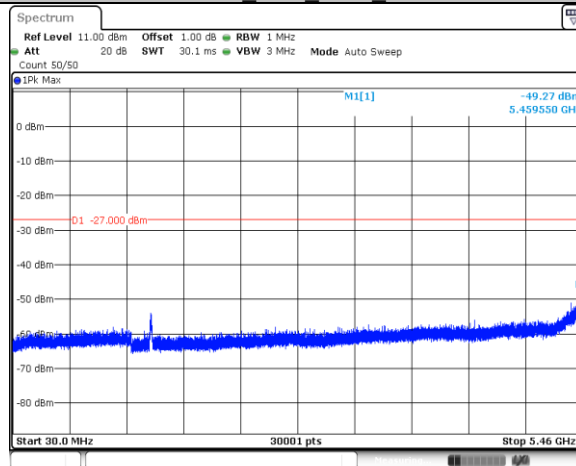
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11N40SISO Ant2 5510 5735-40000



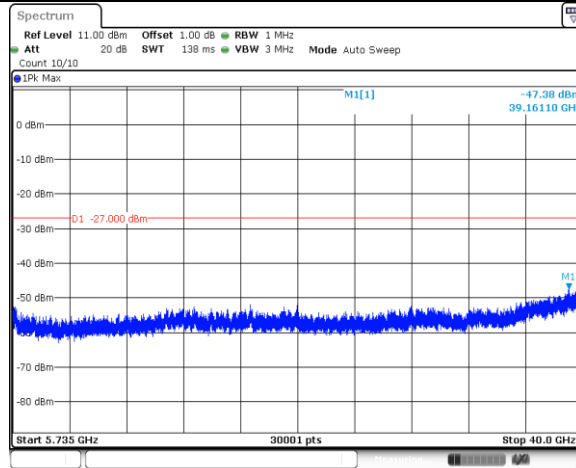
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11N40SISO Ant2 5550 30-5460



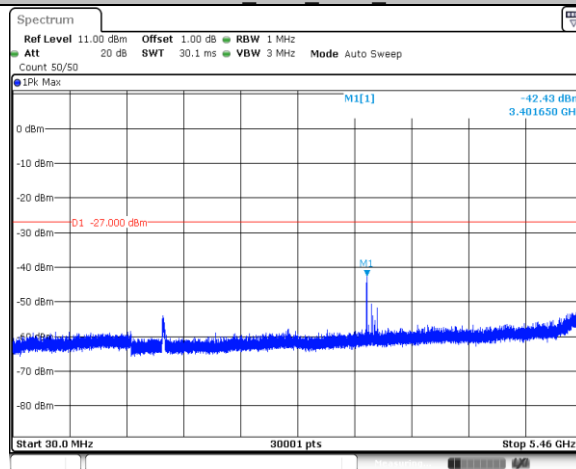
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11N40SISO Ant2 5550 5735-40000



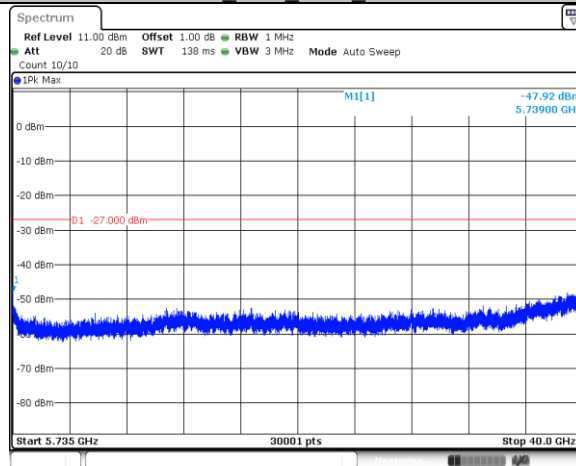
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11N40SISO Ant2 5670 30~5460



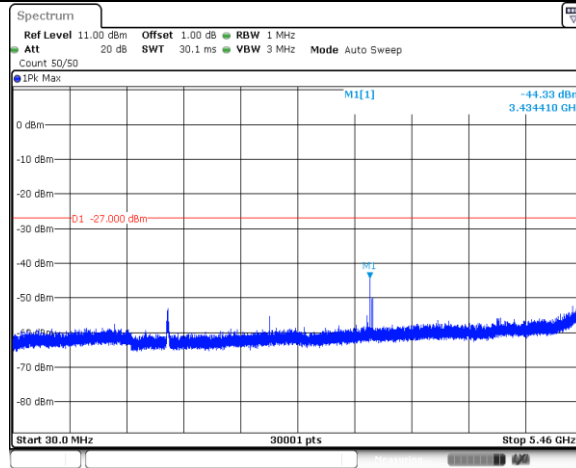
Date: 2 NOV 2023 22:02:11

11N40SISO Ant2 5670 5735~40000



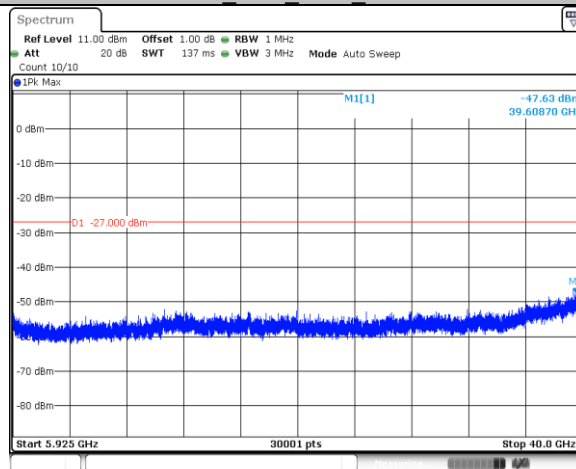
Date: 2 NOV 2023 22:02:16

11N40SISO Ant2 5710 30~5460



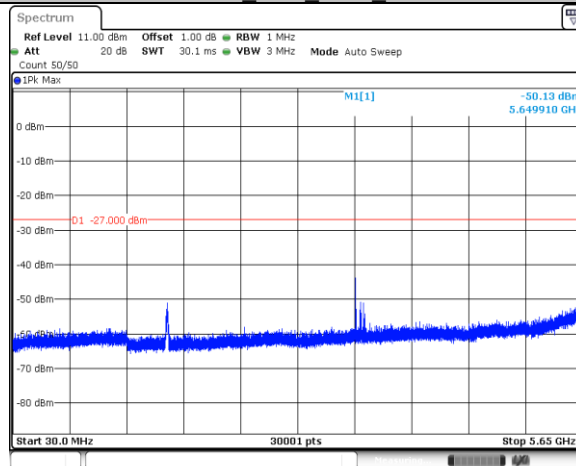
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11N40SISO Ant2 5710 5925-40000



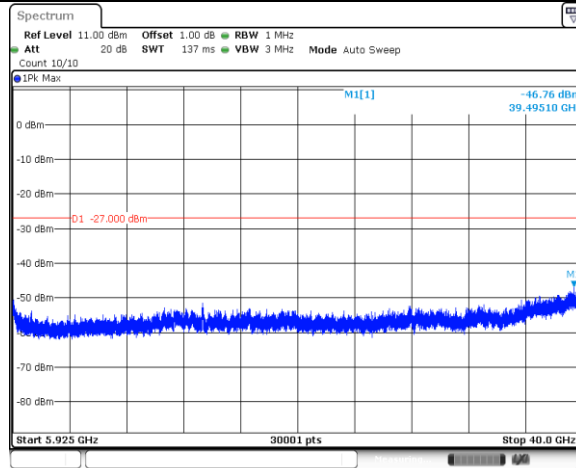
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11N40SISO Ant2 5755 30-5650



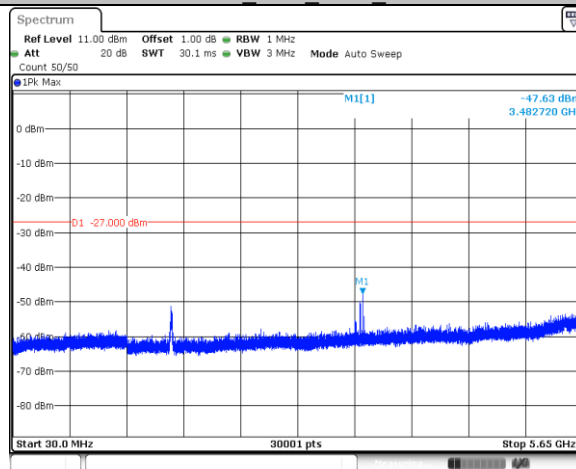
Date: 2 NOV 2023 22:07:23

11N40SISO Ant2 5755 5925-40000



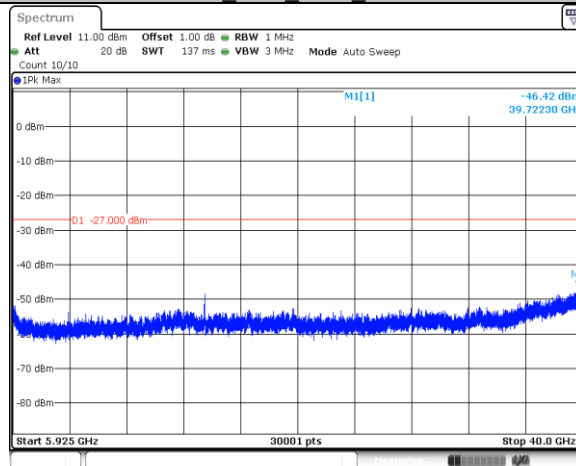
Date: 2 NOV 2023 22:07:28

11N40SISO Ant2 5795 30~5650



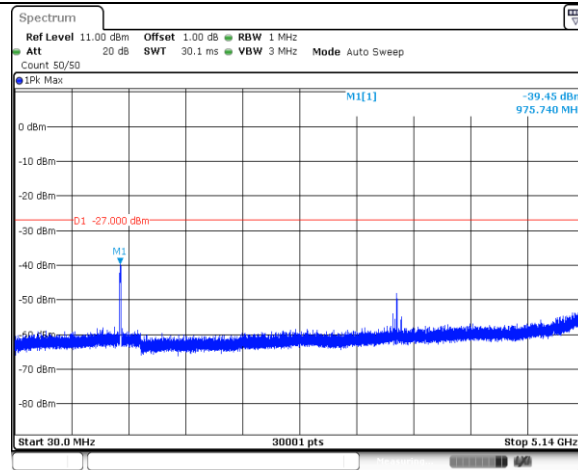
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11N40SISO Ant2 5795 5925~40000



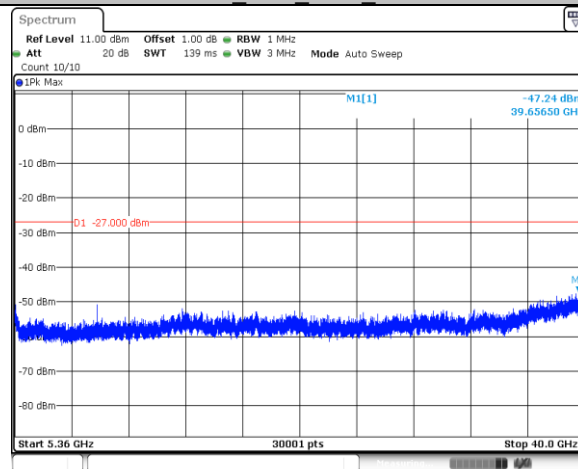
Date: 2 NOV 2023 22:10:09

11AC20SISO Ant2 5180 30~5140



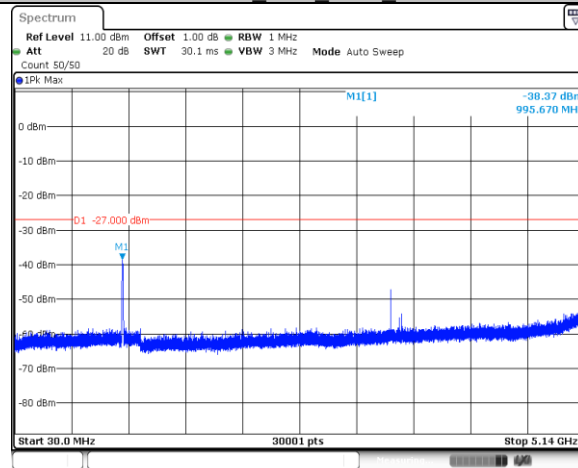
Date: 2 NOV 2023 22:12:08

11AC20SISO Ant2 5180 5360~40000



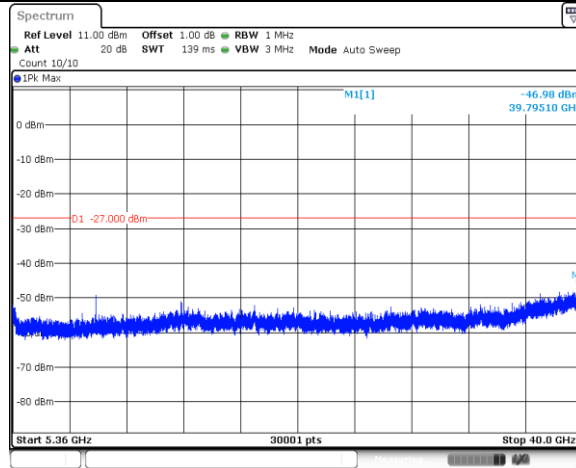
Date: 2 NOV 2023 22:12:13

11AC20SISO Ant2 5200 30~5140



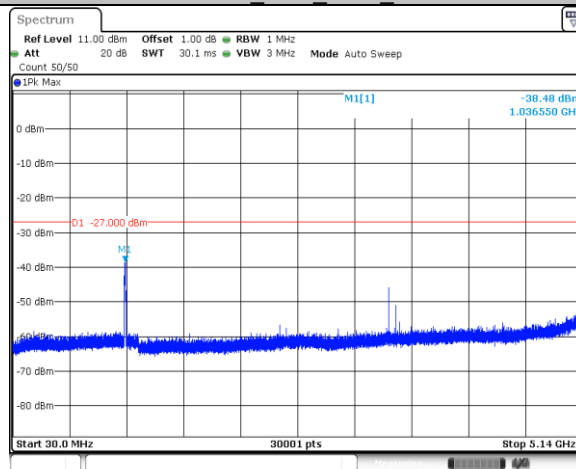
Date: 2 NOV 2023 22:14:14

11AC20SISO Ant2 5200 5360~40000



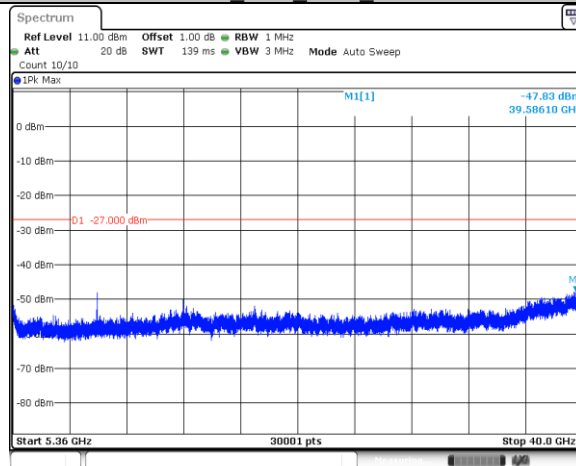
Date: 2 NOV 2023 22:14:19

11AC20SISO Ant2 5240 30~5140



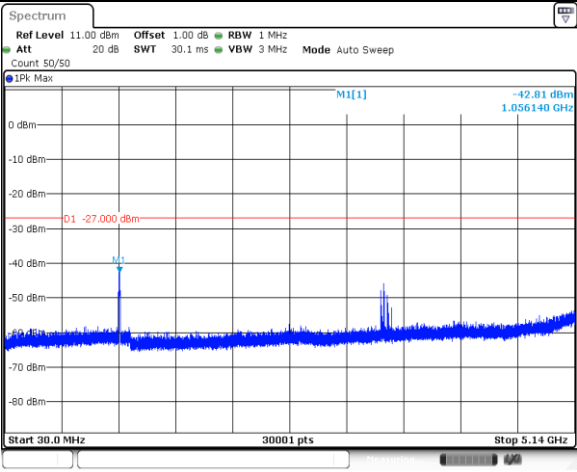
Date: 2 NOV 2023 22:16:54

11AC20SISO Ant2 5240 5360~40000



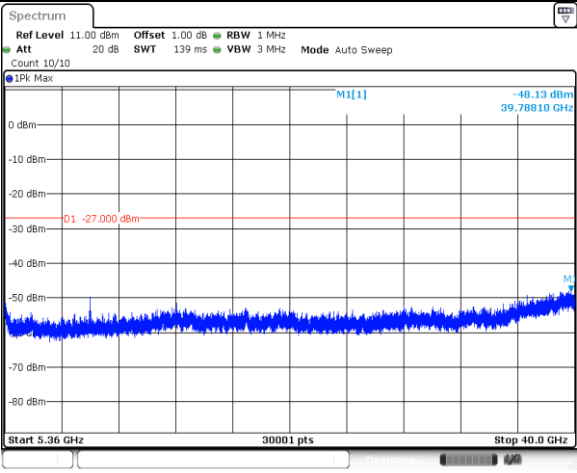
Date: 2 NOV 2023 22:16:59

11AC20SISO Ant2 5260 30~5140



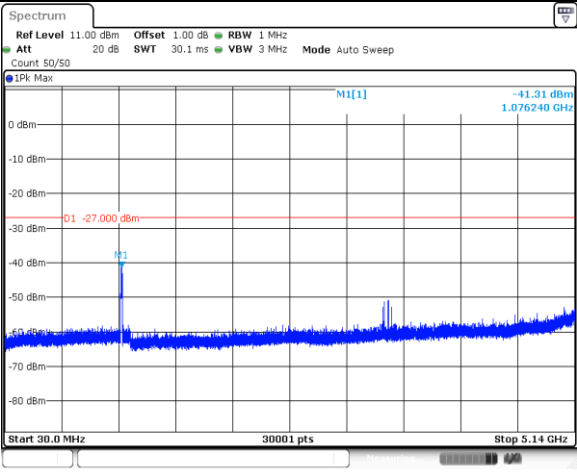
Date: 2 NOV 2023 22:18:33

11AC20SISO Ant2 5260 5360~40000



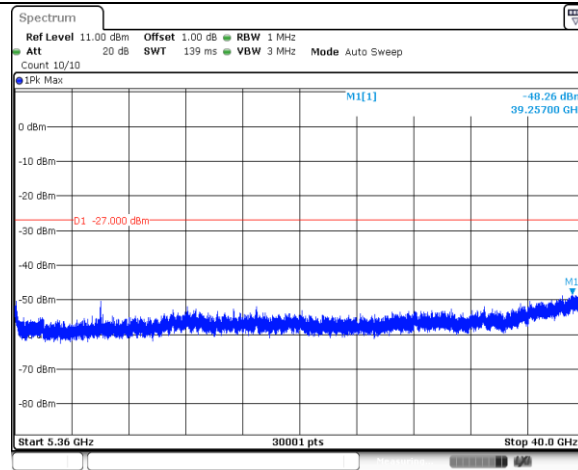
Date: 2 NOV 2023 22:18:39

11AC20SISO Ant2 5280 30~5140



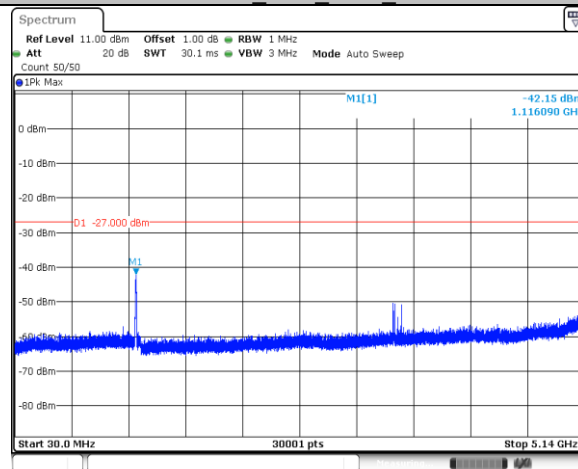
Date: 2 NOV 2023 22:21:06

11AC20SISO Ant2 5280 5360~40000



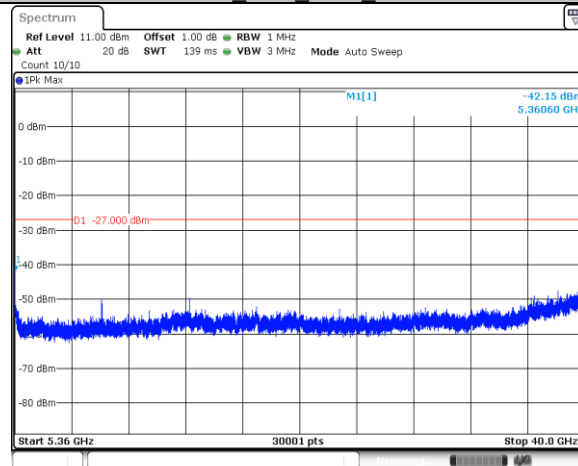
Date: 2 NOV 2023 22:21:12

11AC20SISO Ant2 5320 30~5140



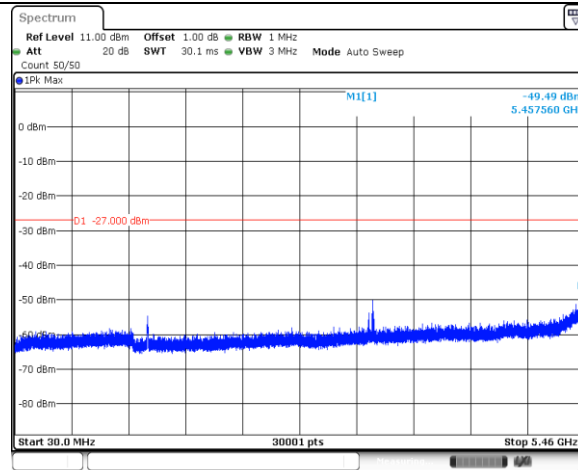
Date: 2 NOV 2023 22:23:30

11AC20SISO Ant2 5320 5360~40000



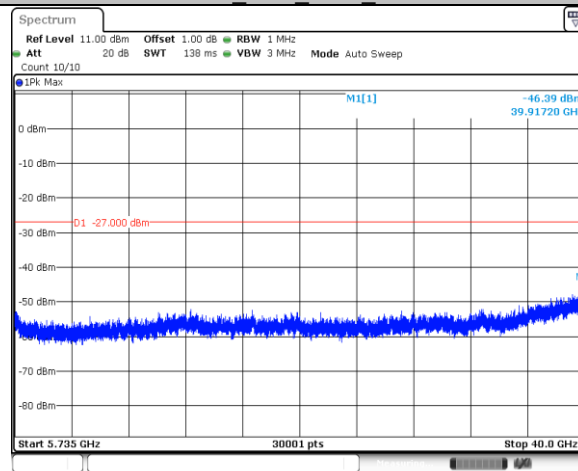
Date: 2 NOV 2023 22:23:35

11AC20SISO Ant2 5500 30~5460



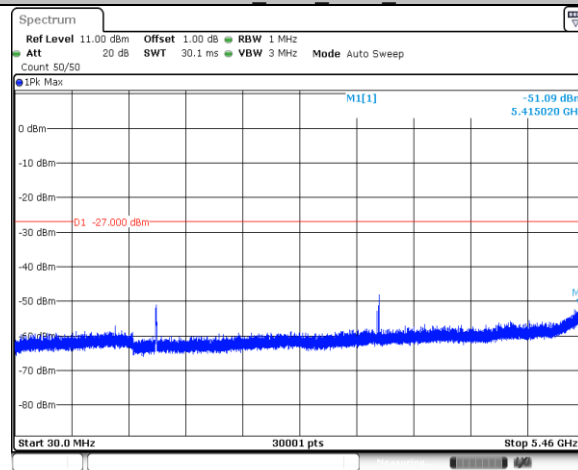
Date: 2 NOV 2023 22:25:49

11AC20SISO Ant2 5500 5735~40000



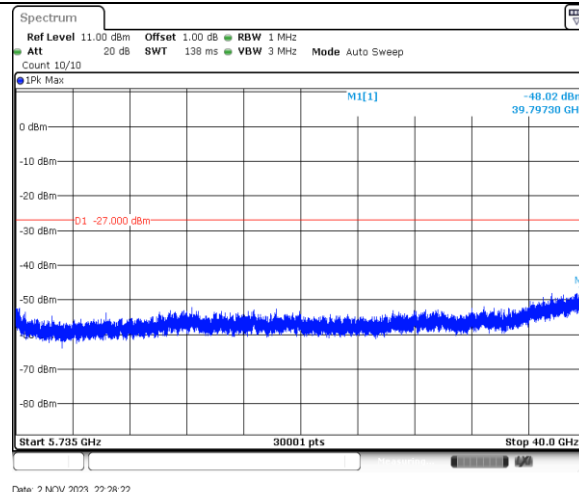
Date: 2 NOV 2023 22:25:54

11AC20SISO Ant2 5580 30~5460

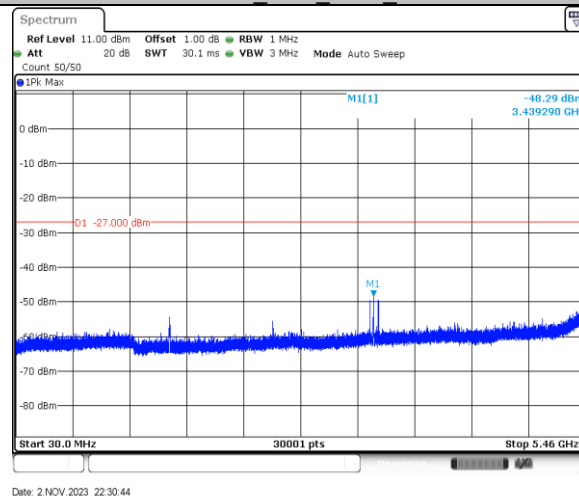


Date: 2 NOV 2023 22:28:17

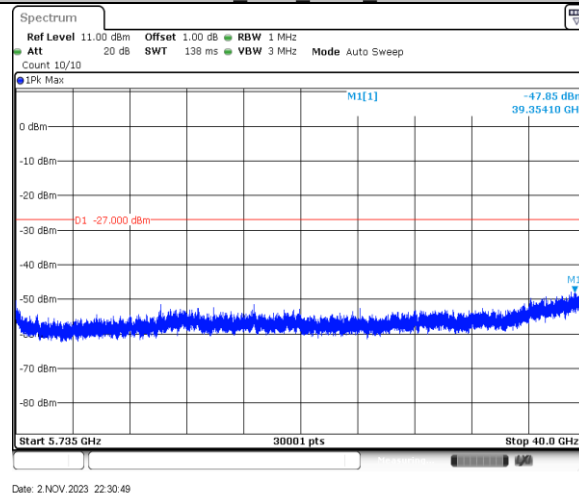
11AC20SISO Ant2 5580 5735~40000



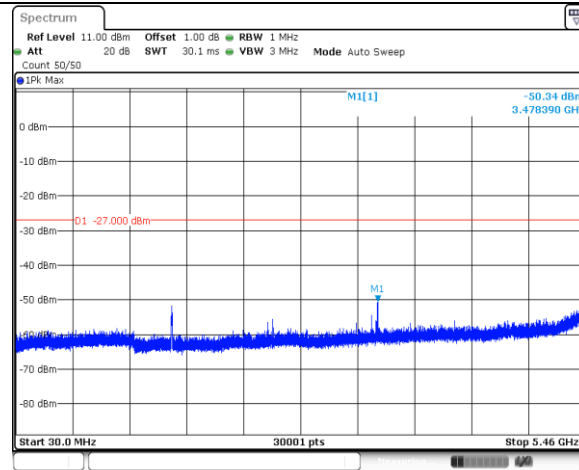
11AC20SISO Ant2 5700 30~5460



11AC20SISO Ant2 5700 5735~40000

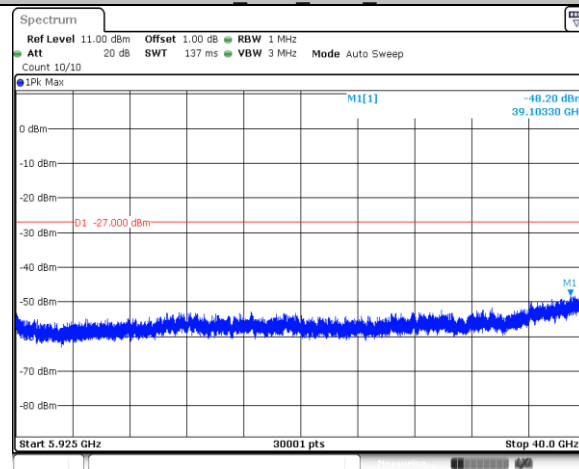


11AC20SISO Ant2 5720 30~5460



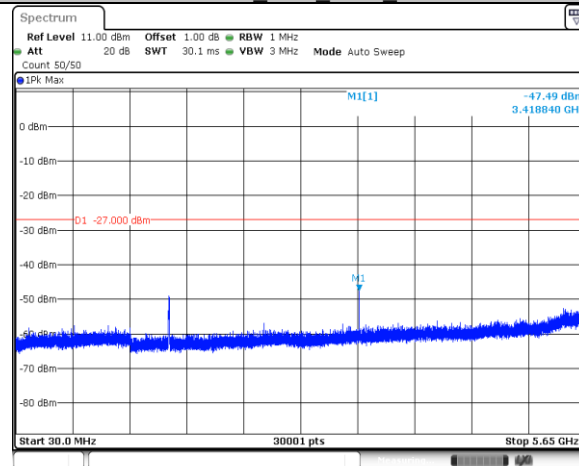
Date: 2 NOV 2023 22:33:10

11AC20SISO Ant2 5720 5925~40000



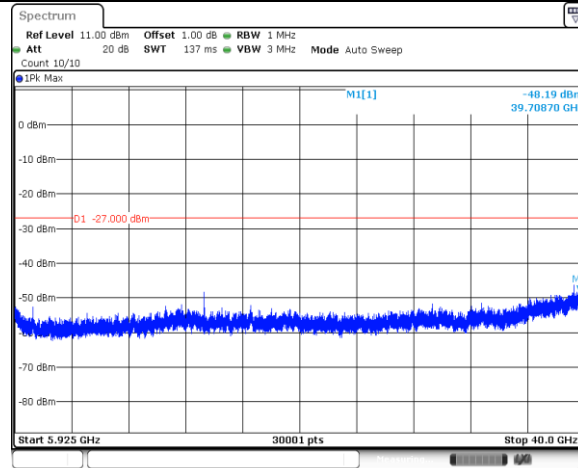
Date: 2 NOV 2023 22:33:15

11AC20SISO Ant2 5745 30~5650



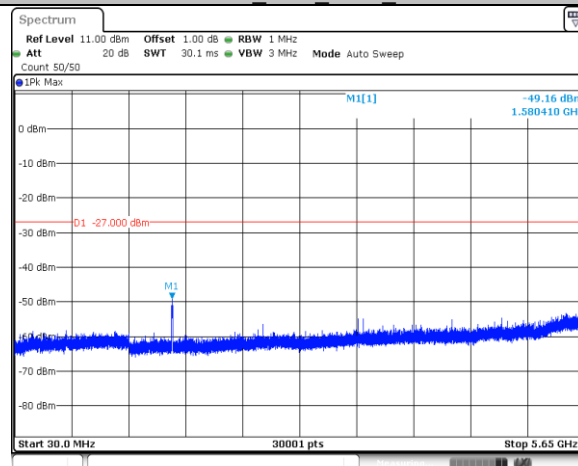
Date: 2 NOV 2023 22:35:33

11AC20SISO Ant2 5745 5925~40000



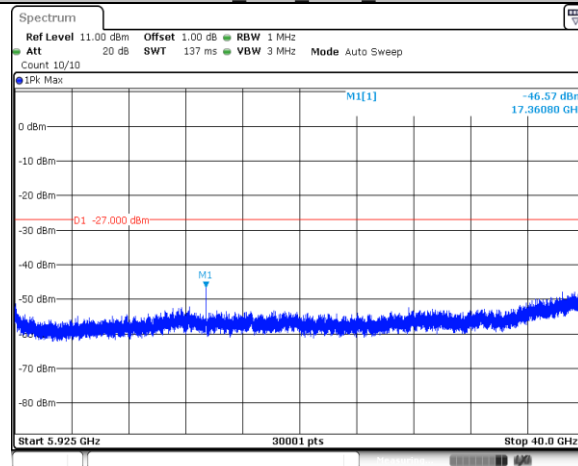
Date: 2 NOV 2023 22:35:38

11AC20SISO Ant2 5785 30~5650



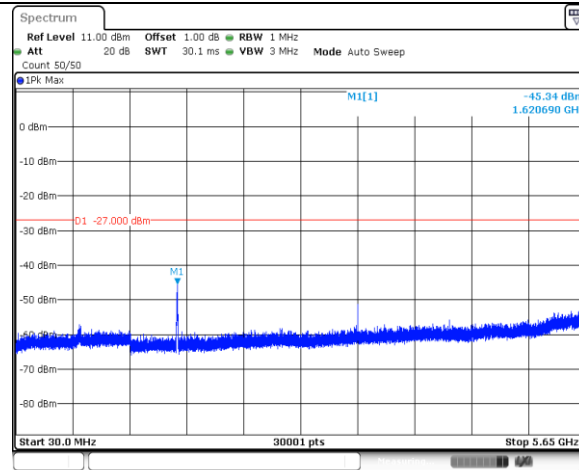
Date: 2 NOV 2023 22:37:32

11AC20SISO Ant2 5785 5925~40000



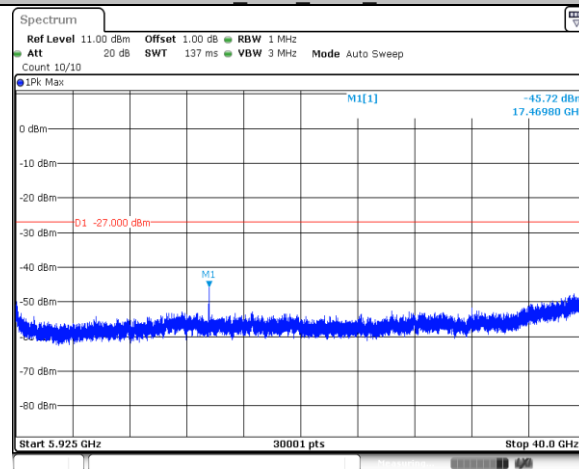
Date: 2 NOV 2023 22:37:37

11AC20SISO Ant2 5825 30~5650



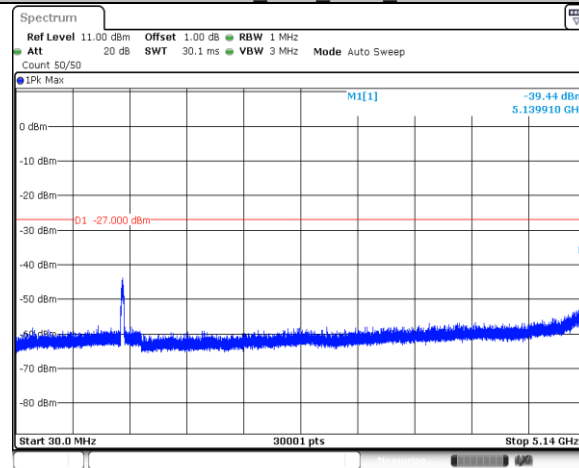
Date: 2 NOV 2023 22:40:08

11AC20SISO Ant2 5825 5925~40000



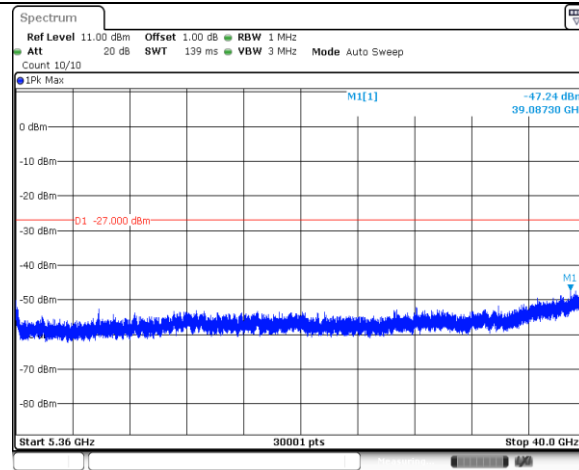
Date: 2 NOV 2023 22:40:13

11AC40SISO Ant2 5190 30~5140



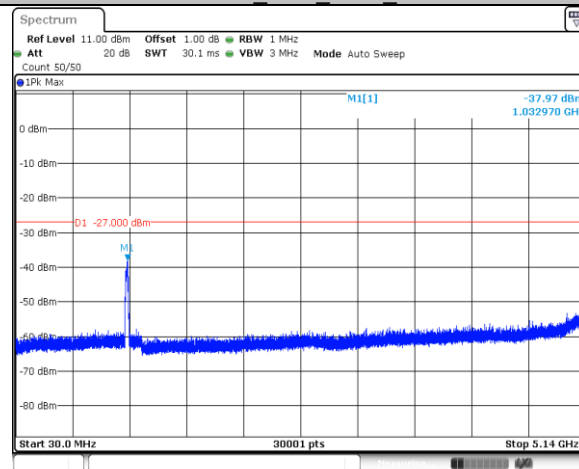
Date: 3 NOV 2023 09:35:57

11AC40SISO Ant2 5190 5360~40000



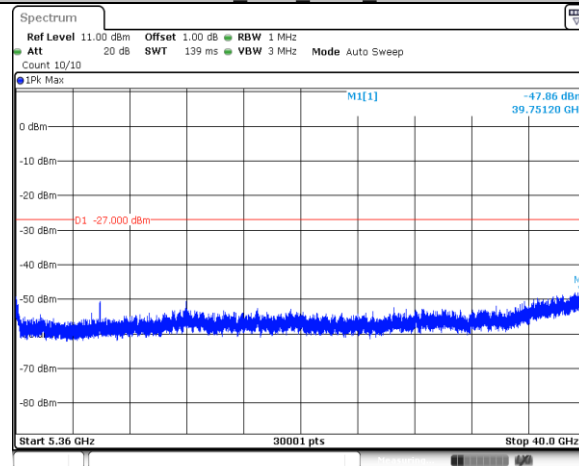
Date: 3.NOV.2023 09:36:03

11AC40SISO Ant2 5230 30~5140



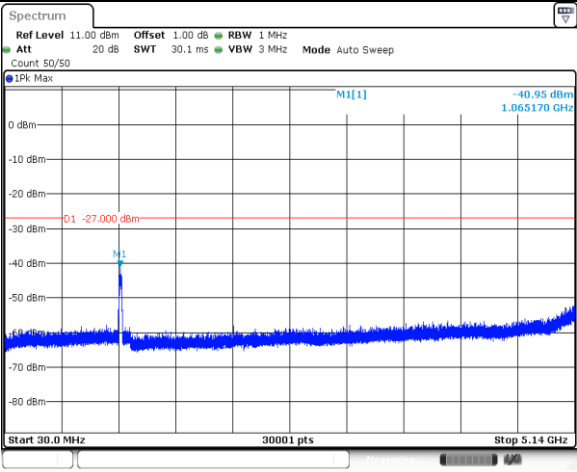
Date: 3.NOV.2023 09:39:22

11AC40SISO Ant2 5230 5360~40000

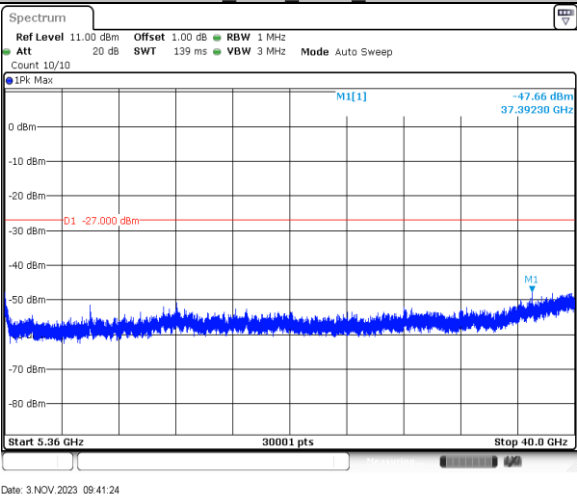


Date: 3.NOV.2023 09:39:27

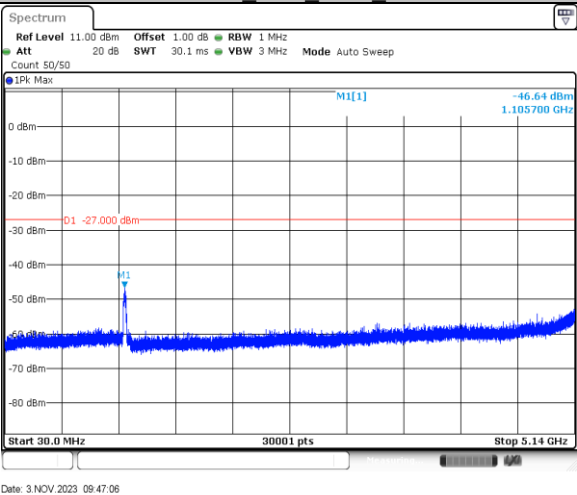
11AC40SISO Ant2 5270 30~5140



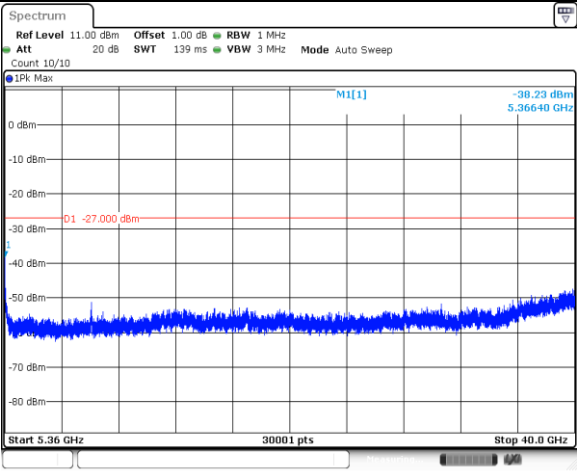
11AC40SISO Ant2 5270 5360~40000



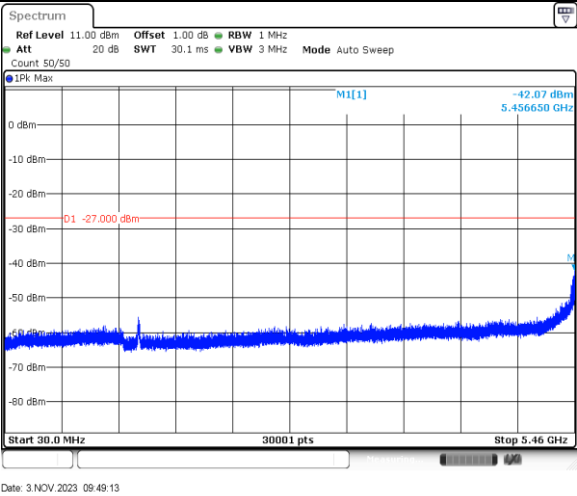
11AC40SISO Ant2 5310 5360~40000



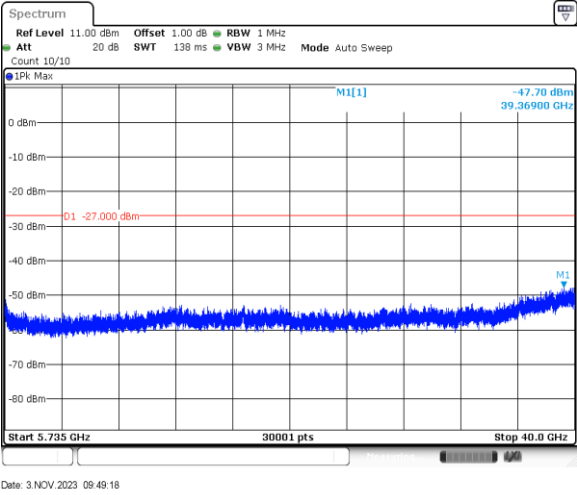
11AC40SISO Ant2 5310 5360~40000



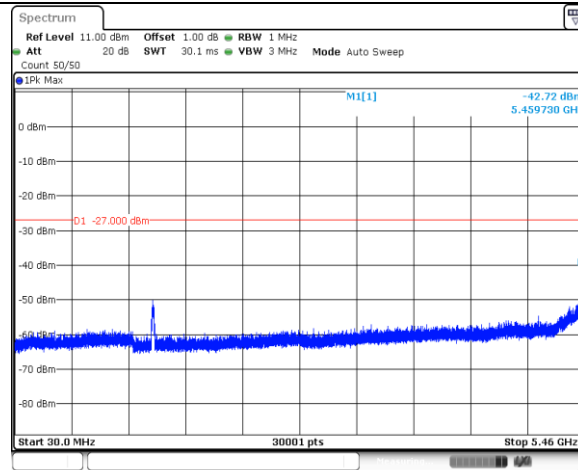
11AC40SISO Ant2 5510 30~5460



11AC40SISO Ant2 5510 5735~40000

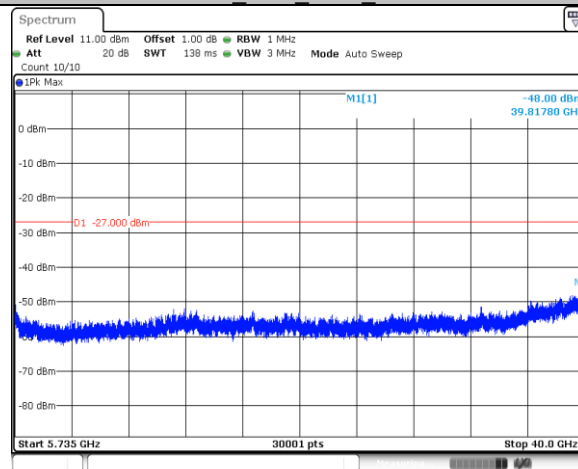


11AC40SISO Ant2 5550 30~5460



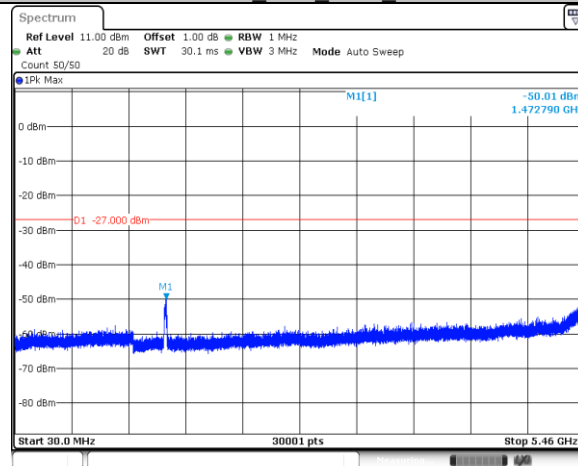
Date: 3.NOV.2023 09:51:34

11AC40SISO Ant2 5550 5735~40000



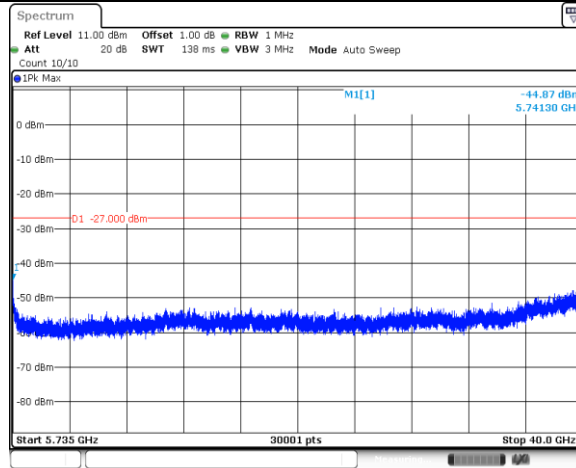
Date: 3.NOV.2023 09:51:39

11AC40SISO Ant2 5670 30~5460



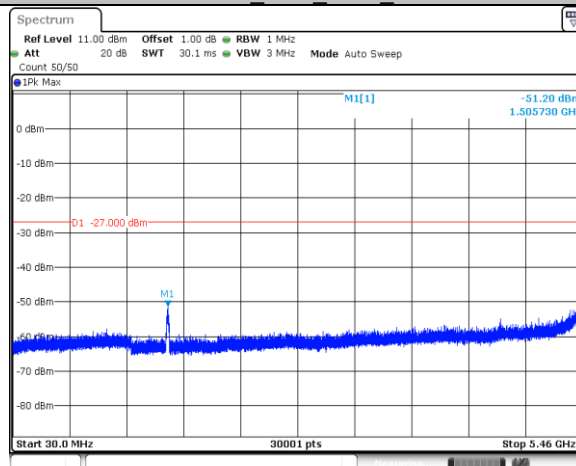
Date: 3.NOV.2023 09:56:03

11AC40SISO Ant2 5670 5735~40000



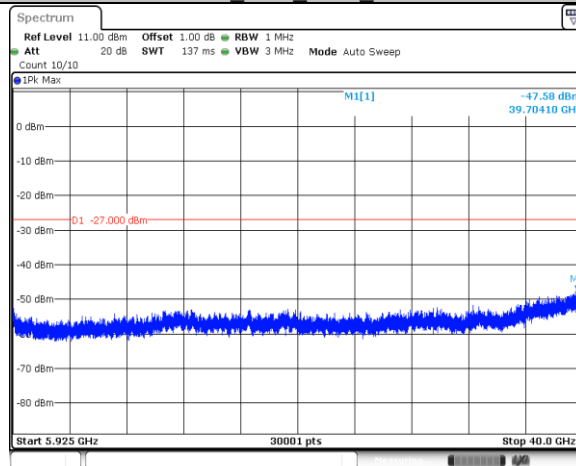
Date: 3.NOV.2023 09:56:08

11AC40SISO Ant2 5710 30~5460



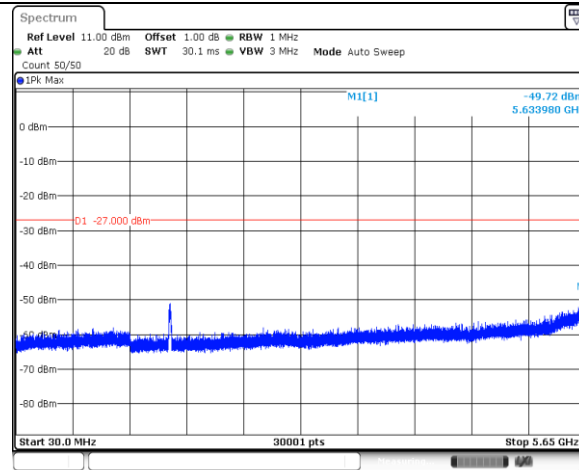
Date: 3.NOV.2023 09:58:47

11AC40SISO Ant2 5710 5925~40000



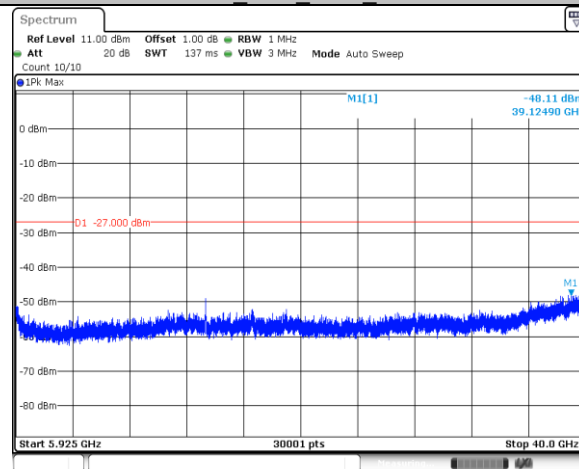
Date: 3.NOV.2023 09:58:52

11AC40SISO Ant2 5755 30~5650



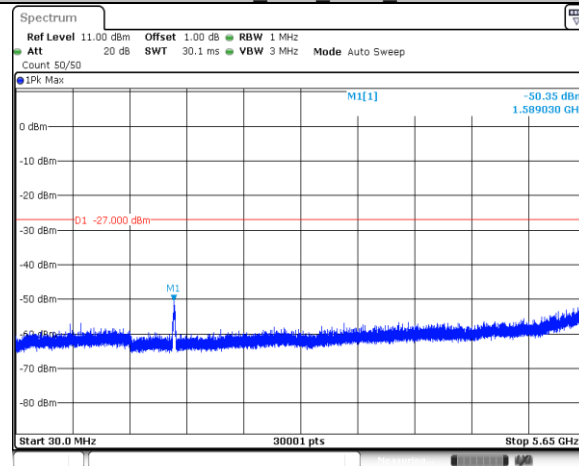
Date: 3.NOV.2023 10:00:53

11AC40SISO Ant2 5755 5925~40000



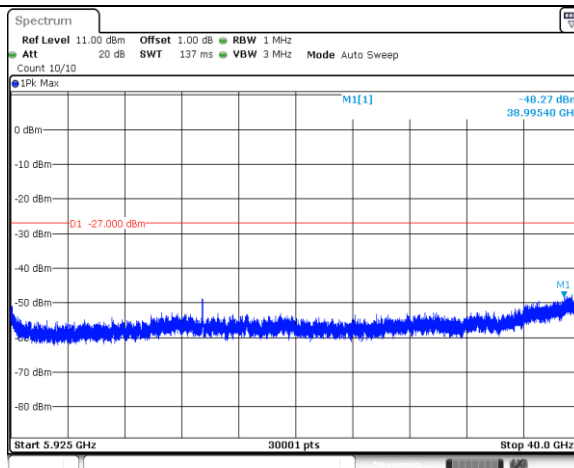
Date: 3.NOV.2023 10:00:58

11AC40SISO Ant2 5795 30~5650



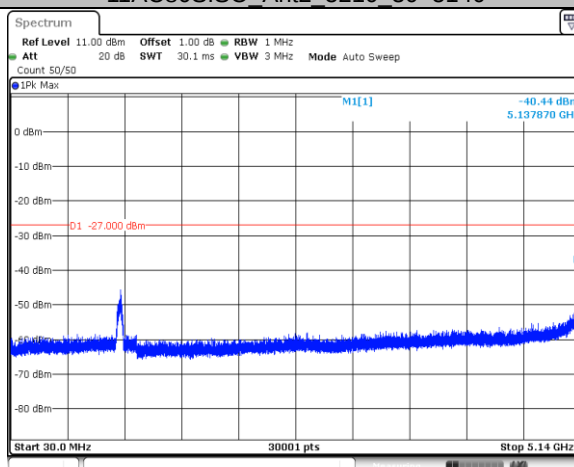
Date: 3.NOV.2023 10:03:04

11AC40SISO Ant2 5795 5925~40000



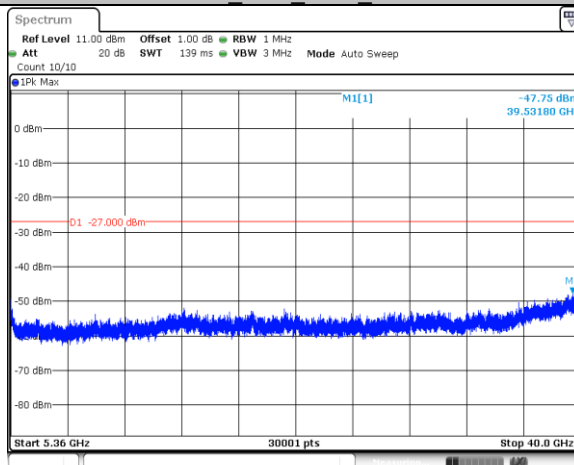
Date: 3.NOV.2023 10:03:09

11AC80SISO Ant2 5210 30~5140



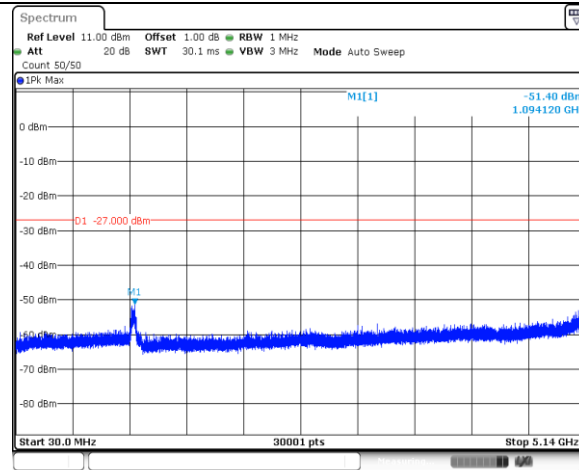
Date: 3.NOV.2023 10:48:57

11AC80SISO Ant2 5210 5360~40000



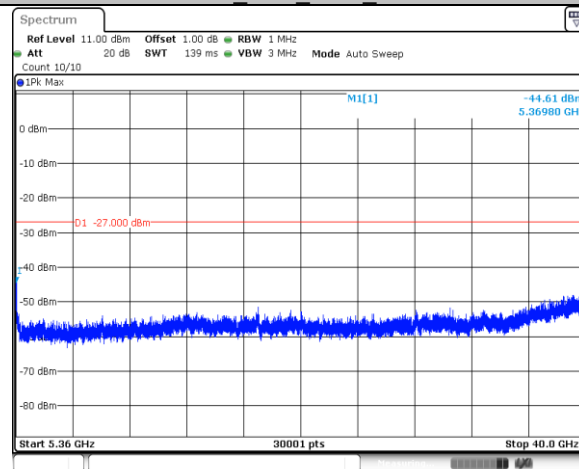
Date: 3.NOV.2023 10:49:02

11AC80SISO Ant2 5290 30~5140



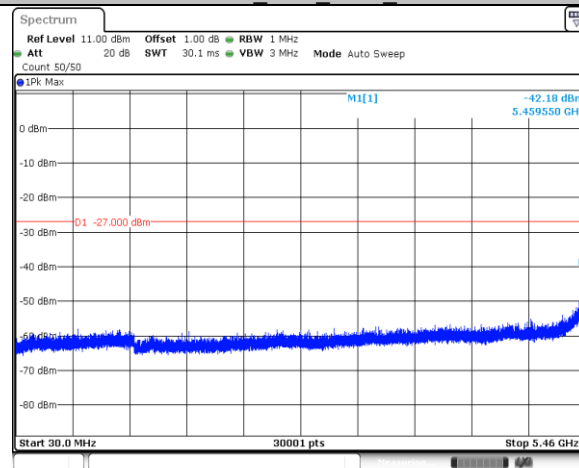
Date: 3.NOV.2023 10:51:59

11AC80SISO Ant2 5290 5360~40000



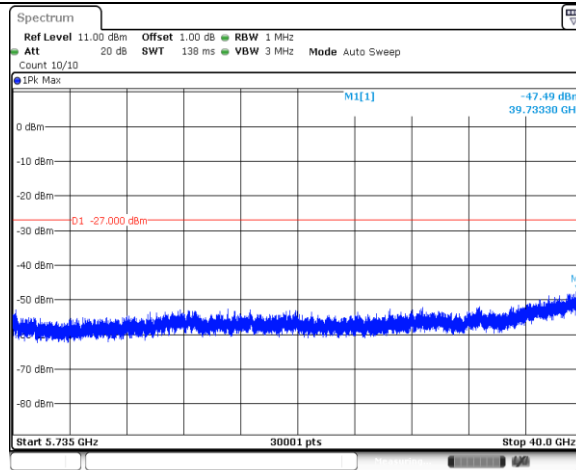
Date: 3.NOV.2023 10:52:04

11AC80SISO Ant2 5530 30~5460



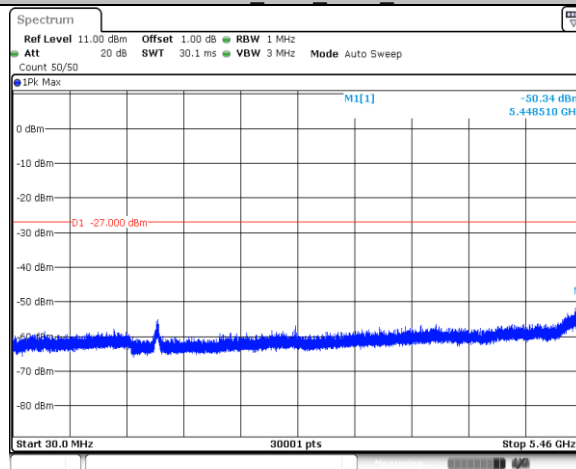
Date: 3.NOV.2023 10:53:59

11AC80SISO Ant2 5530 5735~40000



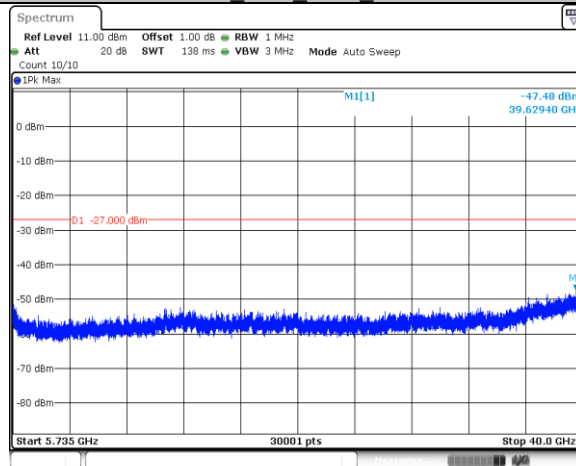
Date: 3.NOV.2023 10:54:04

11AC80SISO Ant2 5610 30~5460



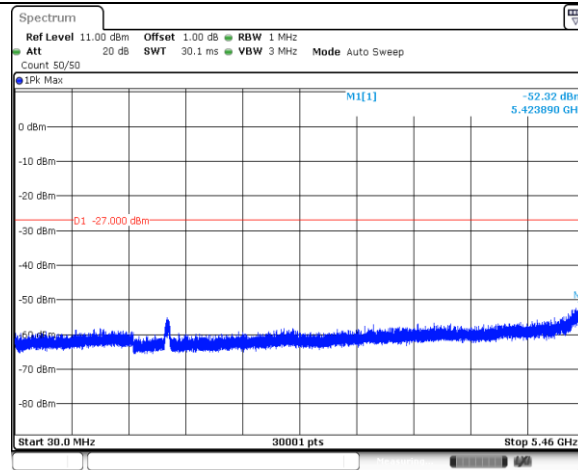
Date: 3.NOV.2023 10:56:56

11AC80SISO Ant2 5610 5735~40000



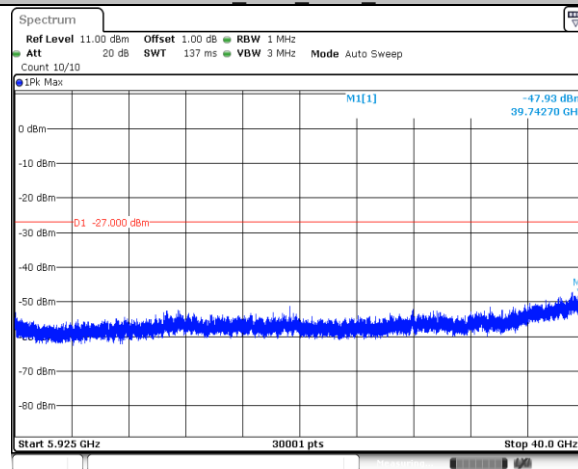
Date: 3.NOV.2023 10:57:01

11AC80SISO Ant2 5690 30~5460



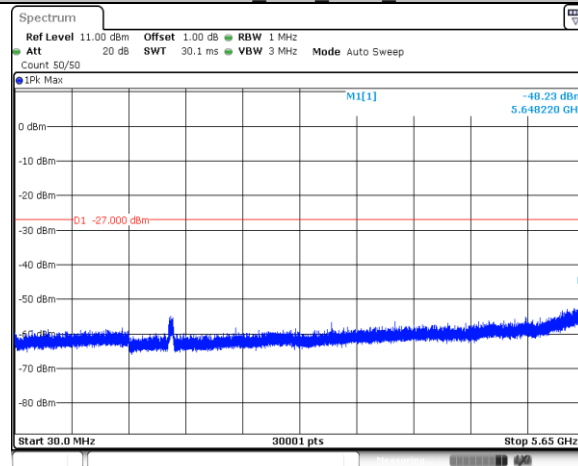
Date: 3.NOV.2023 10:59:36

11AC80SISO Ant2 5690 5925~40000



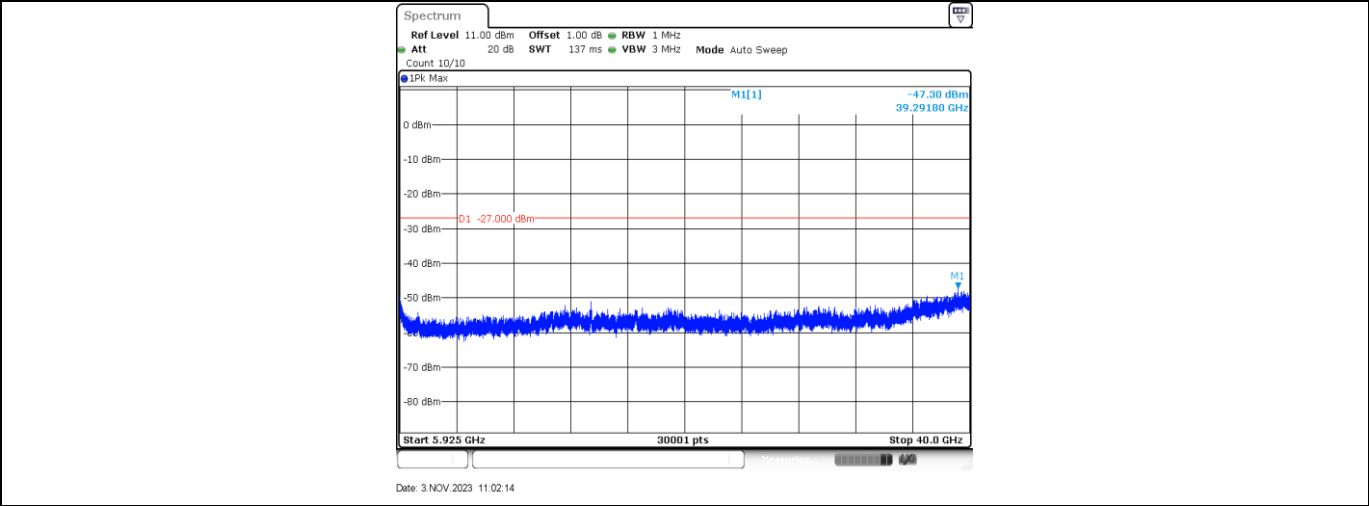
Date: 3.NOV.2023 10:59:41

11AC80SISO Ant2 5775 30~5650



Date: 3.NOV.2023 11:02:09

11AC80SISO Ant2 5775 5925~40000



Transmitting spurious emission test result as below (Band edge measurements):

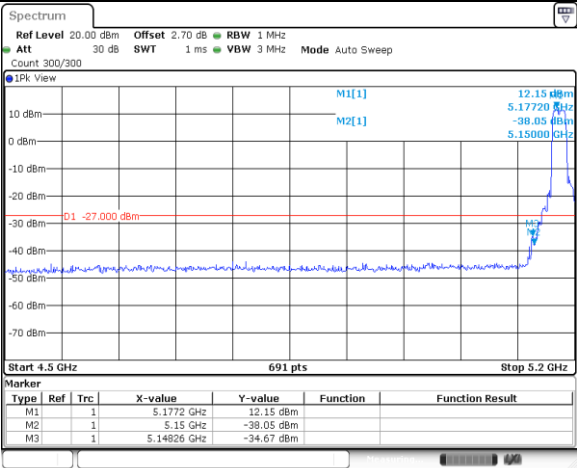
Test Mode	Antenna	Channel Name	Channel (MHz)	Result (dBm)	Limit (dBm)	Verdict
11A	Ant1	Low	5180	-34.67	<=-27	PASS
		High	5320	-32.93	<=-27	PASS
		Low	5500	-34.54	<=-27	PASS
		High	5700	-27.36	<=-27	PASS
		Low	5720	-42.39	<=-27	PASS
		High	5720	-41.26	<=-27	PASS
11N20	Ant1	Low	5180	-35.53	<=-27	PASS
		High	5320	-31.93	<=-27	PASS
		Low	5500	-34	<=-27	PASS
		High	5700	-29.31	<=-27	PASS
		Low	5720	-42.6	<=-27	PASS
		High	5720	-41.92	<=-27	PASS
11N40	Ant1	Low	5190	-35.33	<=-27	PASS
		High	5310	-32.23	<=-27	PASS
		Low	5510	-33.29	<=-27	PASS
		High	5670	-33.03	<=-27	PASS
		Low	5710	-43.72	<=-27	PASS
		High	5710	-42	<=-27	PASS
11AC20	Ant1	Low	5180	-35.8	<=-27	PASS
		High	5320	-34.74	<=-27	PASS
		Low	5500	-34.25	<=-27	PASS
		High	5700	-29.64	<=-27	PASS
		Low	5720	-43.53	<=-27	PASS
		High	5720	-39.51	<=-27	PASS
11AC40	Ant1	Low	5190	-37.07	<=-27	PASS
		High	5310	-35.32	<=-27	PASS
		Low	5510	-33.31	<=-27	PASS
		High	5670	-33.63	<=-27	PASS
		Low	5710	-43.2	<=-27	PASS
		High	5710	-42.56	<=-27	PASS
11AC80	Ant1	Low	5210	-40.36	<=-27	PASS
		High	5290	-38.81	<=-27	PASS
		Low	5530	-38.07	<=-27	PASS
		High	5610	-41.37	<=-27	PASS
		Low	5690	-43.98	<=-27	PASS
		High	5690	-41.65	<=-27	PASS
Test Mode	Antenna	Channel Name	Channel (MHz)	Result (dBm)	Limit (dBm)	Verdict
11A	Ant2	Low	5180	-32.55	<=-27	PASS
		High	5320	-32.10	<=-27	PASS
		Low	5500	-38.1	<=-27	PASS
		High	5700	-29.34	<=-27	PASS
		Low	5720	-44.1	<=-27	PASS
		High	5720	-44.59	<=-27	PASS
11N20	Ant2	Low	5180	-37.54	<=-27	PASS
		High	5320	-33.71	<=-27	PASS
		Low	5500	-41.24	<=-27	PASS
		High	5700	-41.59	<=-27	PASS
		Low	5720	-45.27	<=-27	PASS
		High	5720	-44.61	<=-27	PASS
11N40	Ant2	Low	5190	-35.78	<=-27	PASS
		High	5310	-31.99	<=-27	PASS
		Low	5510	-38.78	<=-27	PASS
		High	5670	-39.87	<=-27	PASS
		Low	5710	-45.18	<=-27	PASS
		High	5710	-44.78	<=-27	PASS
11AC20	Ant2	Low	5180	-40.06	<=-27	PASS
		High	5320	-32.19	<=-27	PASS
		Low	5500	-42.06	<=-27	PASS
		High	5700	-42.21	<=-27	PASS

		Low	5720	-44.8		<=-27	PASS
		High	5720	-44.92		<=-27	PASS
11AC40	Ant2	Low	5190	-35.08		<=-27	PASS
		High	5310	-35.32		<=-27	PASS
		Low	5510	-35.91		<=-27	PASS
		High	5670	-35.83		<=-27	PASS
		Low	5710	-44.81		<=-27	PASS
		High	5710	-44.07		<=-27	PASS
		11AC80	Ant2	Low	5210	-35.59	
High	5290			-33.02		<=-27	PASS
Low	5530			-39.89		<=-27	PASS
High	5610			-43.42		<=-27	PASS
Low	5690			-45.34		<=-27	PASS
High	5690			-44.99		<=-27	PASS
Test Mode	Antenna	Channel Name	Channel (MHz)	Frequency Range (MHz)	Result (dBm)	Limit (dBm)	Verdict
11A	Ant1	Low	5745	5650~5700	-43.01	6.55	PASS
				5700~5720	-26.79	15.60	PASS
				5720~5725	-23.13	25.48	PASS
				5760~5650	-44.24	-27	PASS
		High	5825	5850~5855	-24.61	15.80	PASS
				5855~5875	-27.53	10.16	PASS
				5875~5925	-40.99	-3.01	PASS
				5925~5935	-43.22	-27	PASS
11N20	Ant1	Low	5745	5650~5700	-41.35	-11.58	PASS
				5700~5720	-27.11	15.51	PASS
				5720~5725	-22.93	25.86	PASS
				5760~5650	-44.27	-27	PASS
		High	5825	5850~5855	-24.66	16.25	PASS
				5855~5875	-27.32	11.14	PASS
				5875~5925	-40.79	-10.54	PASS
				5925~5935	-42.41	-27	PASS
11N40	Ant1	Low	5755	5650~5700	-35.11	9.98	PASS
				5700~5720	-24.59	15.58	PASS
				5720~5725	-24.94	15.90	PASS
				5780~5650	-43.71	-27	PASS
		High	5795	5850~5855	-33.03	20.21	PASS
				5855~5875	-35.37	10.97	PASS
				5875~5925	-38.12	-14.98	PASS
				5925~5935	-42.07	-27	PASS
11AC20	Ant1	Low	5745	5650~5700	-42.43	-10.97	PASS
				5700~5720	-27.29	15.46	PASS
				5720~5725	-22.69	23.96	PASS
				5760~5650	-44.02	-27	PASS
		High	5825	5850~5855	-23.74	16.25	PASS
				5855~5875	-27.73	10.32	PASS
				5875~5925	-40.81	-26.61	PASS
				5925~5935	-42.75	-27	PASS
11AC40	Ant1	Low	5755	5650~5700	-34.26	5.93	PASS
				5700~5720	-25.2	15.25	PASS
				5720~5725	-23.08	26.16	PASS
				5780~5650	-43.08	-27	PASS
		High	5795	5850~5855	-31.38	16.39	PASS
				5855~5875	-32.78	10.51	PASS
				5875~5925	-38.14	-15.34	PASS
				5925~5935	-42.72	-27	PASS
11AC80	Ant1	Low	5775	5650~5700	-36.63	9.86	PASS
				5700~5720	-28.21	15.23	PASS
				5720~5725	-27.55	23.86	PASS
				5800~5650	-43.52	-27	PASS
		High	5775	5850~5855	-28.85	25.40	PASS
				5855~5875	-29.35	10.48	PASS
				5875~5925	-35.7	-26.84	PASS

				5925~5935	-42.19	-27	PASS
Test Mode	Antenna	Channel Name	Channel (MHz)	Frequency Range (MHz)	Result (dBm)	Limit (dBm)	Verdict
11A	Ant2	Low	5745	5650~5700	-44.29	9.88	PASS
				5700~5720	-28.91	15.60	PASS
				5720~5725	-24.93	25.86	PASS
				5760~5650	-44.48	-27	PASS
		High	5825	5850~5855	-26.6	16.25	PASS
				5855~5875	-30.69	10.05	PASS
				5875~5925	-43.44	-15.18	PASS
				5925~5935	-44.4	-27	PASS
11N20	Ant2	Low	5745	5650~5700	-44.48	8.03	PASS
				5700~5720	-31.07	15.51	PASS
				5720~5725	-26.98	25.10	PASS
				5760~5650	-45.27	-27	PASS
		High	5825	5850~5855	-28.57	15.80	PASS
				5855~5875	-32.43	11.14	PASS
				5875~5925	-44.06	-22.99	PASS
				5925~5935	-43.71	-27	PASS
11N40	Ant2	Low	5755	5650~5700	-38.28	9.98	PASS
				5700~5720	-28.89	15.31	PASS
				5720~5725	-30.21	24.82	PASS
				5780~5650	-45.7	-27	PASS
		High	5795	5850~5855	-37.15	19.67	PASS
				5855~5875	-38.75	10.17	PASS
				5875~5925	-43.61	-14.63	PASS
				5925~5935	-44.42	-27	PASS
11AC20	Ant2	Low	5745	5650~5700	-44.28	6.55	PASS
				5700~5720	-31.33	15.37	PASS
				5720~5725	-27.8	23.58	PASS
				5760~5650	-45.43	-27	PASS
		High	5825	5850~5855	-28.19	15.80	PASS
				5855~5875	-31.45	11.09	PASS
				5875~5925	-43.67	-25.46	PASS
				5925~5935	-45.12	-27	PASS
11AC40	Ant2	Low	5755	5650~5700	-36.76	5.78	PASS
				5700~5720	-27.53	15.03	PASS
				5720~5725	-26.59	26.60	PASS
				5780~5650	-45.21	-27	PASS
		High	5795	5850~5855	-35.02	16.39	PASS
				5855~5875	-37.57	10.17	PASS
				5875~5925	-43.53	-13.74	PASS
				5925~5935	-44.78	-27	PASS
11AC80	Ant2	Low	5775	5650~5700	-35.74	9.86	PASS
				5700~5720	-28.17	15.23	PASS
				5720~5725	-27.74	22.32	PASS
				5800~5650	-44.17	-27	PASS
		High	5775	5850~5855	-28.62	22.95	PASS
				5855~5875	-28.9	11.08	PASS
				5875~5925	-35.54	-26.84	PASS
				5925~5935	-44.48	-27	PASS

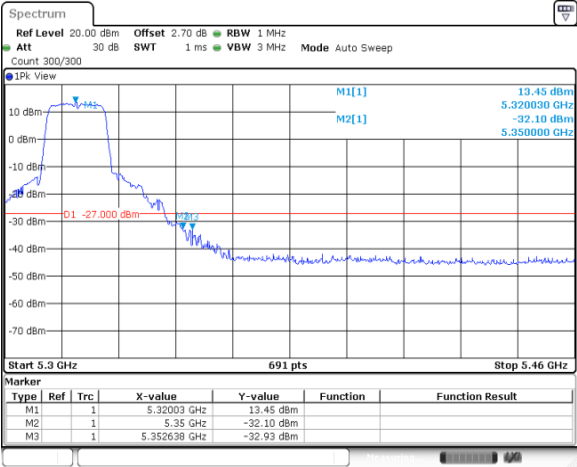


11A Ant1 Low 5180



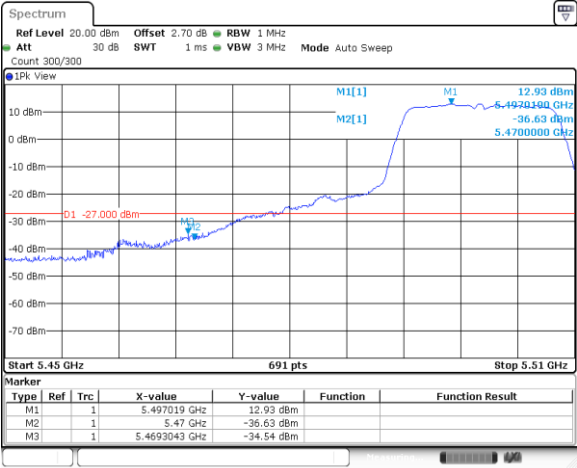
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11A Ant1 High 5320



Date: 2 NOV 2023 09:39:43

11A Ant1 Low 5500

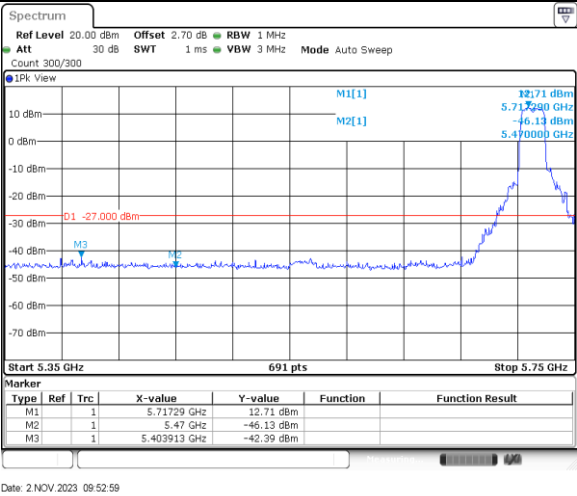


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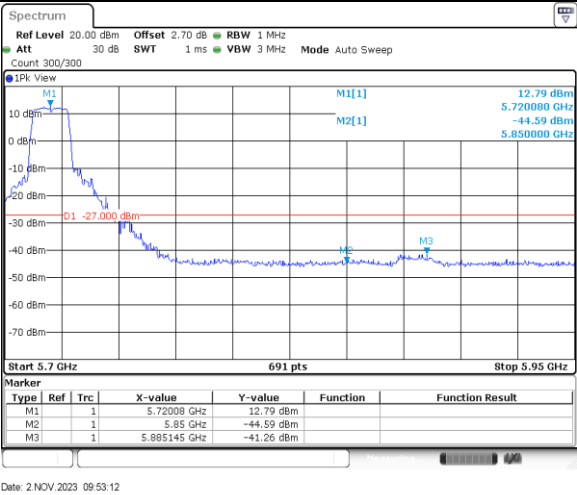
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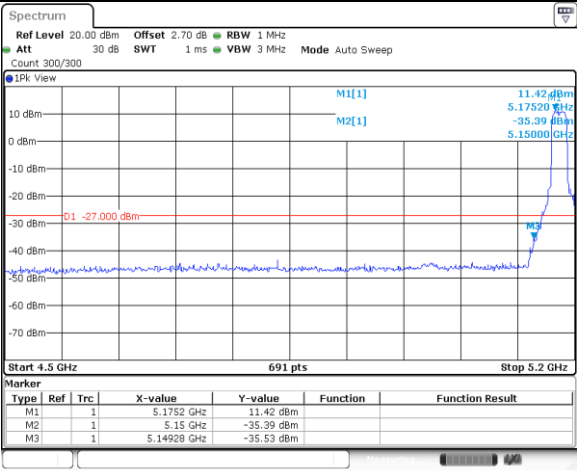
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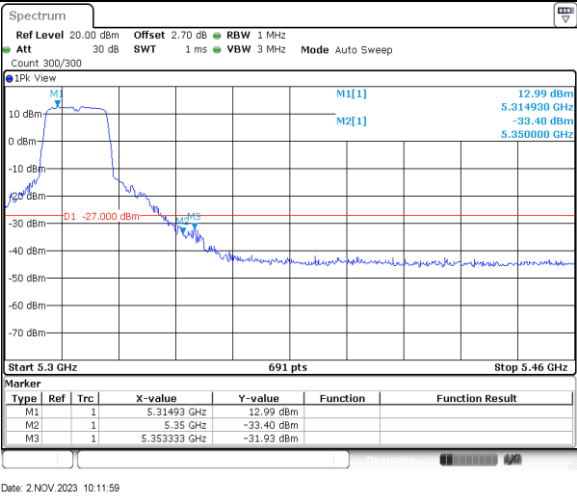
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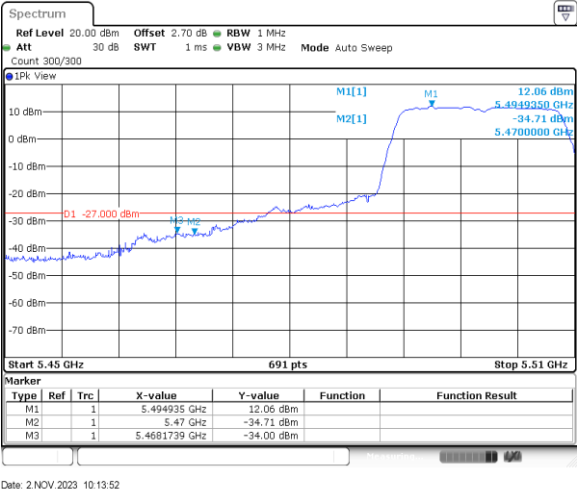
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11N20SISO Ant1 High 5320



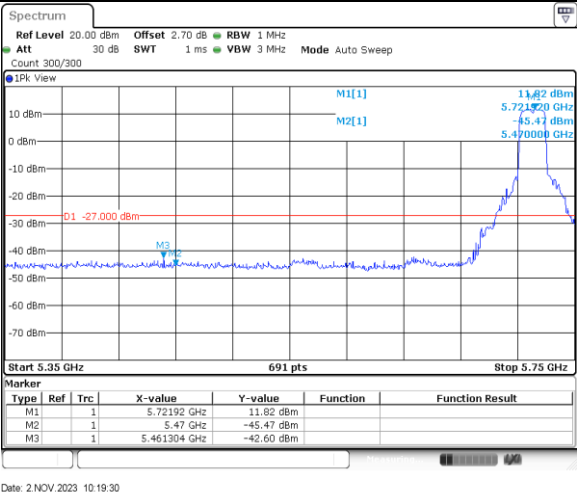
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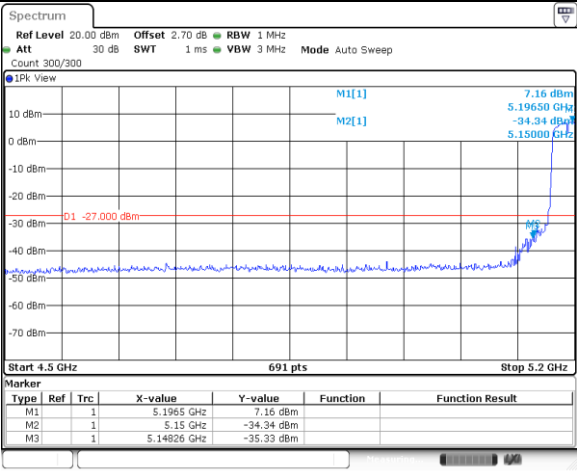
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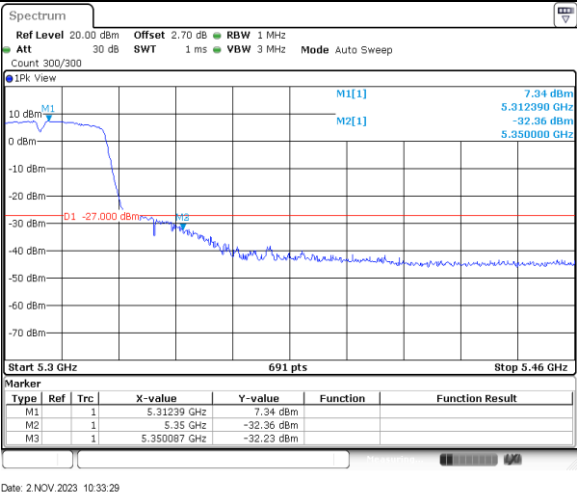
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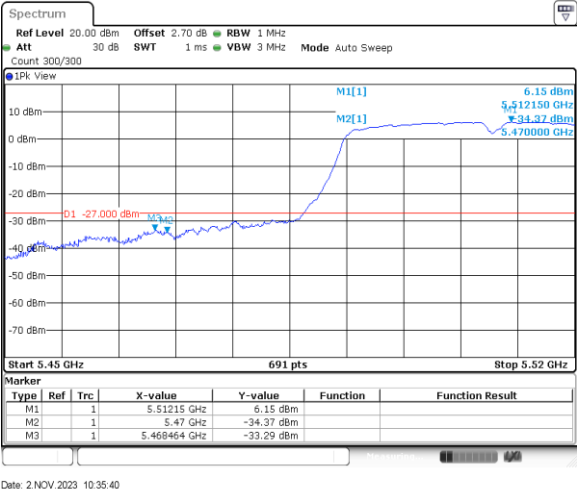
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11N40SISO Ant1 High 5310



11N40SISO Ant1 Low 5510



11N40SISO Ant1 High 5670