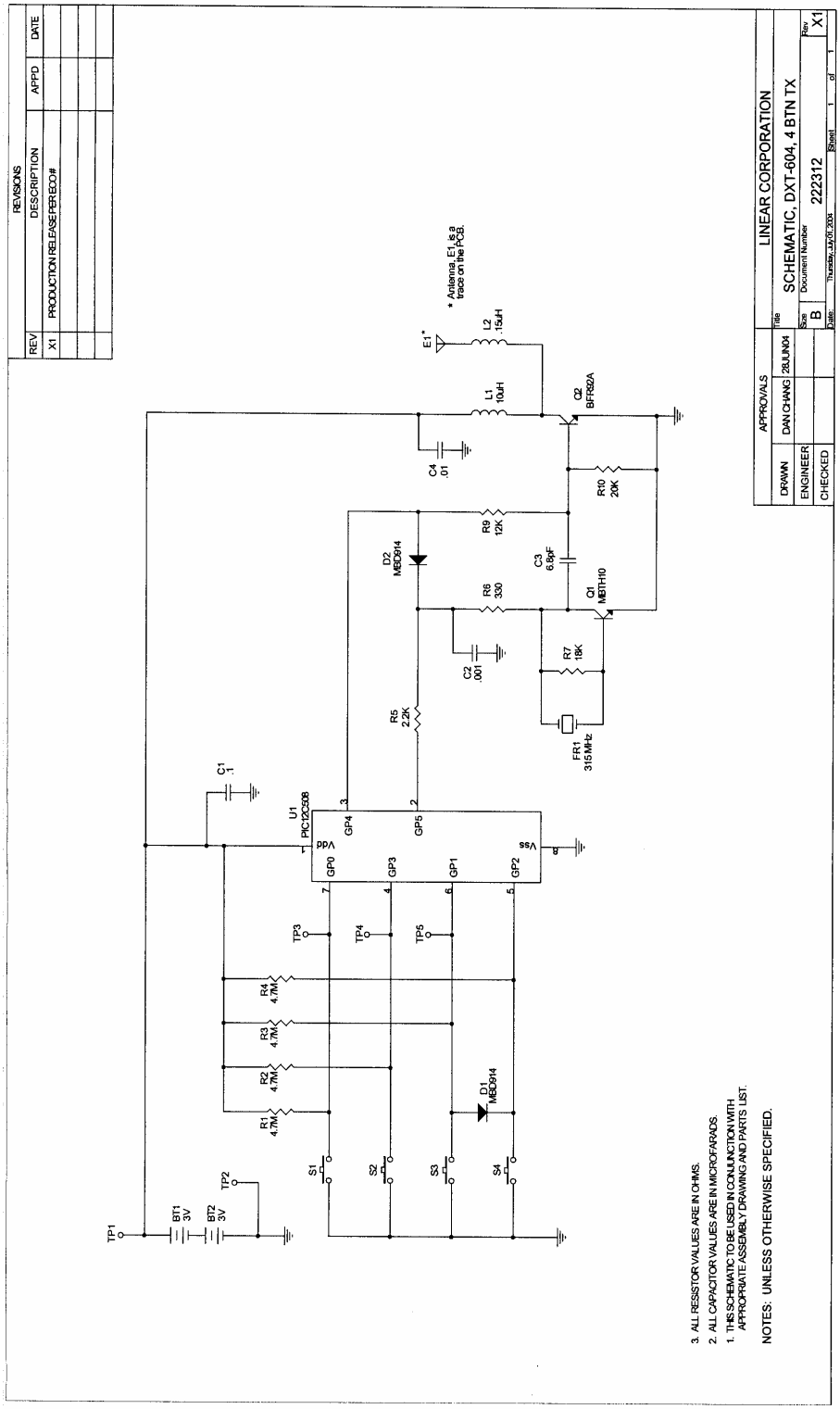
REVISIONS
REV
DESCRIPTION
APPD
DATE
X1
PRODUCTION RELEASE PER ECO#
APPROVALS
DRAWN
DAN CHANG
28JUN04
ENGINEER
CHECKED
LINEAR CORPORATION
Title
SCHEMATIC, DXT-604, 4 BTN TX
Size
B
Document Number
222312
Rev
X1
Date: Thursday, July 01, 2004
Sheet 1 of 1
* Antenna E1 is a trace on the PCB.
E1*
L2 15uH
L1 10uH
C4 .01
Q2 BFR92A
R10 20K
R9 12K
D2 MBD914
C3 6.8pF
R8 330
Q1 MBT-H10
C2 .001
R7 18K
R5 2.2K
FR1 315MHz
C1 .1
U1 PIC12C508
VDD
GP4
GP5
VSS
GP0
GP3
GP1
GP2
TP3
TP4
TP5
R4 4.7M
R3 4.7M
R2 4.7M
R1 4.7M
D1 MBD914
S1
S2
S3
S4
TP1
BT1 3V
BT2 3V
TP2
3. ALL RESISTOR VALUES ARE IN OHMS.
2. ALL CAPACITOR VALUES ARE IN MICROFARADS.
1. THIS SCHEMATIC TO BE USED IN CONJUNCTION WITH APPROPRIATE ASSEMBLY DRAWING AND PARTS LIST.
NOTES: UNLESS OTHERWISE SPECIFIED.