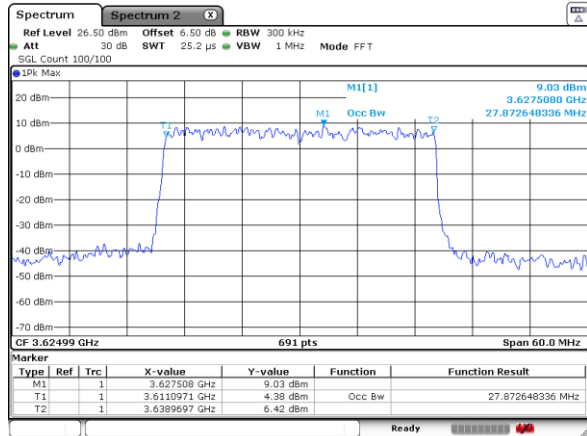




FR1 n78 / 30MHz / CP-OFDM

QPSK

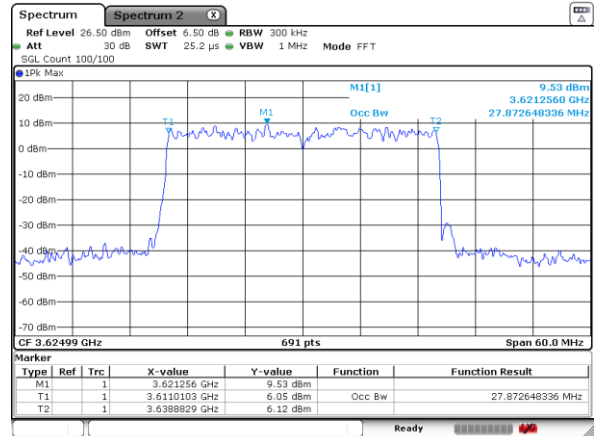
Middle Channel



Date: 28.MAR.2023 23:10:44

16QAM

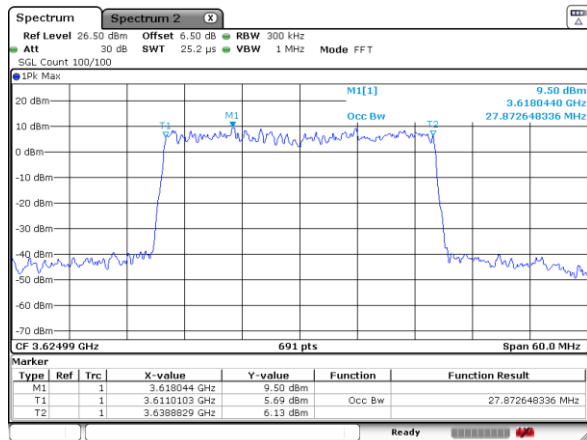
Middle Channel



Date: 28.MAR.2023 23:11:34

64QAM

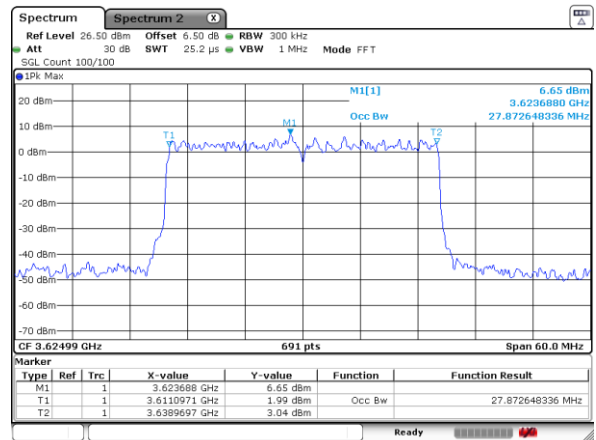
Middle Channel



Date: 28.MAR.2023 23:11:58

256QAM

Middle Channel

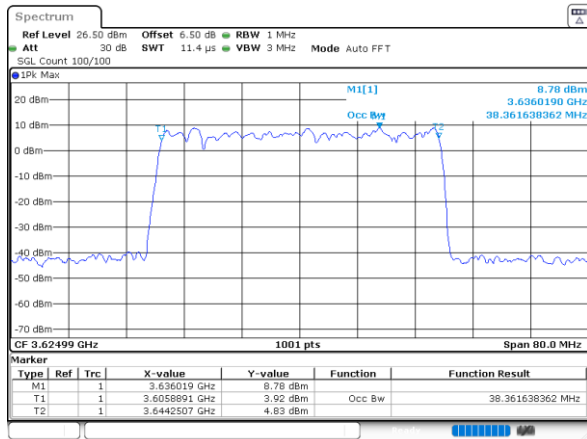


Date: 28.MAR.2023 23:12:52

FR1 n78 / 40MHz / CP-OFDM

QPSK

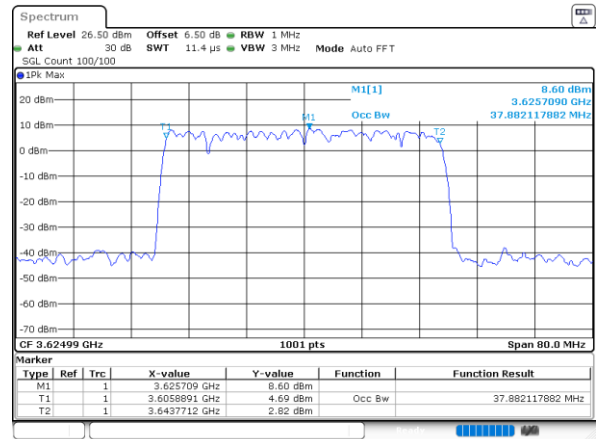
Middle Channel



Date: 21.MAR.2023 01:28:10

16QAM

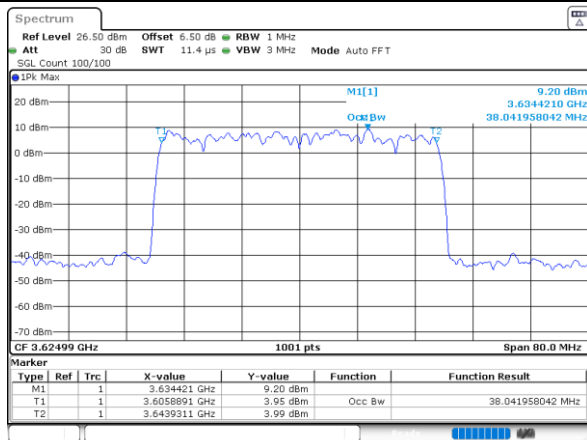
Middle Channel



Date: 21.MAR.2023 01:26:25

64QAM

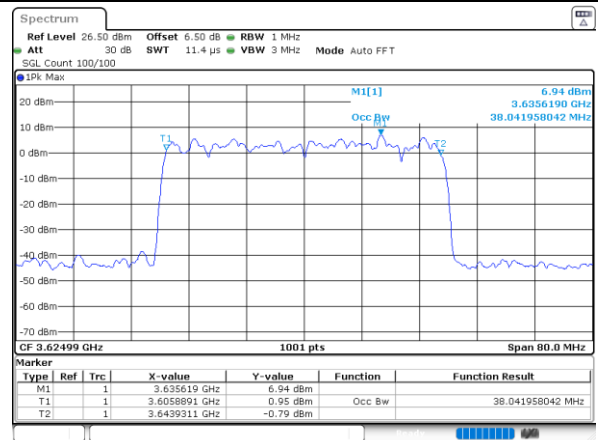
Middle Channel



Date: 21.MAR.2023 01:27:09

256QAM

Middle Channel



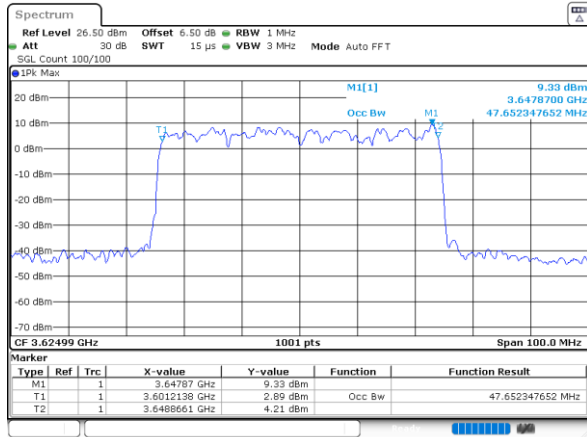
Date: 21.MAR.2023 01:27:35



FR1 n78 / 50MHz / CP-OFDM

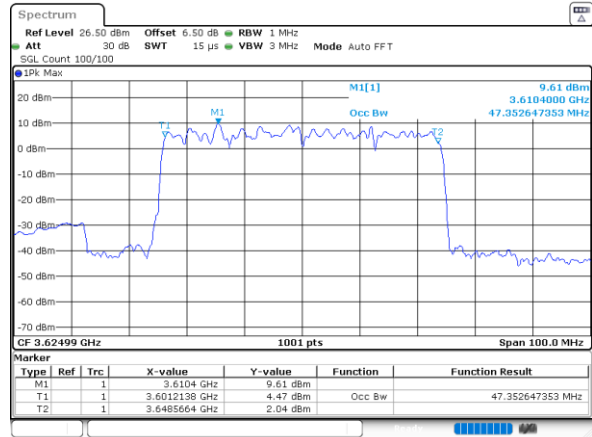
QPSK

Middle Channel



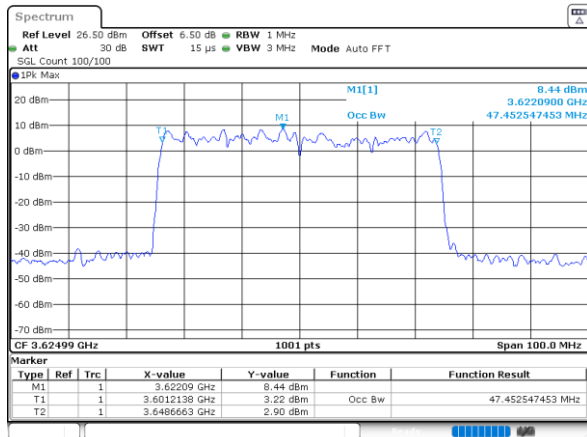
16QAM

Middle Channel



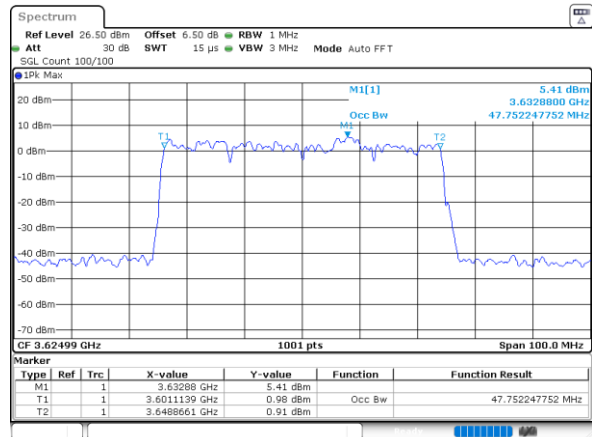
64QAM

Middle Channel



256QAM

Middle Channel

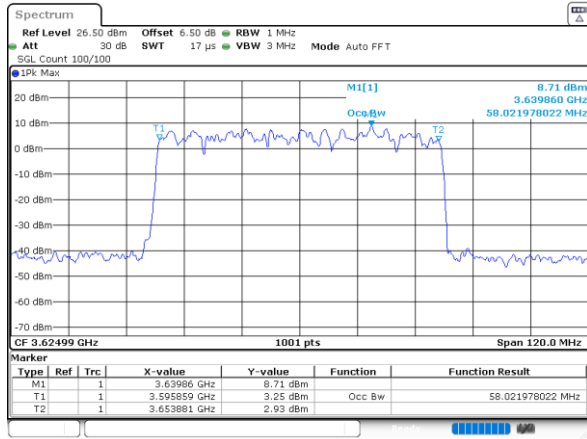




FR1 n78 / 60MHz / CP-OFDM

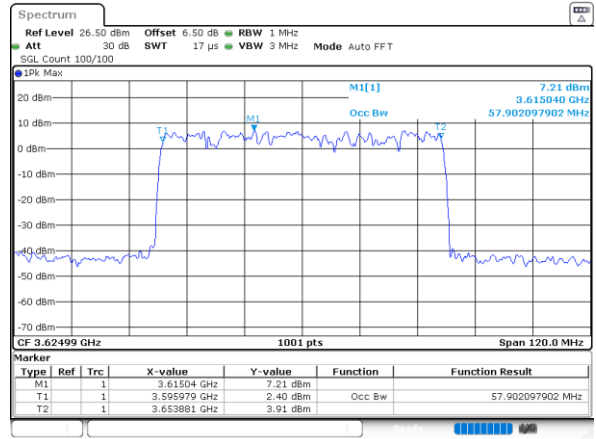
QPSK

Middle Channel



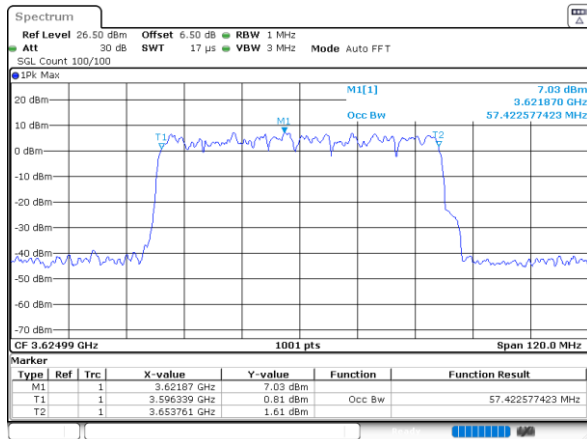
16QAM

Middle Channel



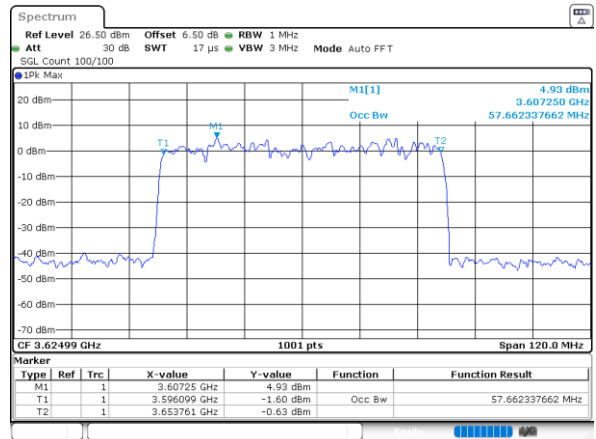
64QAM

Middle Channel



256QAM

Middle Channel

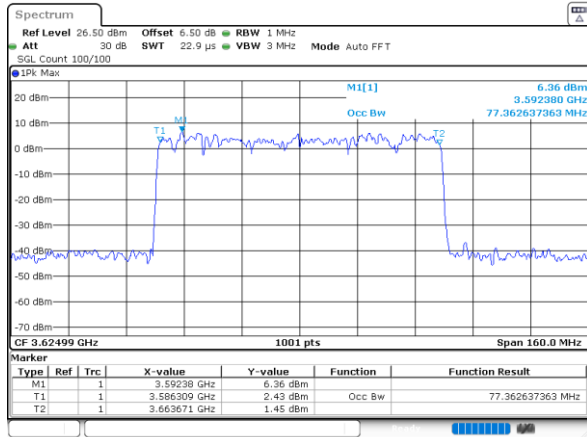




FR1 n78 / 80MHz / CP-OFDM

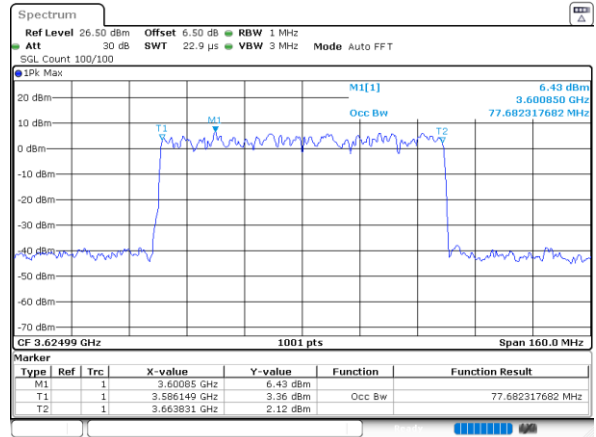
QPSK

Middle Channel



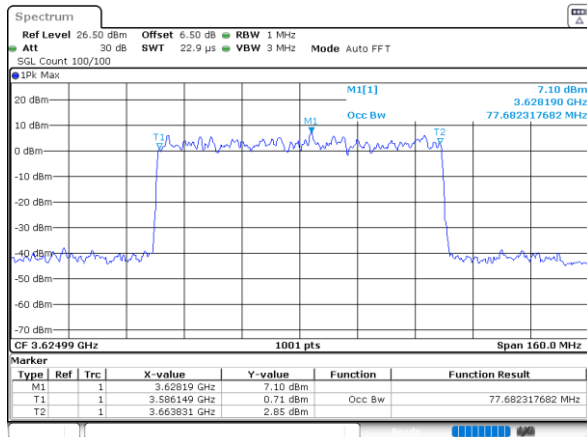
16QAM

Middle Channel



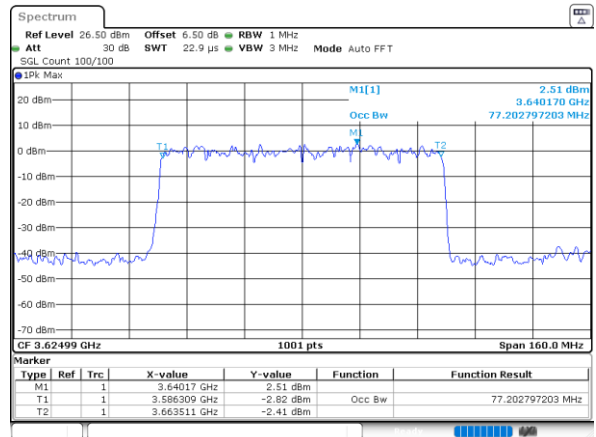
64QAM

Middle Channel



256QAM

Middle Channel

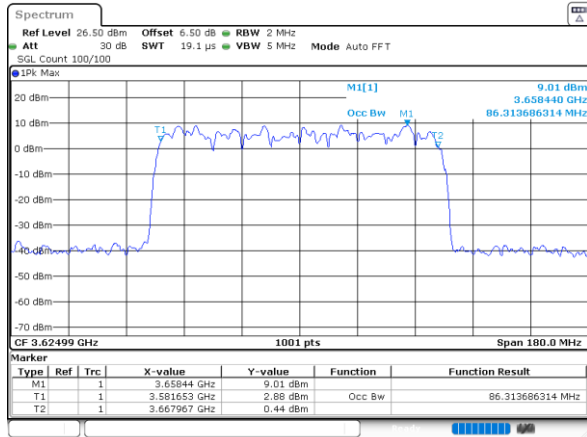




FR1 n78 / 90MHz / CP-OFDM

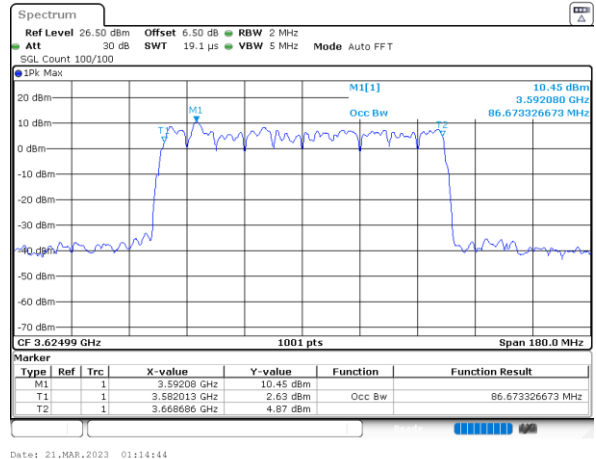
QPSK

Middle Channel



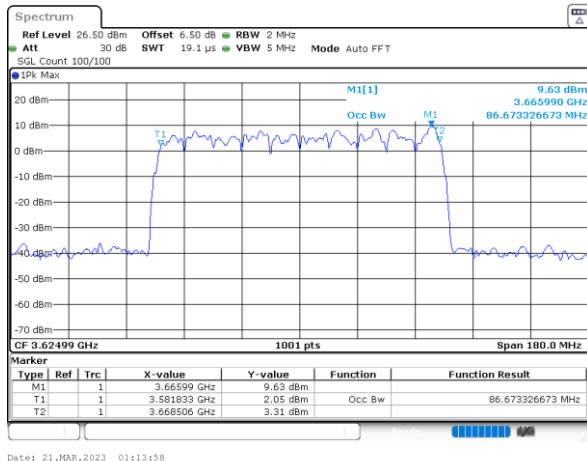
16QAM

Middle Channel



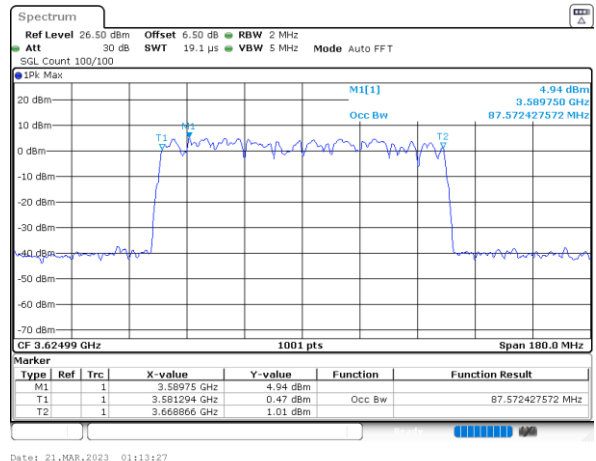
64QAM

Middle Channel



256QAM

Middle Channel

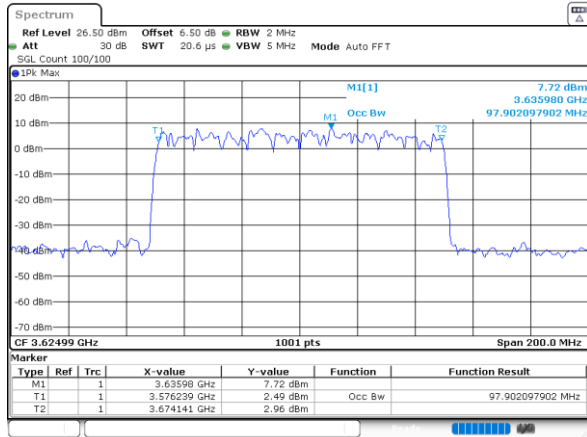




FR1 n78 / 100MHz / CP-OFDM

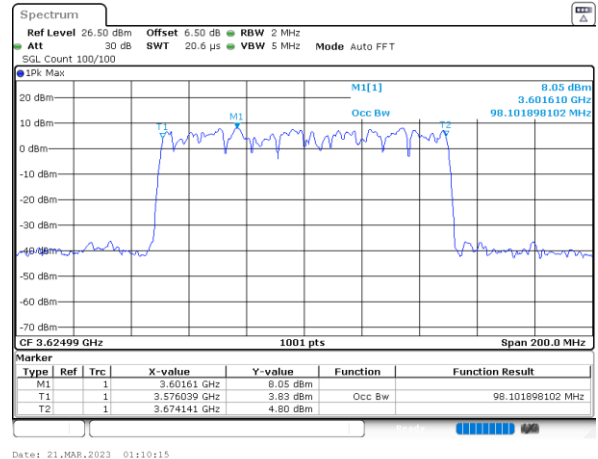
QPSK

Middle Channel



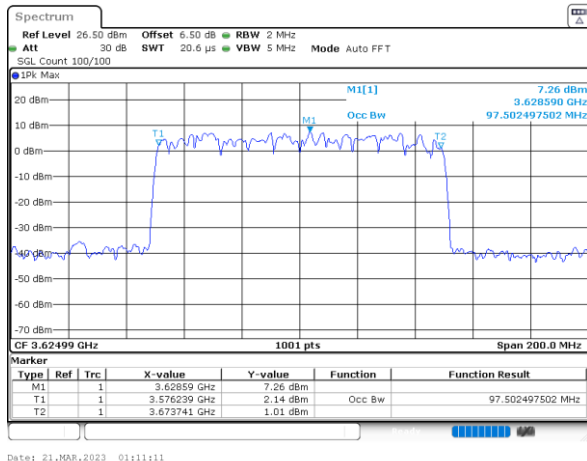
16QAM

Middle Channel



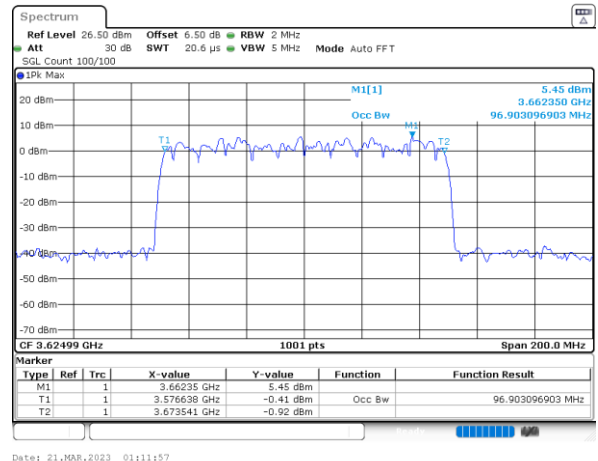
64QAM

Middle Channel



256QAM

Middle Channel





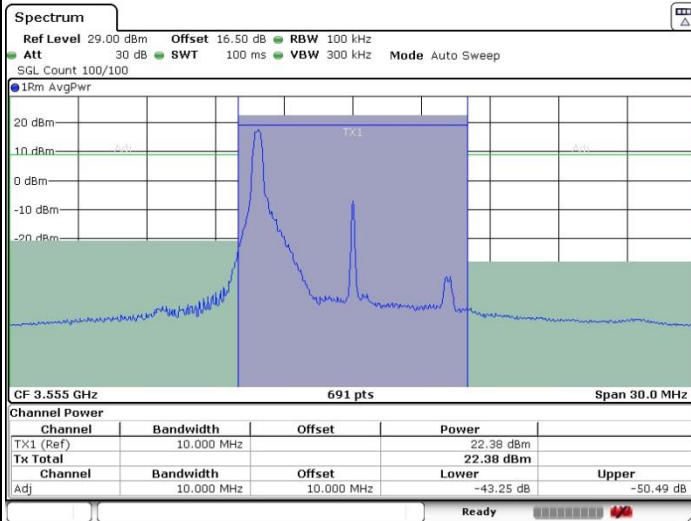
ACLR

FR1 n78 / 10MHz

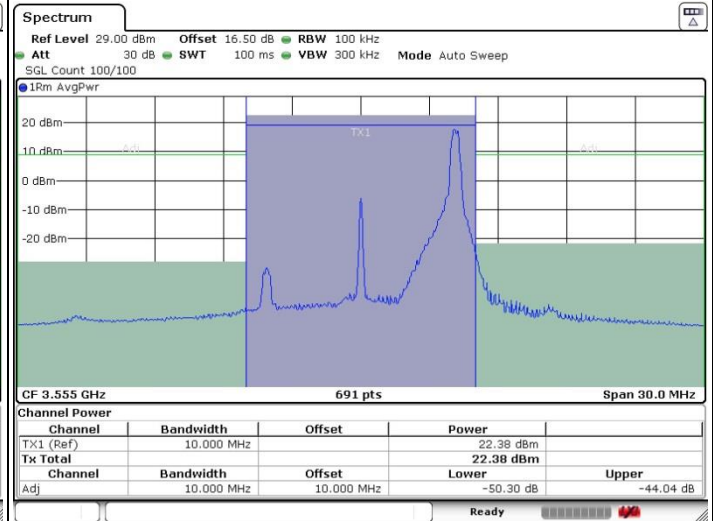
QPSK

Lowest Channel / 1RB0

Lowest Channel / 1RBmax

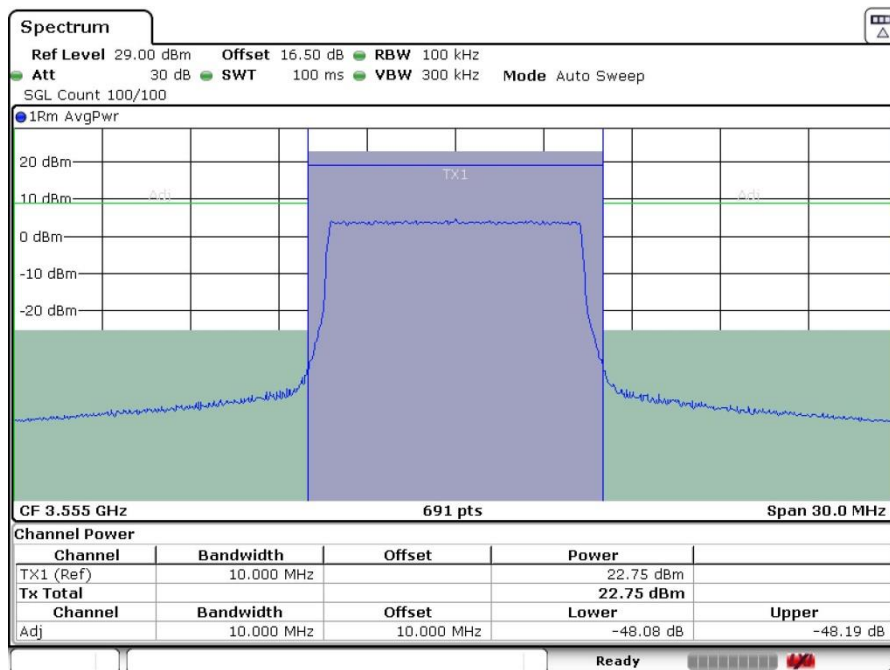


Date: 15.APR.2023 03:22:56



Date: 15.APR.2023 03:22:33

Lowest Channel / FullRB



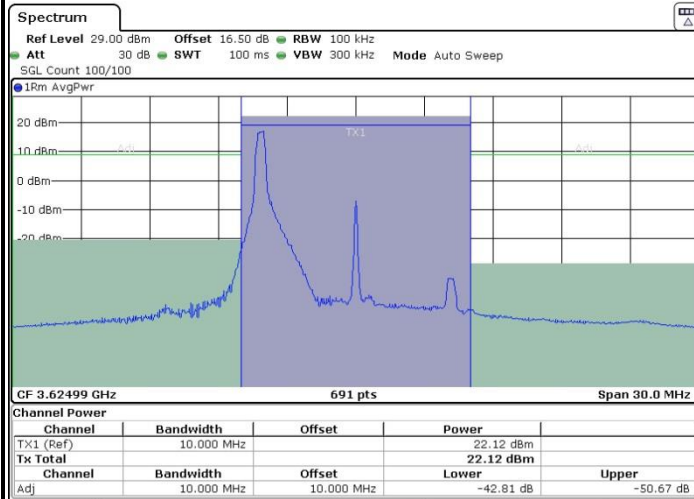
Date: 15.APR.2023 03:29:40



FR1 n78 / 10MHz

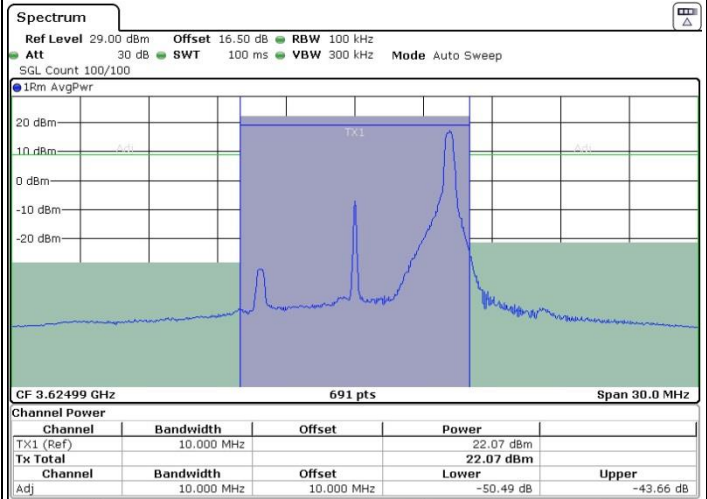
QPSK

Middle Channel / 1RB0



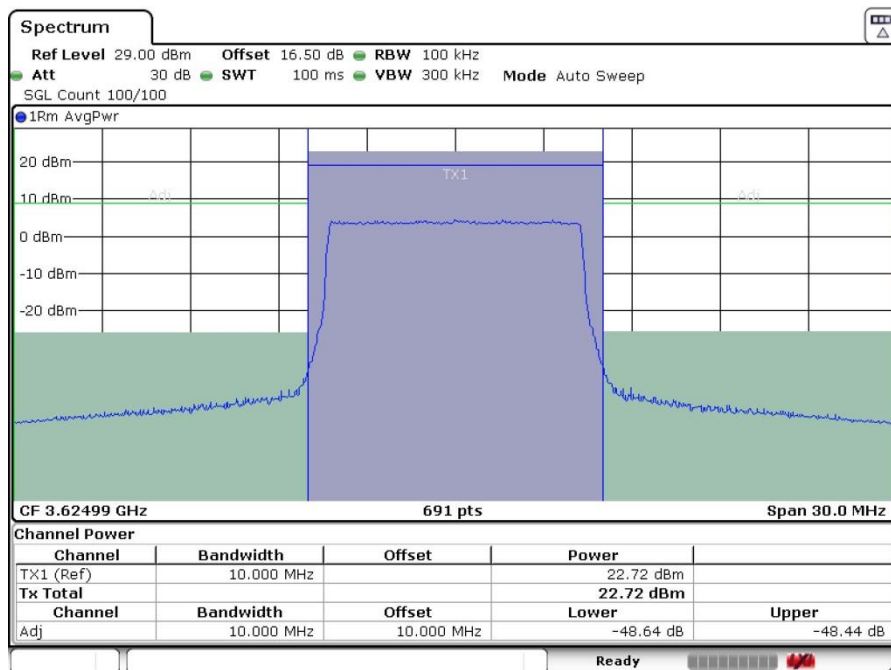
Date: 15.APR.2023 03:18:13

Middle Channel / 1RBmax



Date: 15.APR.2023 03:18:49

Middle Channel / FullRB



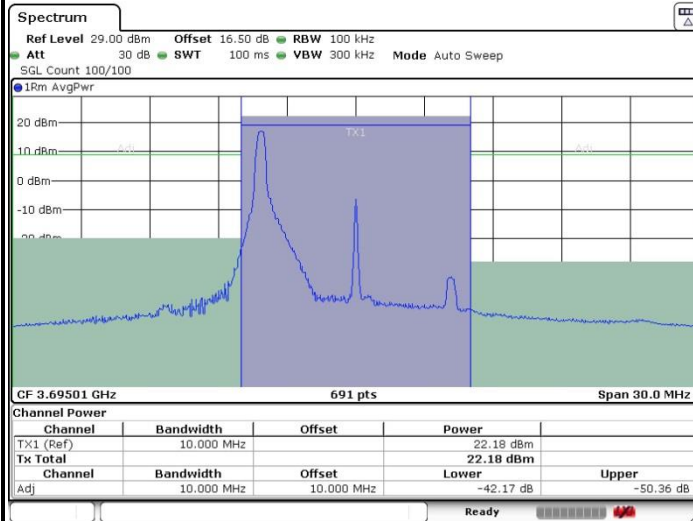
Date: 15.APR.2023 03:14:44



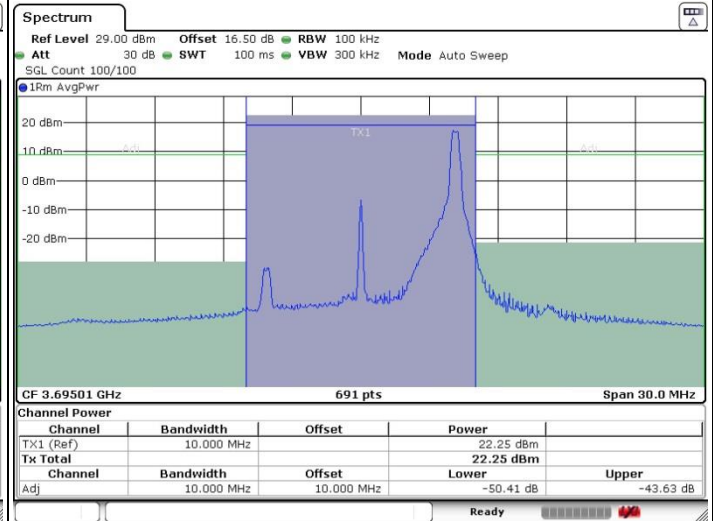
FR1 n78 / 10MHz

QPSK

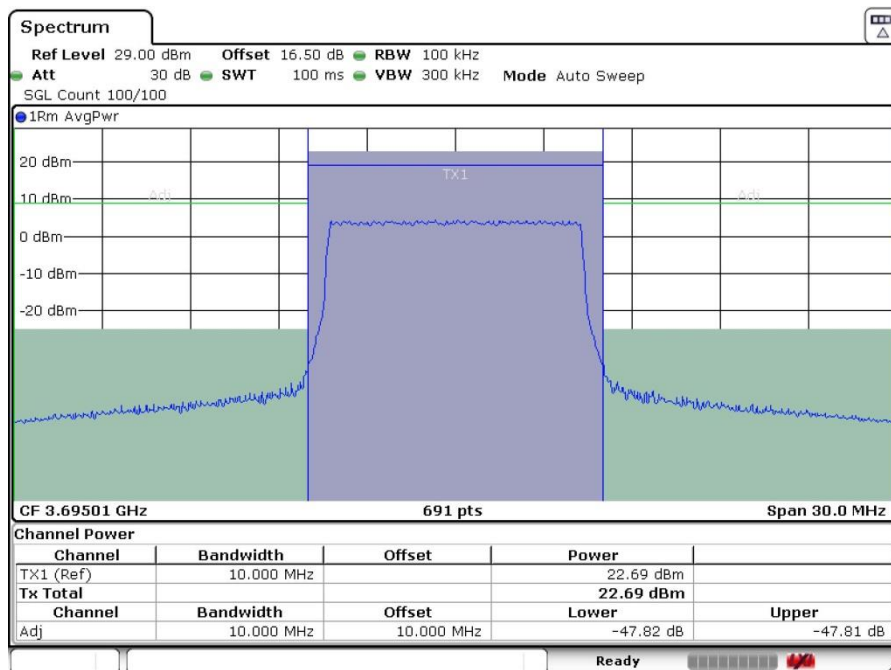
Highest Channel / 1RB0



Highest Channel / 1RBmax



Highest Channel / FullRB

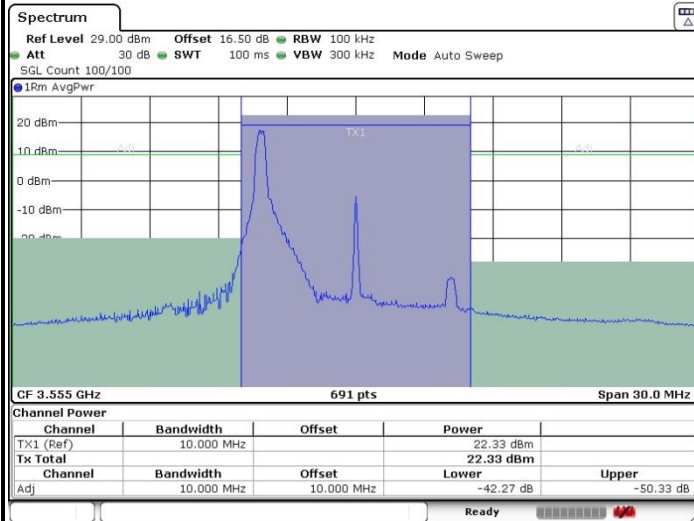




FR1 n78 / 10MHz

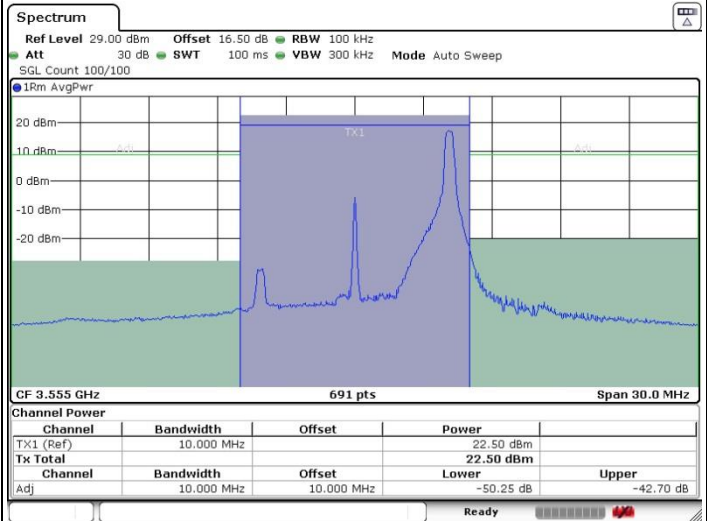
16QAM

Lowest Channel / 1RB0



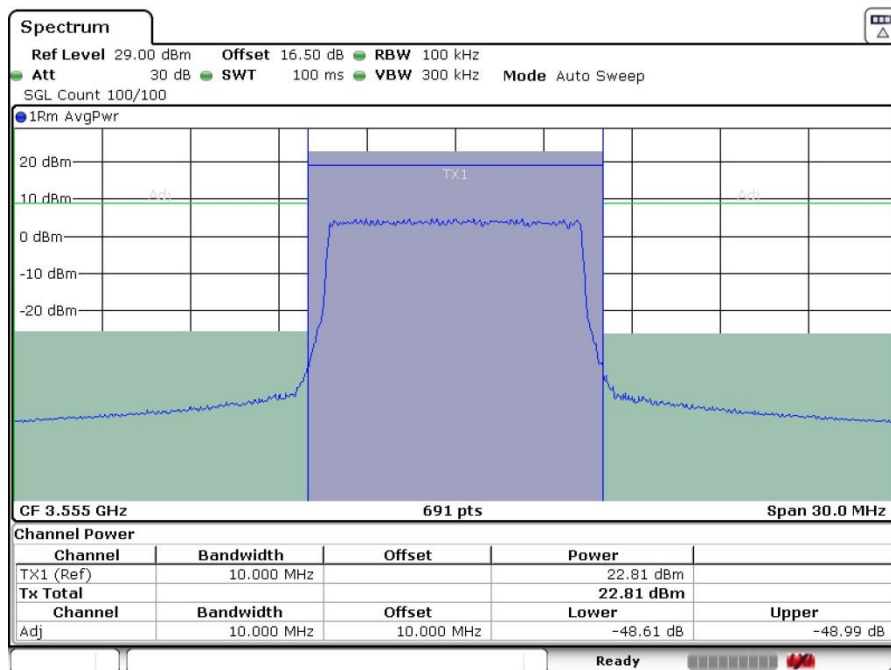
Date: 15.APR.2023 03:23:22

Lowest Channel / 1RBmax



Date: 15.APR.2023 03:21:55

Lowest Channel / FullRB



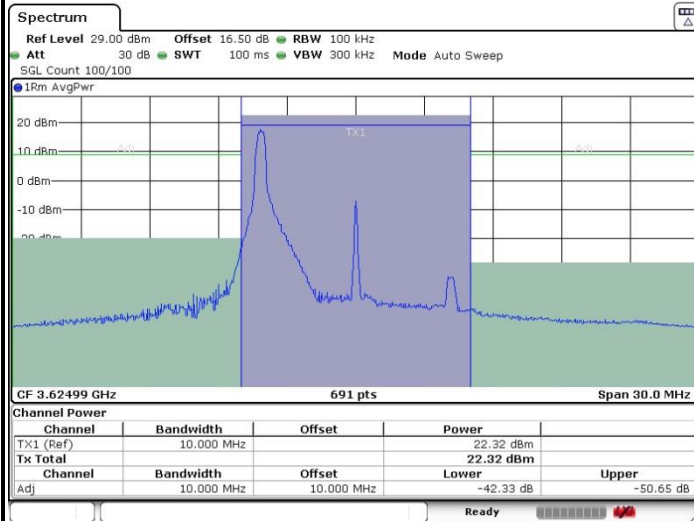
Date: 15.APR.2023 03:29:13



FR1 n78 / 10MHz

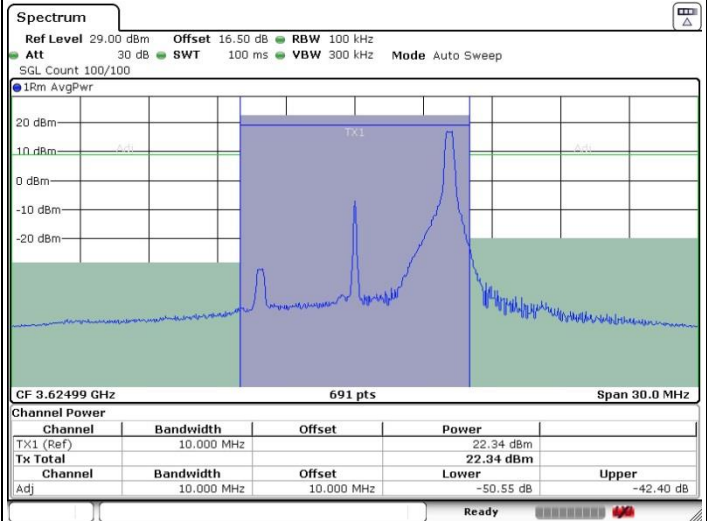
16QAM

Middle Channel / 1RB0



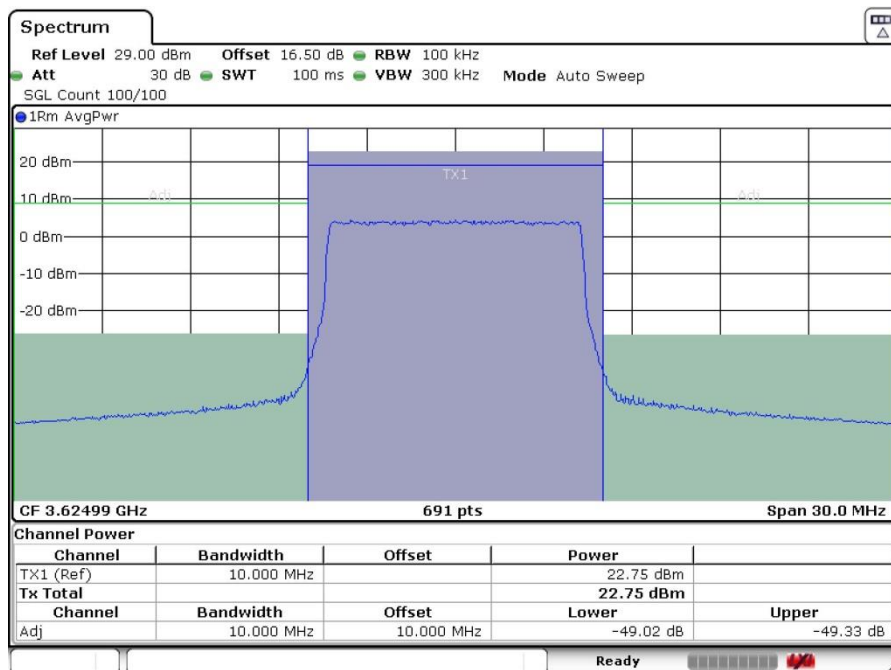
Date: 15.APR.2023 03:17:49

Middle Channel / 1RBmax



Date: 15.APR.2023 03:19:12

Middle Channel / FullRB



Date: 15.APR.2023 03:15:24

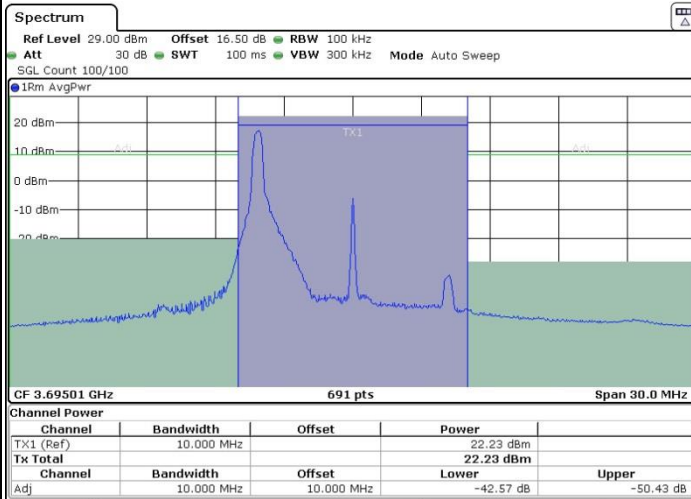


FR1 n78 / 10MHz

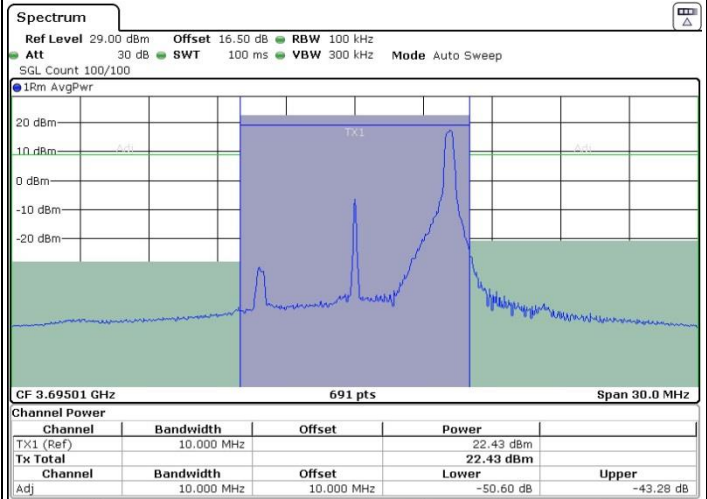
16QAM

Highest Channel / 1RB0

Highest Channel / 1RBmax

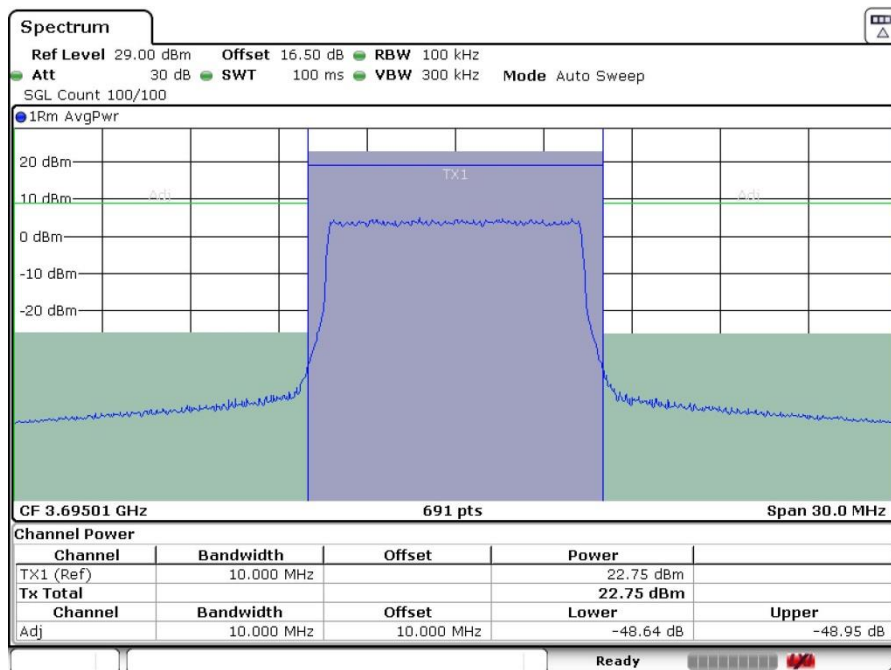


Date: 15.APR.2023 03:34:12



Date: 15.APR.2023 03:35:33

Highest Channel / FullRB



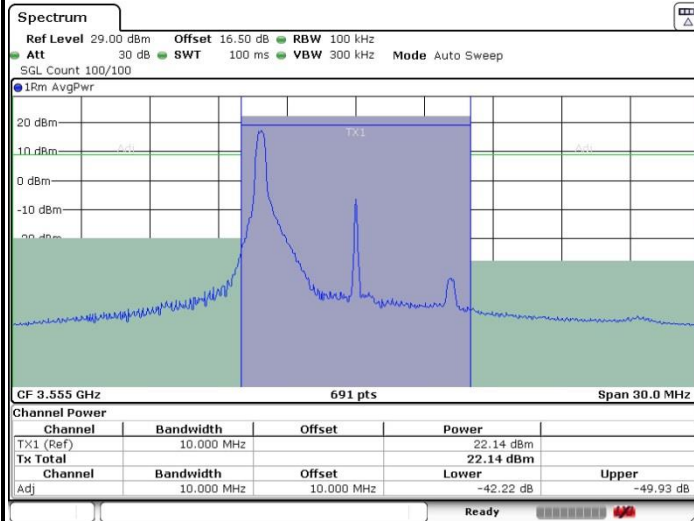
Date: 15.APR.2023 03:31:47



FR1 n78 / 10MHz

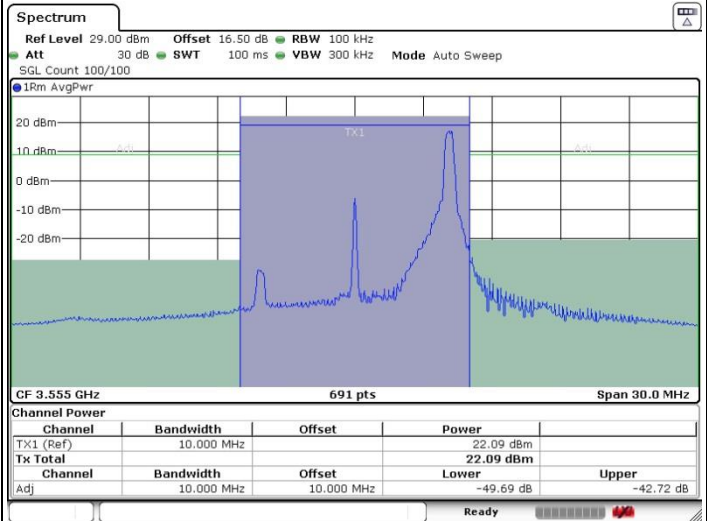
64QAM

Lowest Channel / 1RB0



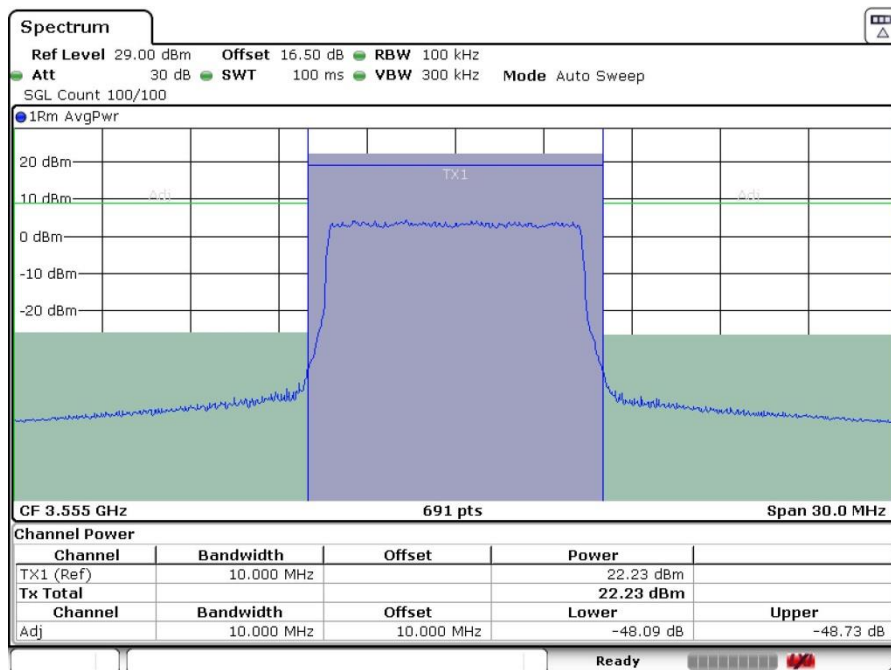
Date: 15.APR.2023 03:23:52

Lowest Channel / 1RBmax



Date: 15.APR.2023 03:21:31

Lowest Channel / FullIRB



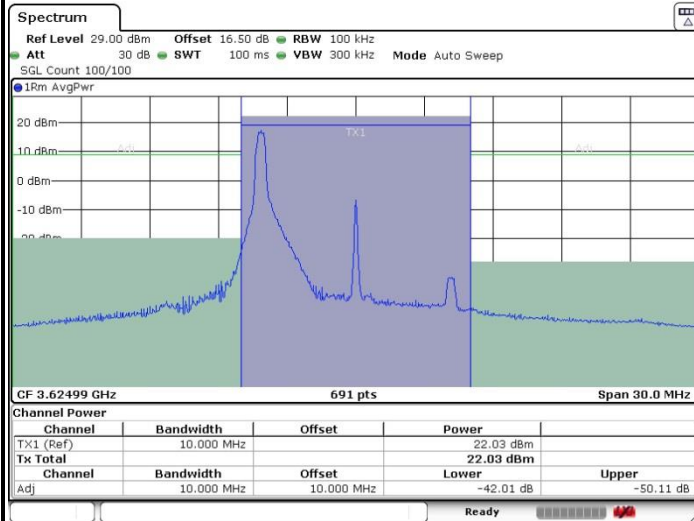
Date: 15.APR.2023 03:28:44



FR1 n78 / 10MHz

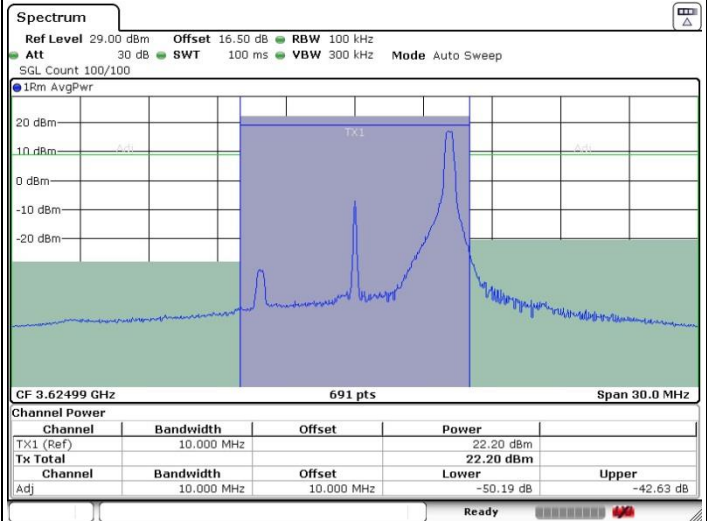
64QAM

Middle Channel / 1RB0



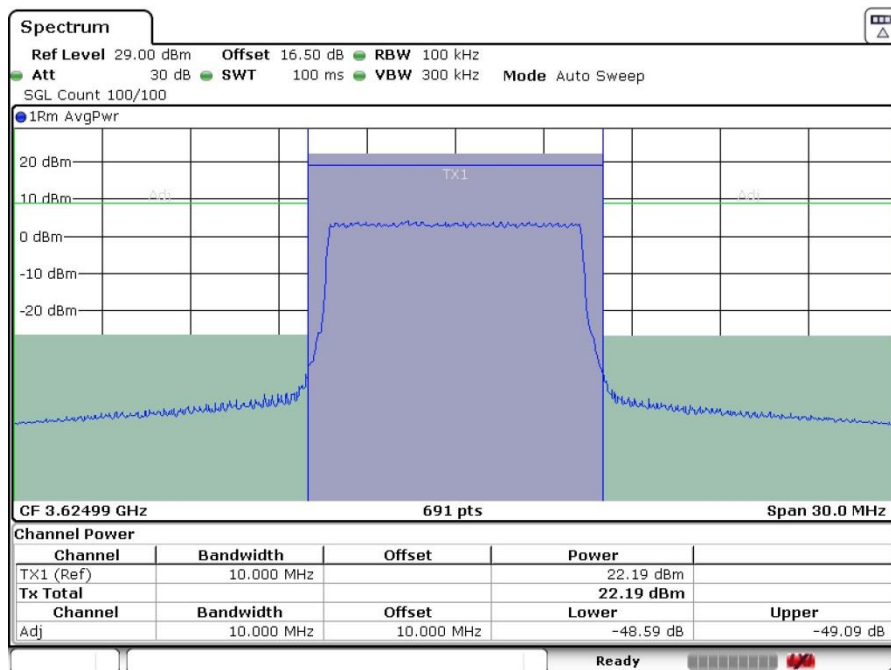
Date: 15.APR.2023 03:17:24

Middle Channel / 1RBmax



Date: 15.APR.2023 03:19:37

Middle Channel / FullRB



Date: 15.APR.2023 03:15:49

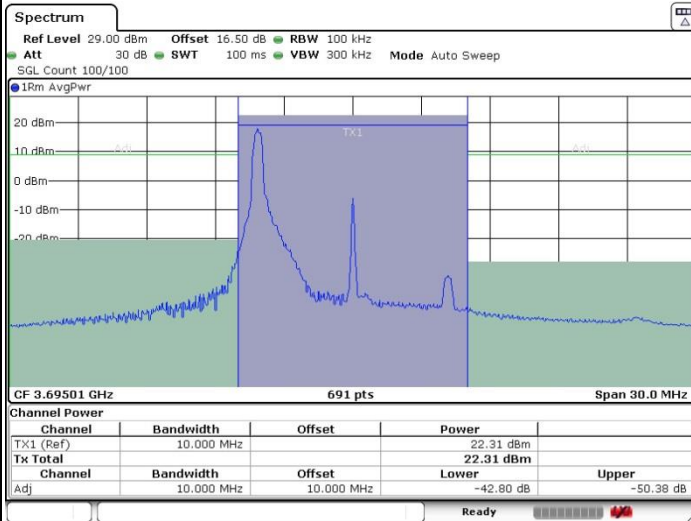


FR1 n78 / 10MHz

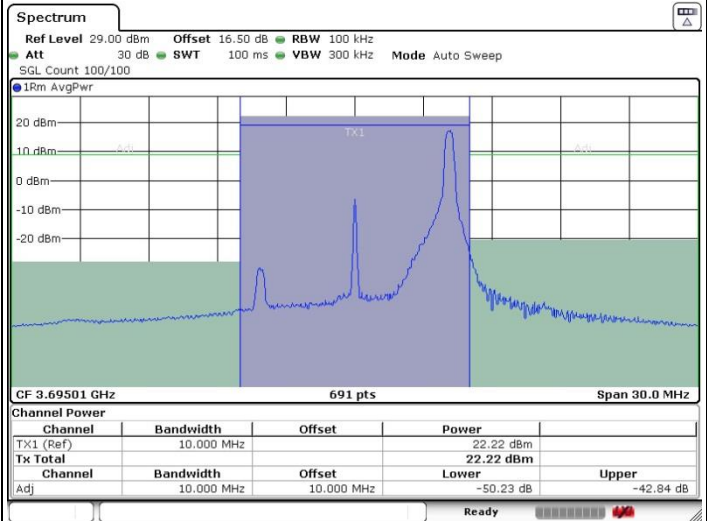
64QAM

Highest Channel / 1RB0

Highest Channel / 1RBmax

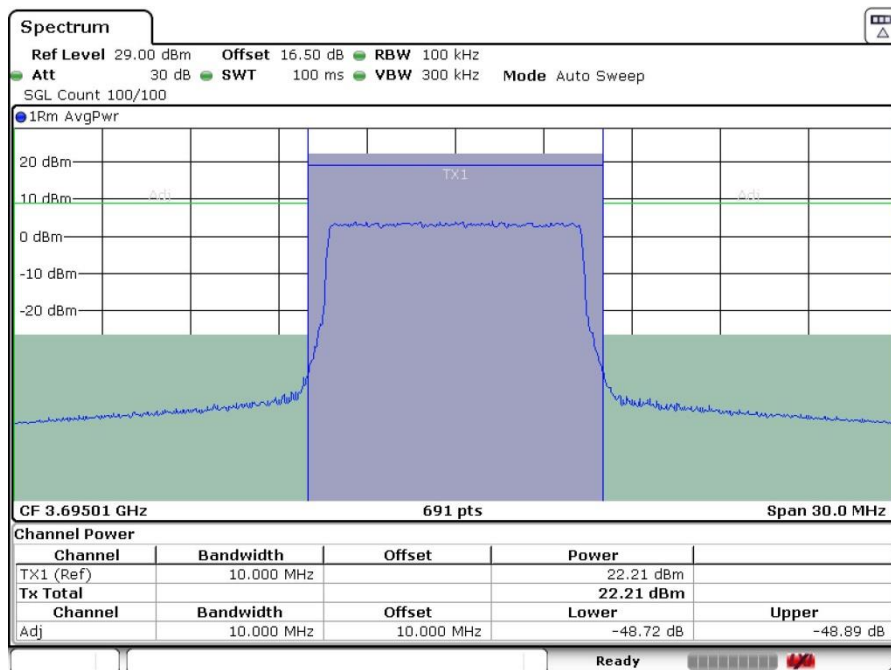


Date: 15.APR.2023 03:33:48



Date: 15.APR.2023 03:36:03

Highest Channel / FullIRB



Date: 15.APR.2023 03:32:14

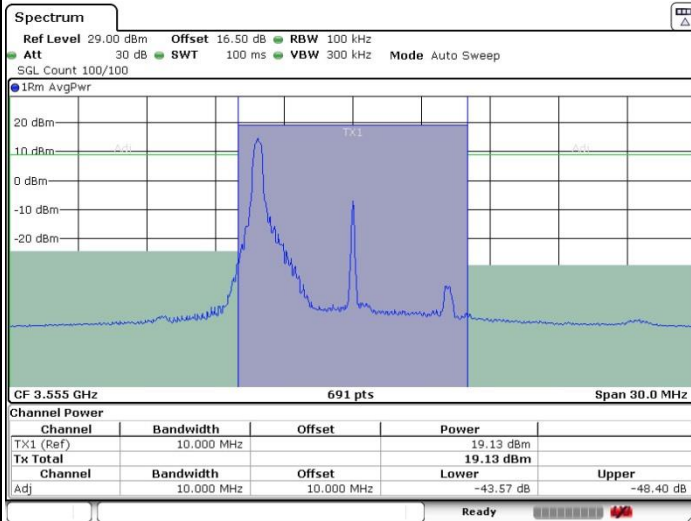


FR1 n78 / 10MHz

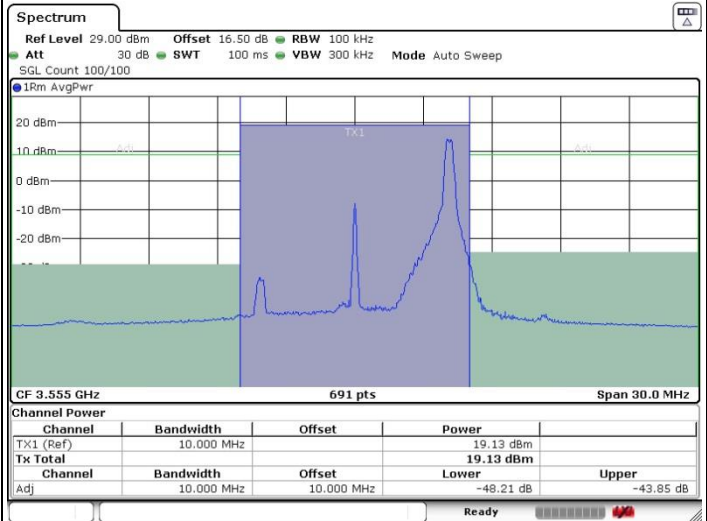
256QAM

Lowest Channel / 1RB0

Lowest Channel / 1RBmax

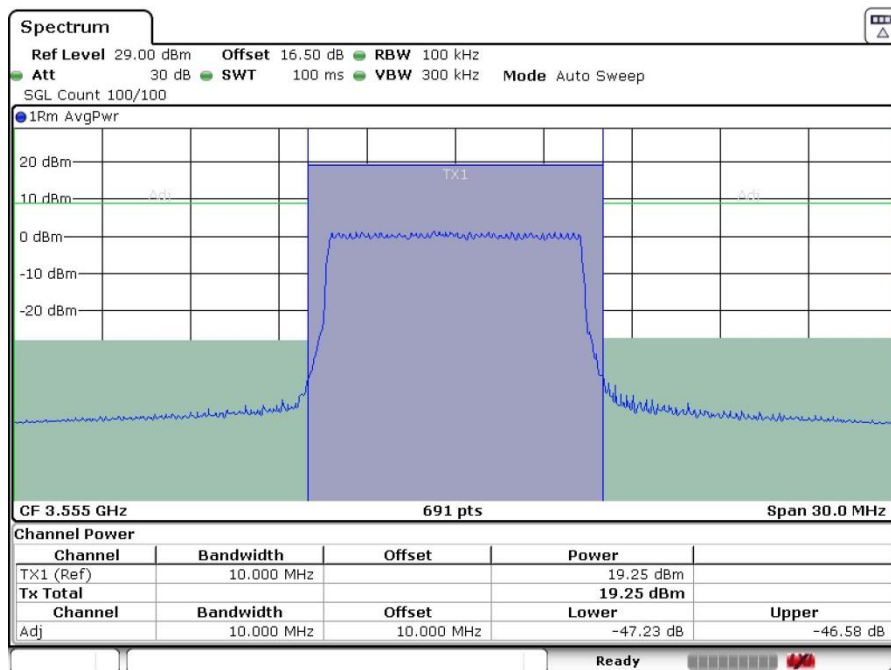


Date: 15.APR.2023 03:24:23



Date: 15.APR.2023 03:20:58

Lowest Channel / FullIRB



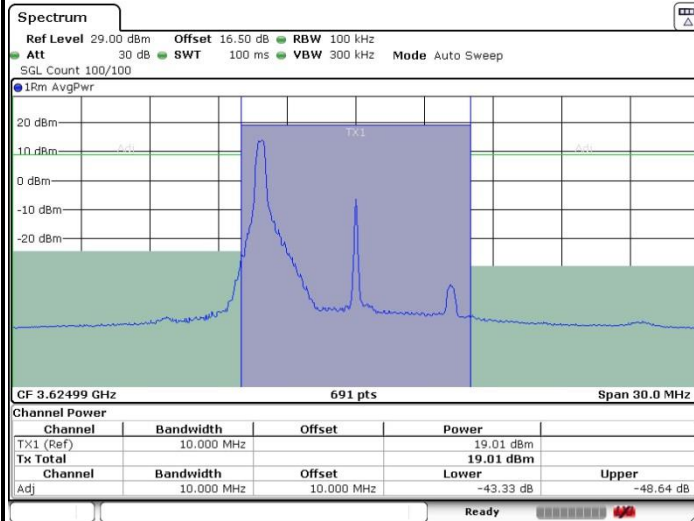
Date: 15.APR.2023 03:28:16



FR1 n78 / 10MHz

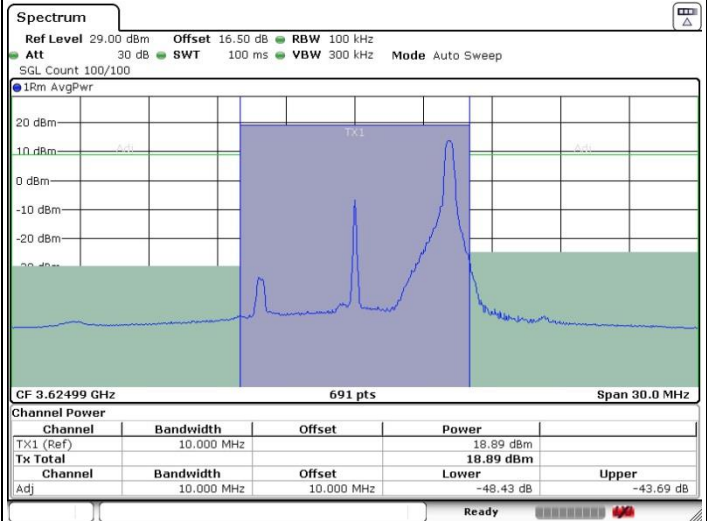
256QAM

Middle Channel / 1RB0



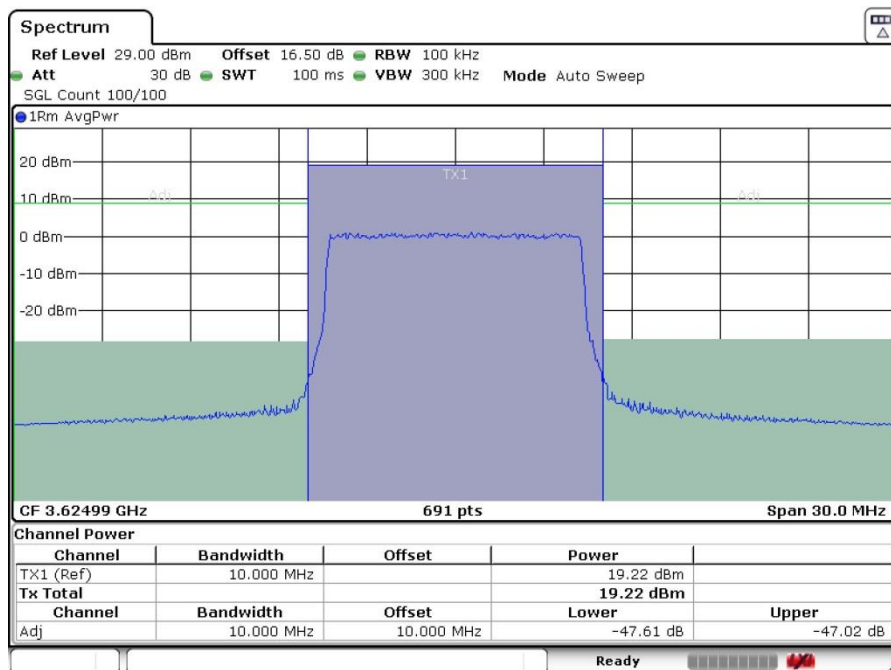
Date: 15.APR.2023 03:16:37

Middle Channel / 1RBmax



Date: 15.APR.2023 03:20:03

Middle Channel / FullRB



Date: 15.APR.2023 03:16:11

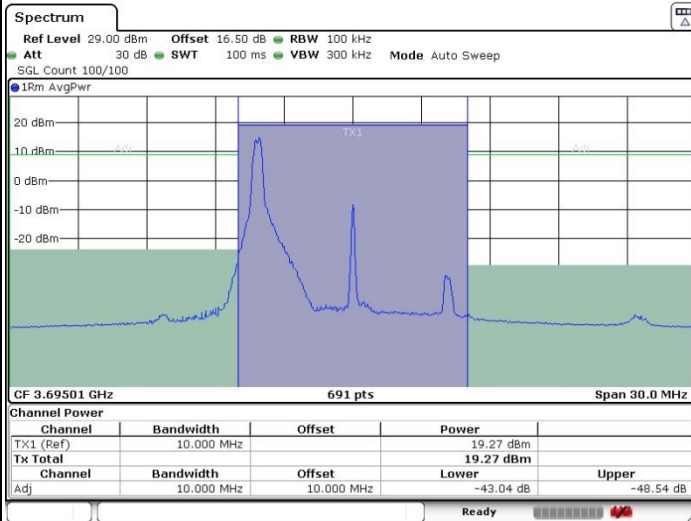


FR1 n78 / 10MHz

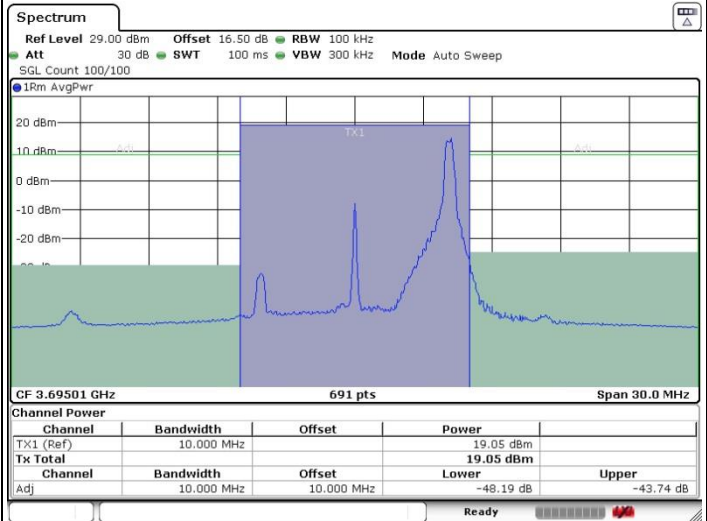
256QAM

Highest Channel / 1RB0

Highest Channel / 1RBmax

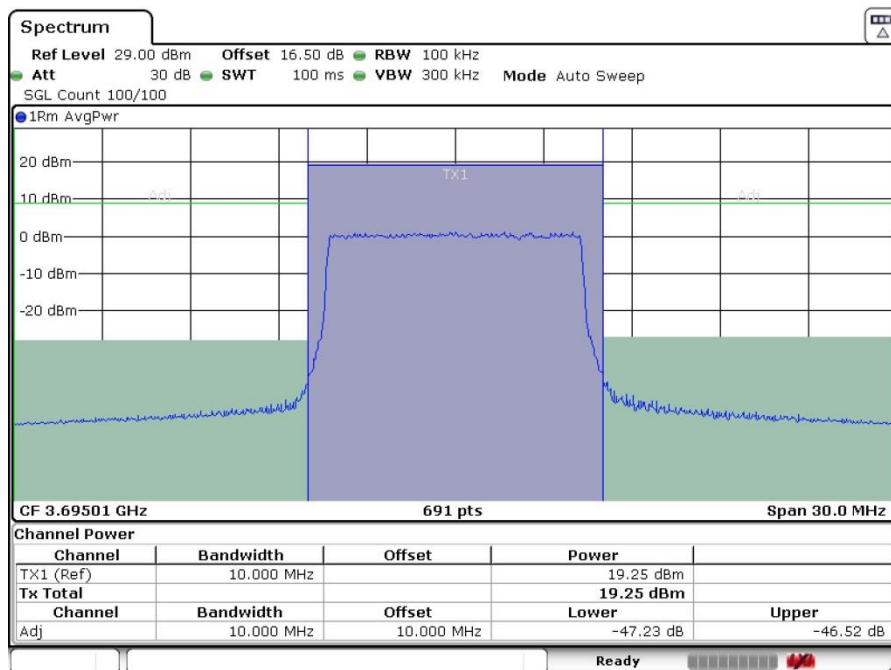


Date: 15.APR.2023 03:33:19



Date: 15.APR.2023 03:36:28

Highest Channel / FullIRB



Date: 15.APR.2023 03:32:51

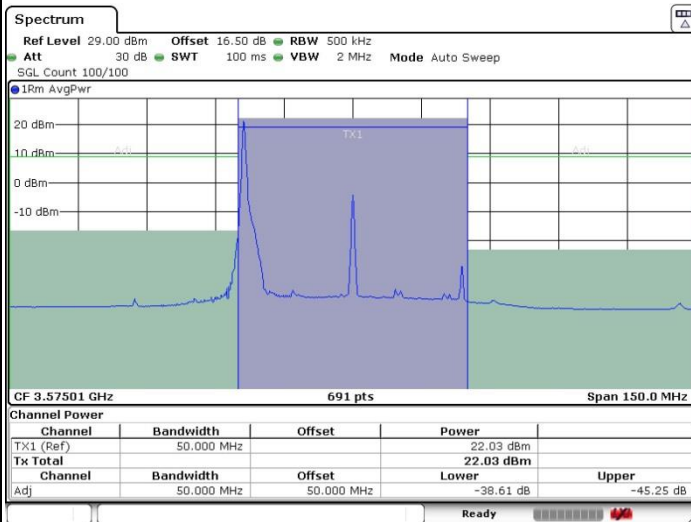


FR1 n78 / 50MHz

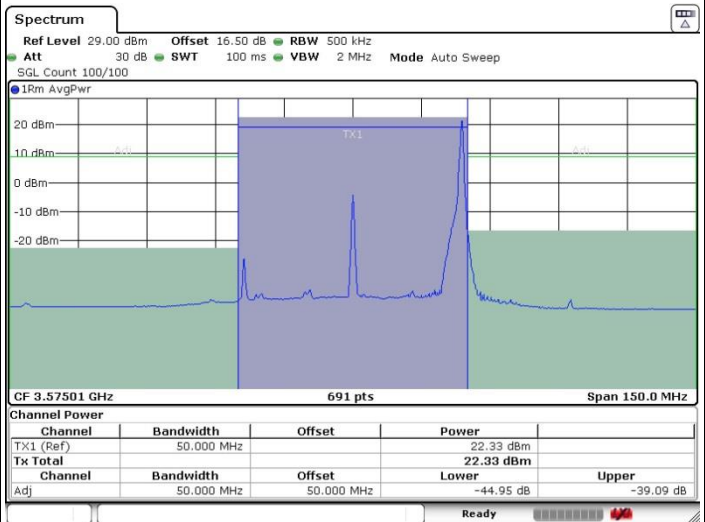
QPSK

Lowest Channel / 1RB0

Lowest Channel / 1RBmax

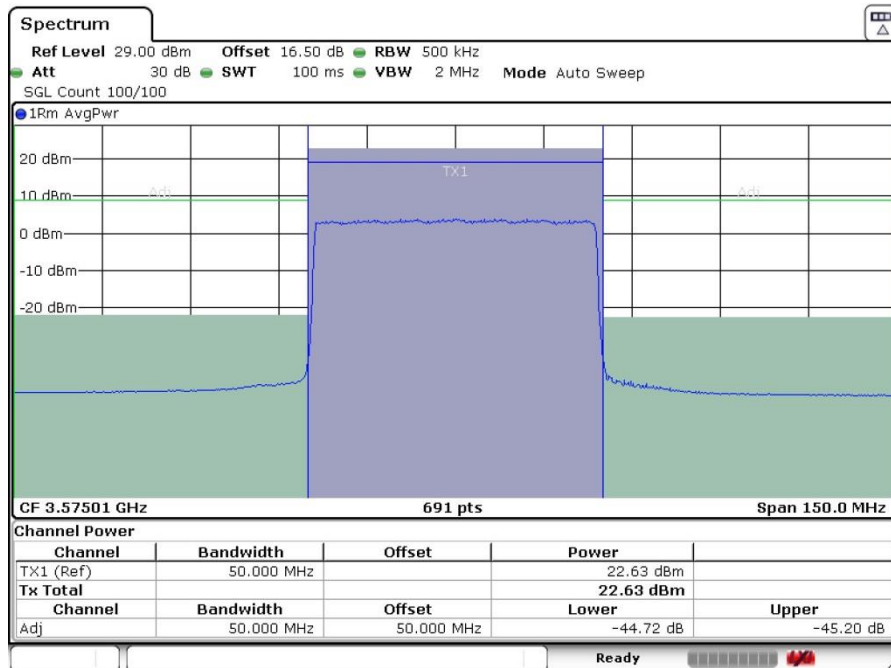


Date: 15.APR.2023 03:04:36



Date: 15.APR.2023 03:00:55

Lowest Channel / FullIRB



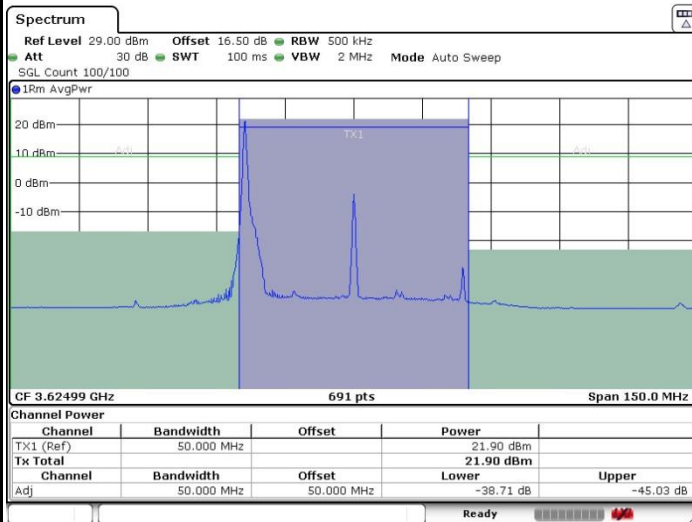
Date: 15.APR.2023 03:05:05



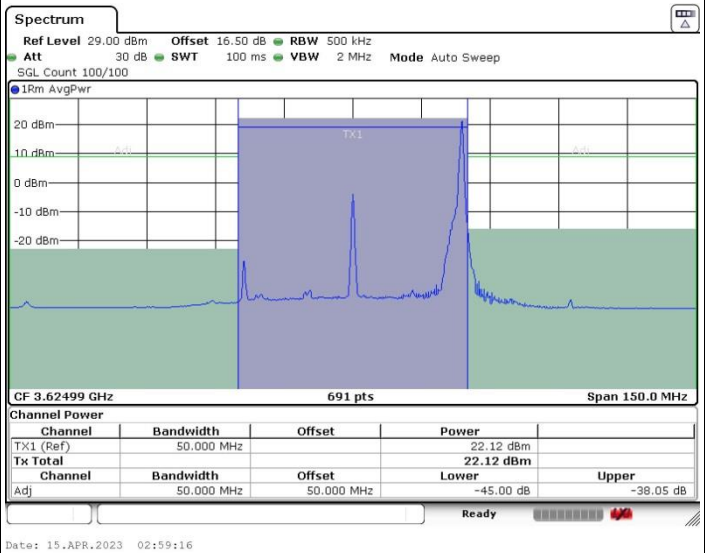
FR1 n78 / 50MHz

QPSK

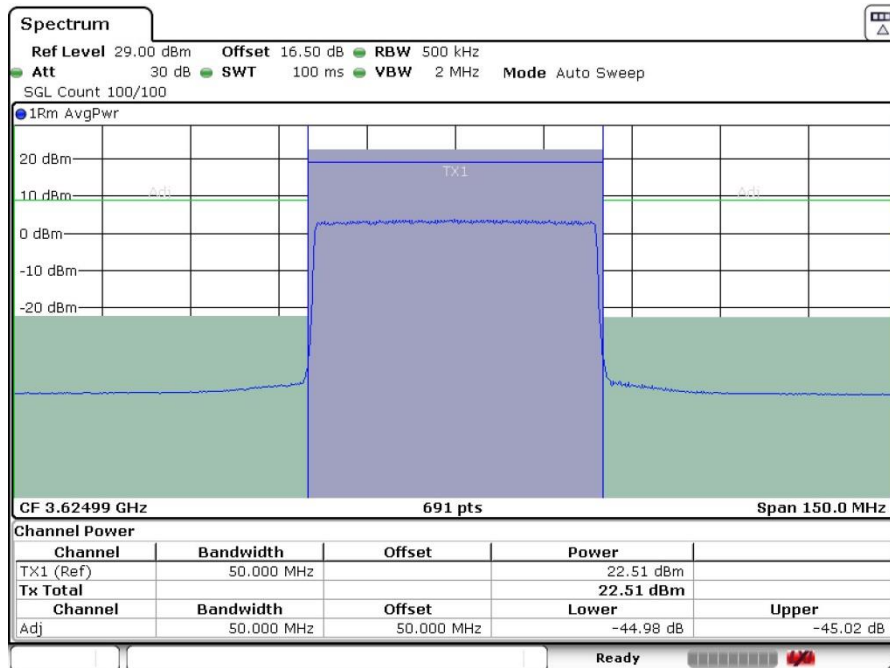
Middle Channel / 1RB0



Middle Channel / 1RBmax



Middle Channel / FullIRB

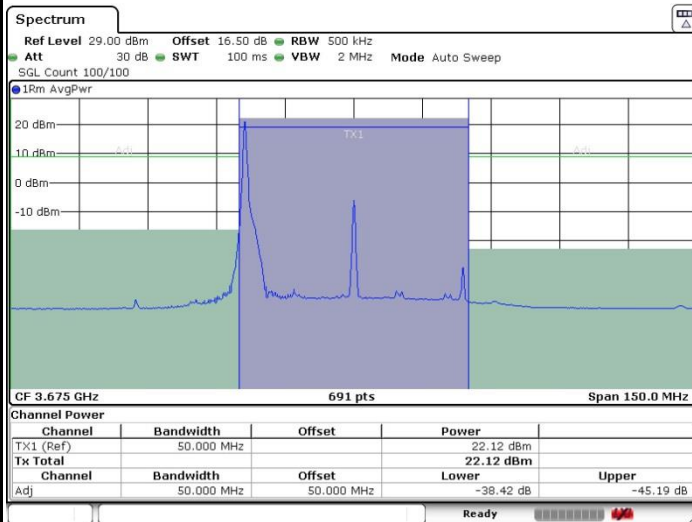




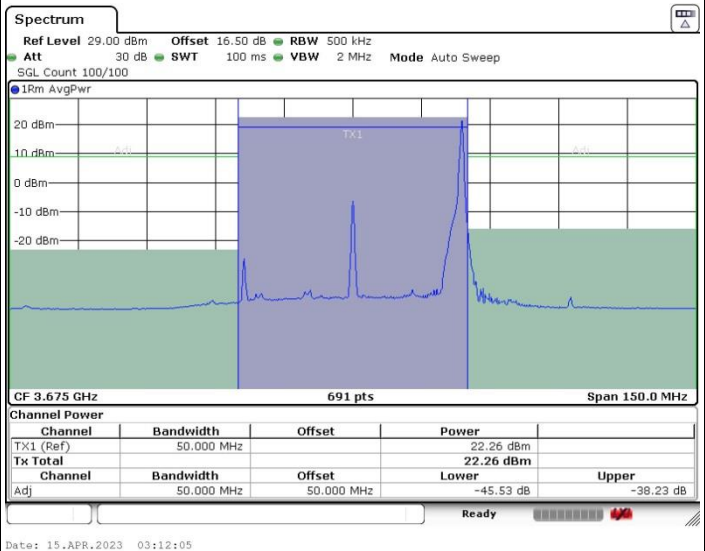
FR1 n78 / 50MHz

QPSK

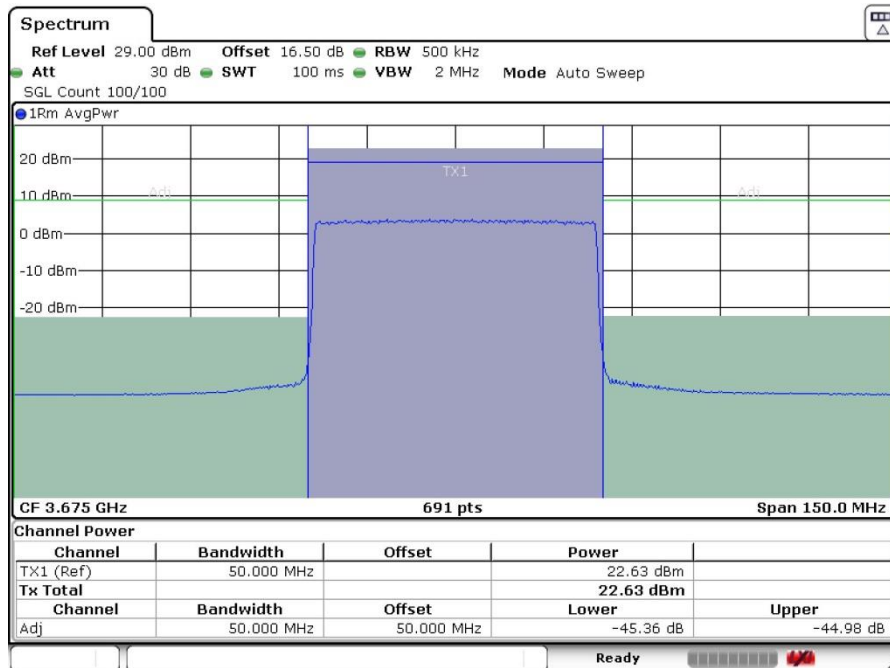
Highest Channel / 1RB0



Highest Channel / 1RBmax



Highest Channel / FullRB



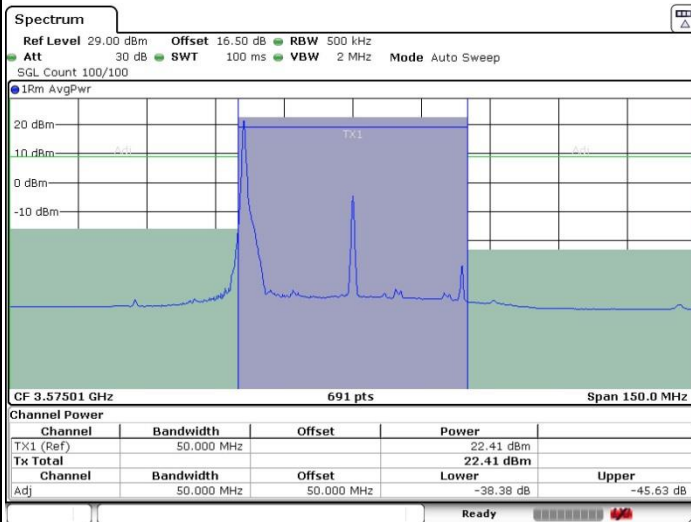


FR1 n78 / 50MHz

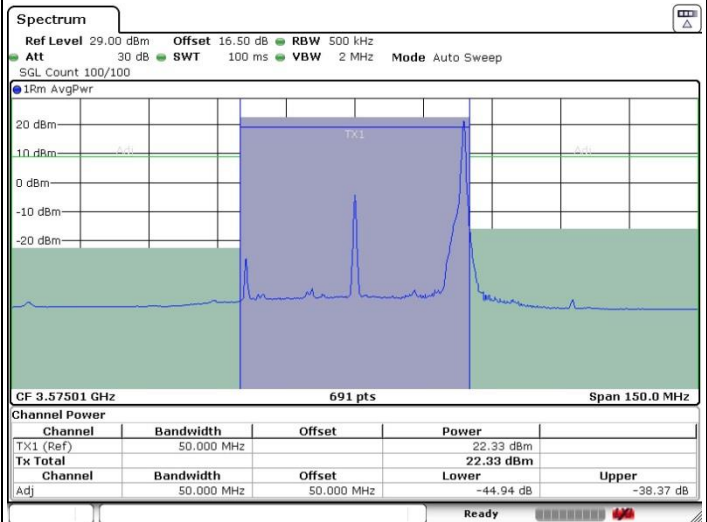
16QAM

Lowest Channel / 1RB0

Lowest Channel / 1RBmax

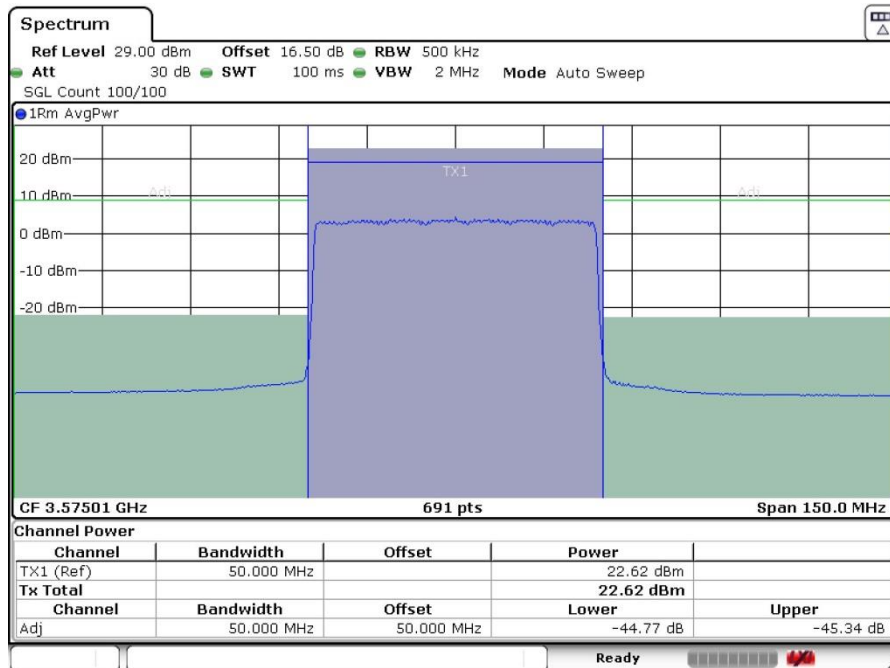


Date: 15.APR.2023 03:04:09



Date: 15.APR.2023 03:02:15

Lowest Channel / FullRB



Date: 15.APR.2023 03:05:38

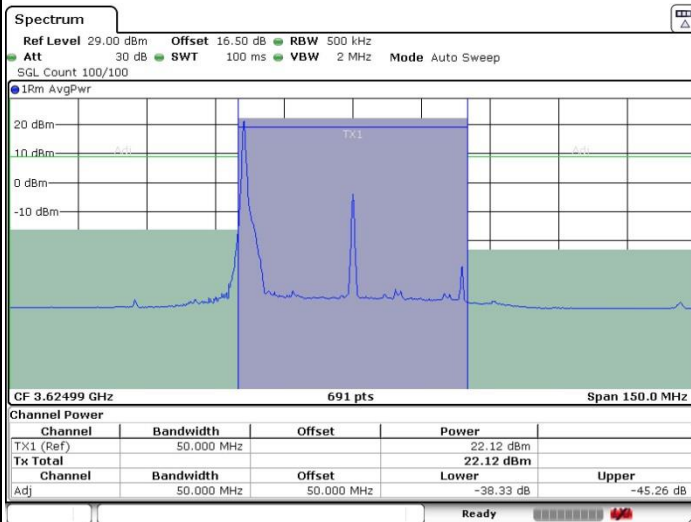


FR1 n78 / 50MHz

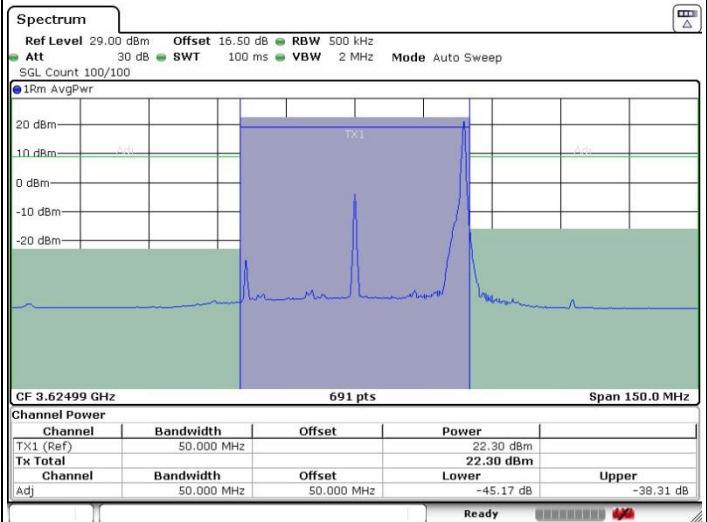
16QAM

Middle Channel / 1RB0

Middle Channel / 1RBmax

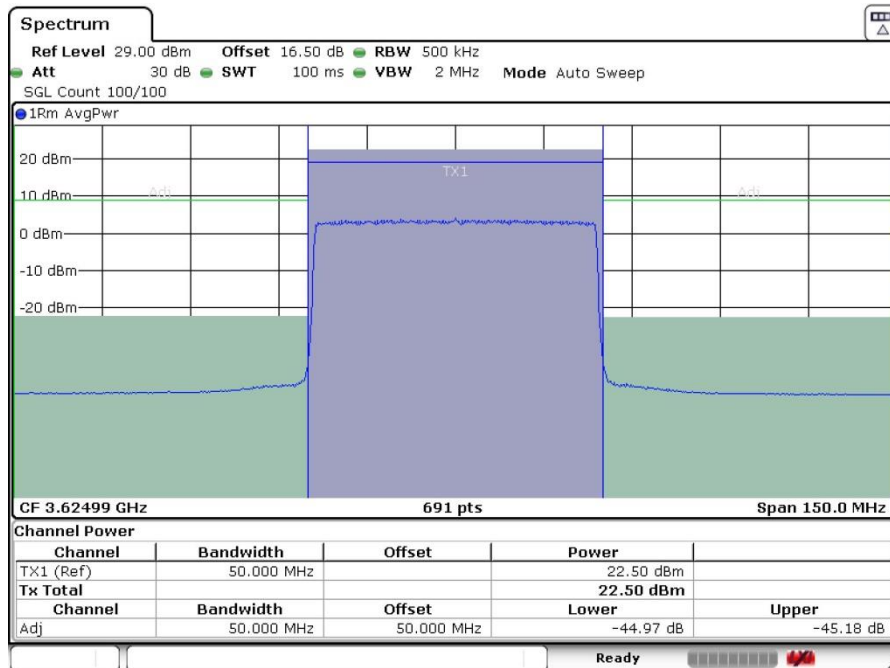


Date: 15.APR.2023 02:56:00

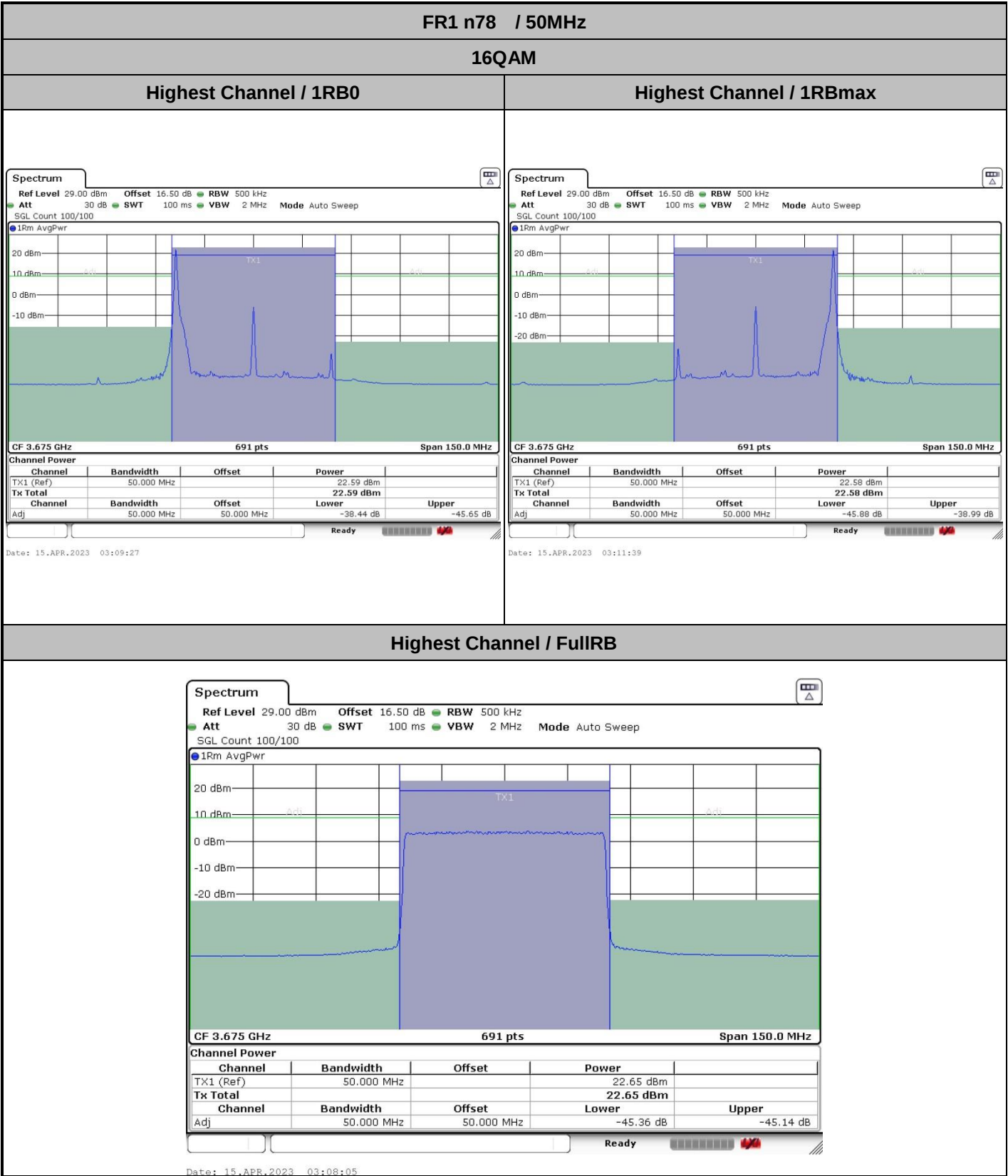


Date: 15.APR.2023 02:58:57

Middle Channel / FullIRB



Date: 15.APR.2023 02:54:51



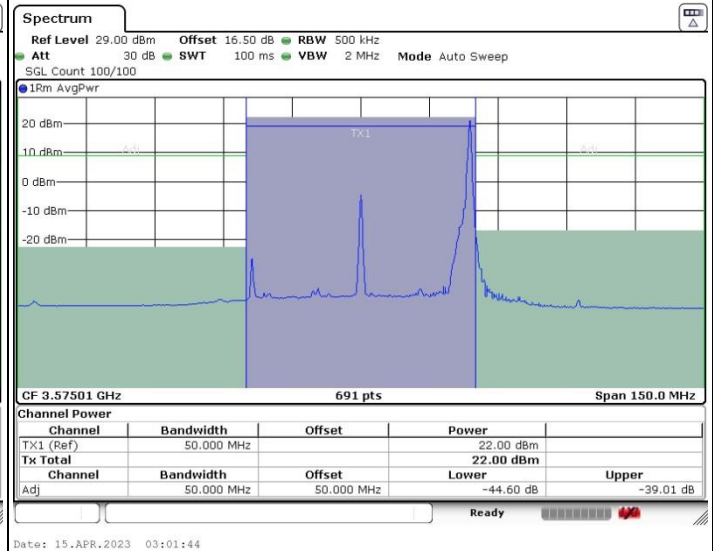
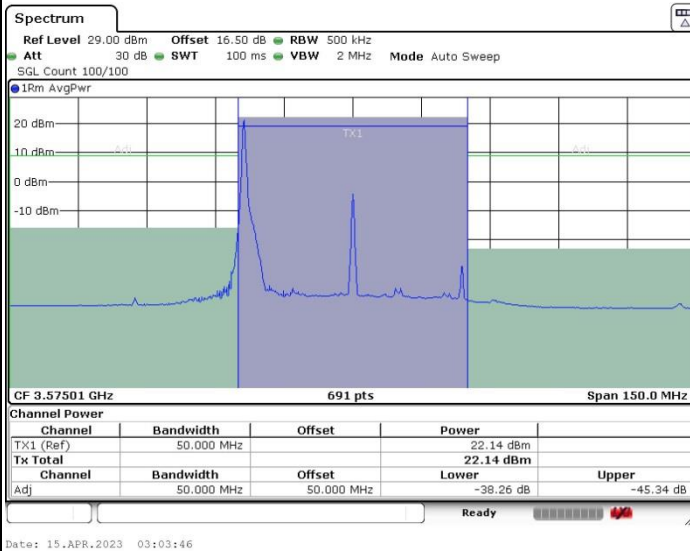


FR1 n78 / 50MHz

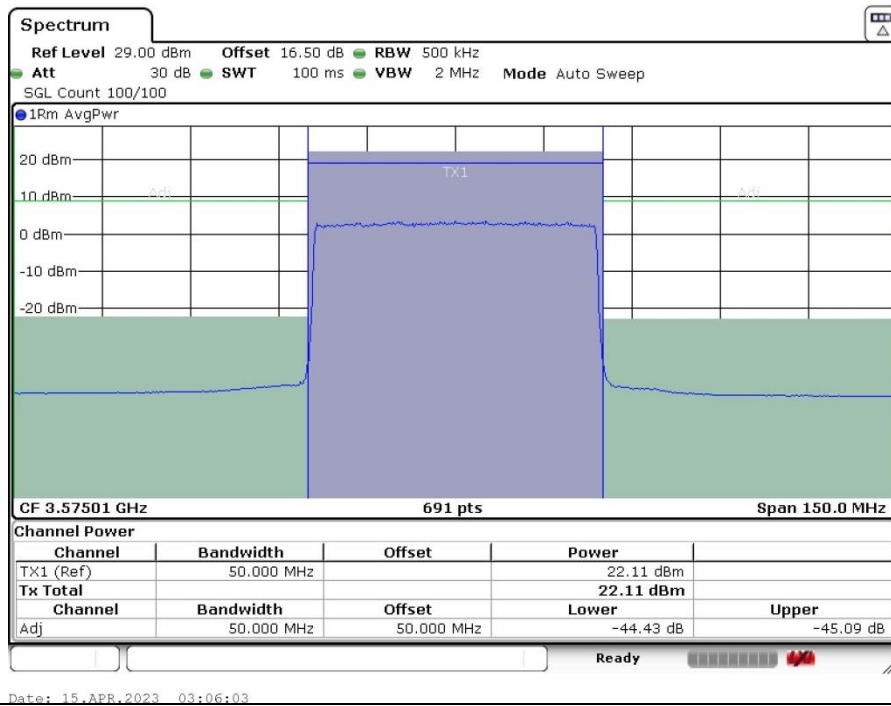
64QAM

Lowest Channel / 1RB0

Lowest Channel / 1RBmax



Lowest Channel / FullRB



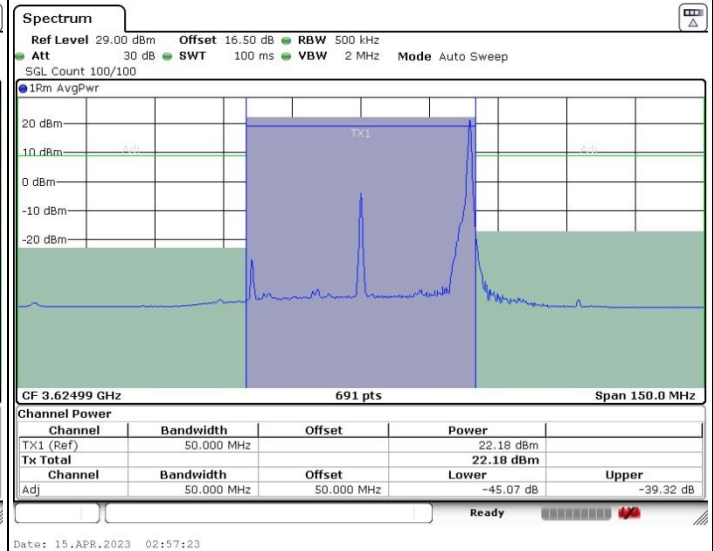
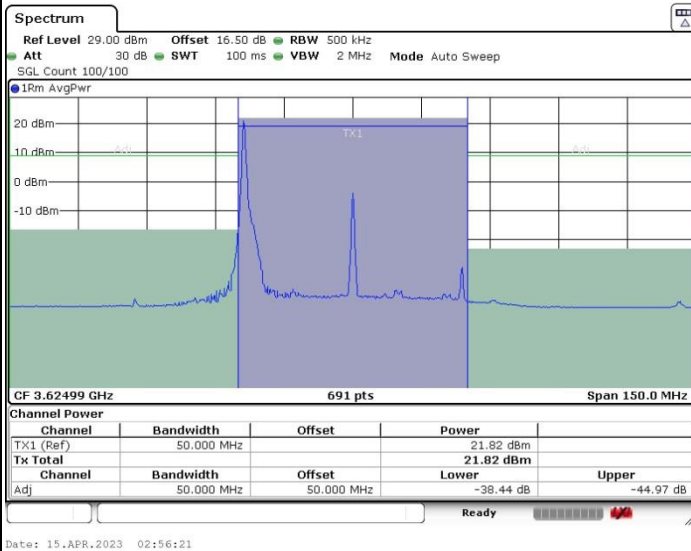


FR1 n78 / 50MHz

64QAM

Middle Channel / 1RB0

Middle Channel / 1RBmax



Middle Channel / FullRB

