

Appendix D.4: Maximum conducted output power

Test Result

Test Mode	Antenna	Freq (MHz)	Channel Power [dBm]	Duty Cycle [%]	DC Factor [dBm]	Result [dBm]	Limit [dBm]	Verdict
11A	Ant0	5180	12.63	97.89	0.09	12.72	≤23.98	PASS
		5200	13.18	97.20	0.12	13.30	≤23.98	PASS
		5240	13.98	97.90	0.09	14.07	≤23.98	PASS
11N20SI SO	Ant0	5180	12.67	97.20	0.12	12.79	≤23.98	PASS
		5200	13.33	97.20	0.12	13.45	≤23.98	PASS
		5240	14.15	97.20	0.12	14.27	≤23.98	PASS
11N40SI SO	Ant0	5190	10.16	94.20	0.26	10.42	≤23.98	PASS
		5230	11.01	95.59	0.20	11.21	≤23.98	PASS
11AC20S ISO	Ant0	5180	11.32	97.76	0.10	11.42	≤23.98	PASS
		5200	11.70	97.04	0.13	11.83	≤23.98	PASS
		5240	12.63	97.04	0.13	12.76	≤23.98	PASS
11AC40S ISO	Ant0	5190	10.11	94.20	0.26	10.37	≤23.98	PASS
		5230	10.95	95.59	0.20	11.15	≤23.98	PASS
11AC80S ISO	Ant0	5210	8.63	91.67	0.38	9.01	≤23.98	PASS
11AX20SI SO	Ant0	5180	11.35	96.19	0.17	11.52	≤23.98	PASS
		5200	11.78	96.19	0.17	11.95	≤23.98	PASS
		5240	12.82	96.19	0.17	12.99	≤23.98	PASS
11AX40SI SO	Ant0	5190	10.25	94.64	0.24	10.49	≤23.98	PASS
		5230	11.35	92.98	0.32	11.67	≤23.98	PASS
11AX80SI SO	Ant0	5210	7.07	50.39	2.98	10.05	≤23.98	PASS

Note:

1. The Duty Cycle Factor is compensated in the graph.
2. The Duty Cycle Factor and RBW Factor is compensated in the data.

Appendix D.5: Maximum power spectral density

Test Result

TestMode	Antenna	Freq(MHz)	Result [dBm/MHz]	Limit[dBm/MHz]	Verdict
11A	Ant0	5180	1.75	≤11.00	PASS
		5200	2.26	≤11.00	PASS
		5240	3.29	≤11.00	PASS
11N20SISO	Ant0	5180	1.81	≤11.00	PASS
		5200	2.56	≤11.00	PASS
		5240	3.50	≤11.00	PASS
11N40SISO	Ant0	5190	-2.73	≤11.00	PASS
		5230	-1.96	≤11.00	PASS
11AC20SISO	Ant0	5180	0.29	≤11.00	PASS
		5200	0.71	≤11.00	PASS
		5240	1.81	≤11.00	PASS
11AC40SISO	Ant0	5190	-2.68	≤11.00	PASS
		5230	-1.98	≤11.00	PASS
11AC80SISO	Ant0	5210	-7.30	≤11.00	PASS
11AX20SISO	Ant0	5180	-0.01	≤11.00	PASS
		5200	0.28	≤11.00	PASS
		5240	1.67	≤11.00	PASS
11AX40SISO	Ant0	5190	-3.01	≤11.00	PASS
		5230	-1.80	≤11.00	PASS
11AX80SISO	Ant0	5210	-6.67	≤11.00	PASS

Note: 1.The Result and Limit Unit is dBm/500 kHz in the band 5.725–5.85 GHz.

2.The Duty Cycle Factor and RBW Factor is compensated in the graph.

Test Graphs

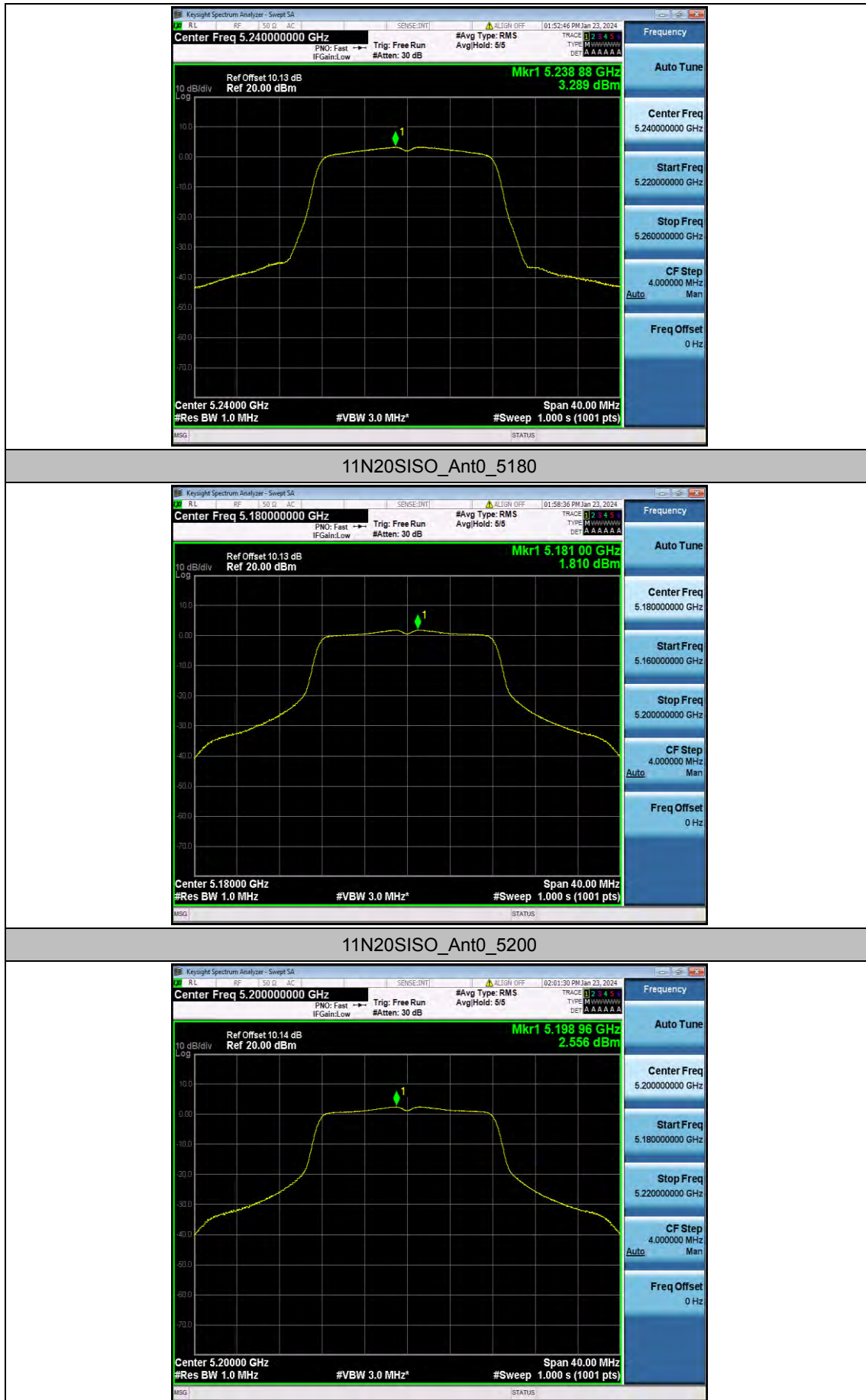
11A_Ant0_5180



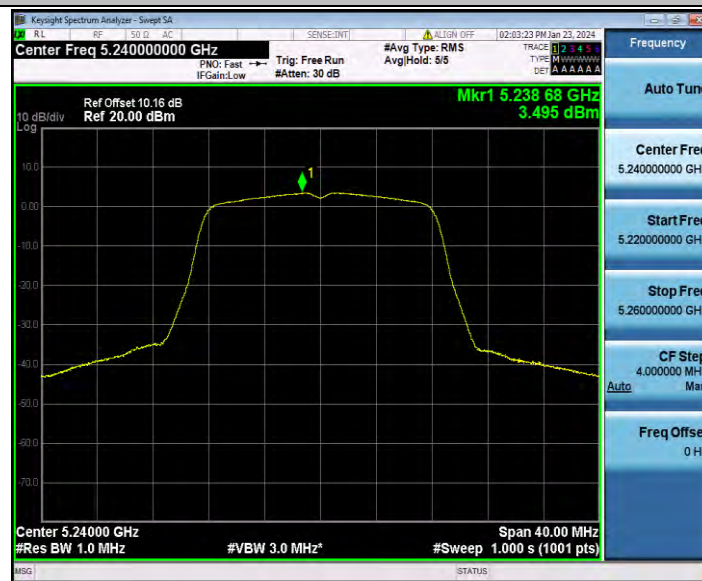
11A_Ant0_5200



11A_Ant0_5240



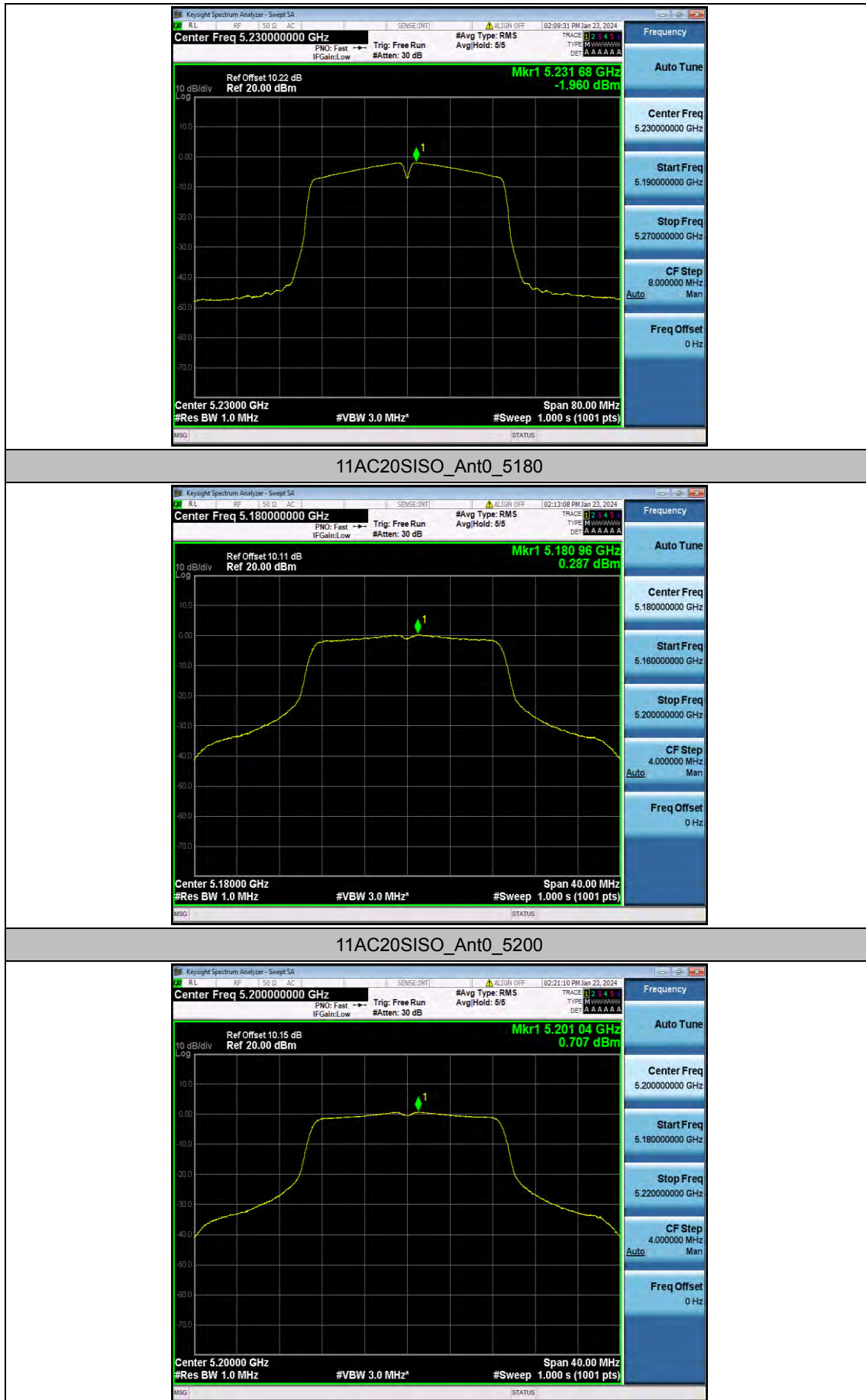
11N20SISO_Ant0_5240



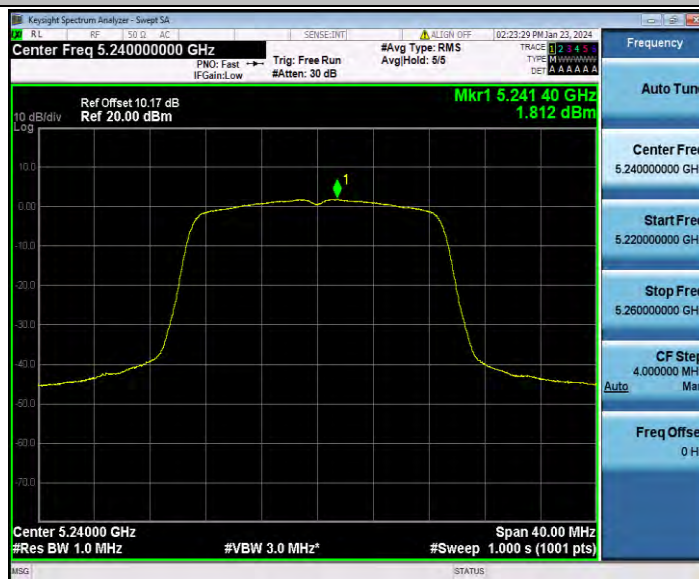
11N40SISO_Ant0_5190



11N40SISO_Ant0_5230



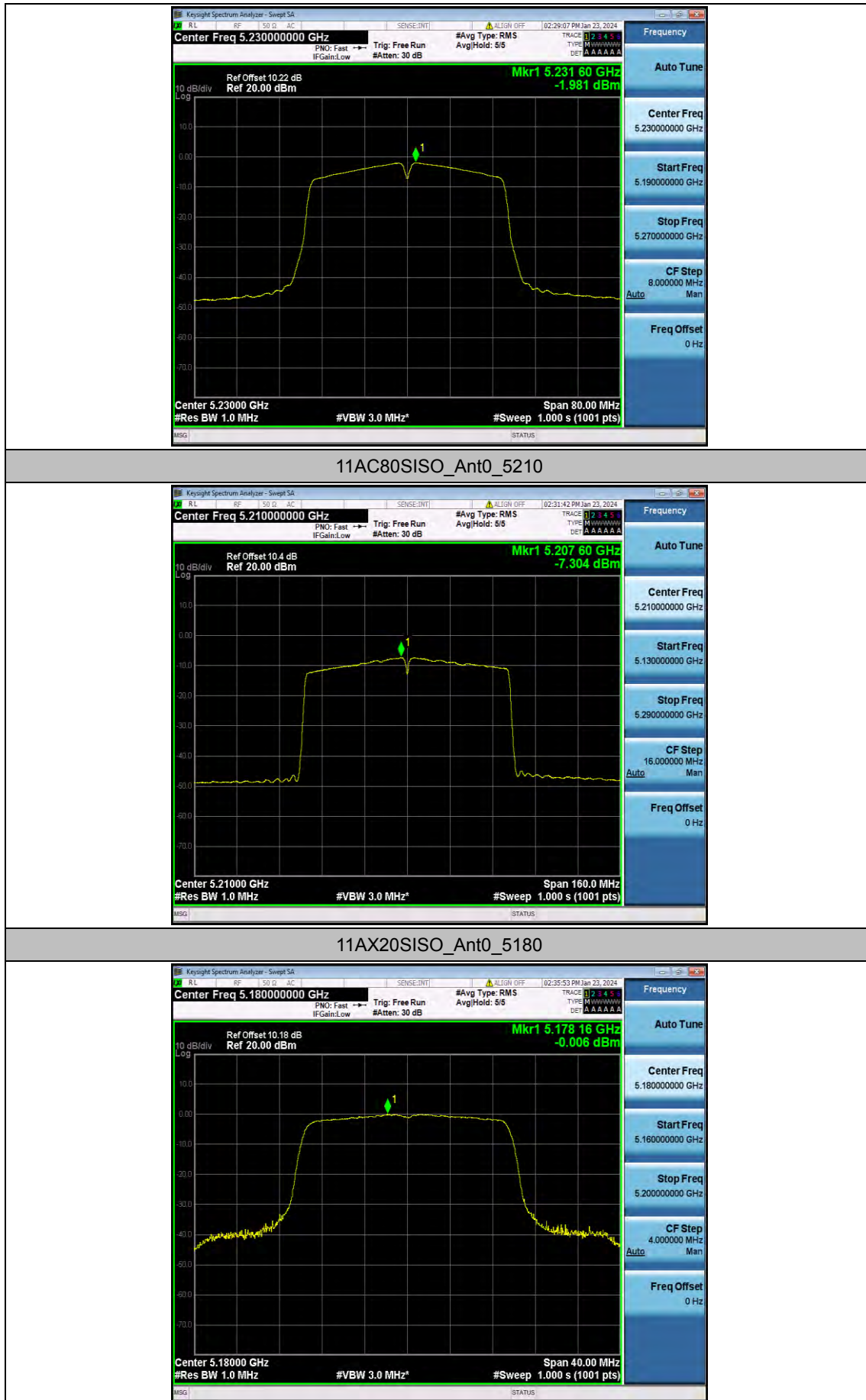
11AC20SISO_Ant0_5240



11AC40SISO_Ant0_5190



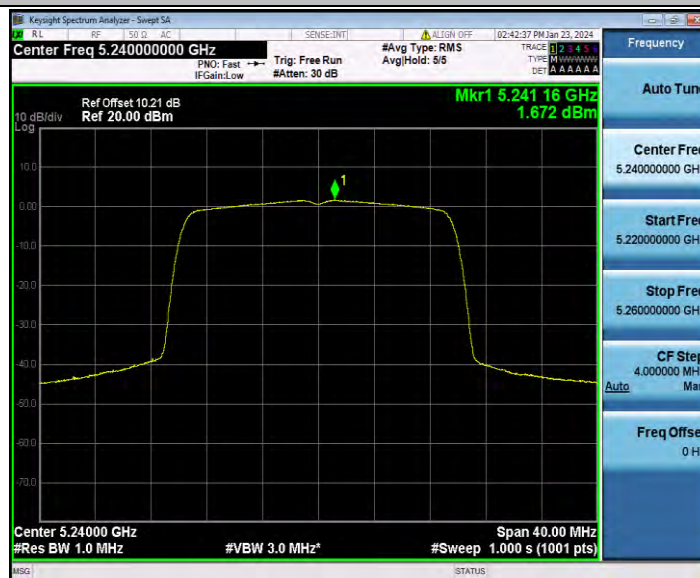
11AC40SISO_Ant0_5230



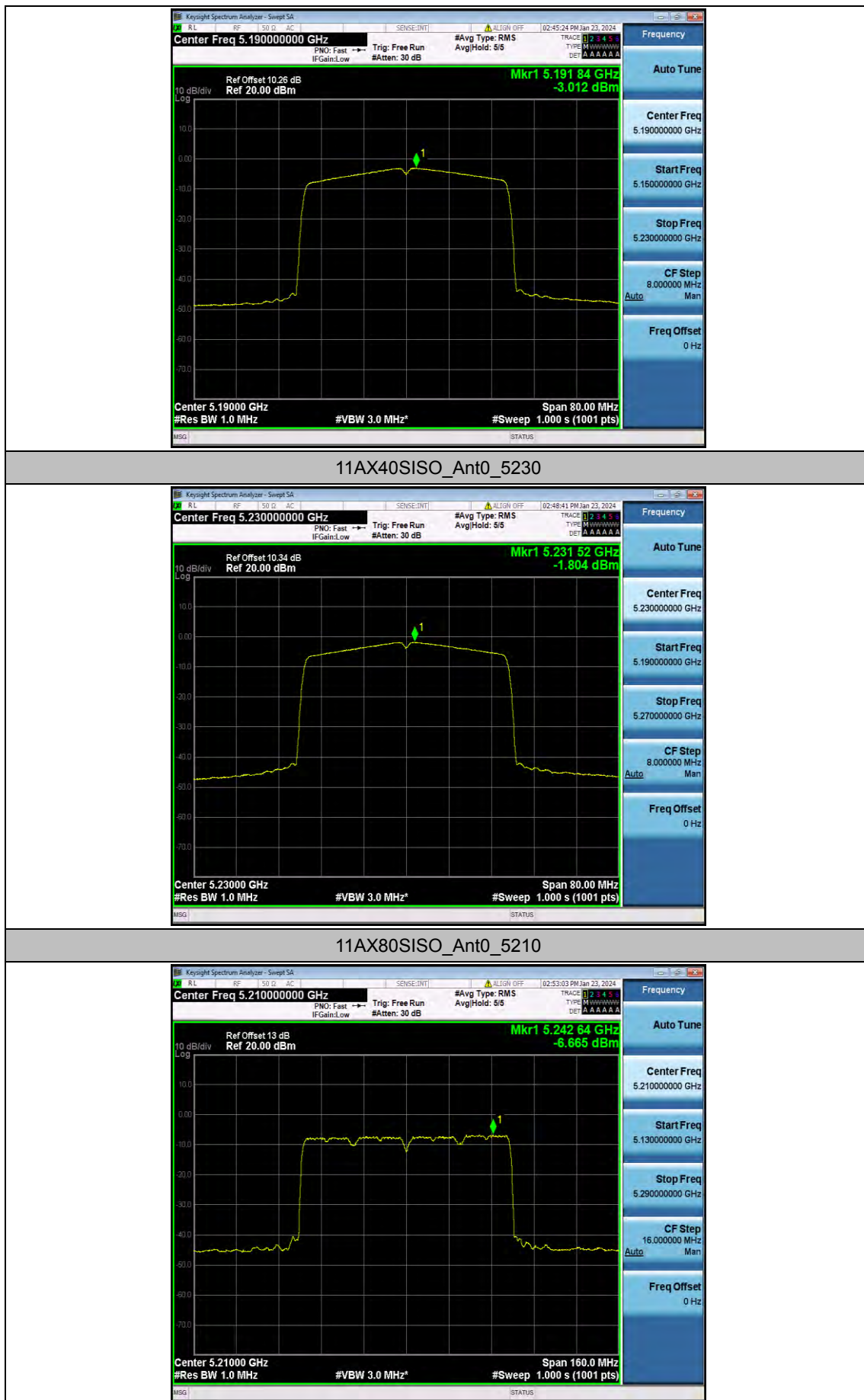
11AX20SISO_Ant0_5200



11AX20SISO_Ant0_5240



11AX40SISO_Ant0_5190

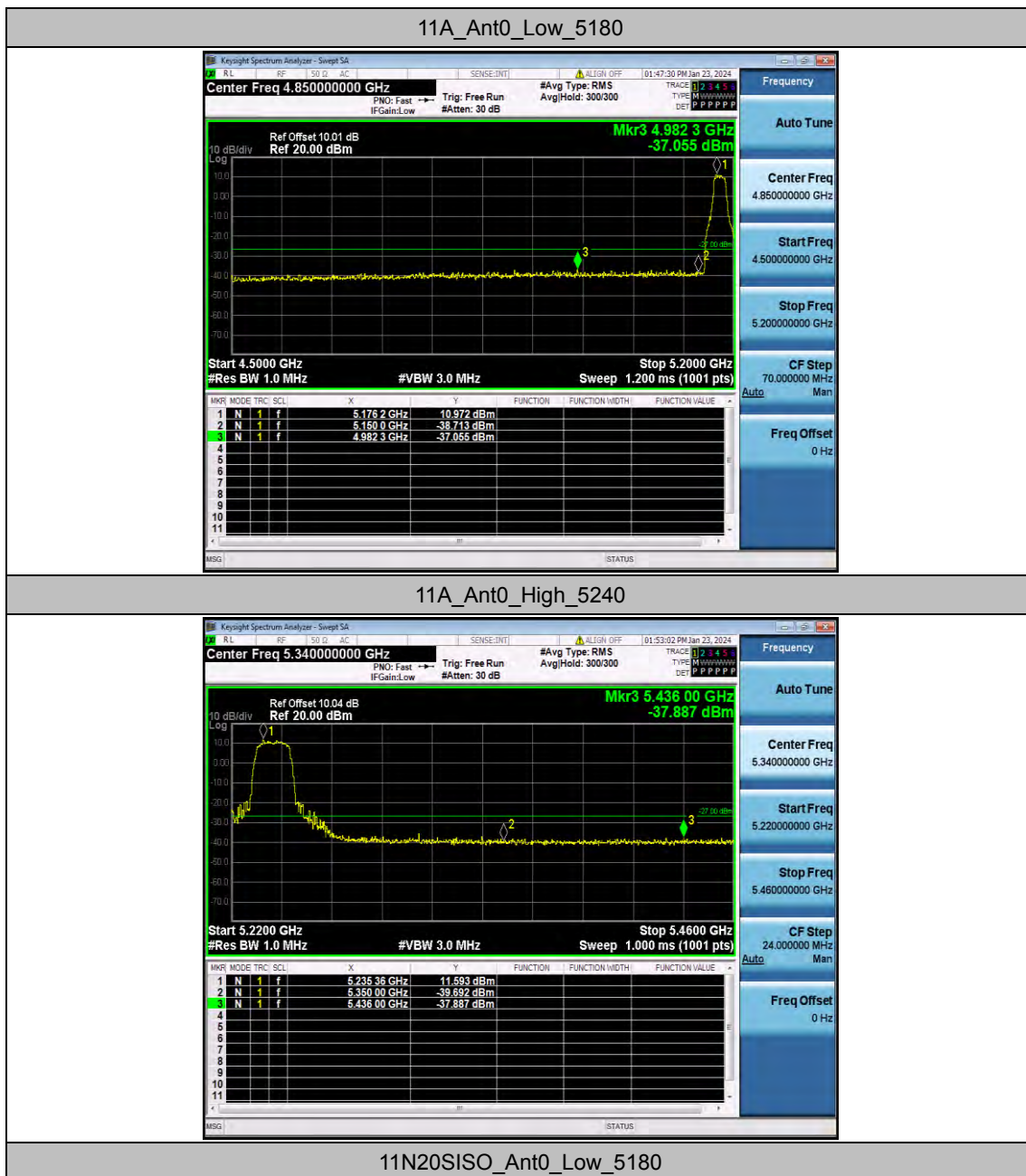


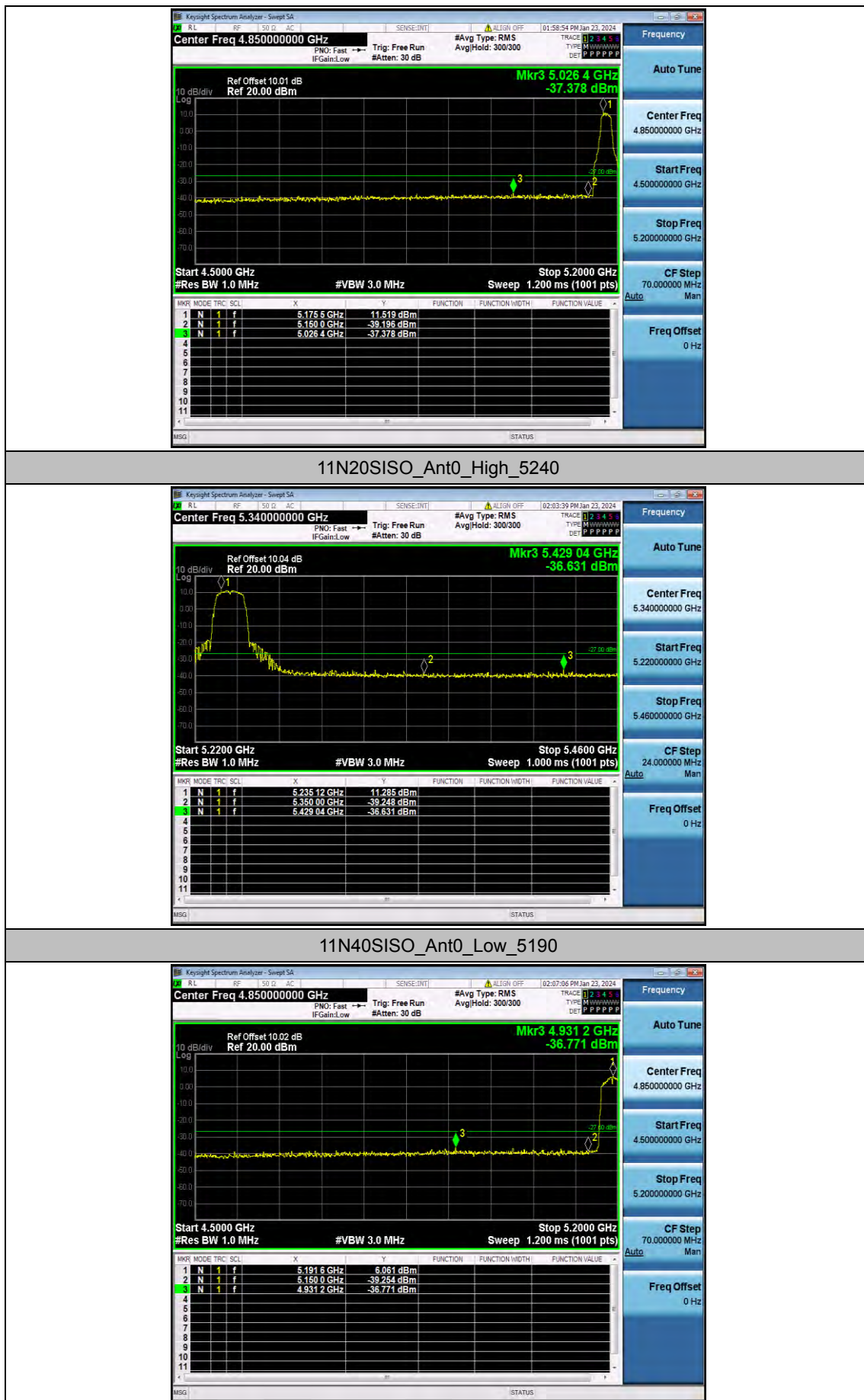
Appendix D.6: Band edge measurements

Test Result B1

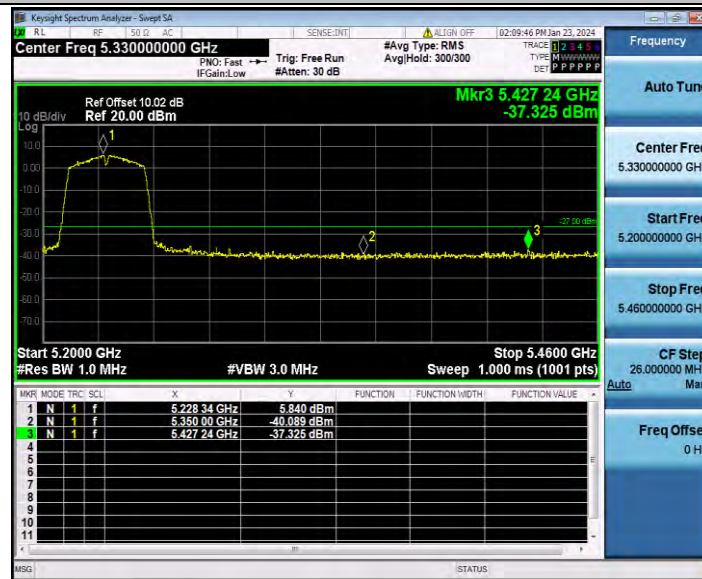
TestMode	Antenna	ChName	Freq(MHz)	Result[dBm]	Limit[dBm]	Verdict
11A	Ant0	Low	5180	-37.06	≤ -27	PASS
		High	5240	-37.89	≤ -27	PASS
11N20SISO	Ant0	Low	5180	-37.38	≤ -27	PASS
		High	5240	-36.63	≤ -27	PASS
11N40SISO	Ant0	Low	5190	-36.77	≤ -27	PASS
		High	5230	-37.33	≤ -27	PASS
11AC20SISO	Ant0	Low	5180	-36.52	≤ -27	PASS
		High	5240	-37.35	≤ -27	PASS
11AC40SISO	Ant0	Low	5190	-37.17	≤ -27	PASS
		High	5230	-37.5	≤ -27	PASS
11AC80SISO	Ant0	Low	5210	-37.48	≤ -27	PASS
		High	5210	-37.42	≤ -27	PASS
11AX20SISO	Ant0	Low	5180	-36.05	≤ -27	PASS
		High	5240	-37.34	≤ -27	PASS
11AX40SISO	Ant0	Low	5190	-37.2	≤ -27	PASS
		High	5230	-37.49	≤ -27	PASS
11AX80SISO	Ant0	Low	5210	-35.42	≤ -27	PASS
		High	5210	-37.88	≤ -27	PASS

Test Graphs B1

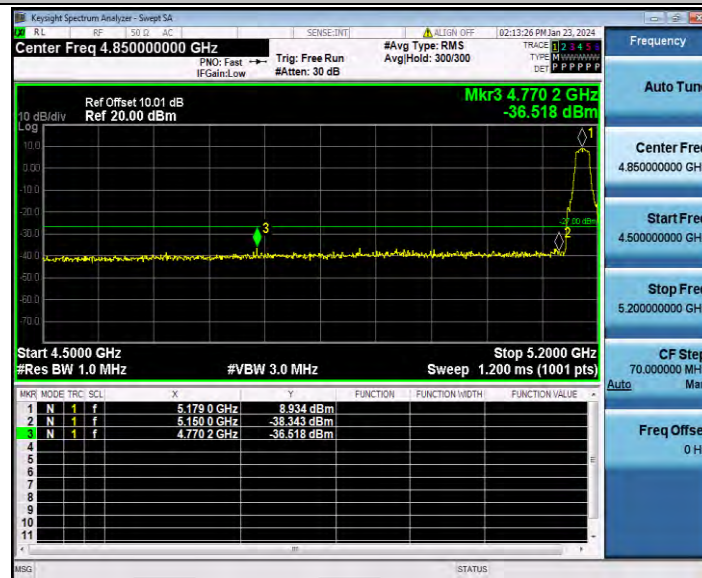




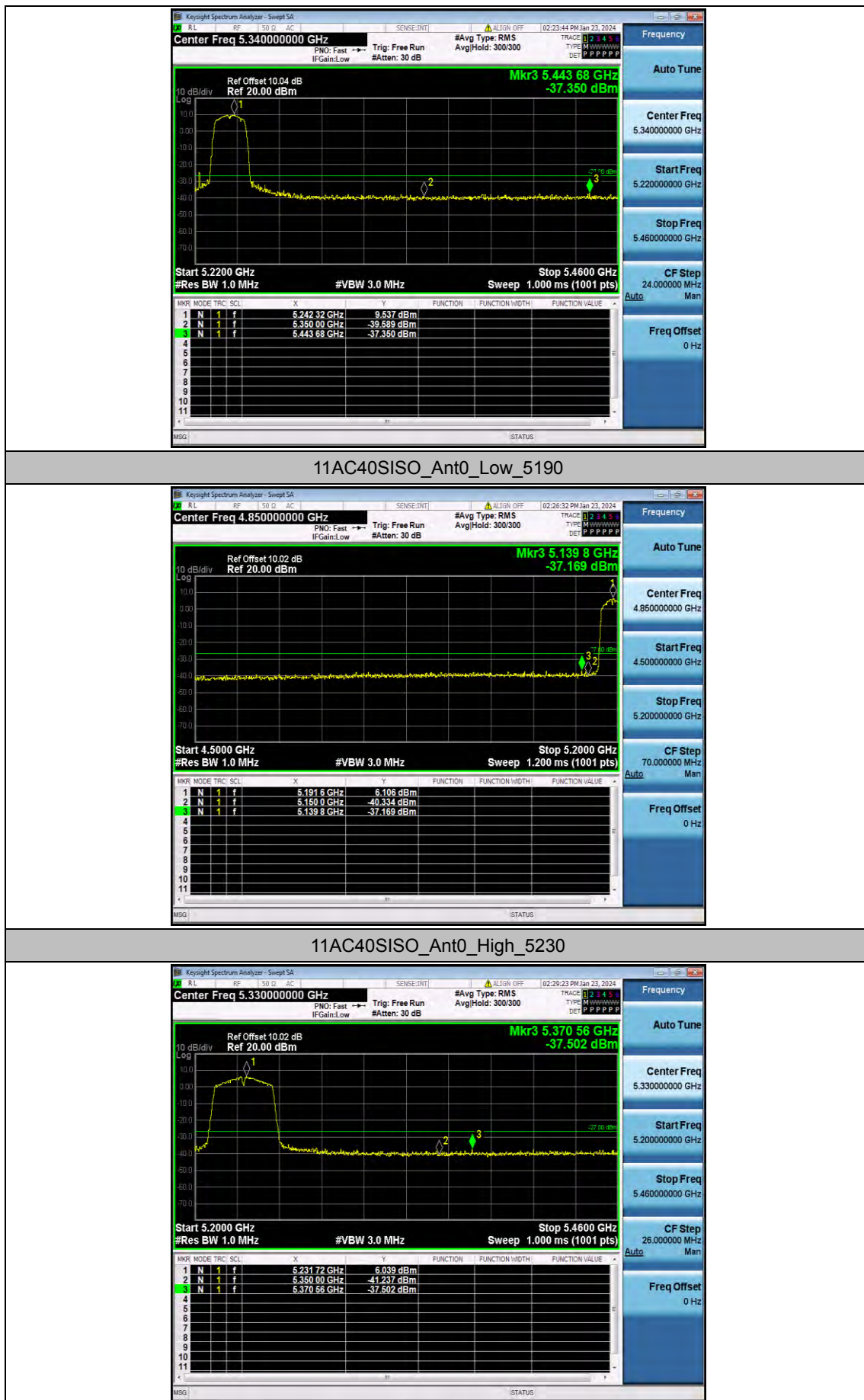
11N40SISO_Ant0_High_5230



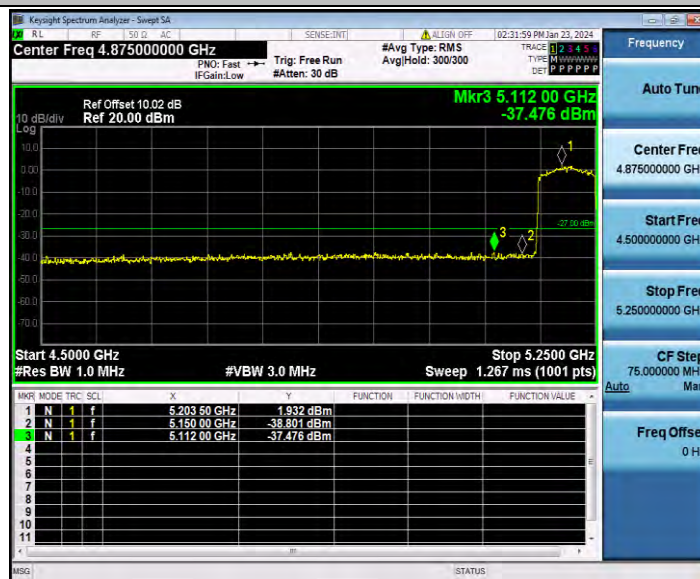
11AC20SISO_Ant0_Low_5180



11AC20SISO_Ant0_High_5240



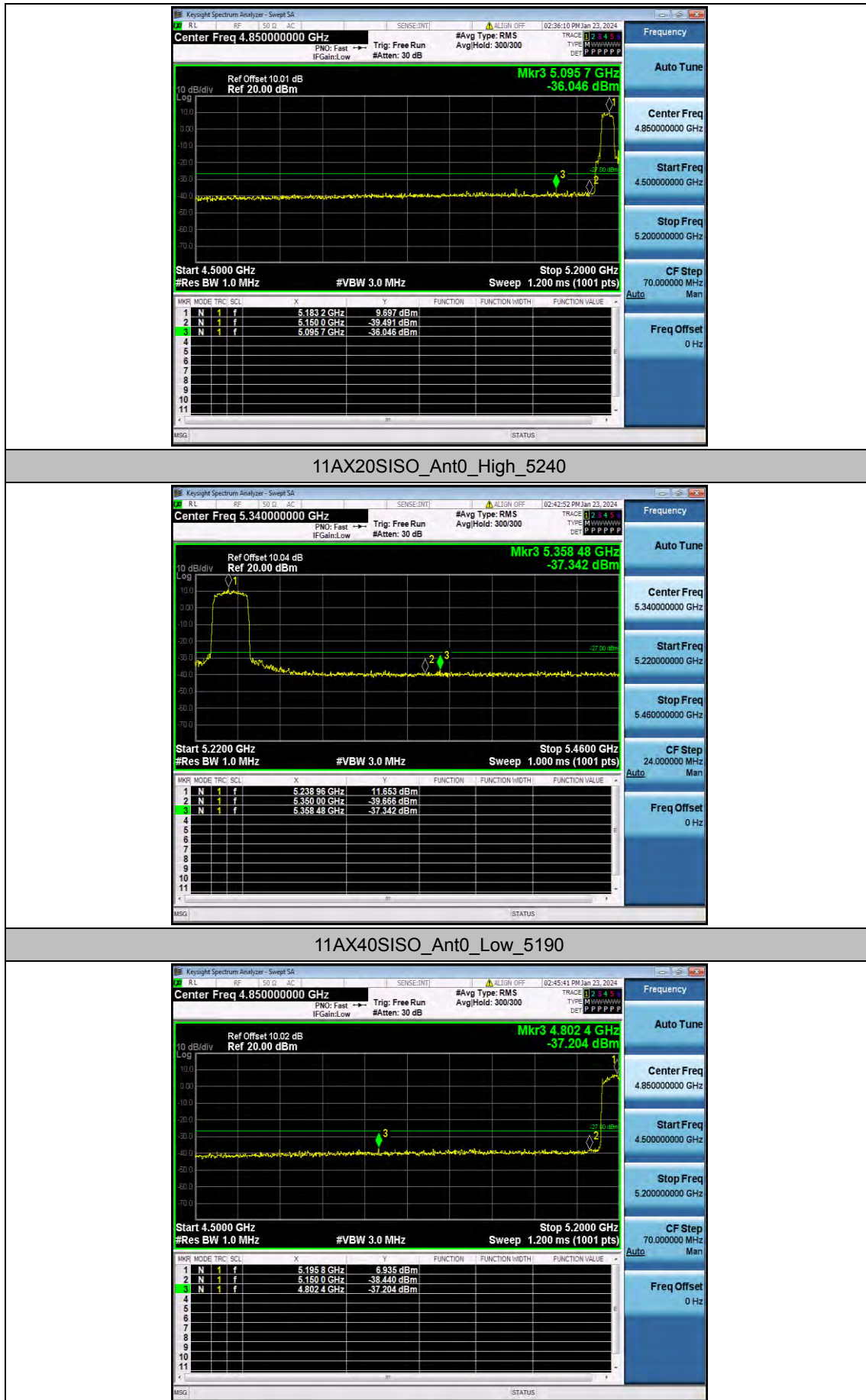
11AC80SISO_Ant0_Low_5210



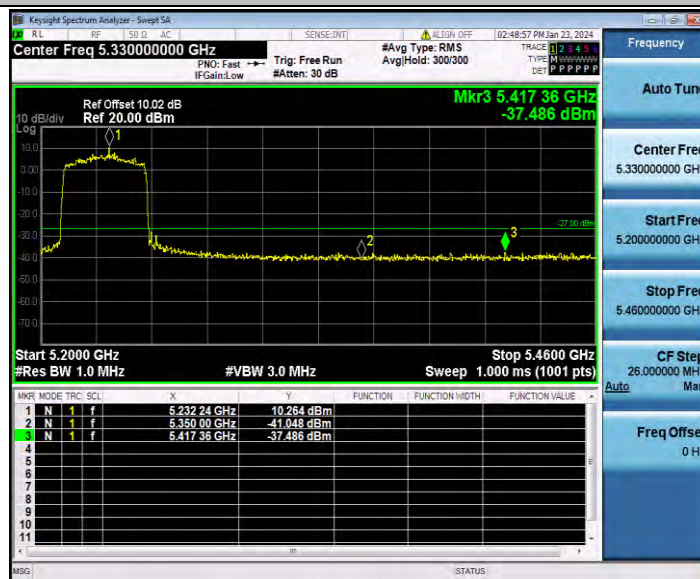
11AC80SISO_Ant0_High_5210



11AX20SISO_Ant0_Low_5180



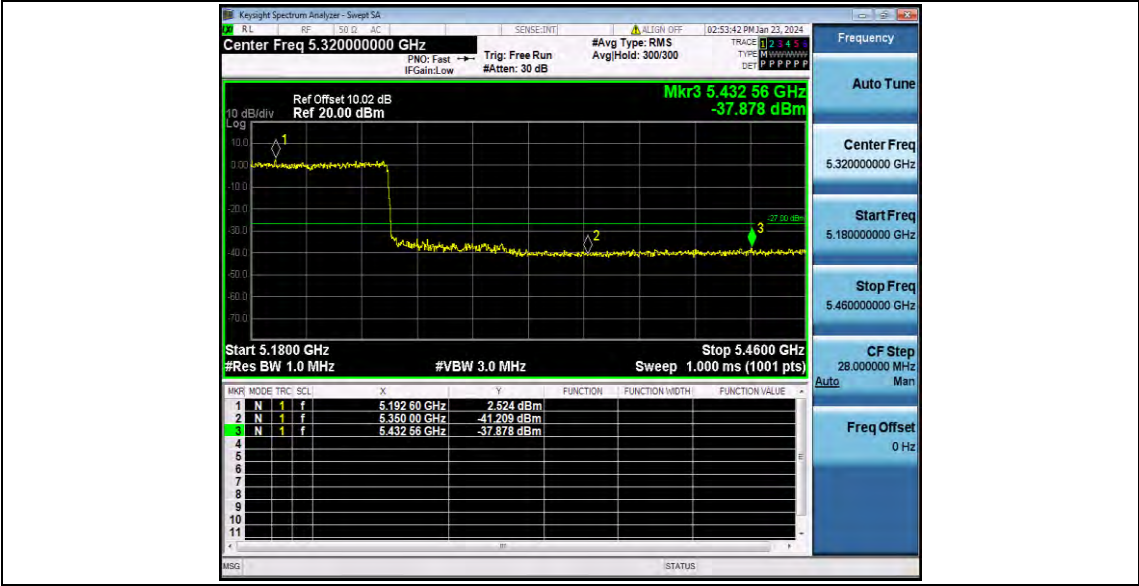
11AX40SISO_Ant0_High_5230



11AX80SISO_Ant0_Low_5210



11AX80SISO_Ant0_High_5210



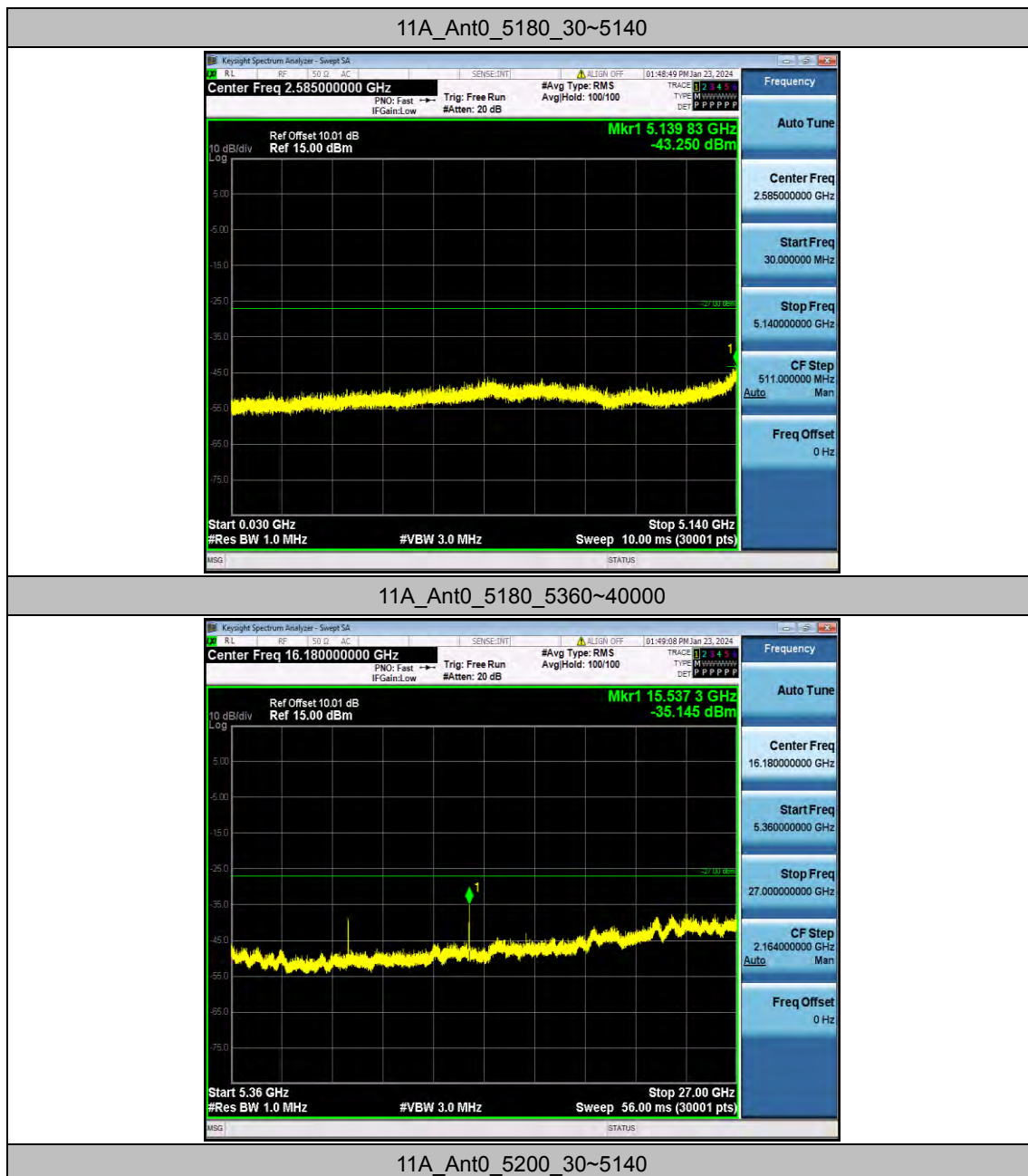
Appendix D.7: Conducted Spurious Emission

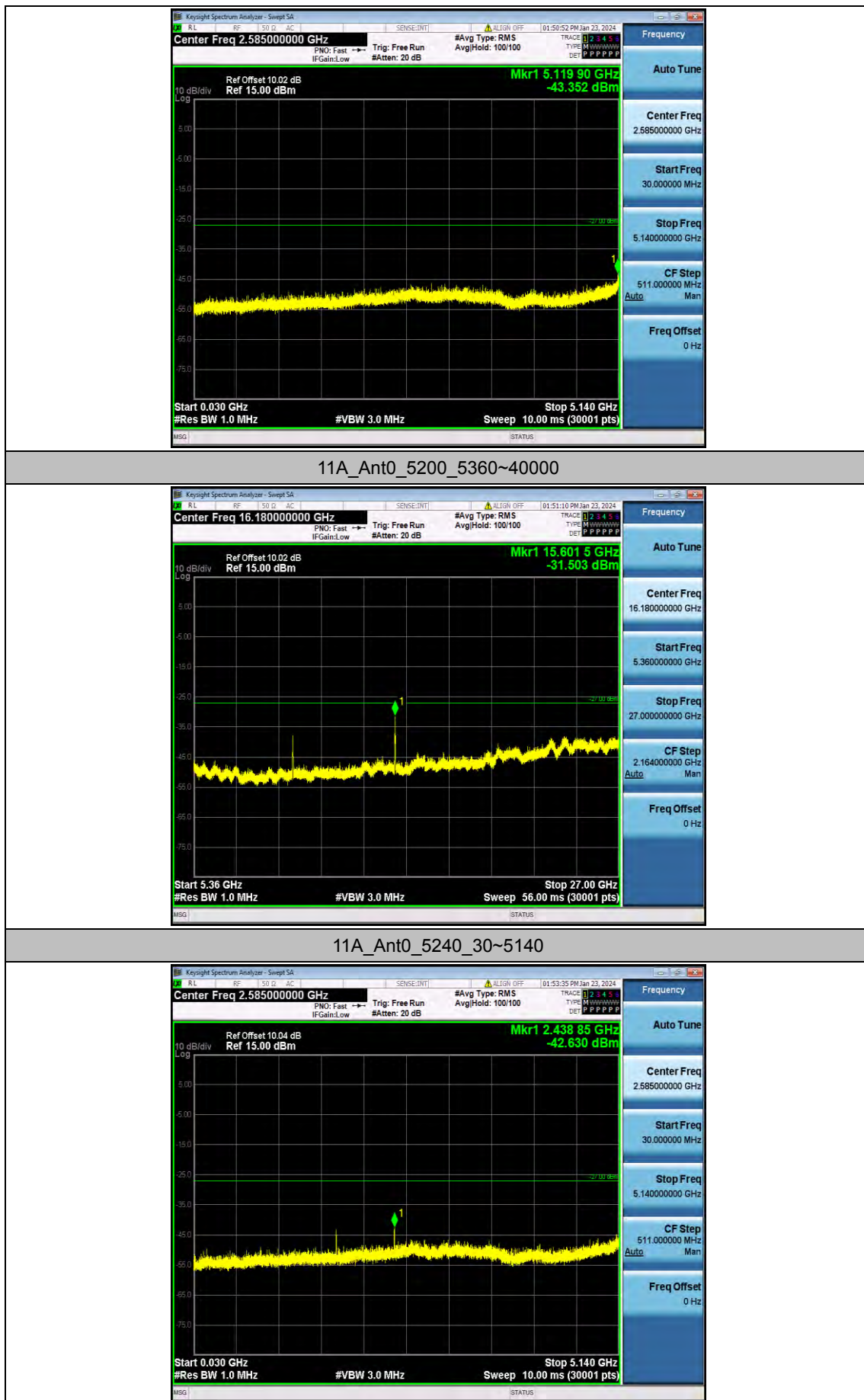
Test Result

TestMode	Antenna	Freq(MHz)	FreqRange [MHz]	Max. Fre [MHz]	Max. Level [dBm]	Limit [dBm]	Verdict
11A	Ant0	5180	30~5140	5139.83	-43.25	≤-27	PASS
			5360~40000	15537.29	-35.15	≤-27	PASS
		5200	30~5140	5119.9	-43.35	≤-27	PASS
			5360~40000	15601.49	-31.5	≤-27	PASS
		5240	30~5140	2438.85	-42.63	≤-27	PASS
			5360~40000	15721.23	-31.61	≤-27	PASS
11N20SISO	Ant0	5180	30~5140	5136.76	-42.17	≤-27	PASS
			5360~40000	15537.29	-36.2	≤-27	PASS
		5200	30~5140	1739.47	-38.33	≤-27	PASS
			5360~40000	15602.21	-32.42	≤-27	PASS
		5240	30~5140	5112.92	-43.85	≤-27	PASS
			5360~40000	10475.7	-33.43	≤-27	PASS
11N40SISO	Ant0	5190	30~5140	5138.81	-43.7	≤-27	PASS
			5360~40000	24272.64	-37.45	≤-27	PASS
		5230	30~5140	2438.68	-45.58	≤-27	PASS
			5360~40000	24344.05	-36.86	≤-27	PASS
11AC20SISO	Ant0	5180	30~5140	5111.38	-42.46	≤-27	PASS
			5360~40000	15545.23	-36.47	≤-27	PASS
		5200	30~5140	1744.58	-38.13	≤-27	PASS
			5360~40000	15602.93	-35.71	≤-27	PASS
		5240	30~5140	5122.46	-45.11	≤-27	PASS
			5360~40000	15718.35	-34.14	≤-27	PASS
11AC40SISO	Ant0	5190	30~5140	1762.29	-40.98	≤-27	PASS
			5360~40000	26071.64	-37.16	≤-27	PASS
		5230	30~5140	5096.74	-44.99	≤-27	PASS
			5360~40000	26721.57	-37.42	≤-27	PASS
11AC80SISO	Ant0	5210	30~5140	5115.13	-42.53	≤-27	PASS
			5360~40000	24177.42	-36.89	≤-27	PASS
11AX20SISO	Ant0	5180	30~5140	827.33	-39.84	≤-27	PASS
			5360~40000	24184.64	-37.59	≤-27	PASS
		5200	30~5140	5129.95	-43.73	≤-27	PASS
			5360~40000	24256.77	-37.41	≤-27	PASS
		5240	30~5140	5139.32	-44.35	≤-27	PASS
			5360~40000	15724.84	-34.76	≤-27	PASS
11AX40SISO	Ant0	5190	30~5140	5133.19	-44.24	≤-27	PASS
			5360~40000	26503	-36.89	≤-27	PASS

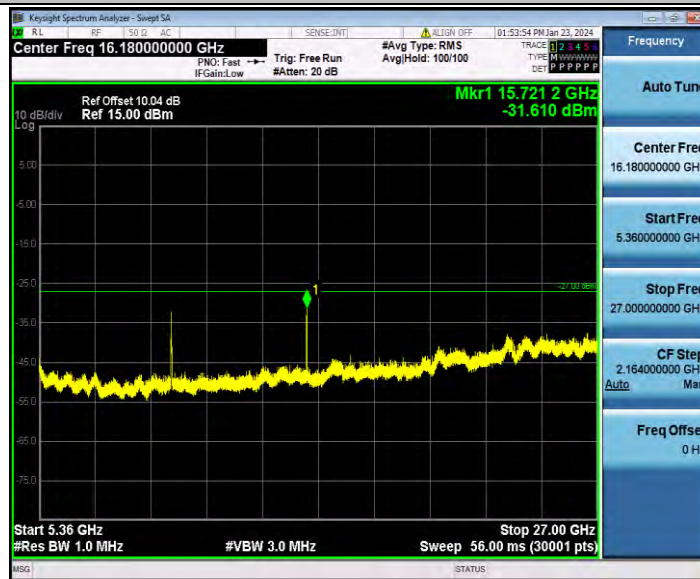
		5230	30~5140	826.65	-41.6	≤ -27	PASS
			5360~40000	26892.52	-37.61	≤ -27	PASS
11AX80SISO	Ant0	5210	30~5140	5121.09	-38.65	≤ -27	PASS
			5360~40000	26888.19	-37.5	≤ -27	PASS

Test Graphs

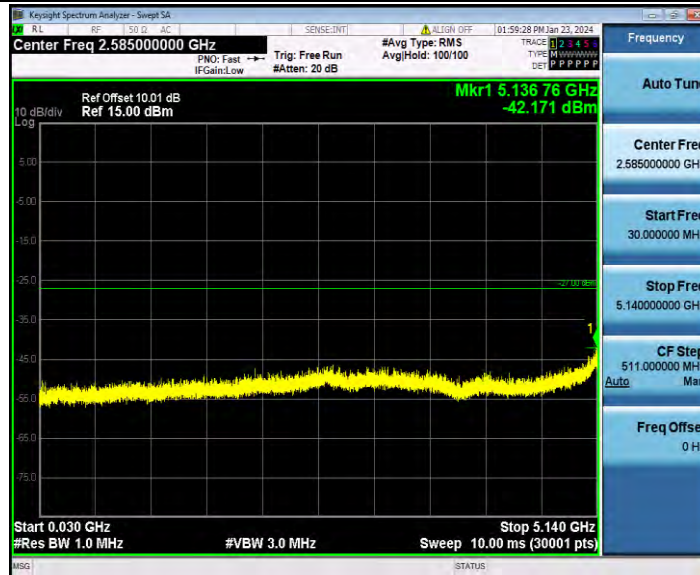




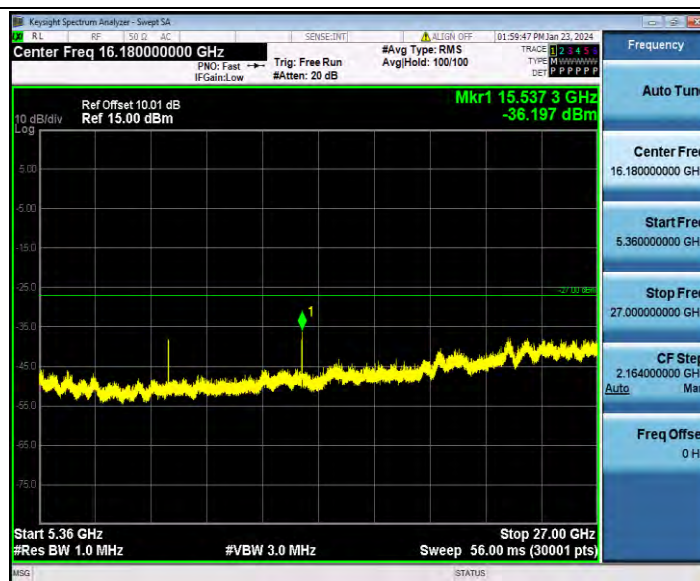
11A_Ant0_5240_5360~40000



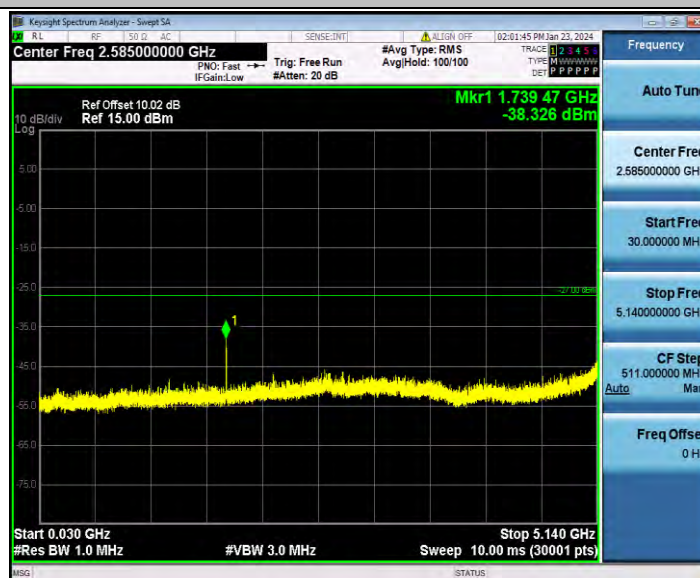
11N20SISO_Ant0_5180_30~5140



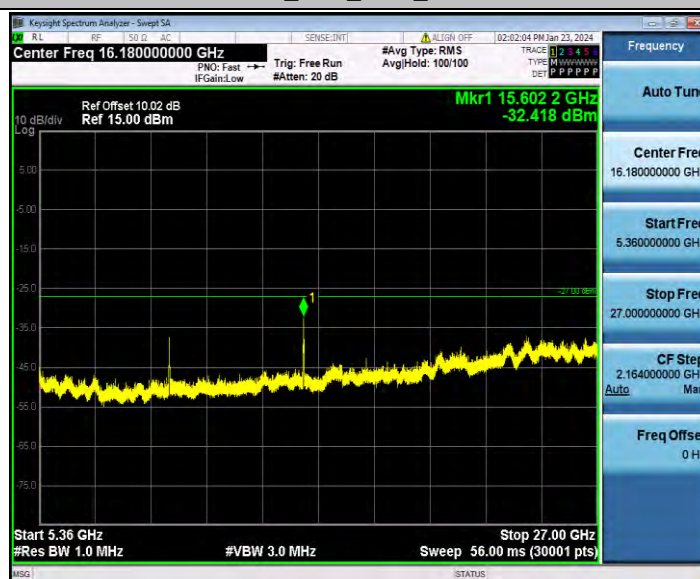
11N20SISO_Ant0_5180_5360~40000



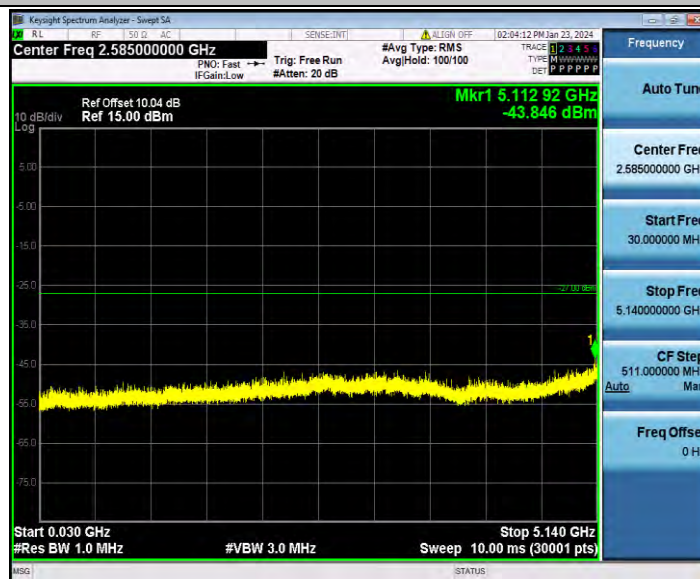
11N20SISO_Ant0_5200_30~5140



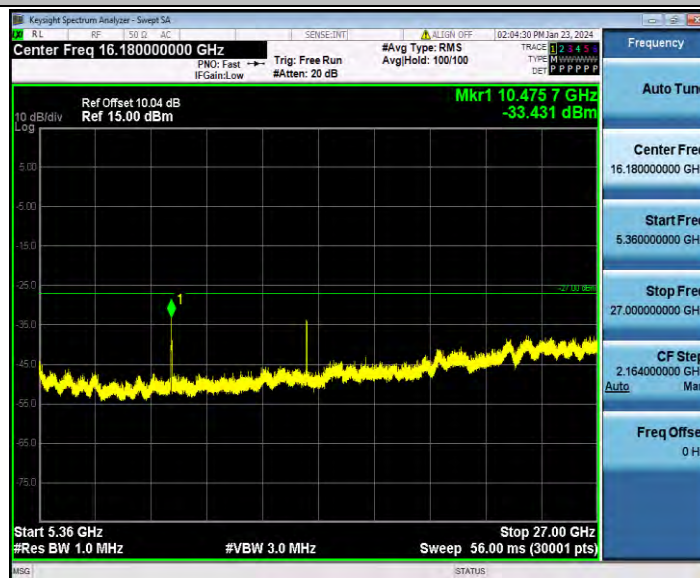
11N20SISO_Ant0_5200_5360~40000



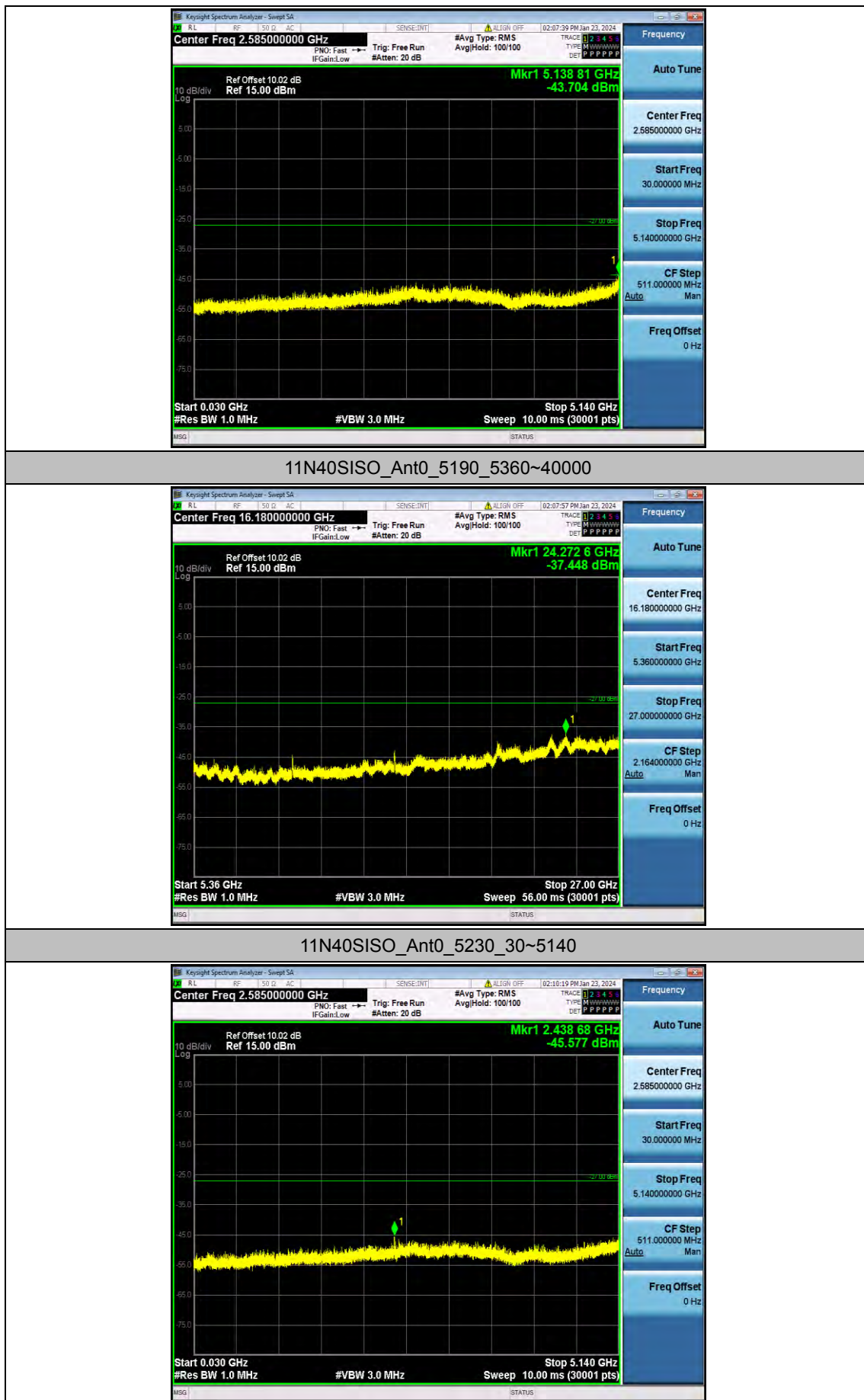
11N20SISO_Ant0_5240_30~5140



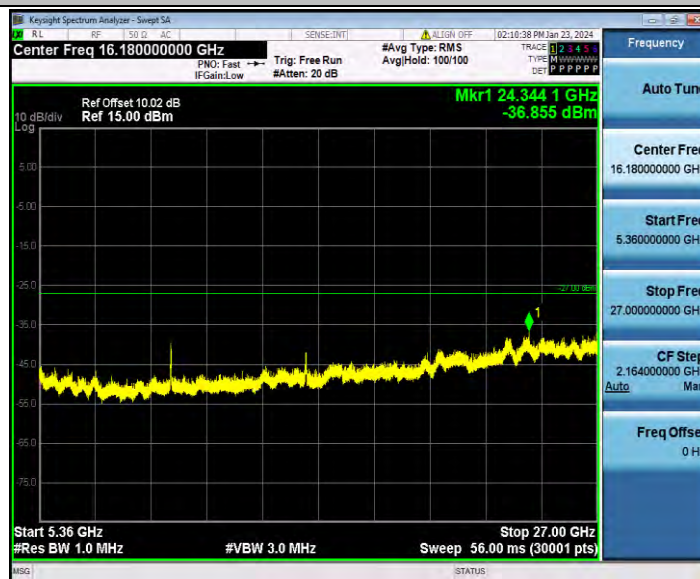
11N20SISO_Ant0_5240_5360~40000



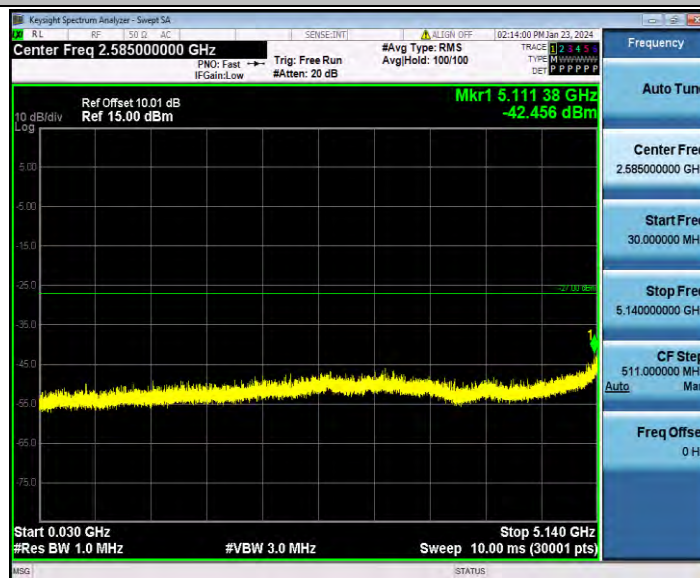
11N40SISO_Ant0_5190_30~5140



11N40SISO_Ant0_5230_5360~40000



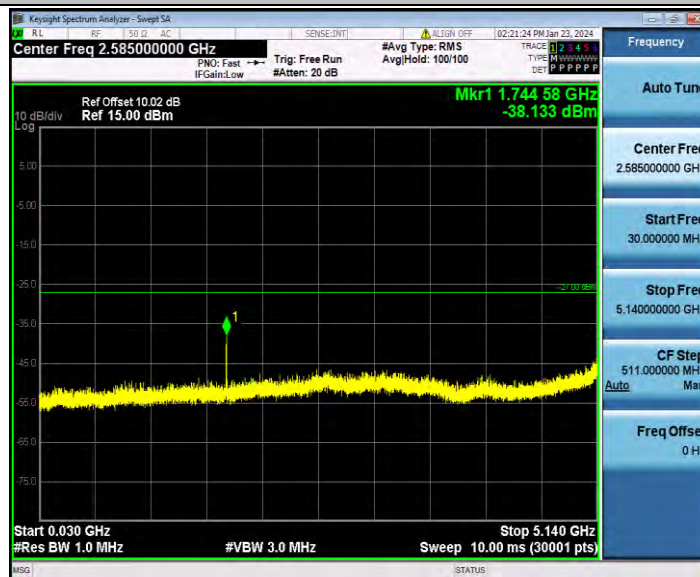
11AC20SISO_Ant0_5180_30~5140



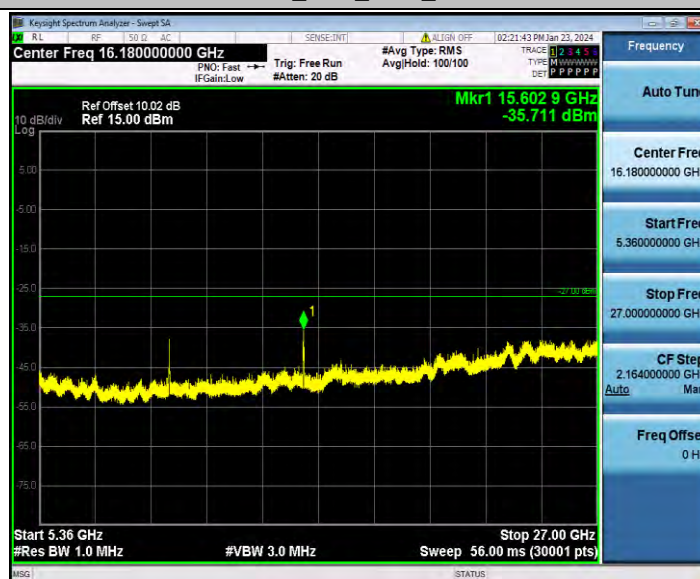
11AC20SISO_Ant0_5180_5360~40000



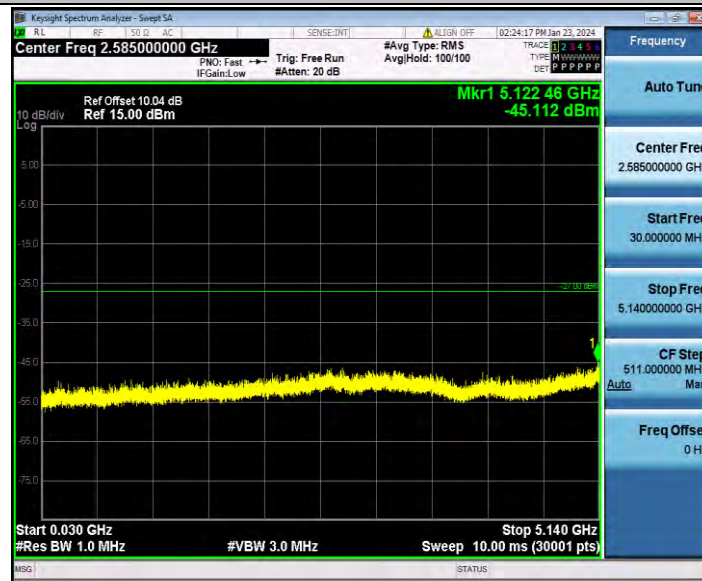
11AC20SISO_Ant0_5200_30~5140



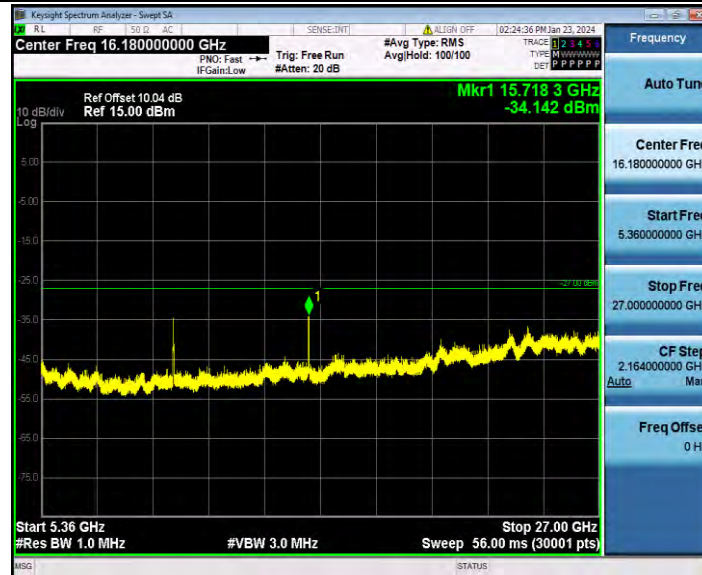
11AC20SISO_Ant0_5200_5360~40000



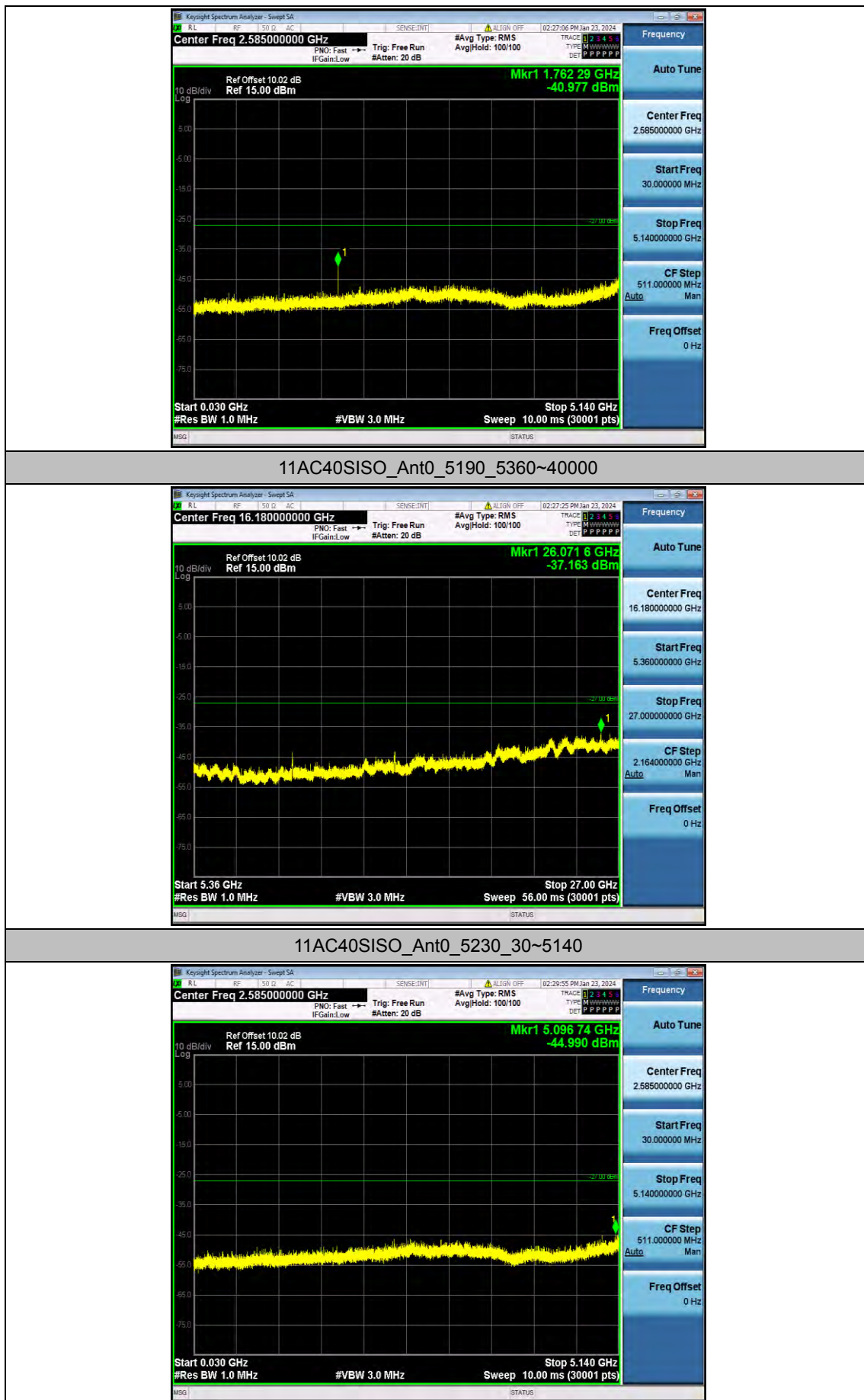
11AC20SISO_Ant0_5240_30~5140



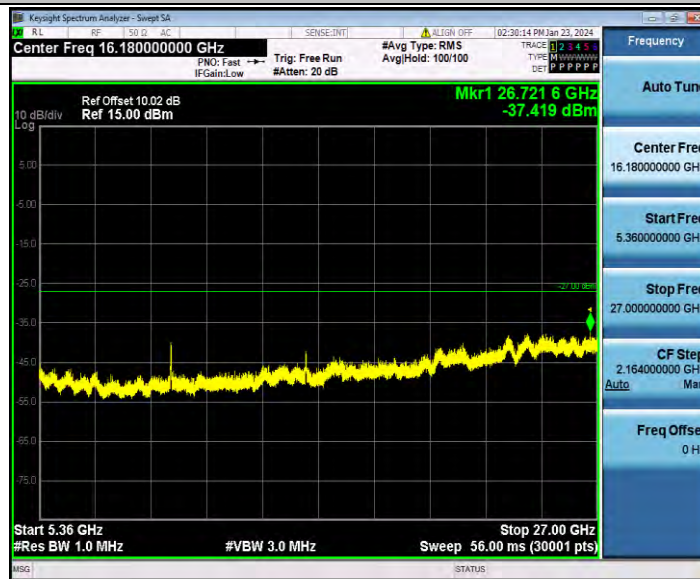
11AC20SISO_Ant0_5240_5360~40000



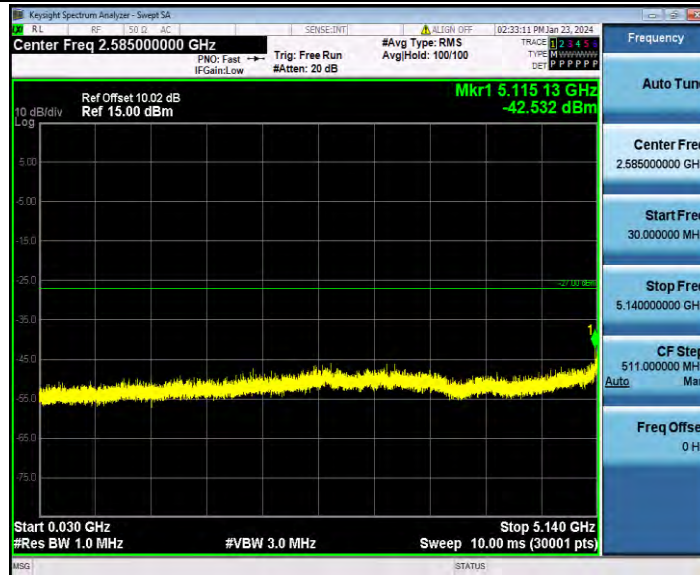
11AC40SISO_Ant0_5190_30~5140



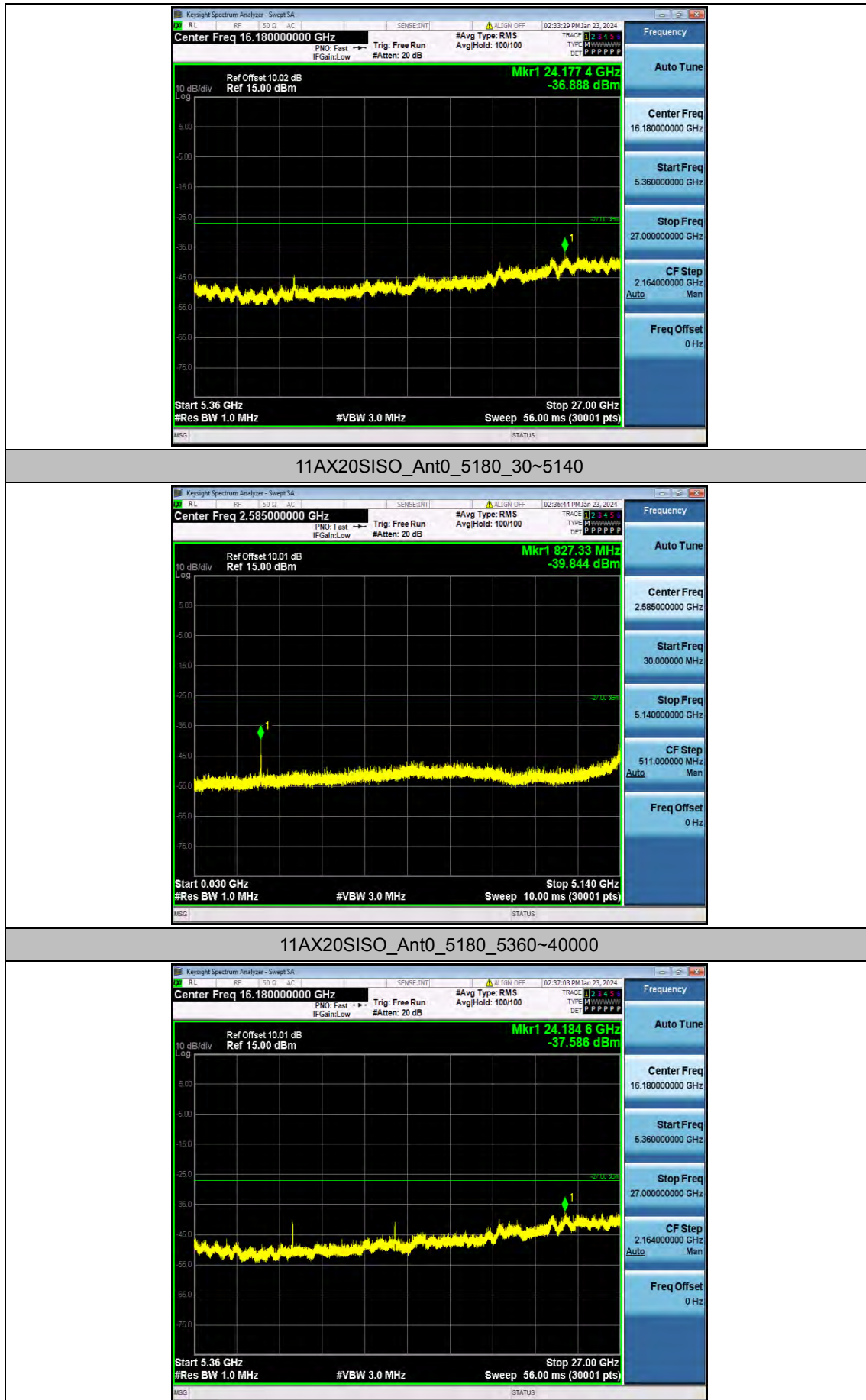
11AC40SISO_Ant0_5230_5360~40000



11AC80SISO_Ant0_5210_30~5140



11AC80SISO_Ant0_5210_5360~40000



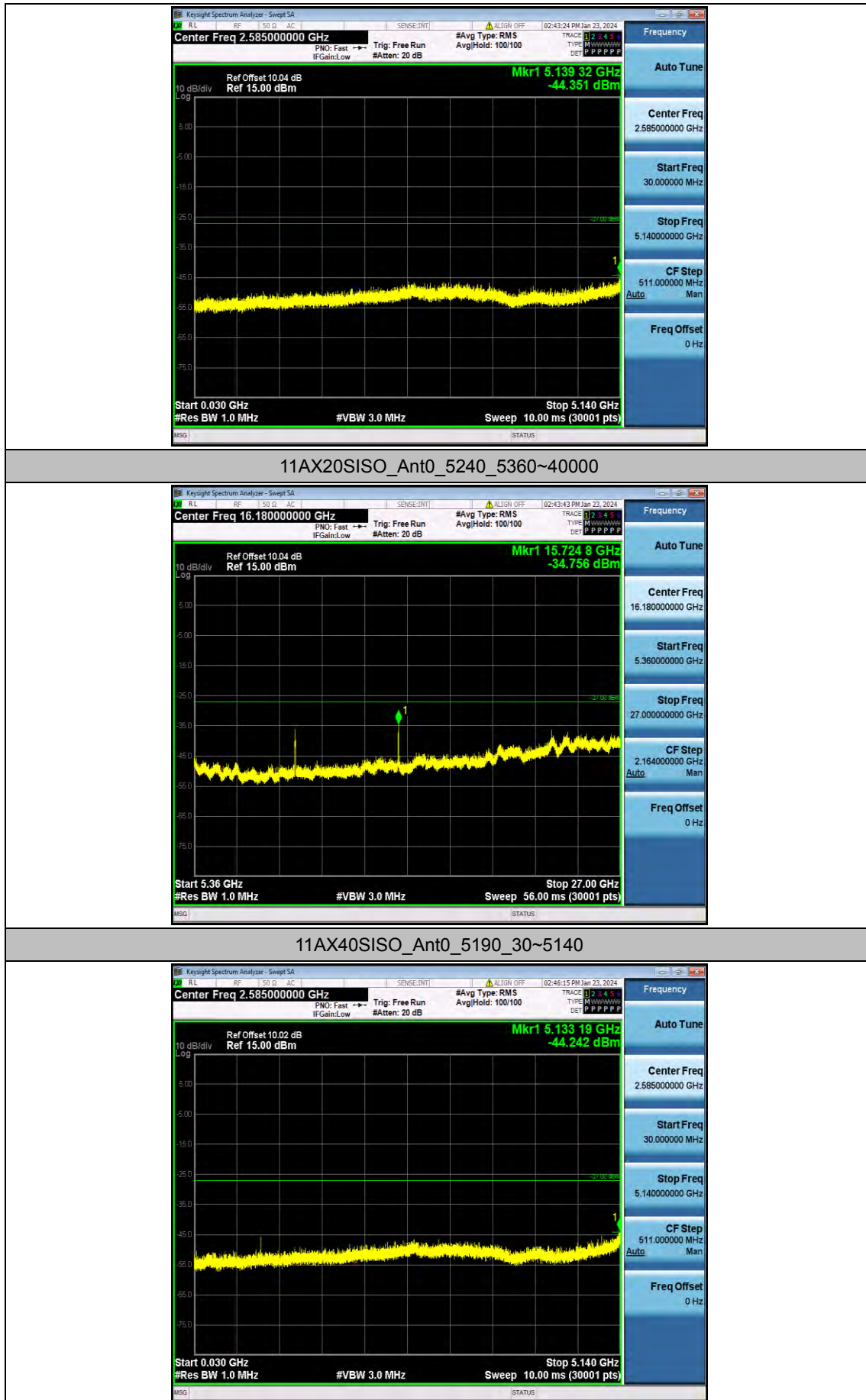
11AX20SISO_Ant0_5200_30~5140



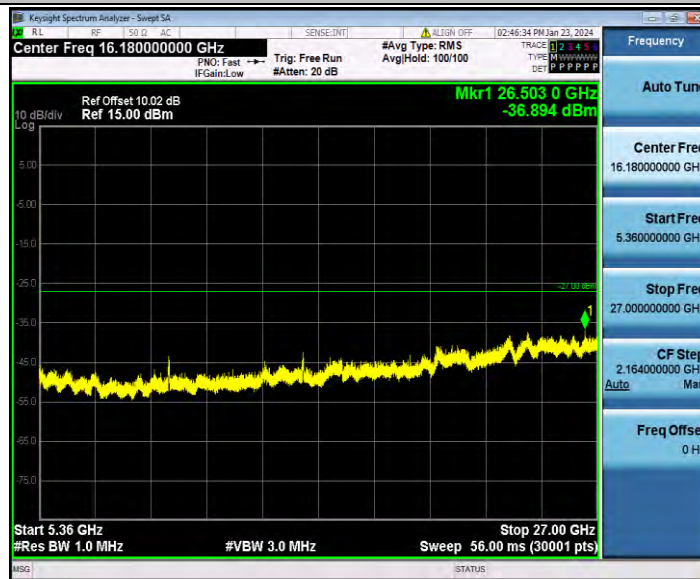
11AX20SISO_Ant0_5200_5360~40000



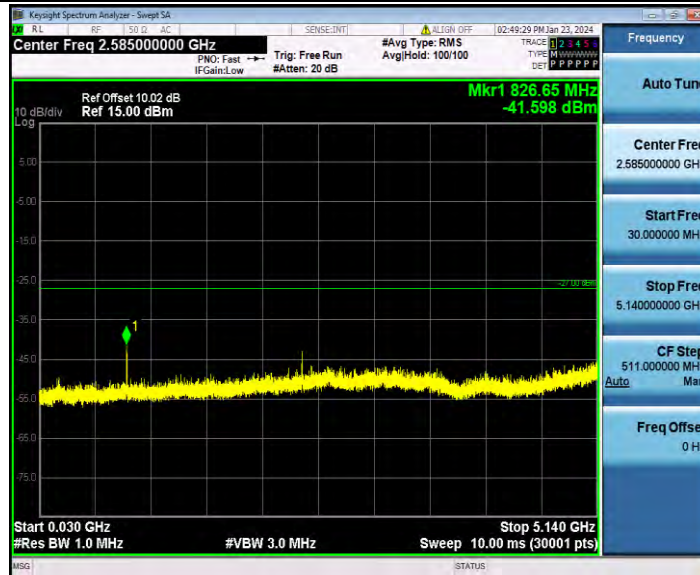
11AX20SISO_Ant0_5240_30~5140



11AX40SISO_Ant0_5190_5360~40000



11AX40SISO_Ant0_5230_30~5140



11AX40SISO_Ant0_5230_5360~40000

