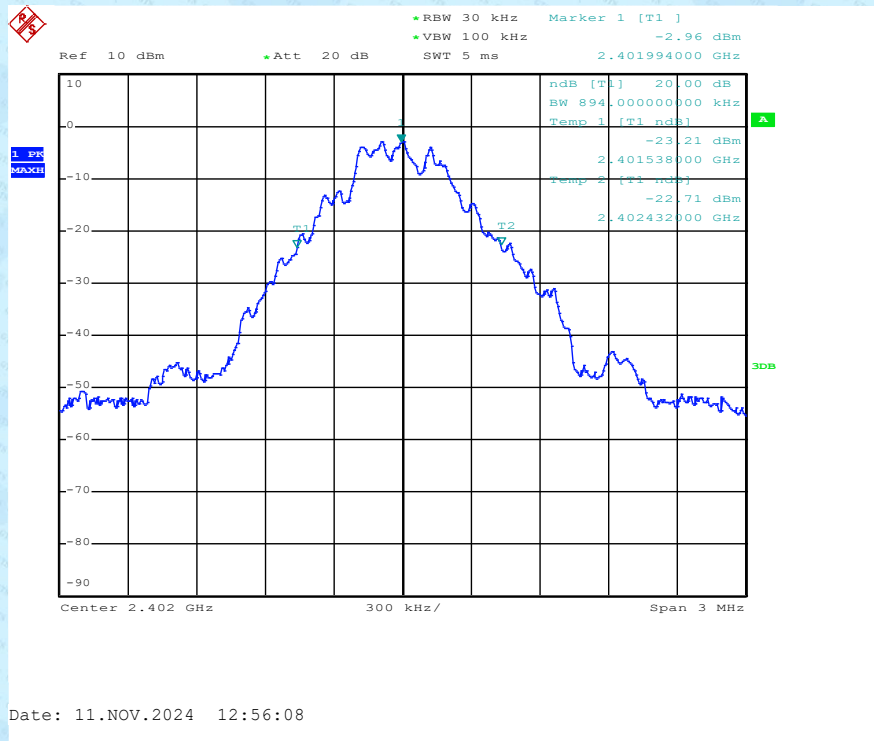


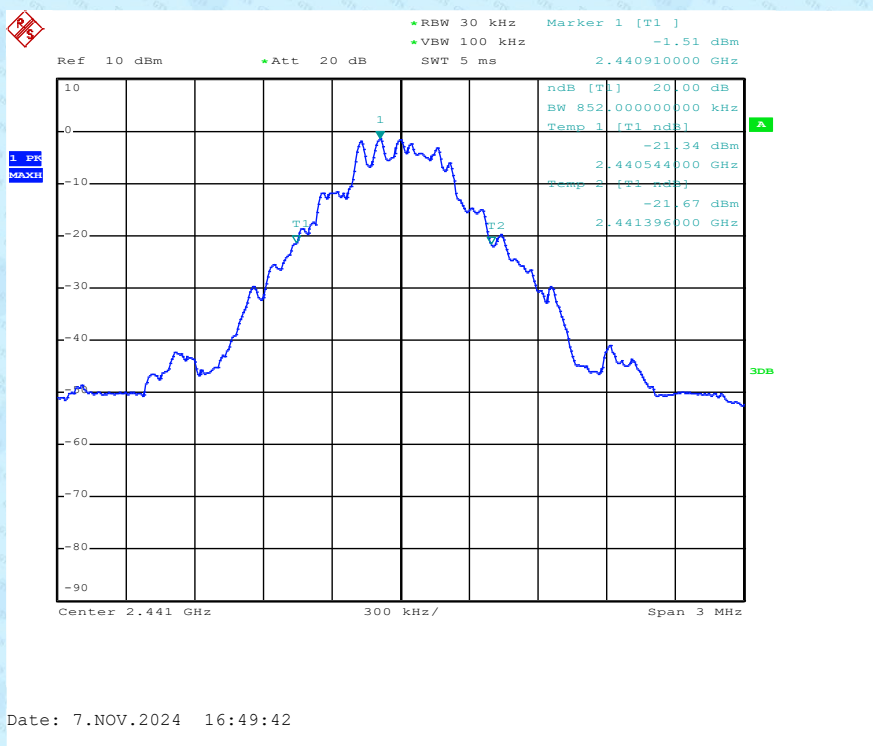
Test plot as follows:

GFSK

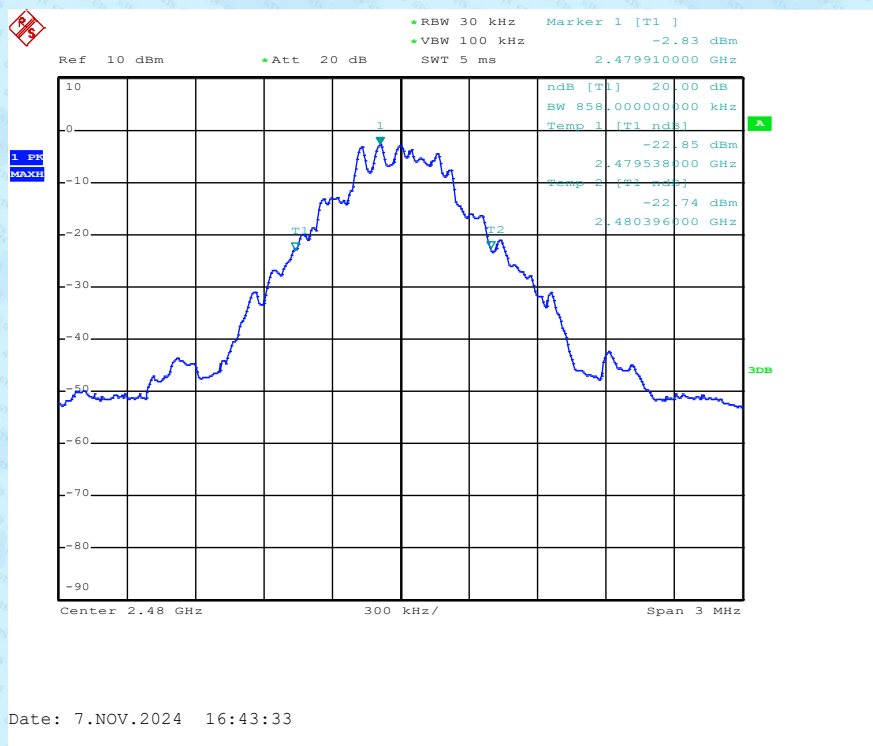
Lowest channel



Middle channel



Highest channel

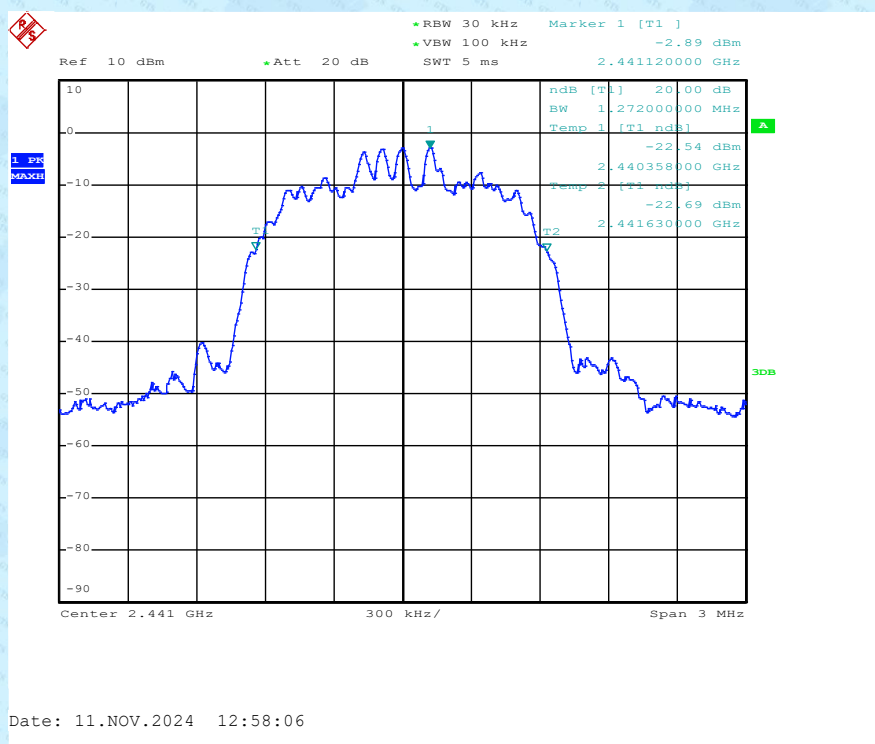


Lowest channel

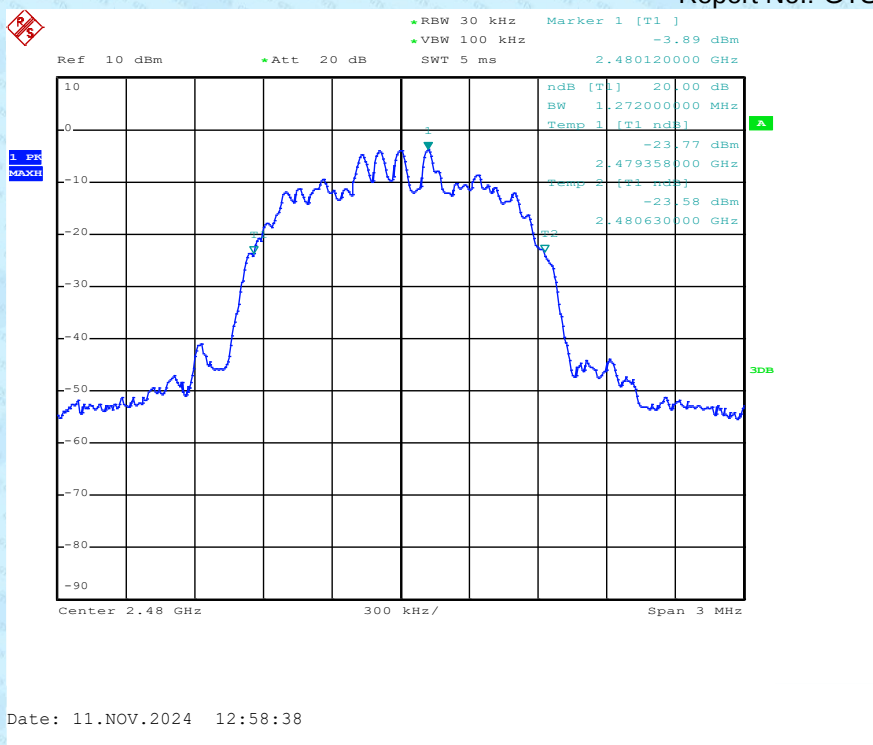
11/4DQPSK



Middle channel

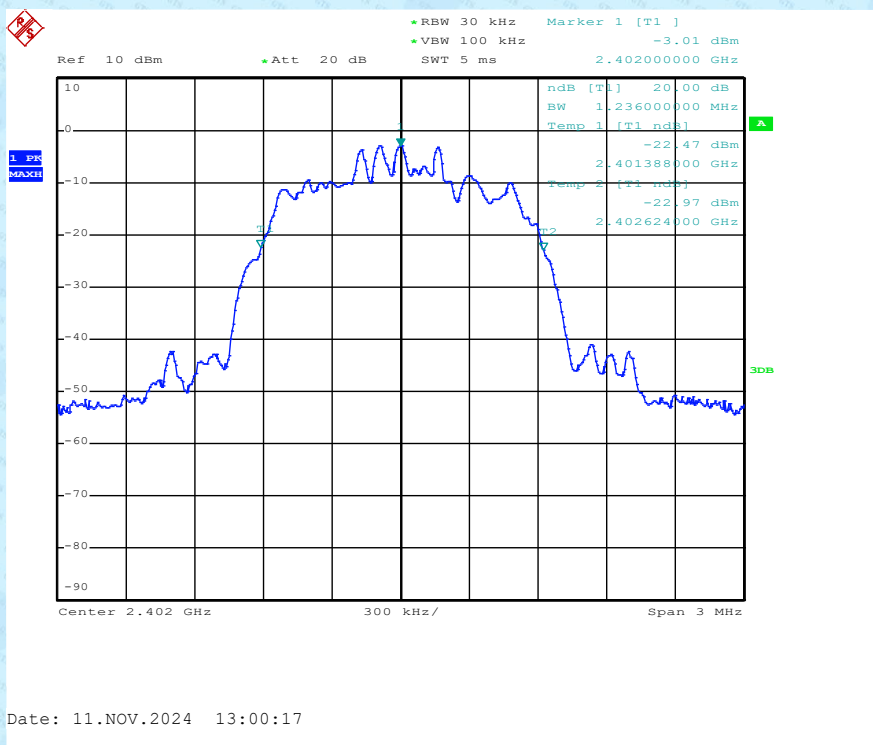


Highest channel

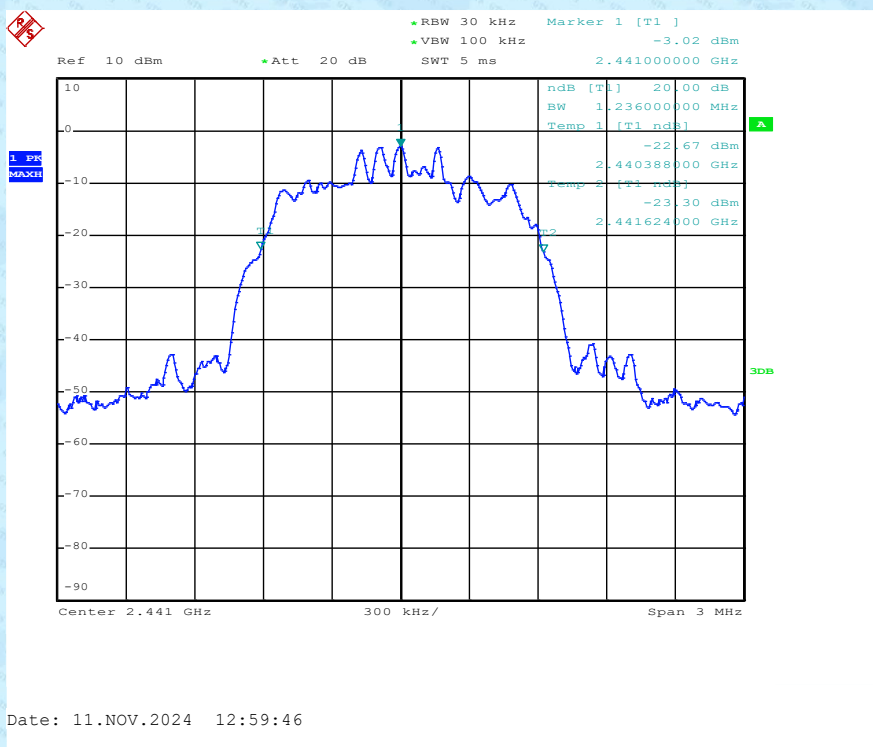


8DPSK

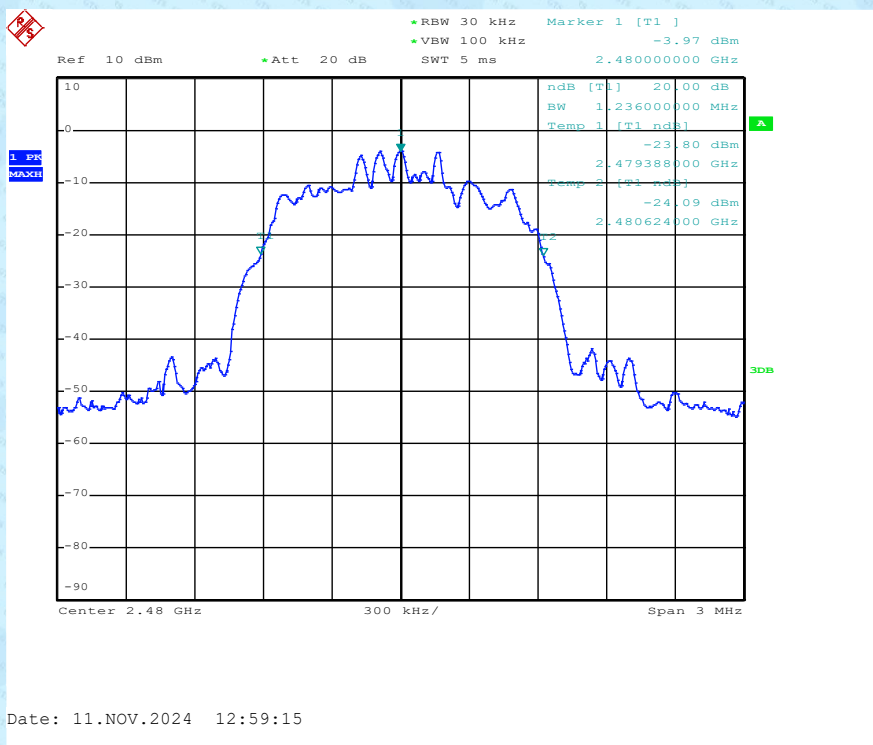
Lowest channel



Middle channel



Highest channel



8 Test Setup Photo

Reference to the **appendix I** for details.

9 EUT Constructional Details

Reference to the **appendix II** for details.

-----End-----