MEASUREMENT AND TECHNICAL REPORT

DIRECTED ELECTRONICS INCORPORATED
1 Viper Way
Vista, CA 92083

DATE: 16 October 2006

This Report Concerns:	Original Grant: X	Class II Change:
Equipment Type:	7541VPX HHU	
Deferred grant requested per 47 CFR 0.457(d)(1)(ii)?	Yes: **Defer until:**	No: X
Company Name **agrees to notify the Commission by:** **of the intended date of announcement of the product so that the grant can be issued on that date.**	N/A	
Transition Rules Request per 15.37?	Yes:	No: X*

(*) FCC Part 15, Paragraph(s) **15.231(a), 15.231(b), 15.231(c)**

Report Prepared by:

TÜV AMERICA, INC
10040 Mesa Rim Road
San Diego, CA 92121-2912
Phone: 858 678 1400
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TABLE OF CONTENTS

TÜV AMERICA INC 10040 Mesa Rim Road San Diego, CA 92121-2912 Phone 858 678 1400 FAX 858 546 0364

1.0 GENERAL INFORMATION

1.1 Product Description

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1.2 Related Submittal Grant

None

1.3 Tested System Details

The FCC ID's for all equipment, plus descriptions of all cables used in the tested system are:

None

1.4 Test Methodology

Purpose of Test: To demonstrate compliance with the following tests.

Test Description	Paragraph Number	Pass/Fail
Deactivation	15.231(a)	Pass
Field Strength of Fundamental	15.231(b)	Pass
Emissions Bandwidth	15.231(c)	Pass
Field Strength of Emissions	15.231(e)	N/A

Testing was performed according to the procedures in FCC/ANSI C63.4 and CSA 108.8-M1983.

1.5 Test Facility

The open area test site and conducted measurement data were tested by:

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The Test Site Data and performance comply with ANSI C63.4 and are registered with the FCC, 7435 Oakland Mills Road, Columbia Maryland 21046. All Measurement Data is acquired according to the content of FCC Measurement Procedure and ANSI C63.4, unless supplemented with additional requirements as noted in the test report.

2.0 SYSTEM TEST CONFIGURATION

2.1 Justification

The EUT was initially tested for FCC emissions in the following configuration:

See Test Setup Photos Exhibit

2.2 EUT Exercise Software

None

2.3 Special Accessories

None

2.4 Equipment Modifications

None

2.5 Configuration of Test System

See Test Setup Photos Exhibit

3.0 DEACTIVATION EQUIPMENT/DATA
FIELD STRENGTH OF FUNDAMENTAL EQUIPMENT/DATA
EMISSION BANDWIDTH EQUIPMENT/DATA
FIELD STRENGTH OF EMISSIONS EQUIPMENT/DATA

Test Conditions: DEACTIVATION: FCC Part 15.231(a)
FIELD STRENGTH OF FUNDAMENTAL: FCC Part 15.231(b)
EMISSION BANDWIDTH: FCC Part 15.231(c)
FIELD STRENGTH OF EMISSIONS: FCC Part 15.231(e)

The following measurements were performed at the San Diego Testing Facility:

☐ **- Test not applicable**

■ - Roof (Small Open Area Test Site)

Test Equipment Used:

Model No.	Prop. No.	Description	Manufacturer	Serial No.	Date Cal'ed
3146	6641	Log Periodic Antenna	EMCO	106X	07/06
3115	6669	Double Ridge Antenna	EMCO	9412-4364	08/06
AMF-5D-010180-35-10P	6786	Preamplifier	Miteq	549460	Verified
FF6549-1	777	High Pass Filter	Sage	004	Verified
FF6549-1	777	High Pass Filter	Sage	004	Verified
AA-19030.00.0	7492	30' Coaxial Cable	United Microwave	--	N/A
E4440A	7500	Spectrum Analyzer	Hewlett Packard	MY43362168	01/06

Remarks: One year calibration cycle for all test equipment and sites.

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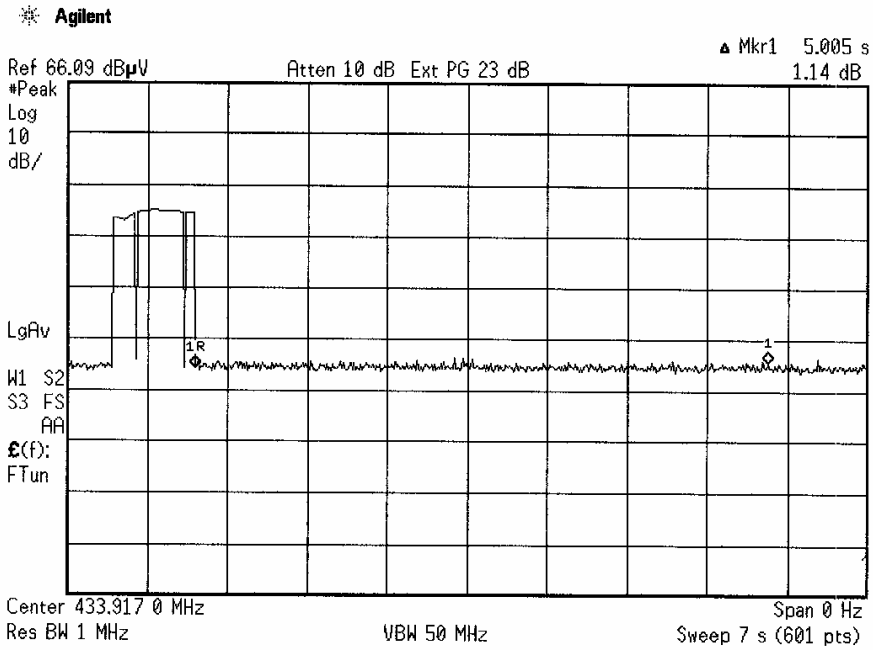
FCC Part 15.231(a) – Deactivation



FCC Part 15.231(b) - Field Strength of Fundamental

REPORT No: SC605588	TESTER: Jim Owen	SPEC:	FCC Part 15 para 15.231(b)
CUSTOMER: Directed Electronics		TEST DIST:	3 Meters
E U T: 7541 HHU		TEST SITE:	Roof
EUT MODE: Transmit		BICONICAL:	N/A
DATE: September 27, 2006		LOG:	243
NOTES: Duty Cycle= 55%		OTHER:	453

above 1GHz: RBW & VBW 1 MHz for Pk; AVG = PK - 20LOG(Duty Cycle)
below 1GHz: RBW & VBW 100 kHz for Pk; AVG = PK - 20LOG(Duty Cycle)
CF = Antenna Factor + Cable Loss - Preamplifier Gain + Preselector Loss

v.beta231

FREQ (MHz)	VERT. (dBuv) pk	VERT. (dBuv) DCav	HORIZ (dBuv) pk	HORIZ (dBuv) DCav	CF (dB/m)	MAX LEVEL (dBuV/m) pk	MAX LEVEL (dBuV/m) av	SPEC LIMIT (dBuV/m) pk	SPEC LIMIT (dBuV/m) av	MARGIN (dB) pk	MARGIN (dB) av	EUT Rotation	Antenna Height	Notes
433.910	58.9	53.7	66.5	61.3	16.9	**83.4**	**78.2**	100.8	80.8	-17.5	-2.6	257	2.4	
867.820	20.0	14.8	20.0	14.8	23.5	**43.5**	**38.3**	80.8	60.8	-37.3	-22.5			ambient
1301.730	45.0	39.8	41.0	35.8	-12.4	**32.6**	**27.4**	80.8	60.8	-48.2	-33.4	133	1.2	
1735.640	45.6	40.4	42.3	37.1	-9.0	**36.6**	**31.4**	80.8	60.8	-44.2	-29.4	81	1	
2169.550	49.2	44.0	48.8	43.6	-6.7	**42.5**	**37.3**	80.8	60.8	-38.3	-23.5			
2603.460	42.8	37.6	41.3	36.1	-4.9	**37.9**	**32.7**	80.8	60.8	-42.9	-28.1			ambient
3037.370	41.8	36.6	42.6	37.4	-2.3	**40.3**	**35.1**	80.8	60.8	-40.5	-25.7			ambient
3471.280	40.1	34.9	40.6	35.4	-0.6	**40.0**	**34.8**	80.8	60.8	-40.8	-26.0			ambient
3905.190	38.4	33.2	39.3	34.1	0.1	**39.4**	**34.2**	80.8	60.8	-41.5	-26.7			ambient
4339.100	39.0	33.8	38.5	33.3	-1.0	**38.0**	**32.8**	80.8	60.8	-42.8	-28.0			ambient

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Pulse Duty Cycle Correction Factor

FCC 15.35(c) and ANSI C63.4:2003 Clause 13.1.4.2.

Calculation:

Average Reading = Peak Reading (dBuV/m) + 20 * log(duty cycle)

Where duty cycle correction is allowed, the following methods are employed to determine the correction factor:

1) Turn on the transmitter and set it to transmit the pulse train continuously.
2) Tune the spectrum analyzer (Agilent E4440A) to the transmitter frequency and set the resolution bandwidth wide enough to encompass all significant components of the signal of interest. Video bandwidth is set to the widest bandwidth available.
3) Set the spectrum analyzer SPAN to zero. Set the SWEEP to 100 ms. This will be used to demodulate and detect the pulse train.
4) Set the TRIGger to Video. Spin the data control wheel to move the green trigger threshold line to the middle of the pulse amplitude.
5) Set the TRIGGER DELAY (page 2 of the TRIG menu) to center the pulse in the display.
6) If able, adjust the transmitter controls, jumper wires, or software to maximize the transmitted duty cycle.
7) Measure the pulse width by determining the time difference between the rise and fall of the pulse. Use Marker Delta.
8) When the pulse train is less than 100 ms, including blanking intervals, calculate the duty cycle by averaging the sum of the pulse widths over one complete pulse train. When the pulse train exceeds 100 ms, calculate the duty cycle by averaging the sum of the pulse widths over the 100 ms width with the highest average value.
9) When the pulse train consists of long and short pulses measure samples of each with sweep times sufficiently small enough to allow measurement. Count the number of long and short pulses in one period or 100 ms. Multiply the number of long pulses times the long pulse width and the number of short pulses times the short time width. Sum the products.
10) The duty cycle is the value of the sum of the pulse widths in one period or 100 ms, divided by the length of the period or 100 ms. This should result in a decimal fraction between 0.10 and 0.99. The result is the duty cycle.
11) Multiply the logarithm (base 10) of the duty cycle by 20 to create the duty cycle factor. The duty cycle factor is then added to the peak detector reading and then compared to the average detector limit.

A)	Period (ms) =	90	(100 ms Maximum)
B)	Long Pulse (ms) =	.7667	
C)	Nr. Of Long Pulses	30.1 (estimated)	
D)	Short Pulse (ms) =	.383	
E)	Nr. Of Short Pulses	38.7 (estimated)	
F)	Duty Cycle =	.44 = 7.1dB*	(Maximum Allowance is 20 dB)

Comments : *Client opted to use 5.2 dB Duty Cycle Correction (55%) and is applied to Margin on data record

$$\text{Duty Cycle (F)} = 20 \times \log \left(\frac{\text{Nr. of Long Pulses} \times \text{Long Pulse} + \text{Nr. of Short Pulses} \times \text{Short Pulse}}{\text{Period}} \right)$$

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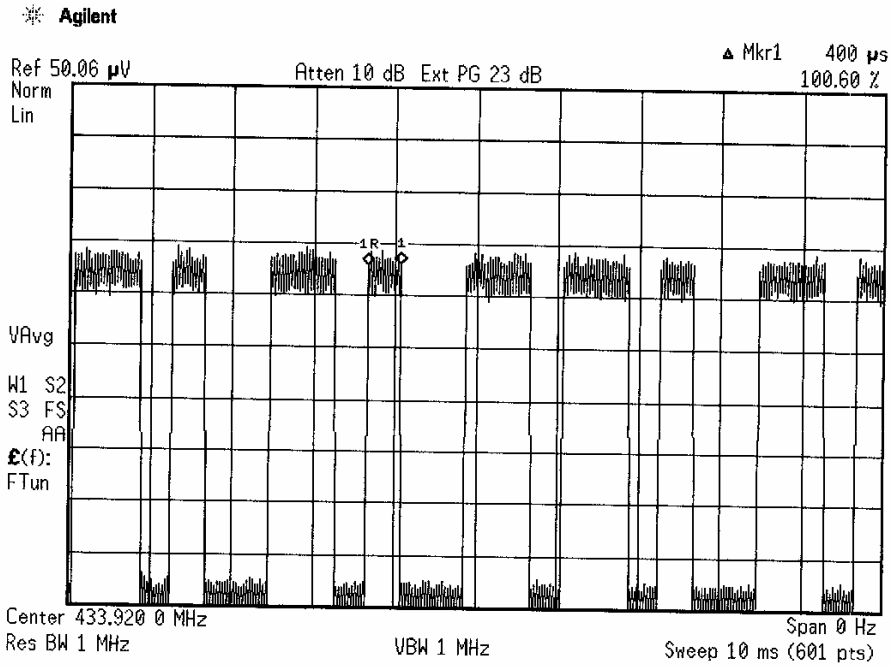
FCC Part 15.231(b) - Duty Cycle



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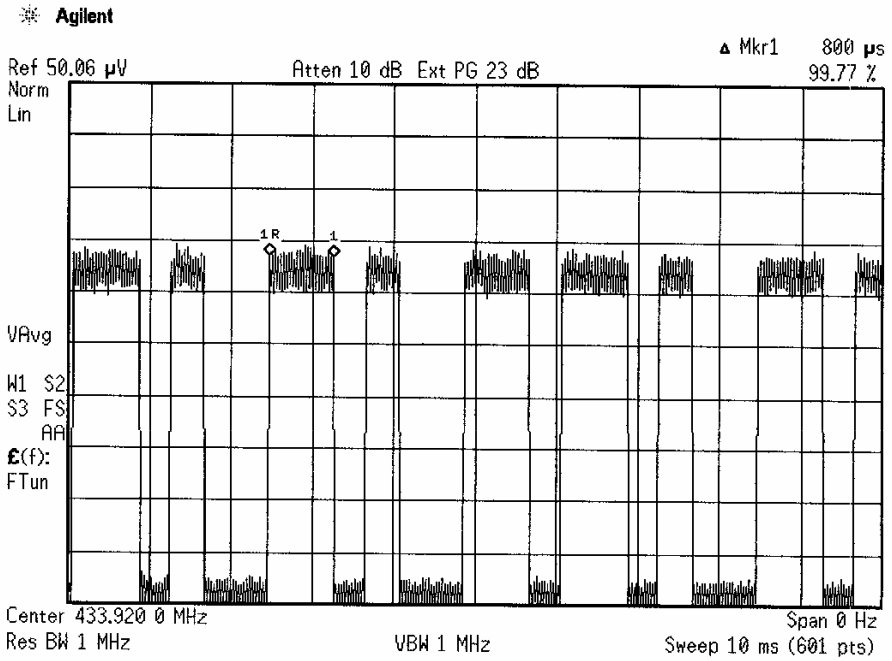

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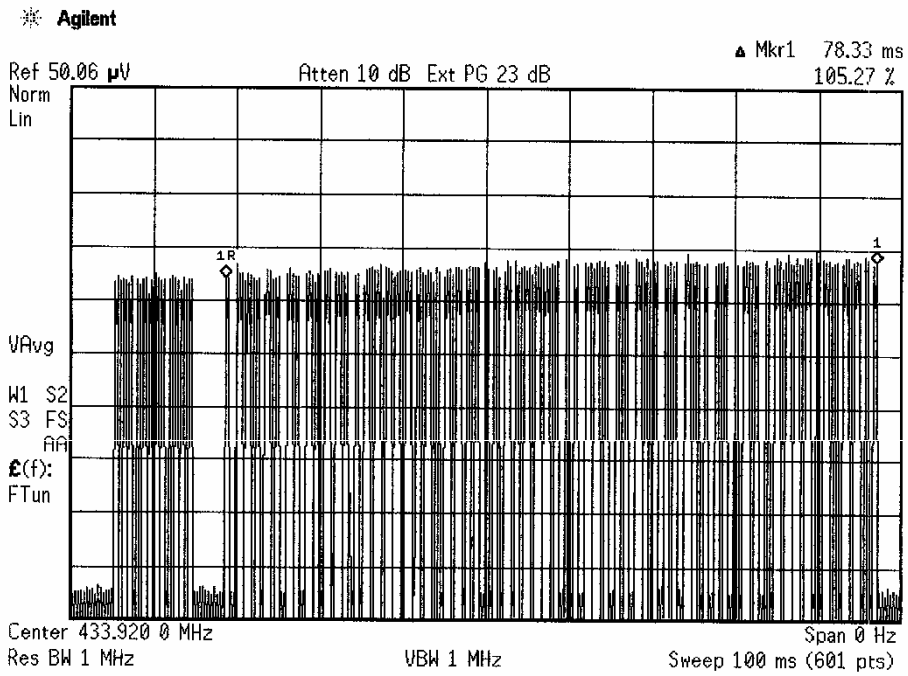

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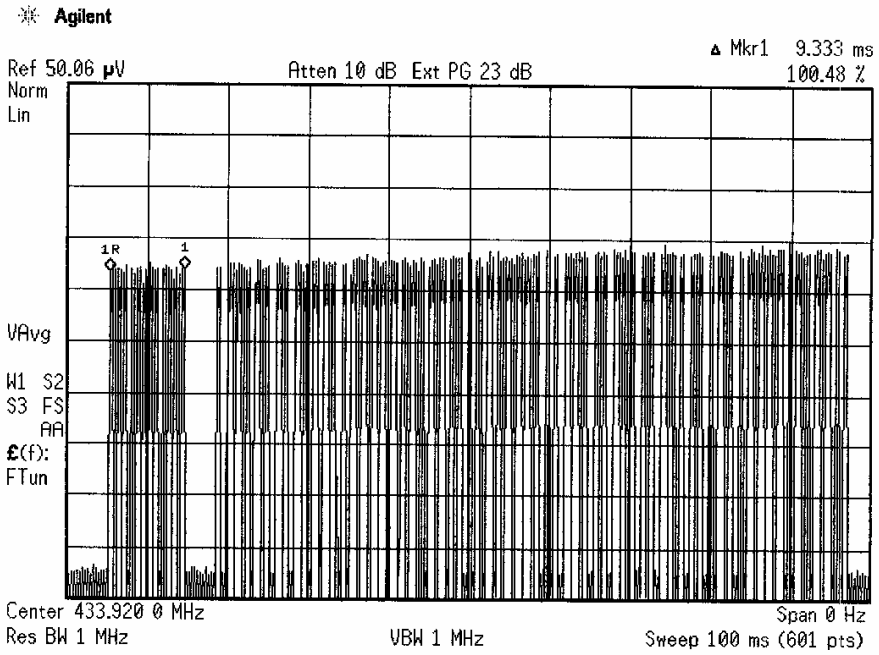

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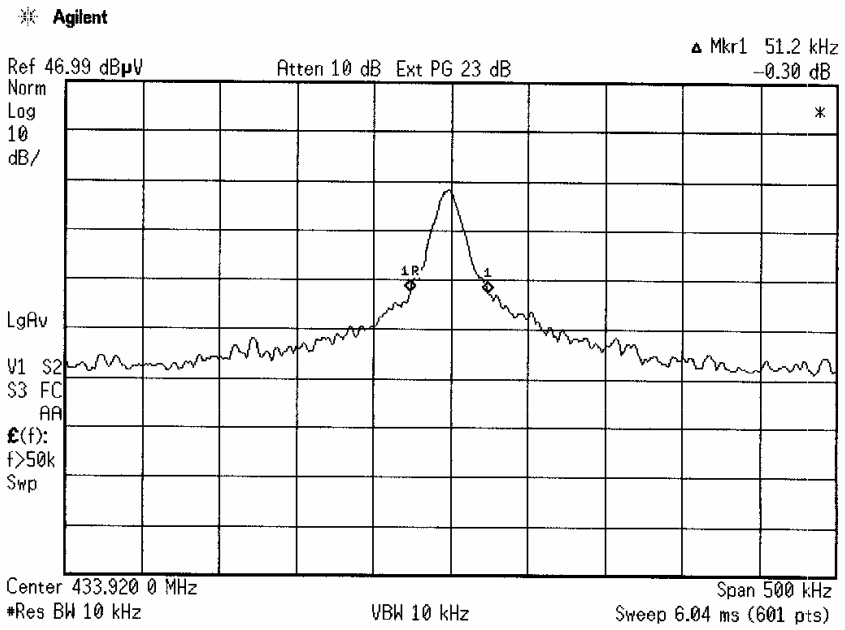
FCC Part 15.231(c) - Emission Bandwidth



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4.0 ATTESTATION STATEMENT

GENERAL REMARKS:

SUMMARY:

All tests were performed per CFR 47, Part(s) 15.231(a), 15.231(b), 15.231(c)

■ - Performed

The Equipment Under Test

■ - **Fulfills** the requirements of CFR 47, Part(s) 15.231(a), 15.231(b), 15.231(c)

Testing Start Date: 31 May 2006

Testing End Date: 01 June 2006

- TÜV AMERICA, INC. -

Reviewing Engineer:

David Gray
(EMC Engineer)

Test Engineer:

Jim Owen
(EMC Engineer)

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