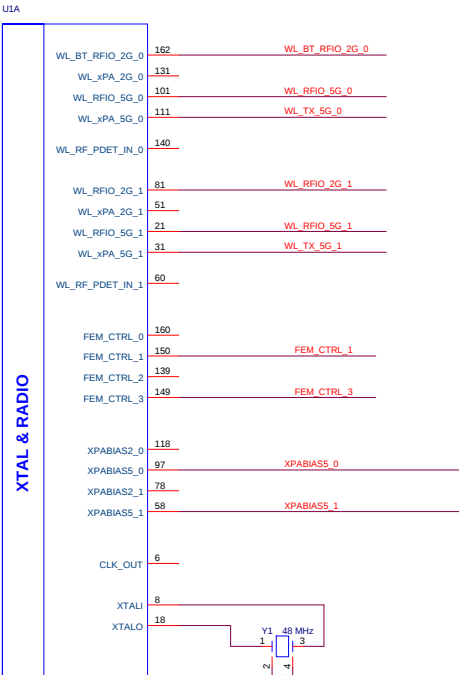


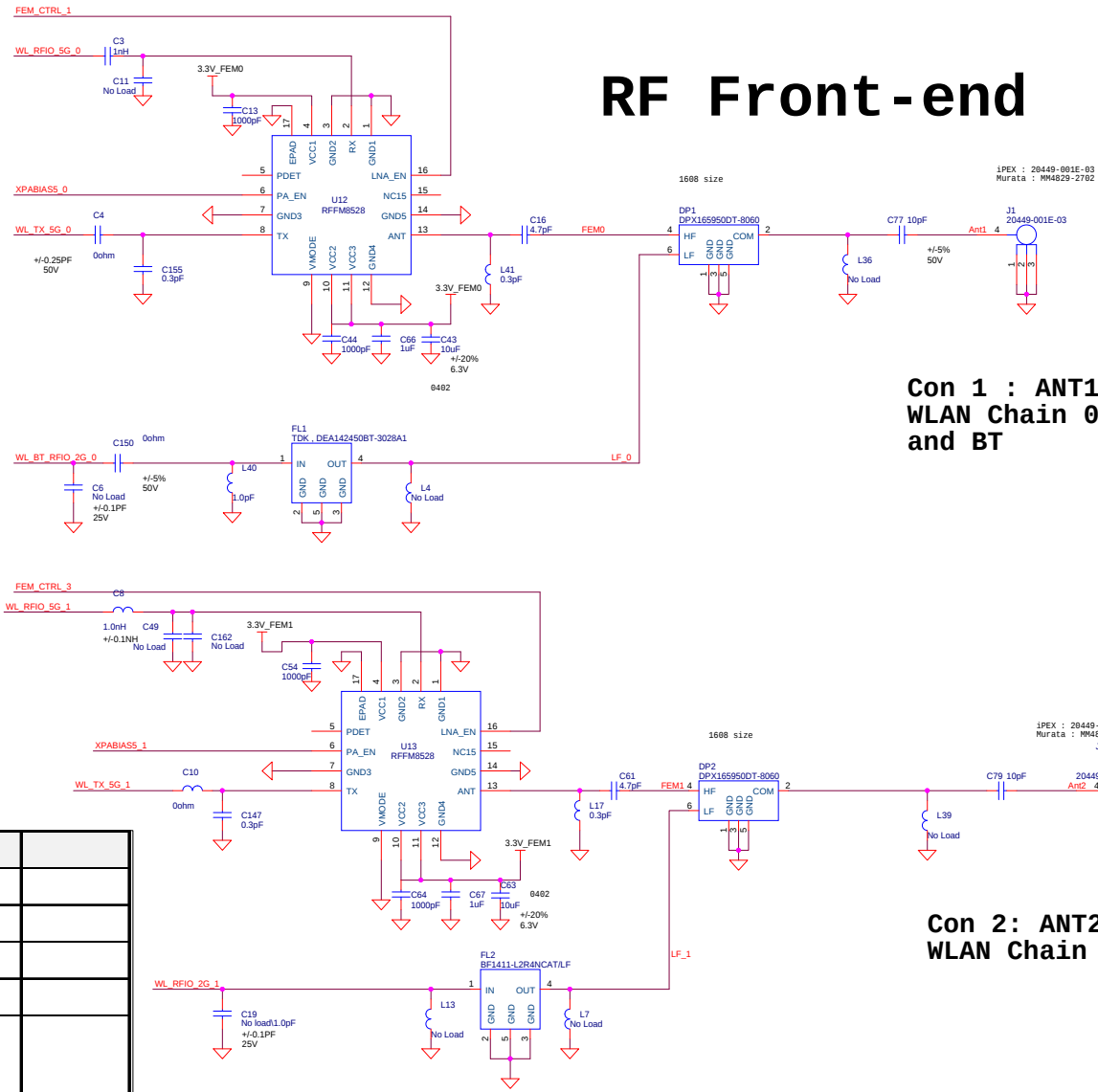
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QCA6174A-5

Operation Mode	XPABIAS5_0	XPABIAS5_1	FEM_CTR_1	FEM_CTR_3	
5G_TX_0	H	L	L	L	
5G_RX_0	L	L	H	L	
5G_TX_1	L	H	L	L	
5G_RX_1	L	L	L	H	
2G_TXRX or BT_TXRX or 5G_LNA_Bypass or SLEEP or IDLE	L	L	L	L	
5G_TX_0_Low_Power	H	L	L	L	
5G_TX_1_Low_Power	L	H	L	L	



線路圖/T77H643.02/00/3 OF 7

TITLE

CCA, NFA364AXP-5, 020, QCA6174A-5, 802.11AC/BA/B/G/N, 2X2 WIFI_PCIE_BTUSB M.2_2230

SIZE

C

DRAWING NO

LD20-Y8878

REV

B

SCALE

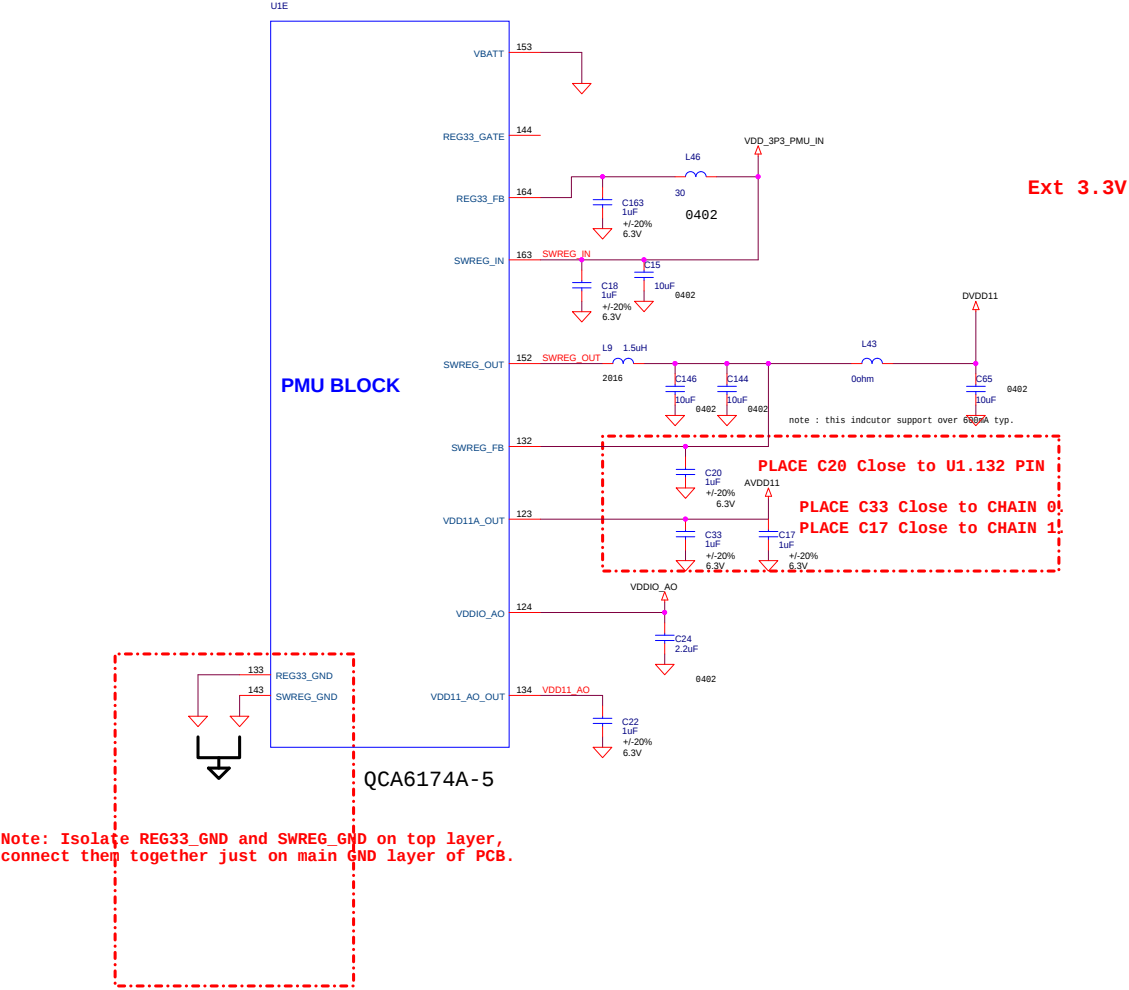
NONE

SHEET

3 OF 7

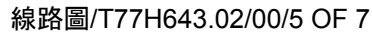
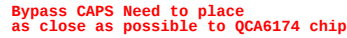
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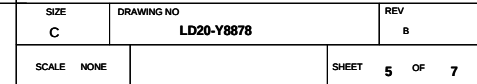


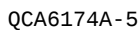
線路圖/T77H643.02/00/4 OF 7

TITLE			
CCA, NFA364AXP-5, 020, QCA6174A-5, 802.11AC/ABGN, 2X2 WIFI_PCIE_BTUSB M.2_2230			
SIZE	DRAWING NO	REV	
C	LD20-Y8878	B	
SCALE	NONE	SHEET	4 OF 7



1. Bypass cap Values can be changing over the bringup period.
2. Bypass Caps that are not close to the IC will not have a significant contribution to lowering the noise level
3. If caps are fairly far, the supply trace should be as wide as possible if vias are needed, have at least 2 vias for every DC connection.
4. All digital should have their own traces and not mixed with Analog Power
5. RF and BB and LO can be tied together
6. Synth must have its own supply
7. ADC/DAC circuits must have their own supply





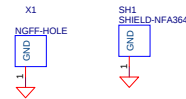
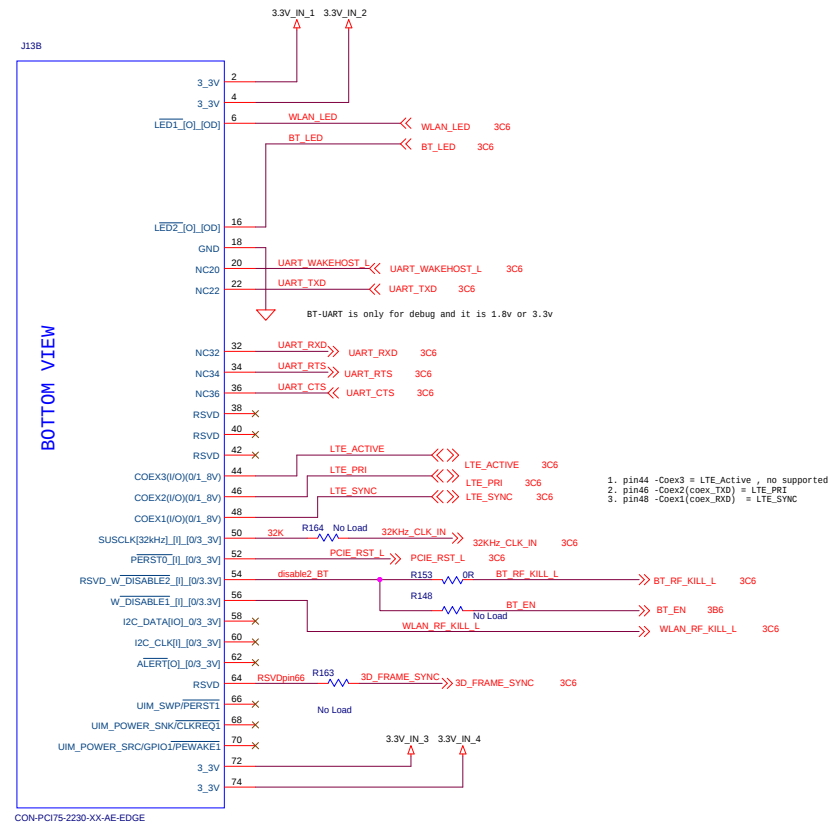
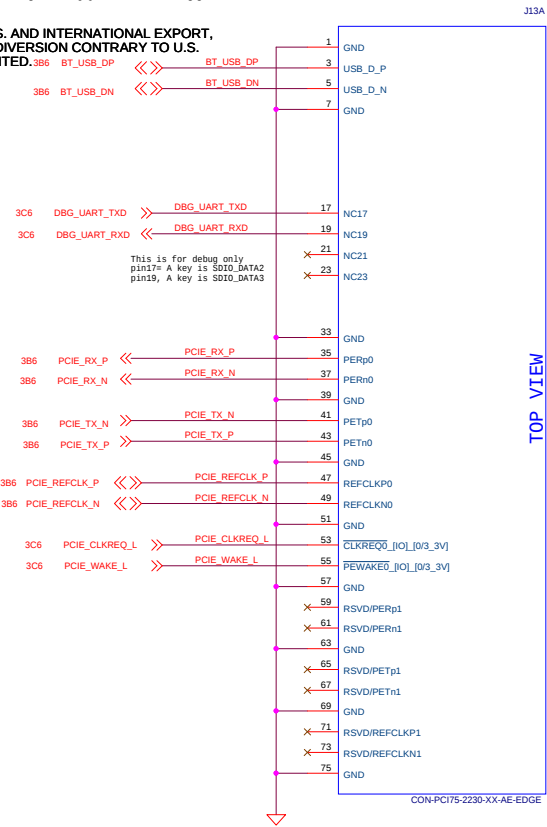
QCA6174A-5

線路圖/T77H643.02/00/6 OF 7

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QUALCOMM Atheros, INC. 1700 TECHNOLOGY DRIVE SAN JOSE, CA 95110 U.S.A				TITLE CCA, NFA364AXP-5, 020, QCA6174A-5, 802.11ACI/AB/G/N, 2X2 WIFI_PCIE_BTUSB M.2_2230			
SIZE C		DRAWING NO LD20-Y8878		REV B			
SCALE NONE				SHEET 7 OF 7			