System Description

1. System Structure Diagram

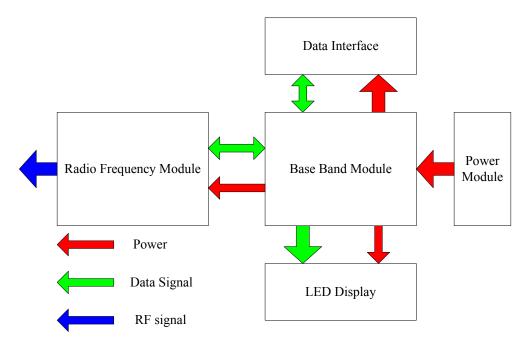


Diagram 1: System Structure Diagram

The system is consist of radio frequency module, base band module, power module, data interface and LED display. under the control of base band module, radio frequency module transmits the modulated carrier signal with varying frequency and power. transmit the signal to a certain area by antenna within which the tags receive signal then feedback its signal. after going into the radio frequency module, the feedback signal will be demodulated to form base band signal.

Base band performs decoding the tag signal and communicating with PC. and power module provide all the direct voltage.

2. Radio Frequency Structure Diagram

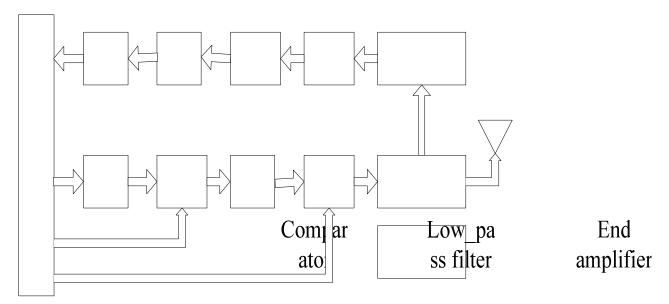


Diagram 2: Radio Frequency Structure Diagram

The radio frequency module is consist of transmitting and receiving parts controlled by Baseband circuit. the phase lock loop generates carrier and with frequency ranged from 902MHz~928Mhz, which could be amplified by the front amplifier and end amplified. Front pass return tag's signal via demodulator to mixer where the signal is detected and amplified to output to the baseband circuit.

Modulation signal

The radio frequency module is consist of transmitting and receiving part,

Power con

·transmitting part

The transmitting part is as below according to the RF signal flow.

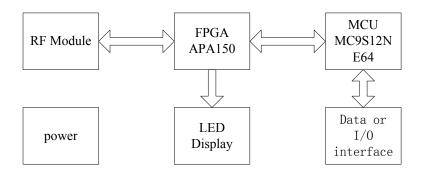
Temperature-compensated oscillator (12.8MHz) \rightarrow PLL915A phase lock loop \rightarrow RF2302 power front amplifier \rightarrow end amplifier with power adjustable \rightarrow microwave switcher with maximum output power

30dBm(1W).

·receiving part

The receiving part is as below according to the return signal flow Demodulated network \rightarrow front differential amplifier \rightarrow end amplifier \rightarrow comparator output \rightarrow output double channel digital signal.

3. Base Band Structure Diagram



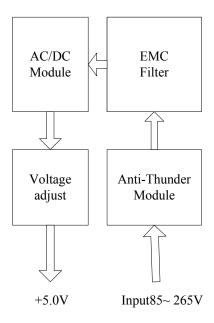
Base Band Structure Diagram

The baseband module is consist of coding/decoding part, and control parts, which respectively is FPGA and MCU.

The coding/decoding part employs built-in-FLASH modeled APA150 .the APA150 performs coding/decoding for tag signal and controlling the transmitting circuit.

The control unit employs MCU produced by Freescale company. with 19200serial interface and 10Mbps Etherent network interface, MCU performs order analyzing, data uploading, and data storage etc.

4. Power Structure Diagram



Power Structure Diagram

Power module output is +5V direct current to interrogator, the maximum output current is 2A.