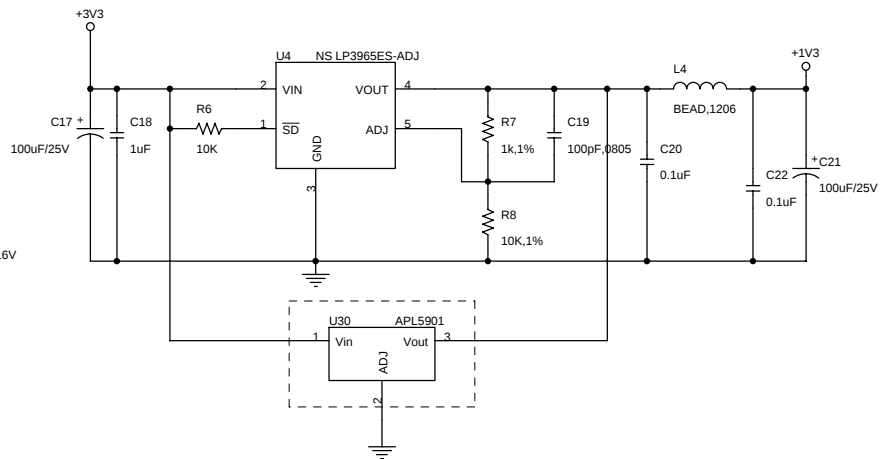
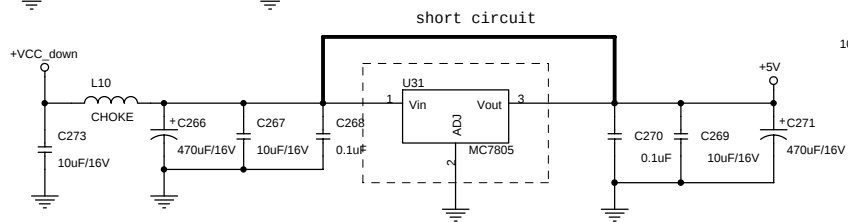
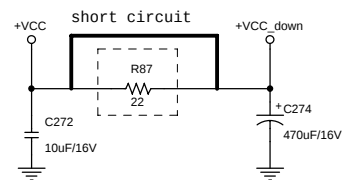
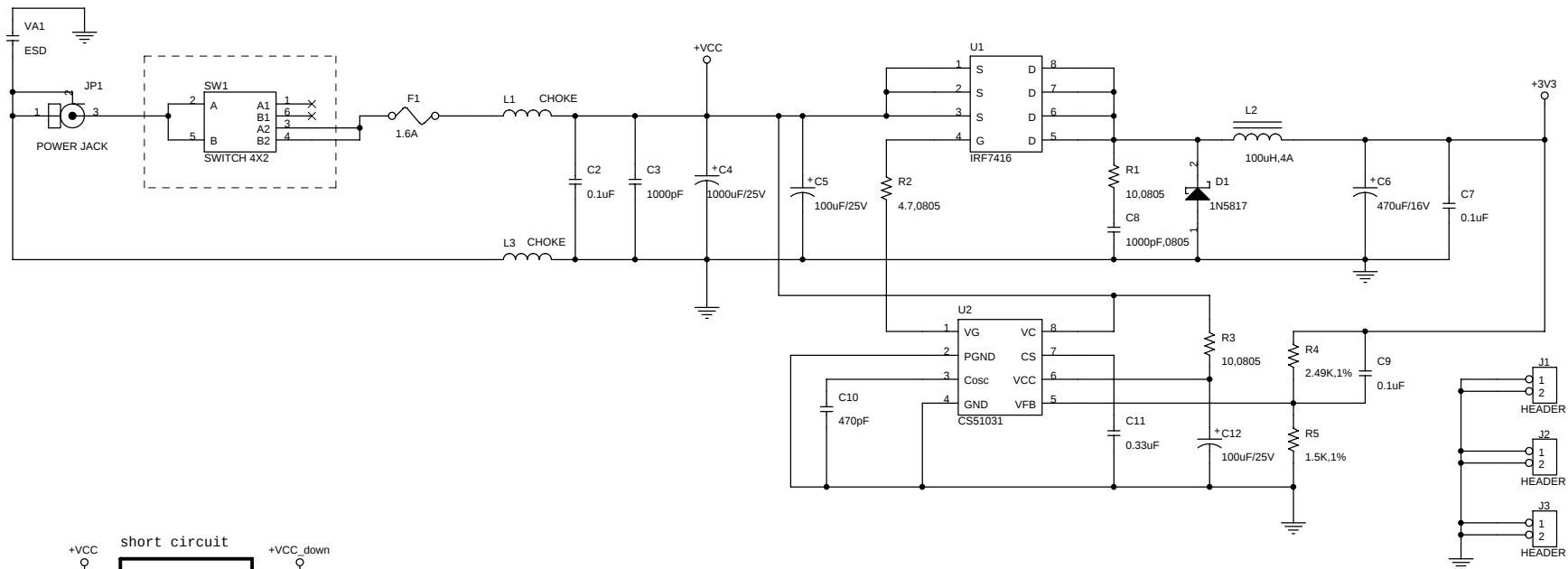
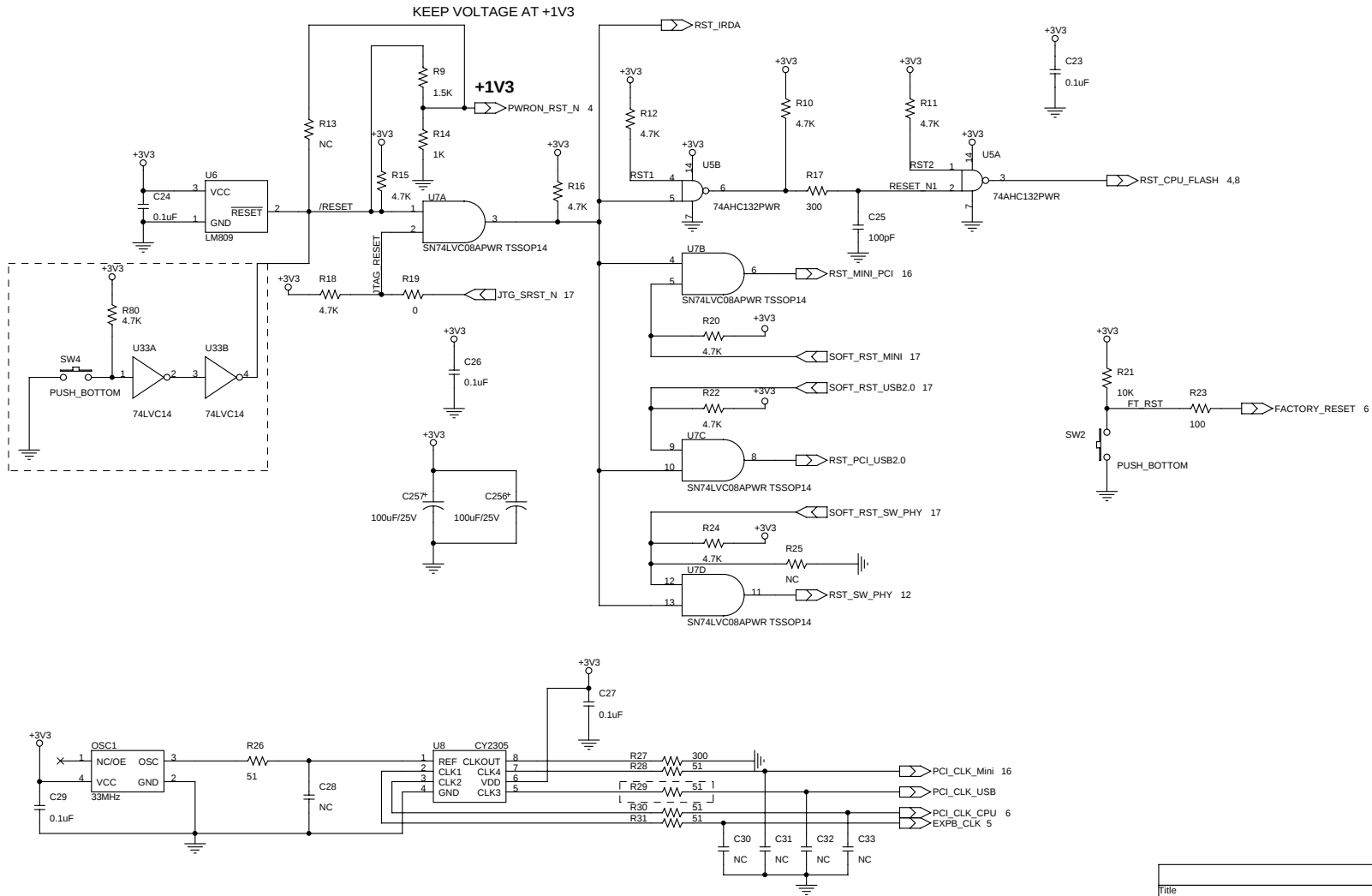
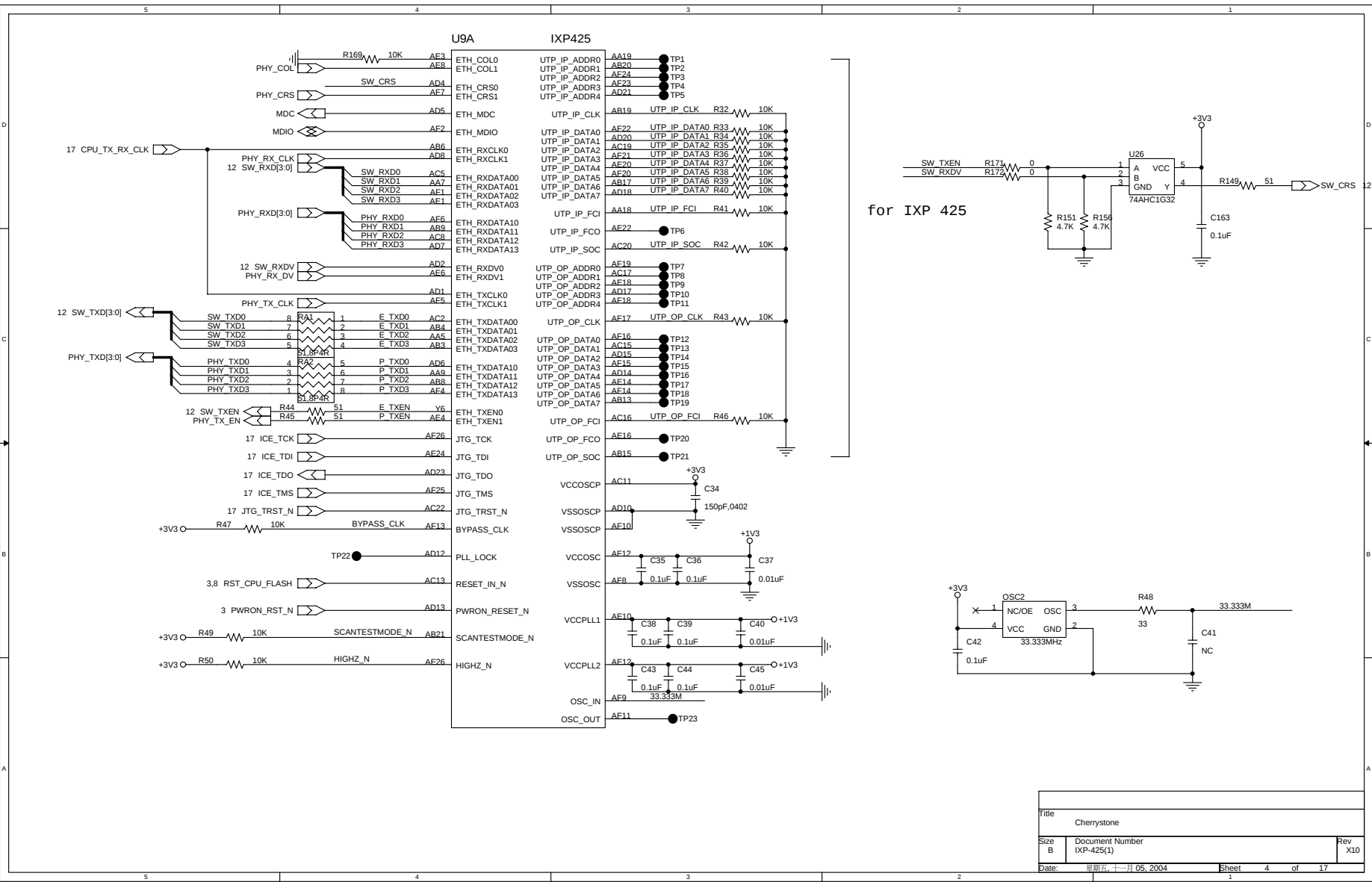




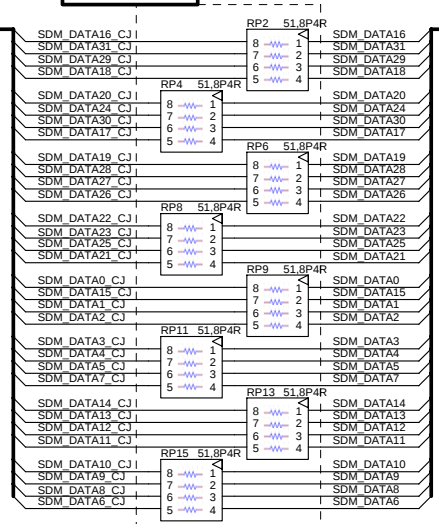
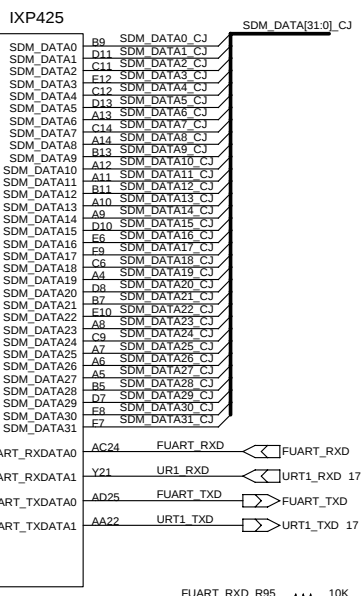
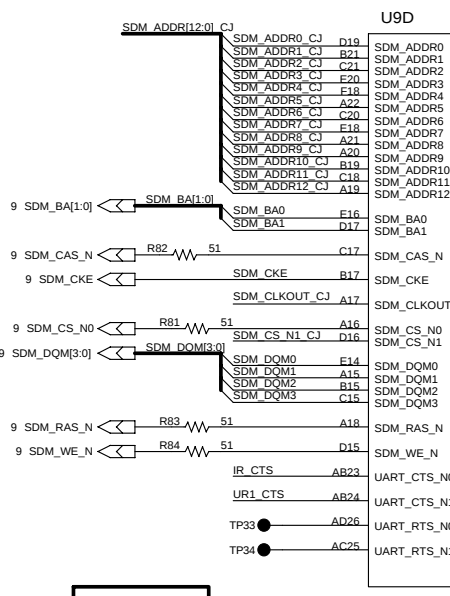
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| File        |                   |               |
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| Size        | Document Number   | Rev           |
| Custom      | POWER             | X10           |
| Date        | 星期五, 十一月 05, 2004 | Sheet 2 of 17 |



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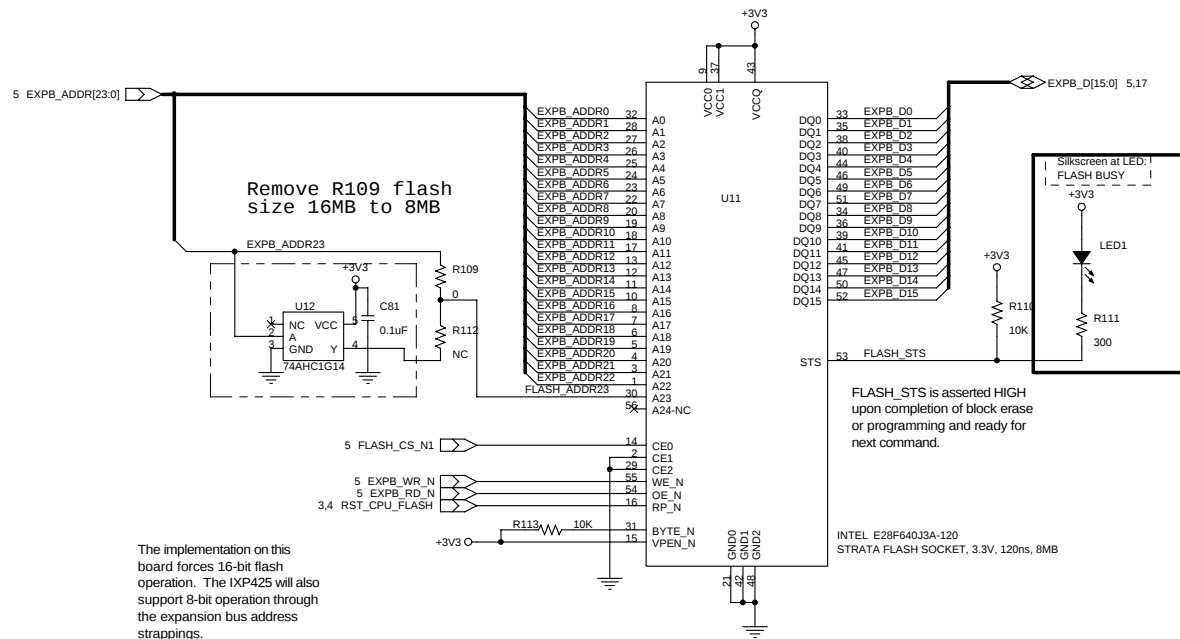




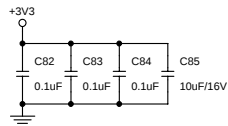
**LAYOUT NOTE:**  
These series damping resistors should be placed as close as possible to IXP425. Their values were tuned based on signal integrity simulations.

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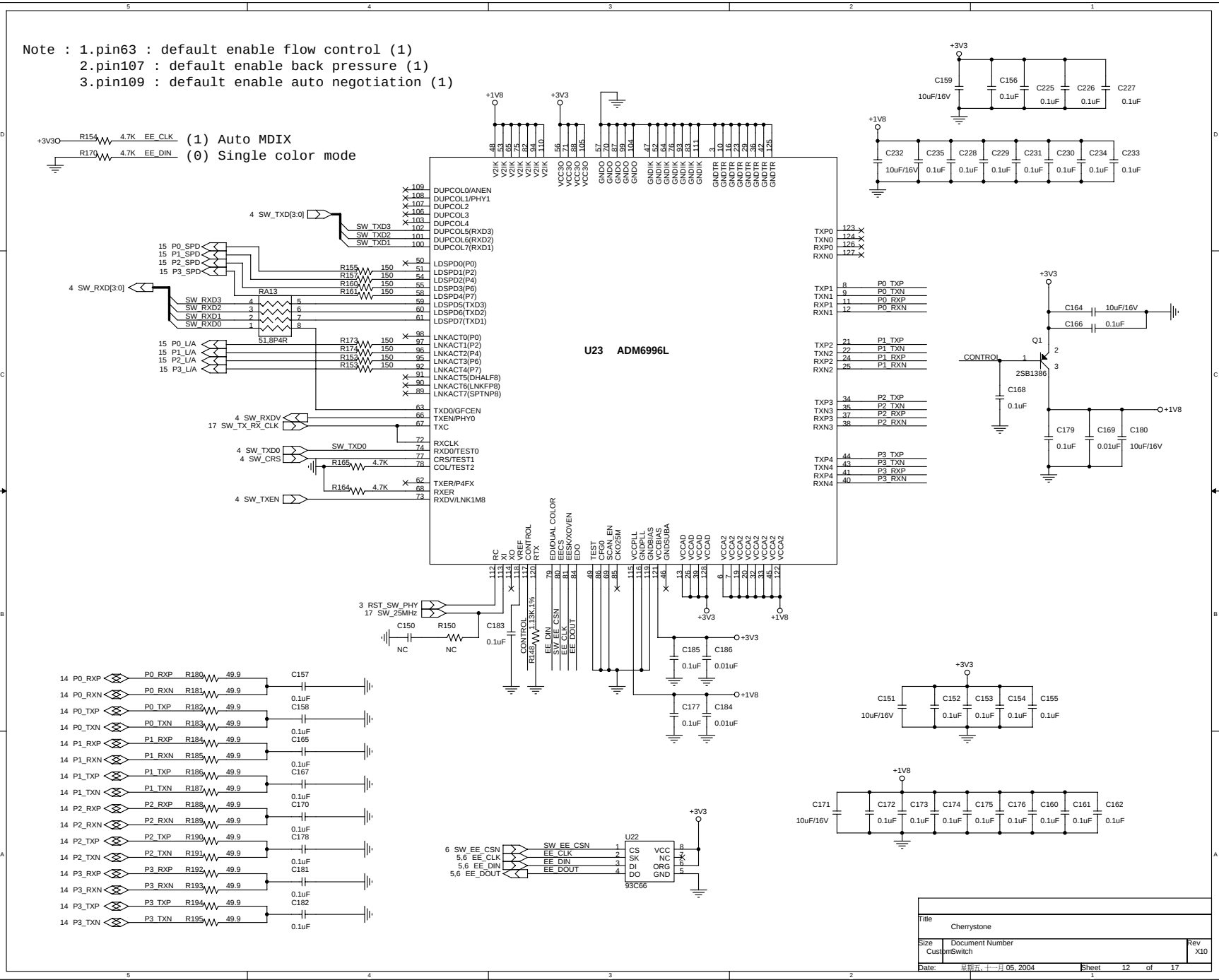
The implementation on this board forces 16-bit flash operation. The IXP425 will also support 8-bit operation through the expansion bus address strappings.



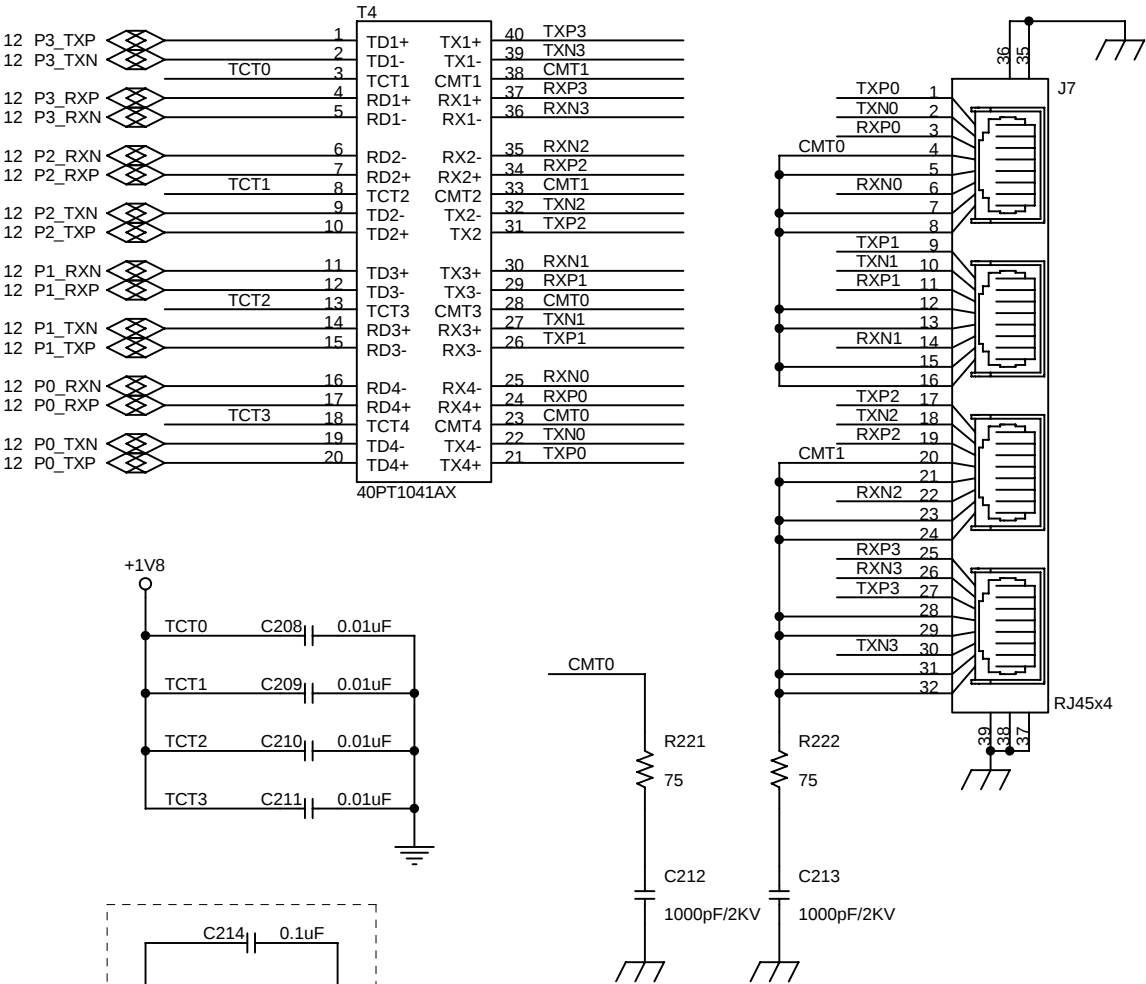
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| Date:       | 星期五, 十一月 05, 2004     | Sheet 8 of 17 |



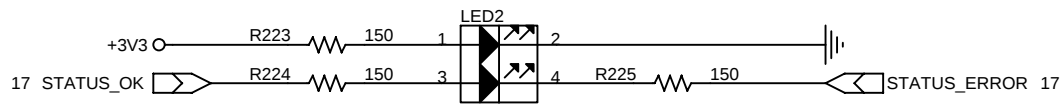
Note : 1.pin63 : default enable flow control (1)  
2.pin107 : default enable back pressure (1)  
3.pin109 : default enable auto negotiation (1)



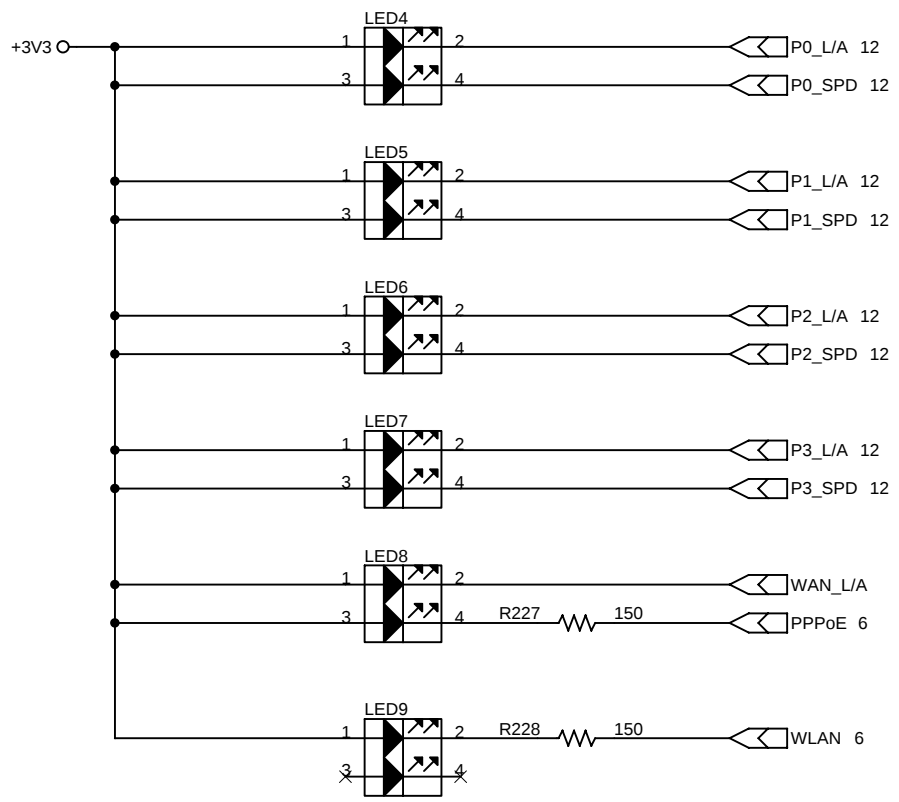
LAN



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| Cherrystone |                   |                |
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| A           | RJ-45             | X10            |
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OK : Green  
Error : Yellow and Blinking



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| Title       |                   |                |
| Cherrystone |                   |                |
| Size        | Document Number   | Rev            |
| A           | LED               | X10            |
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