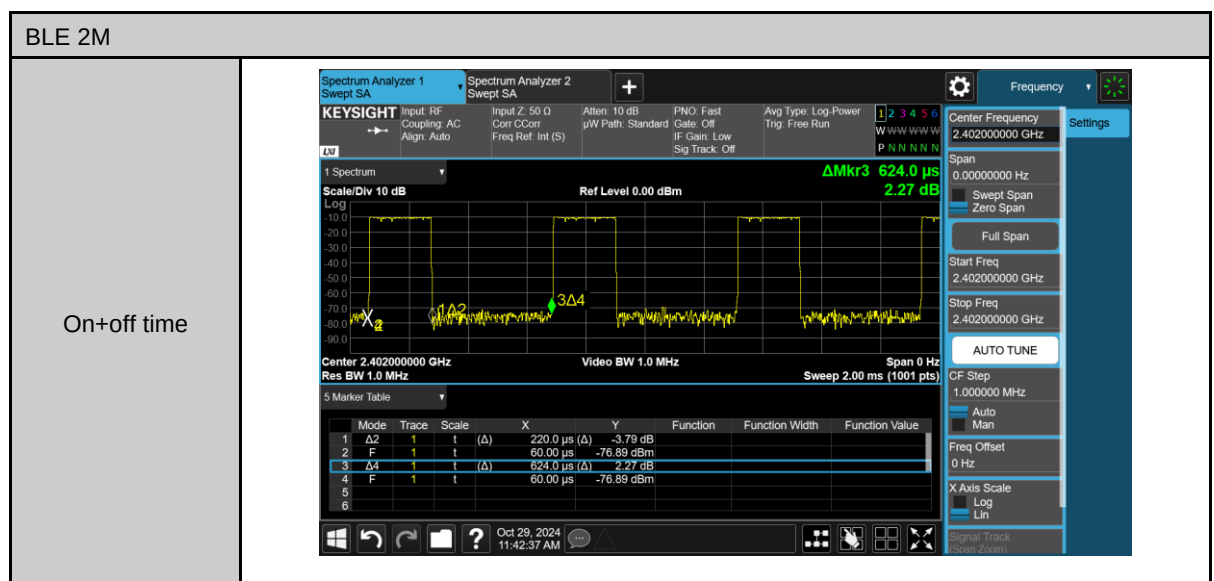
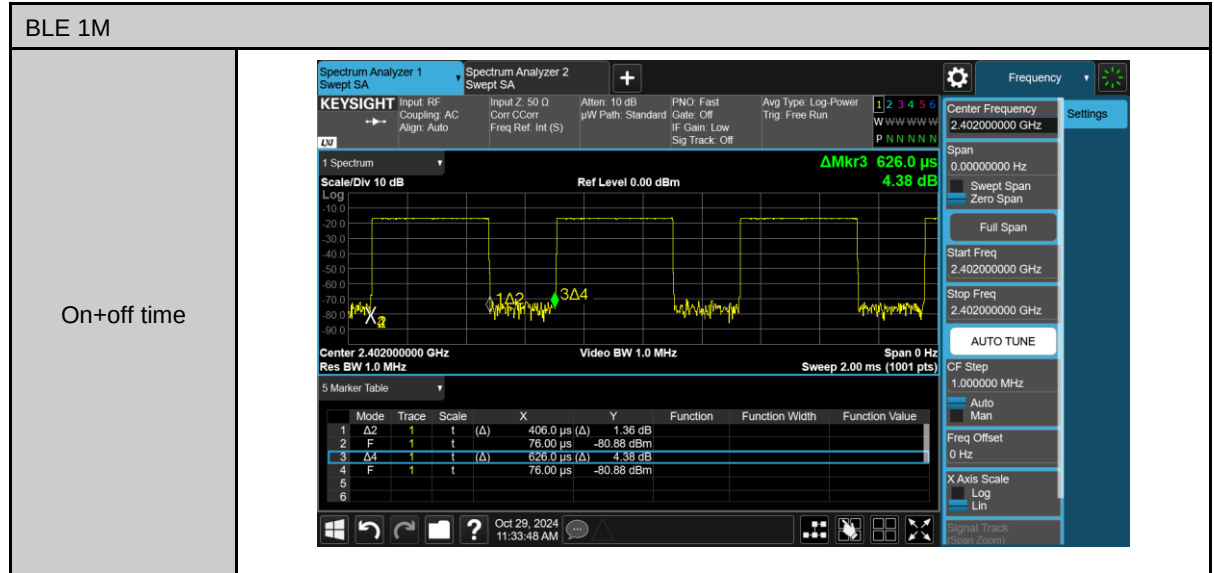


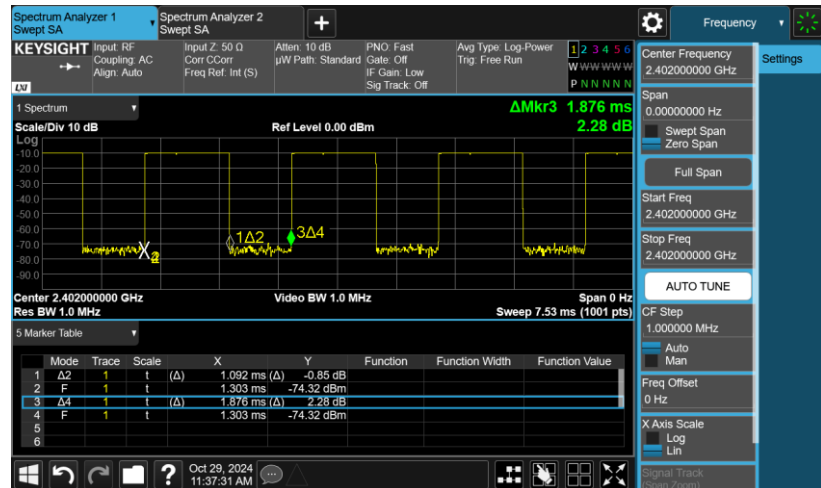
## Appendix B. Test Plots

### Duty cycle



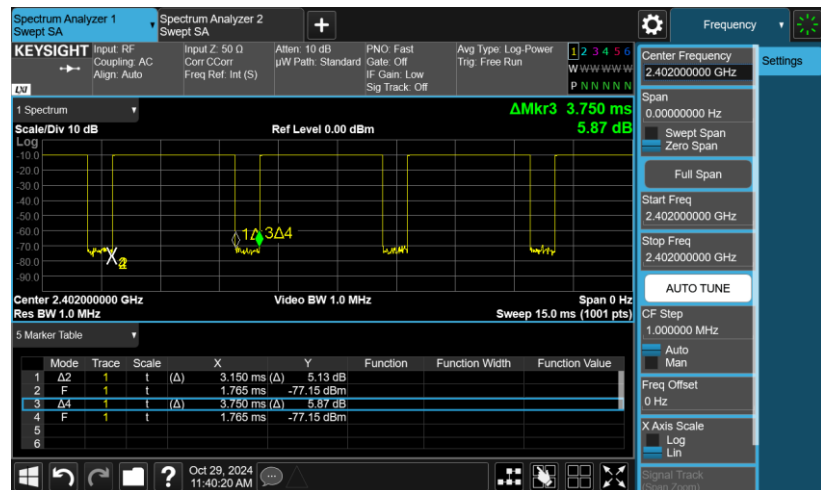
BLR C2

On+off time

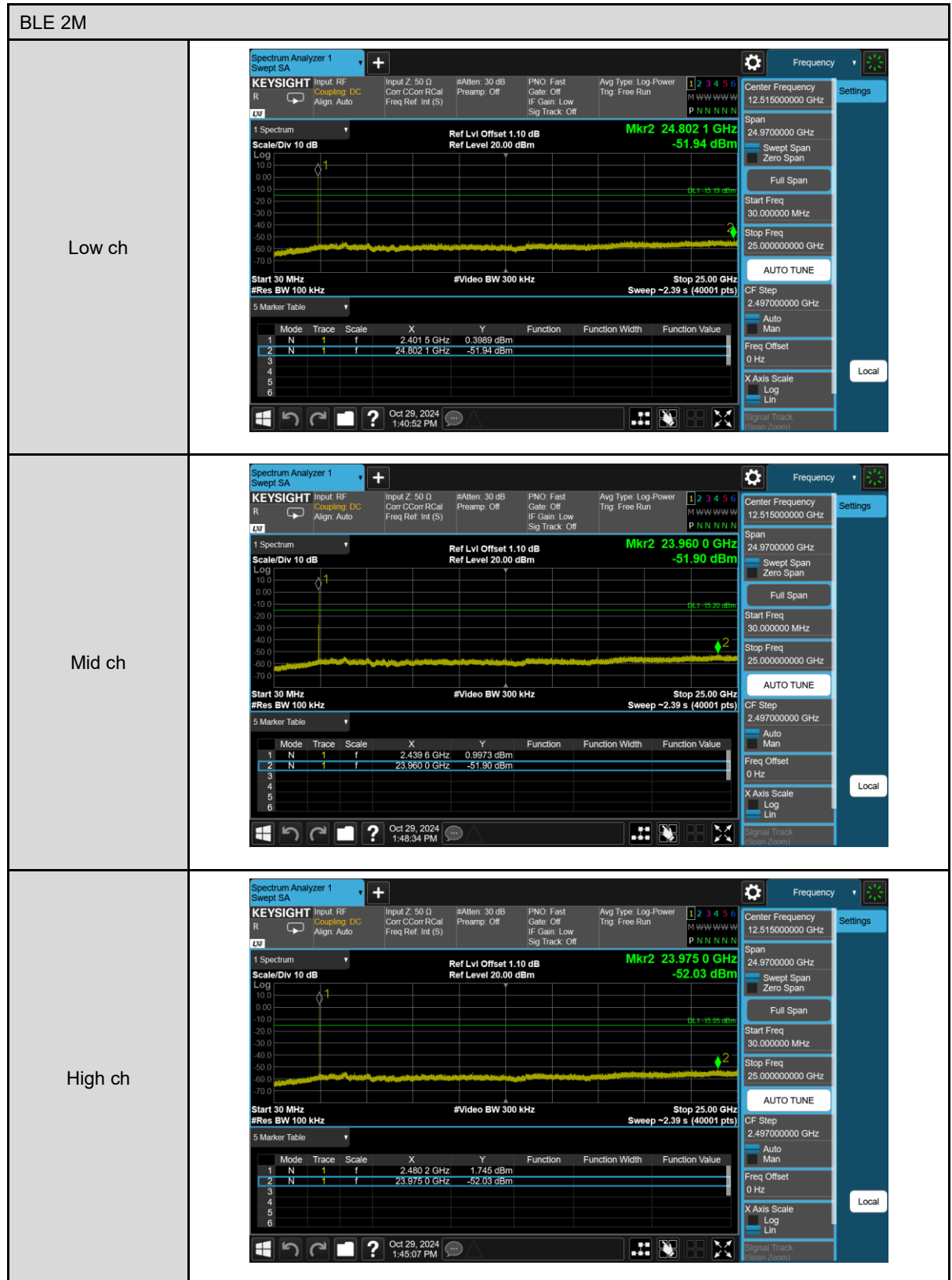


BLR C8

On+off time

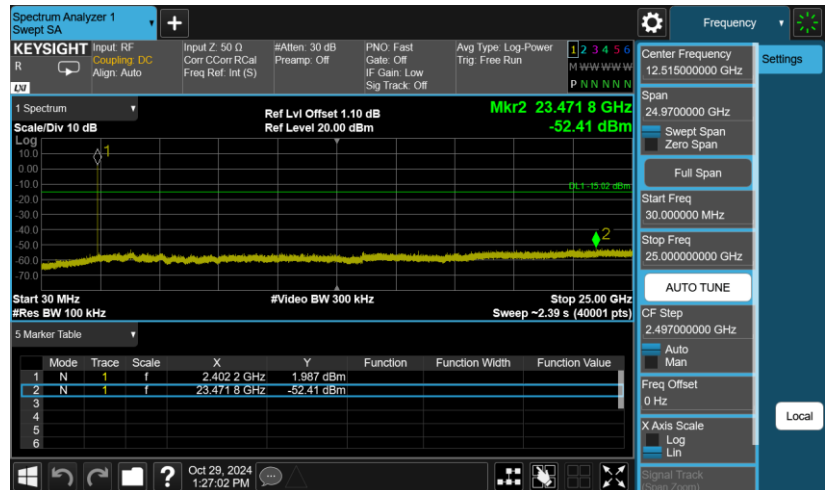


# Out of Band Conducted Spurious Emission

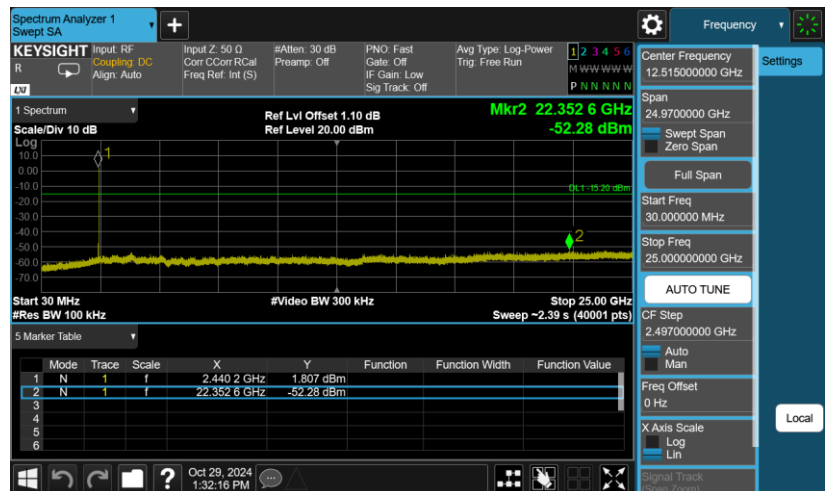


BLR C8

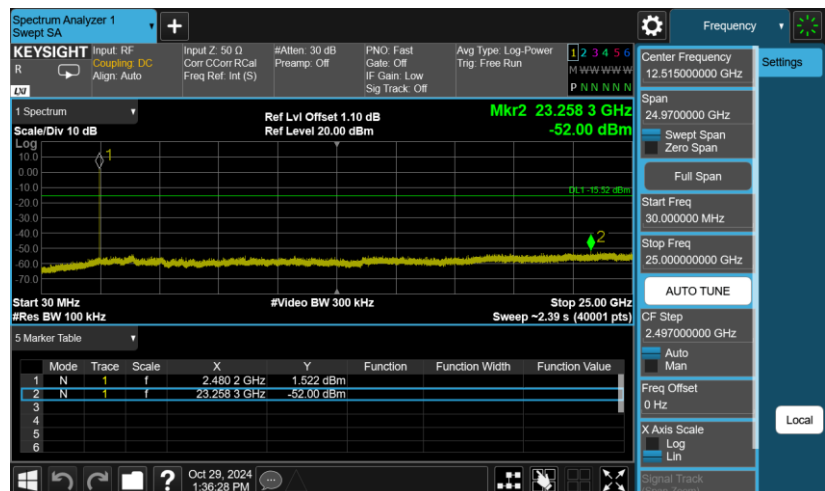
Low ch



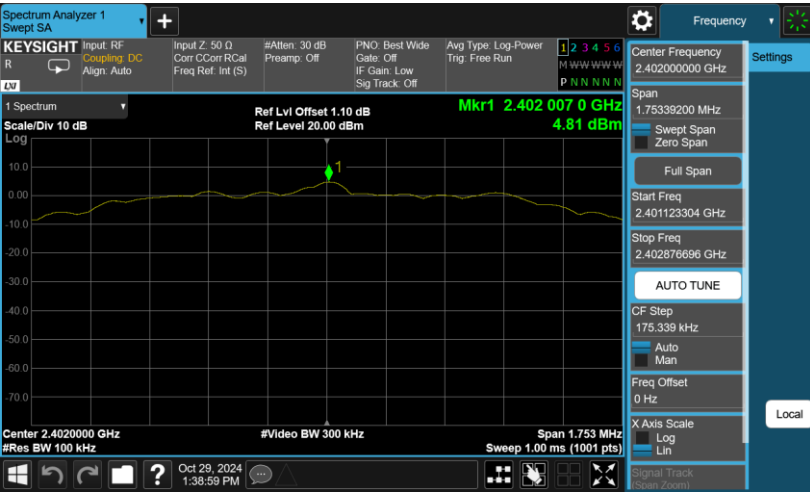
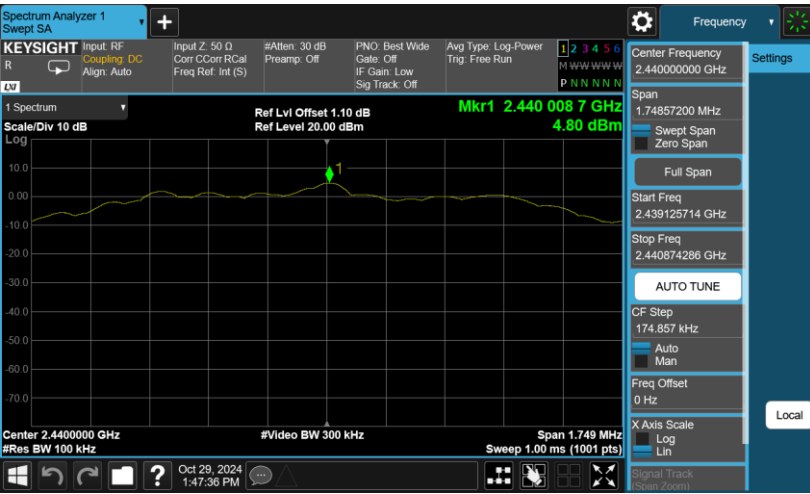
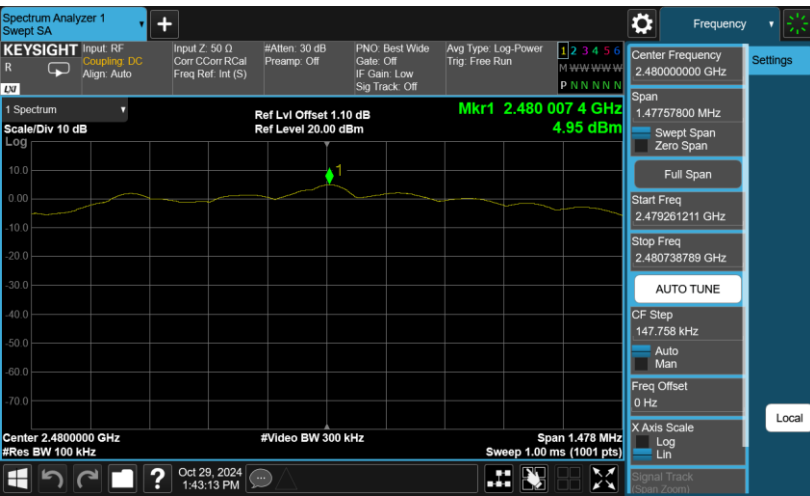
Mid ch



High ch

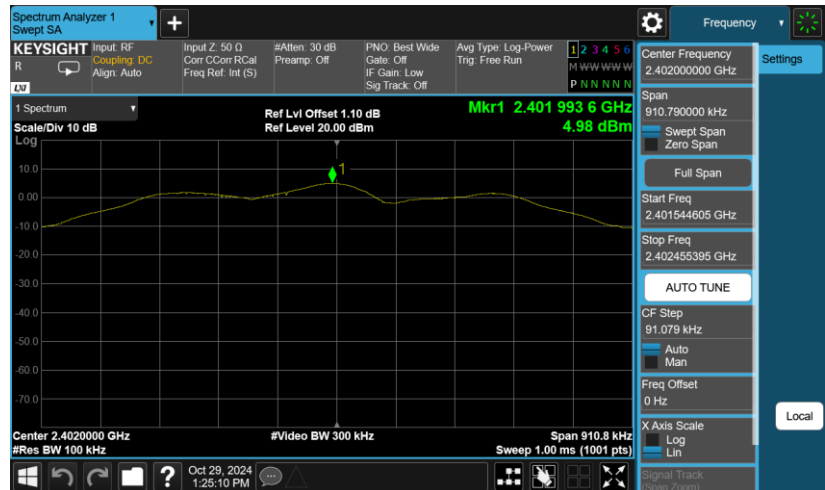


Reference level

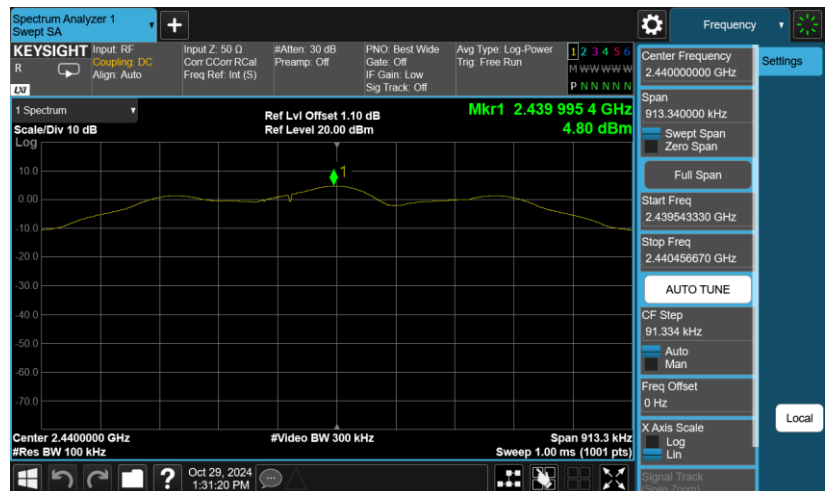
|         |                                                                                      |
|---------|--------------------------------------------------------------------------------------|
| BLE 2M  |    |
| Mid ch  |   |
| High ch |  |

BLR C8

Low ch



Mid ch



High ch

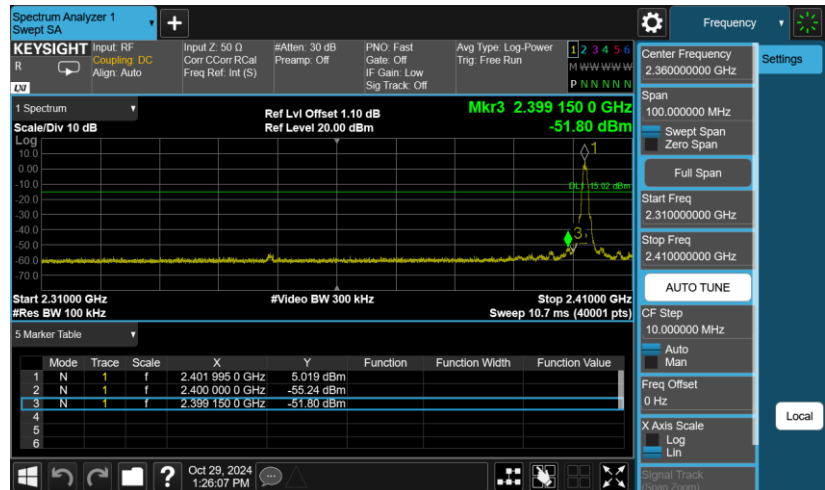


## Conducted Band Edge

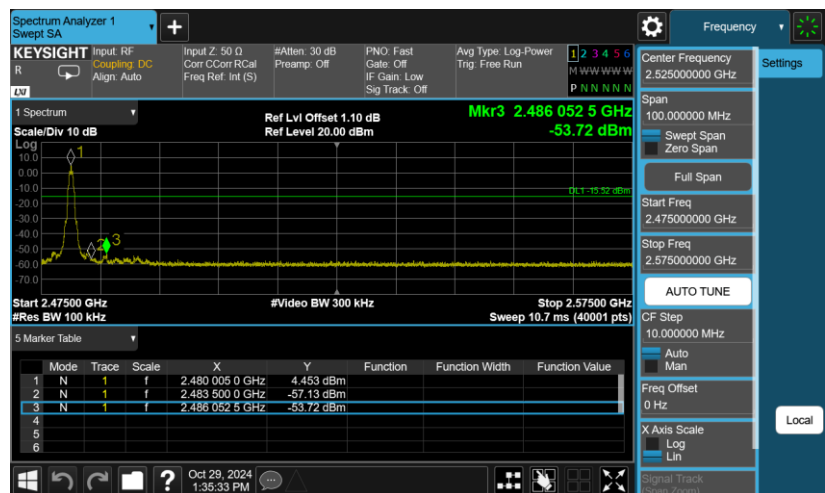


BLR C8

Low ch

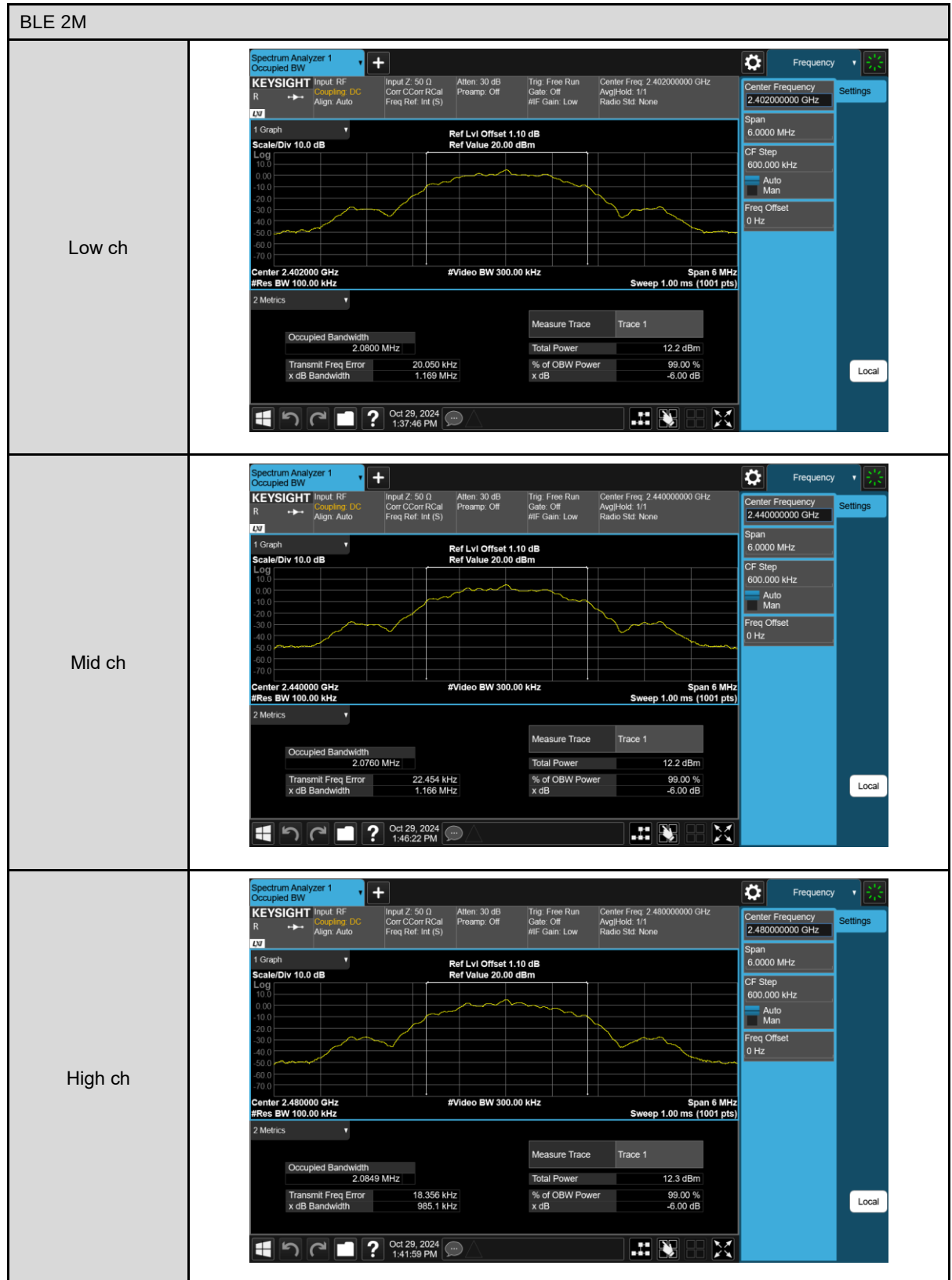


High ch





## 6 dB Bandwidth

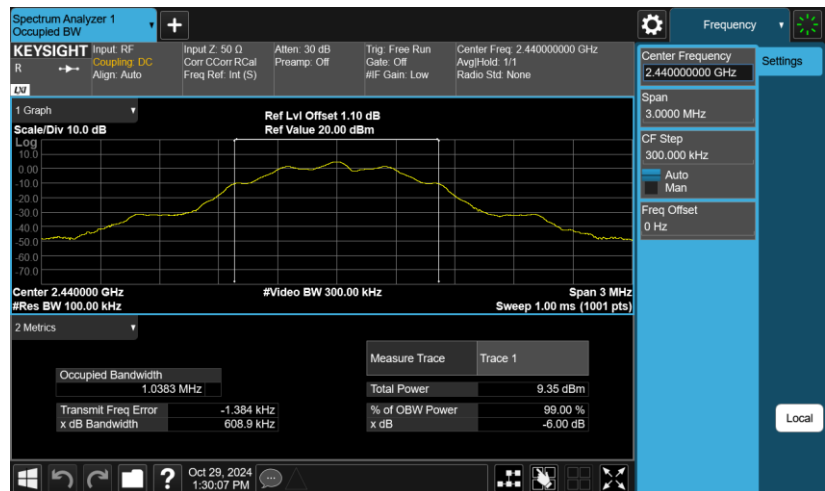


BLR C8

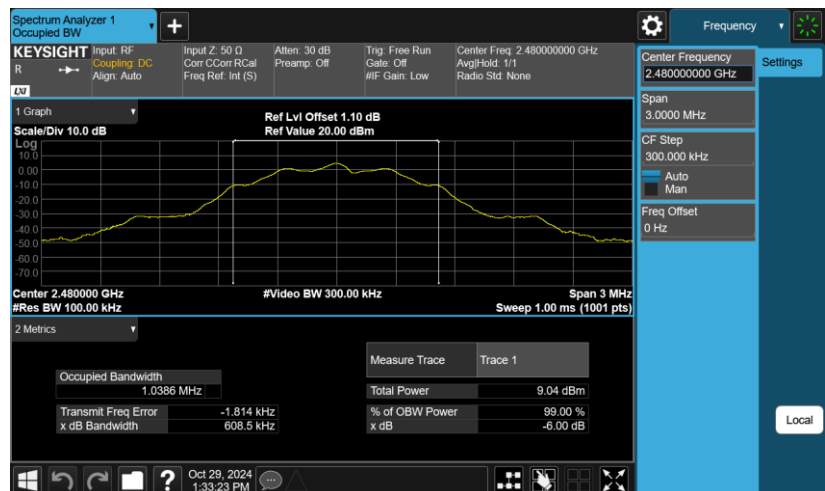
Low ch



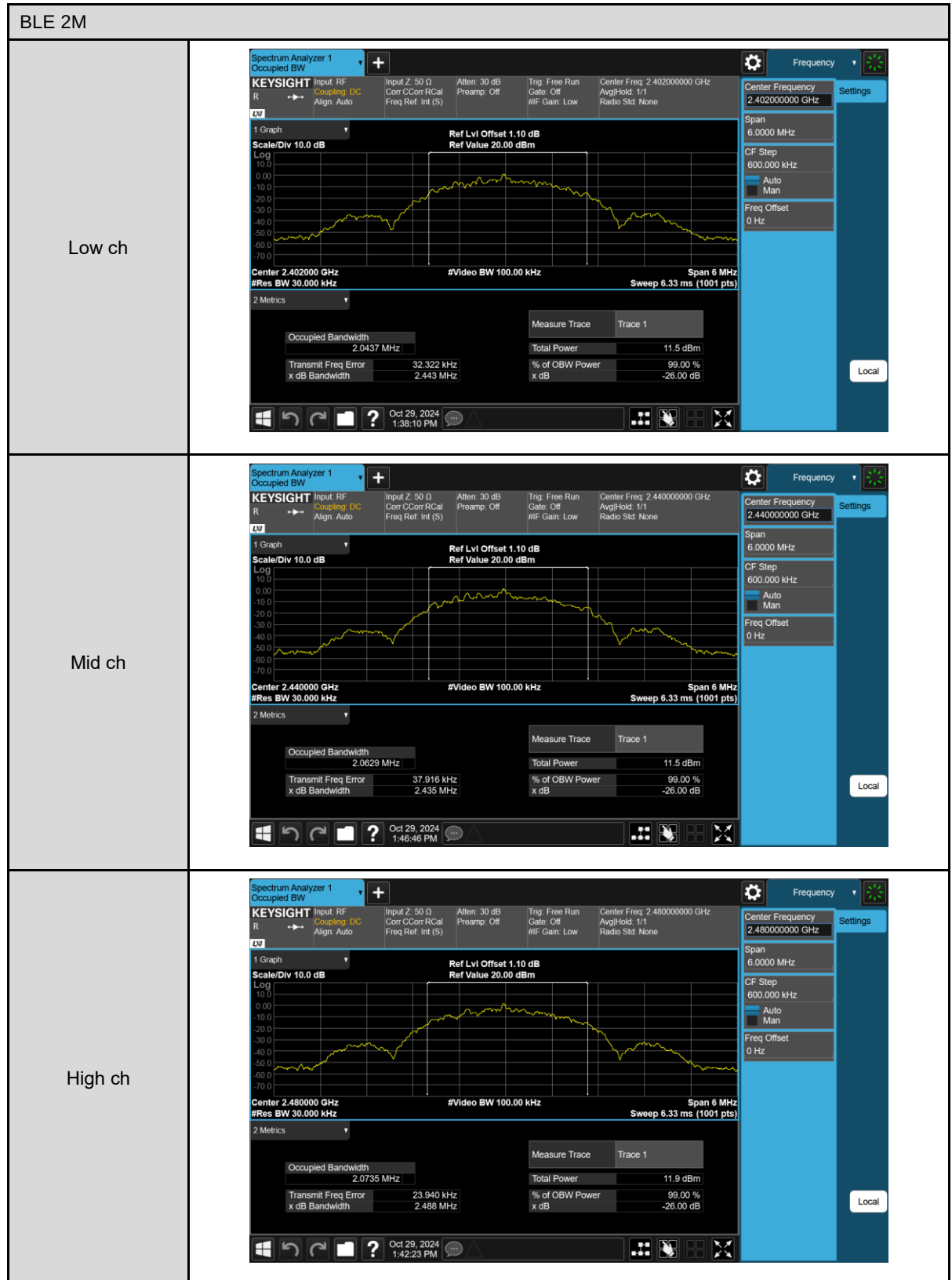
Mid ch



High ch

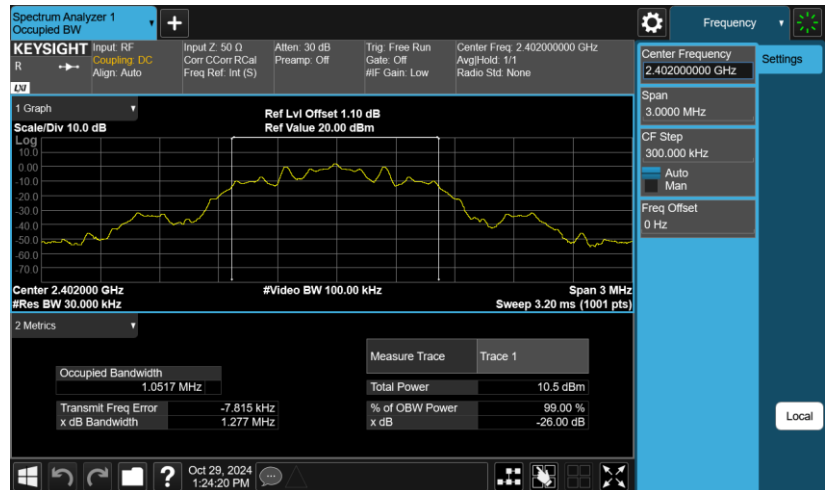


99 % Occupied Bandwidth

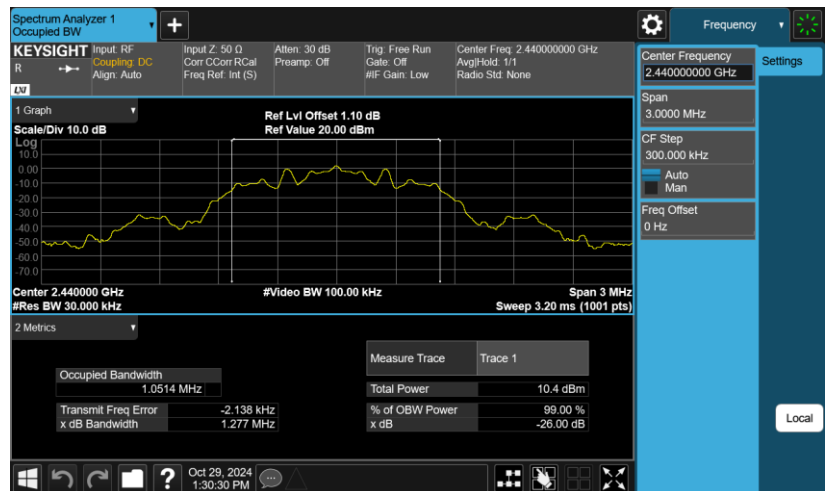


BLR C8

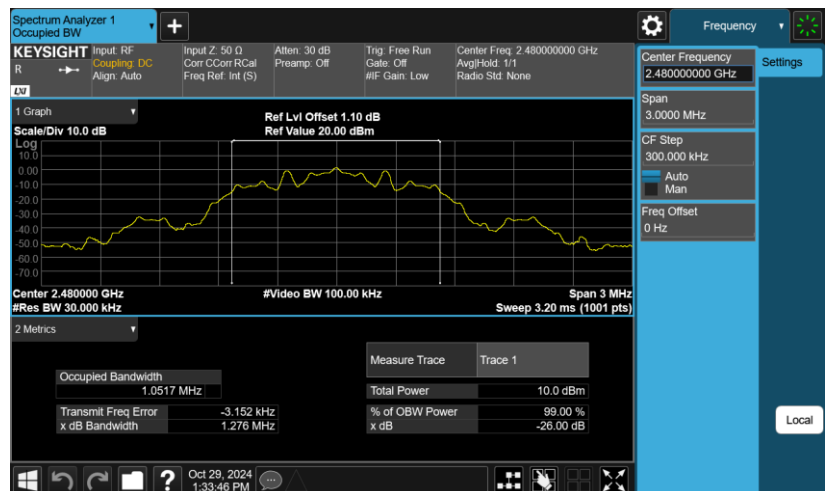
Low ch



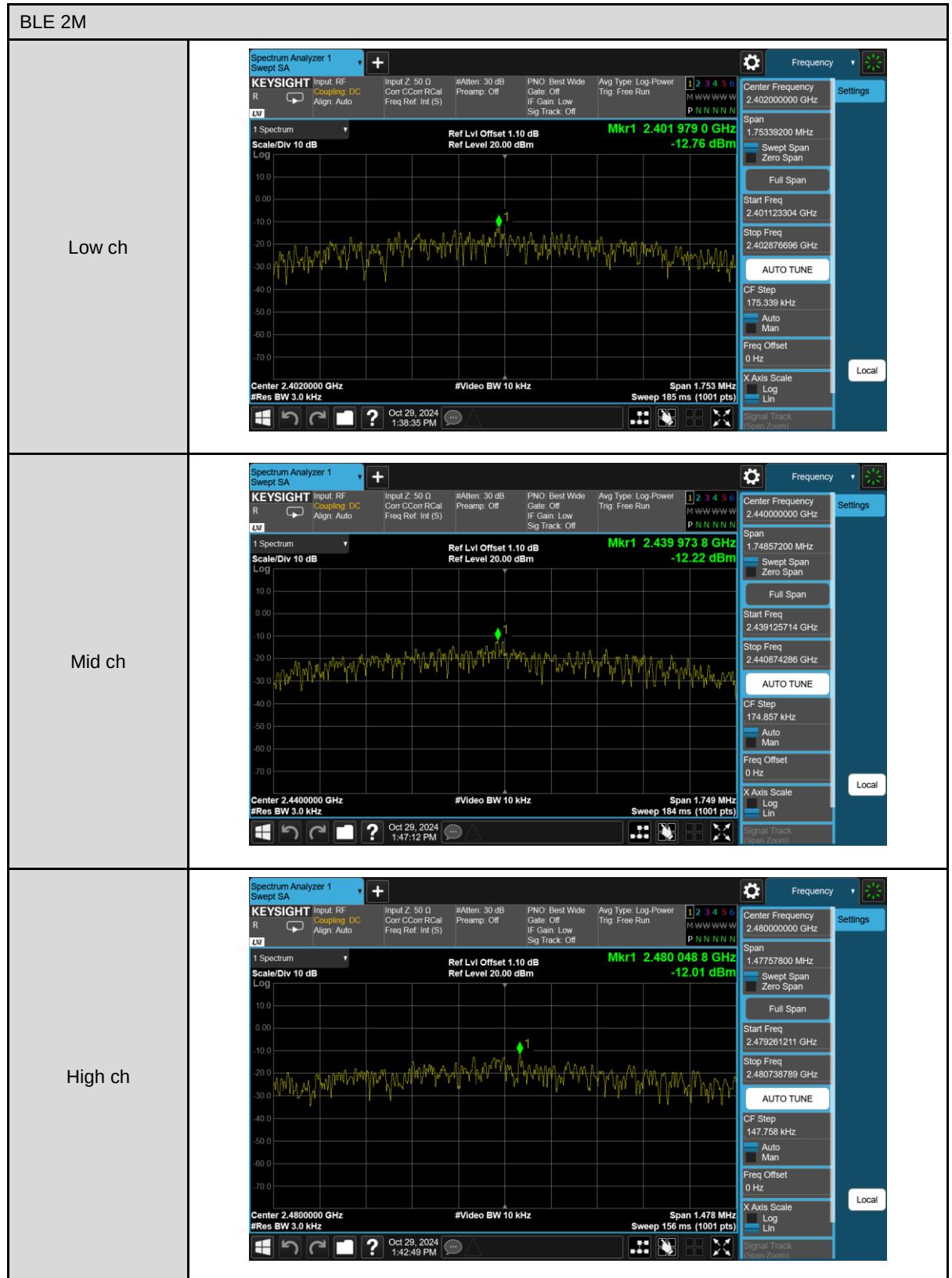
Mid ch



High ch

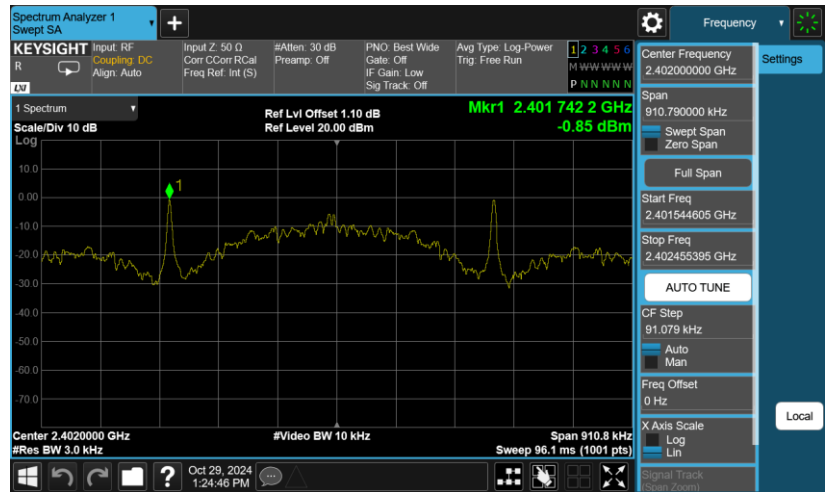


# Power Density

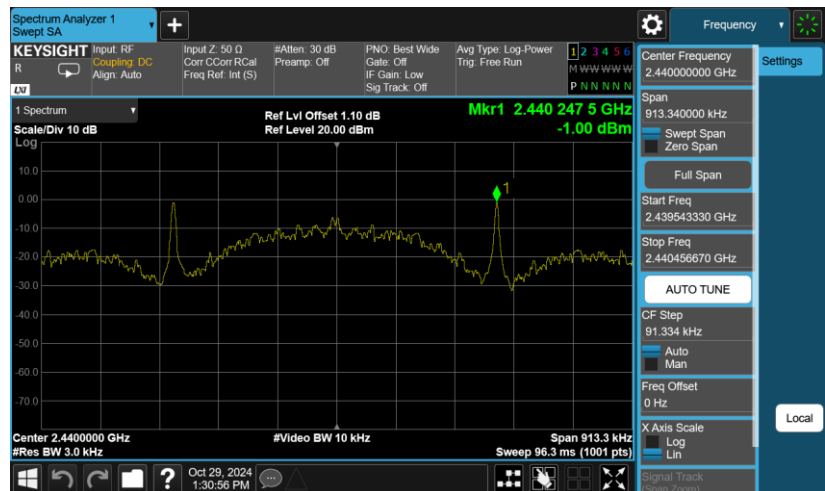


BLR C8

Low ch



Mid ch



High ch

