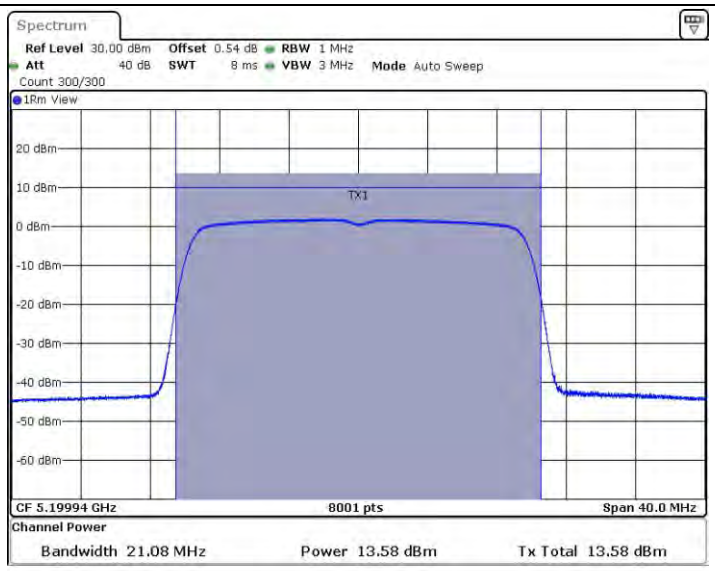
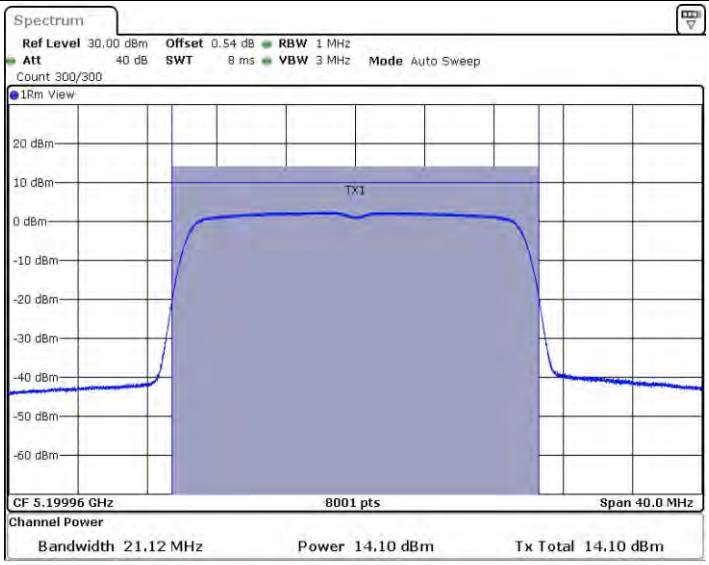
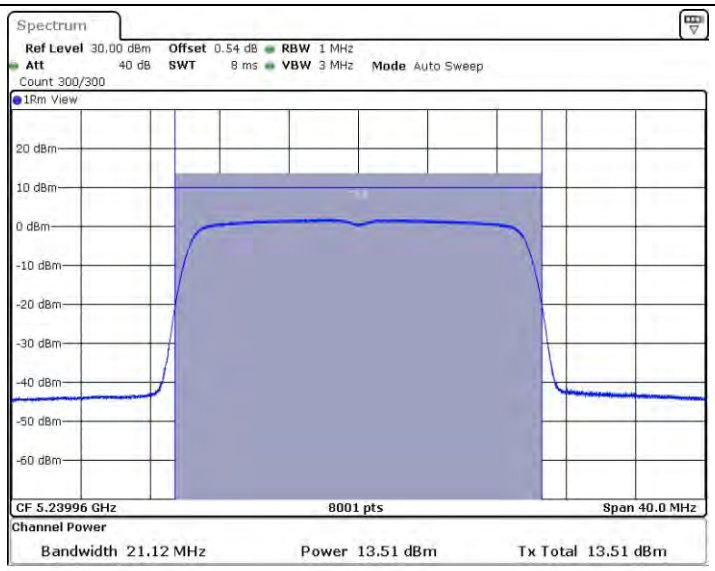


11AX20SISO_Ant0_5200



11AX20SISO_Ant1_5200

11AX20SISO_Ant0_5240



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11AX20SISO_Ant0_5260

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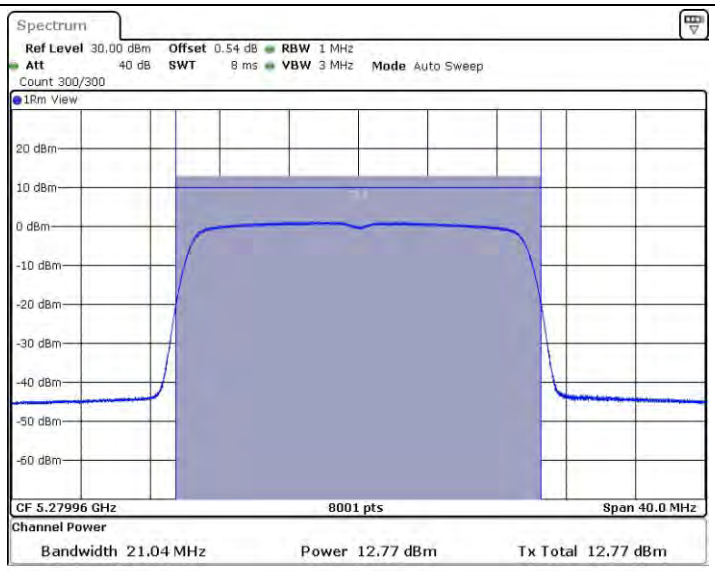

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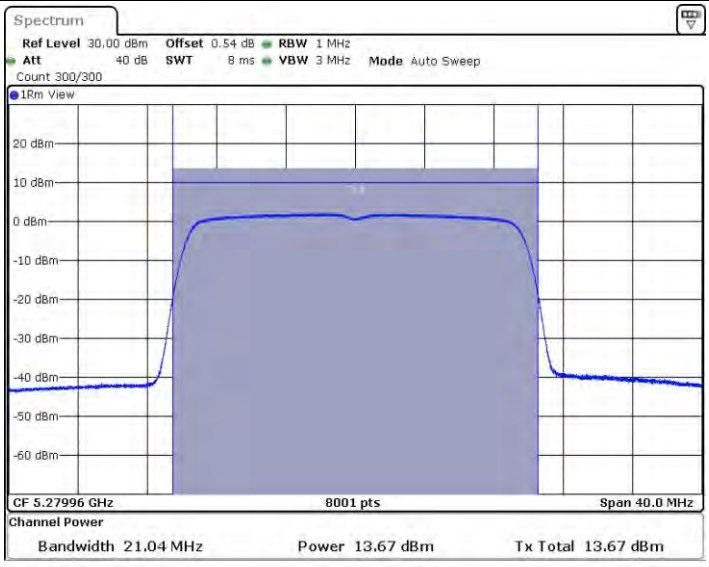

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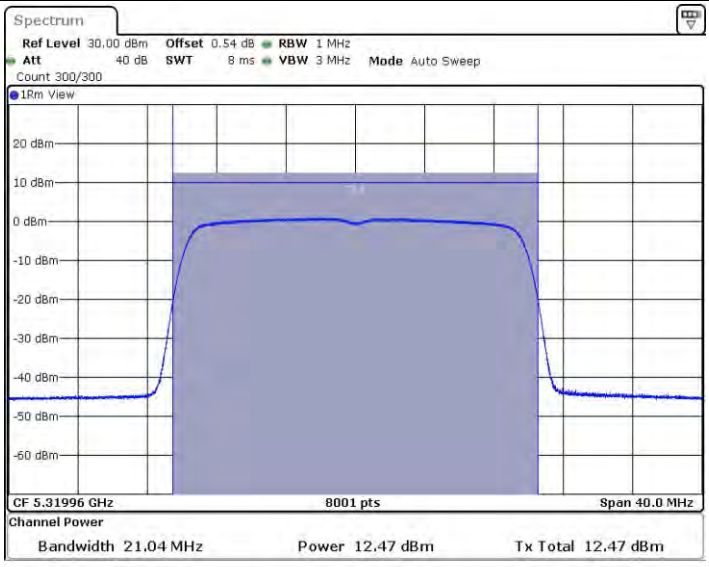

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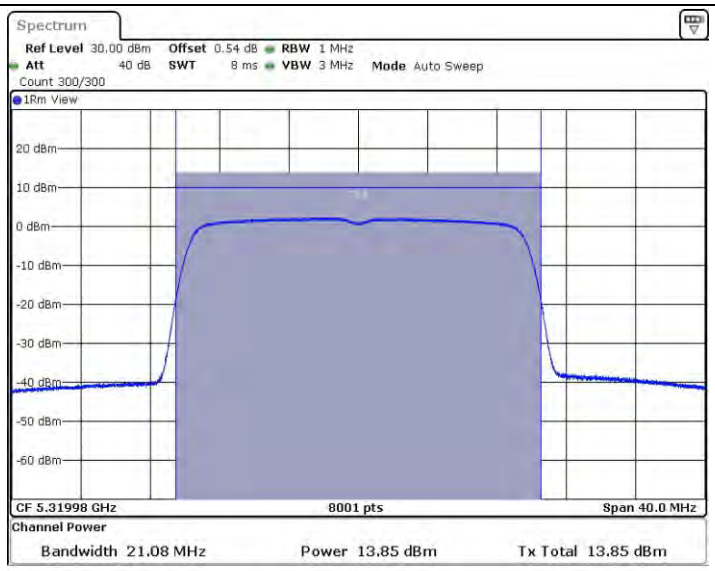

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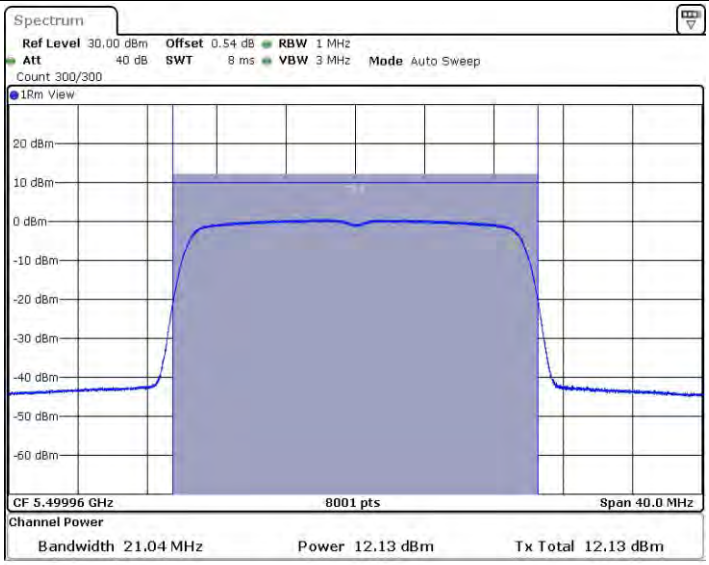

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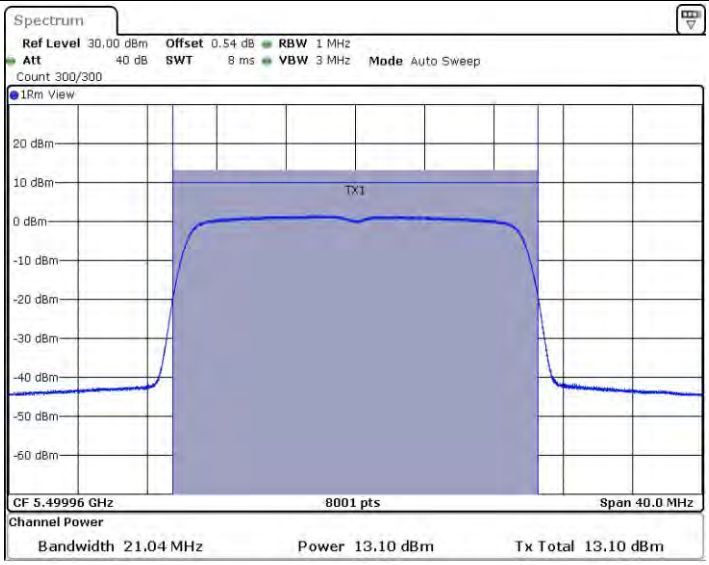

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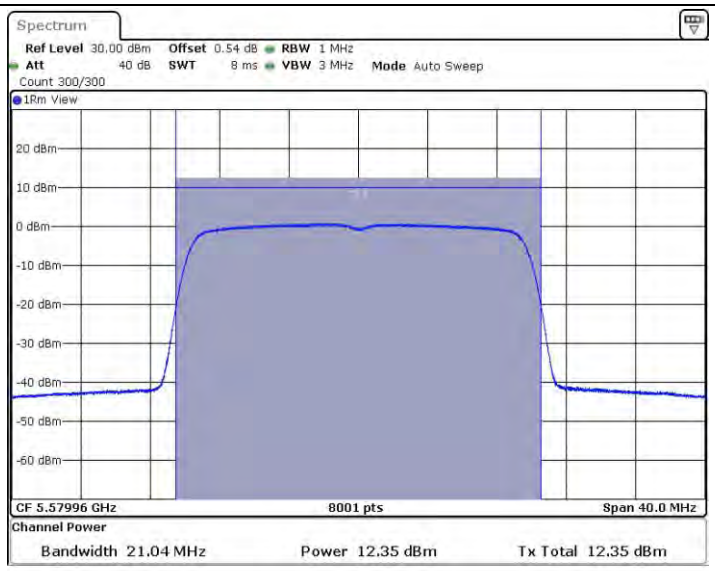

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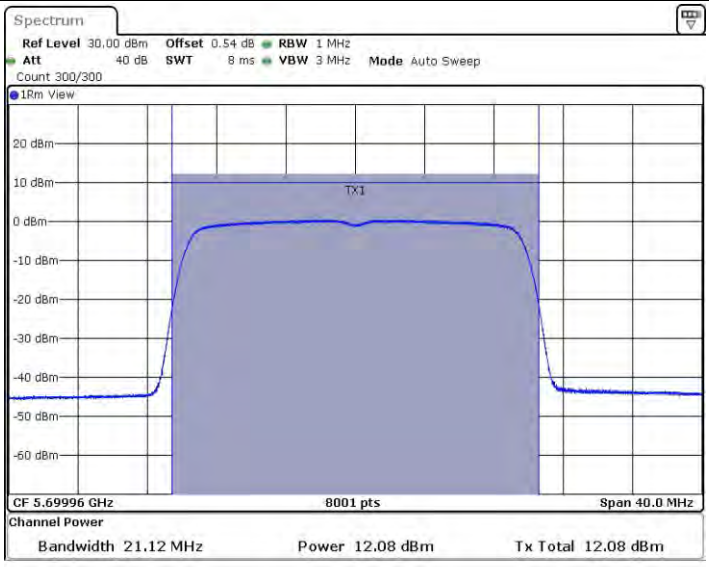

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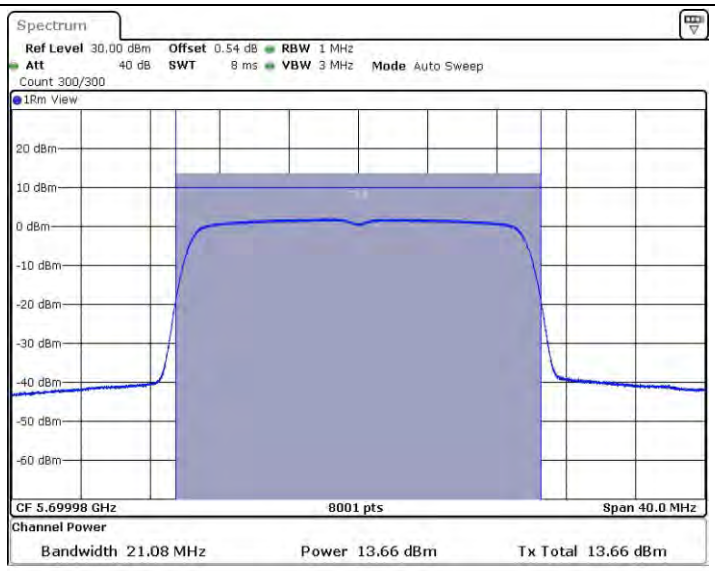

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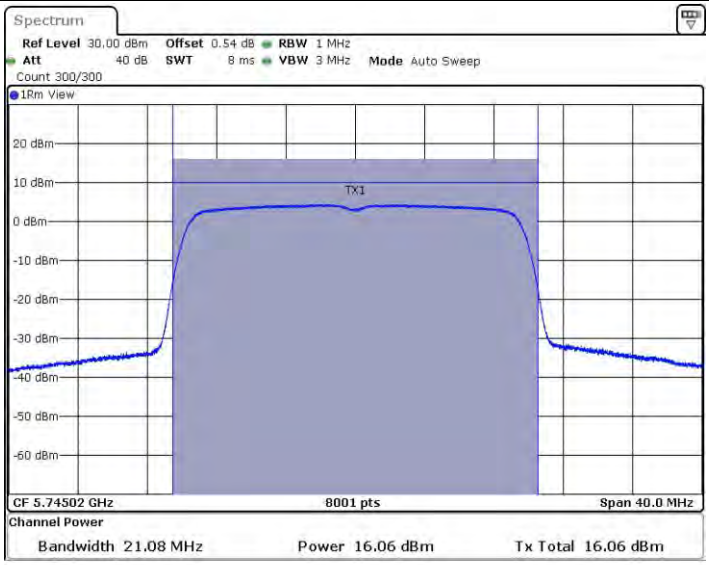

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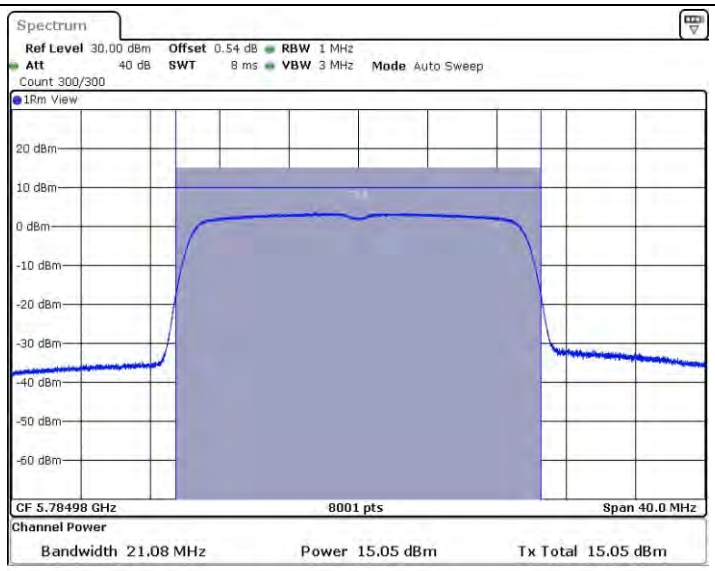

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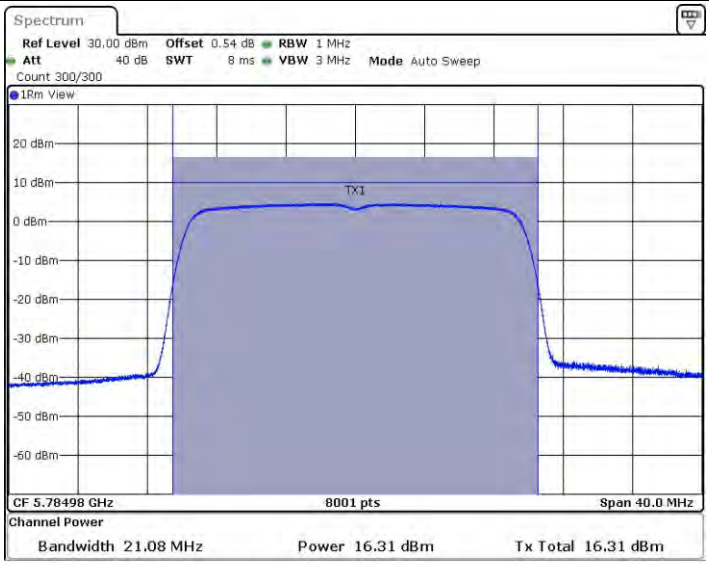

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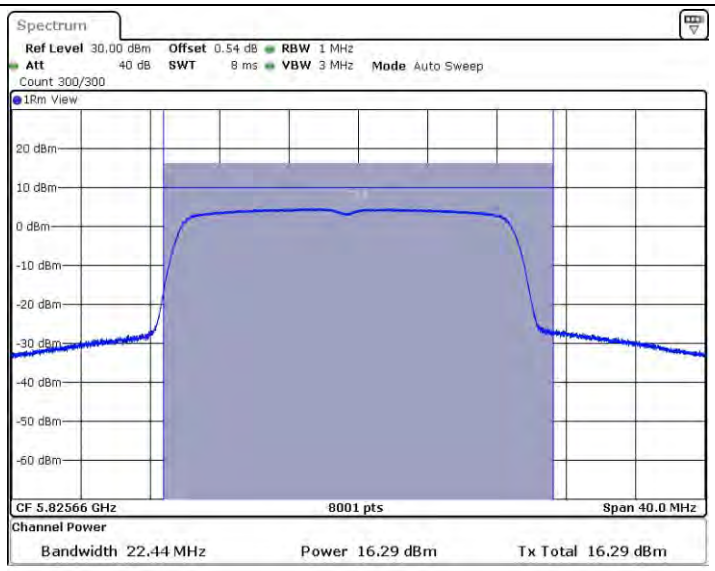

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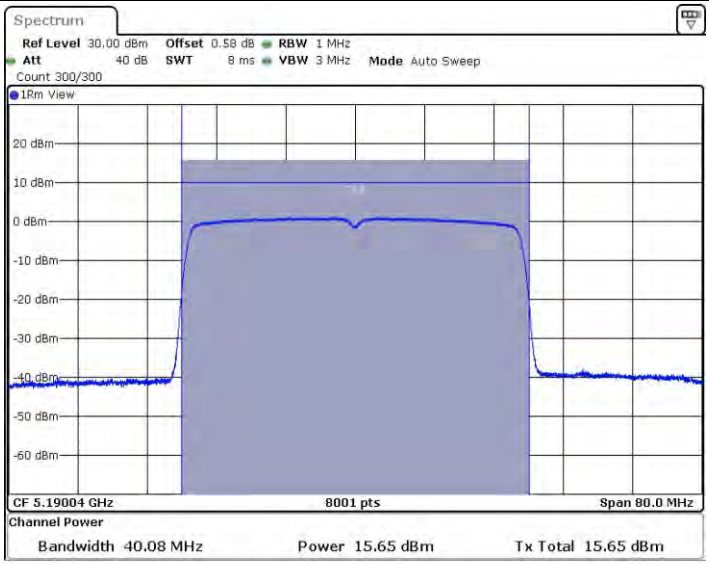

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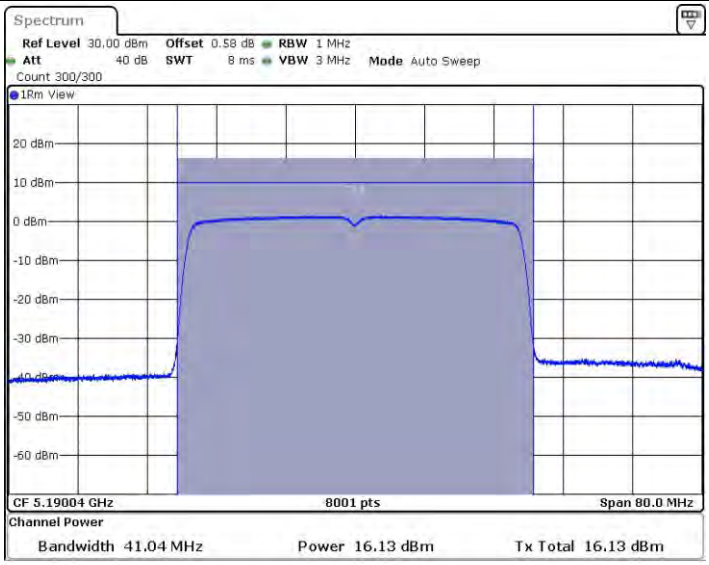

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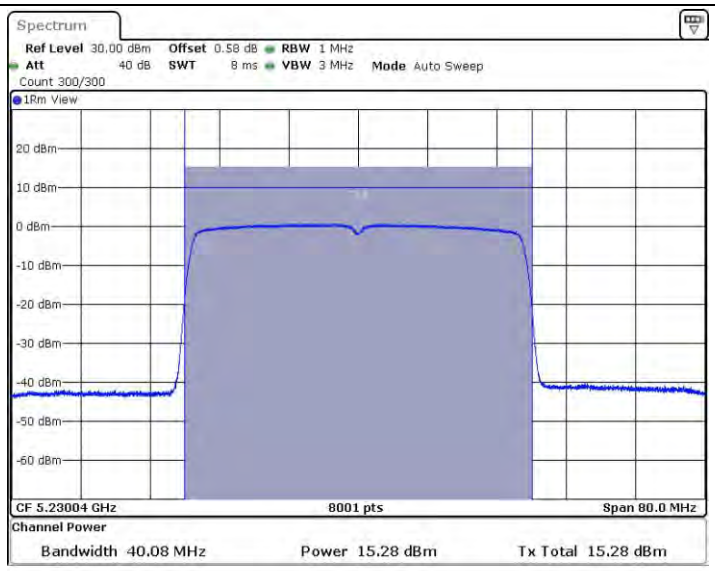

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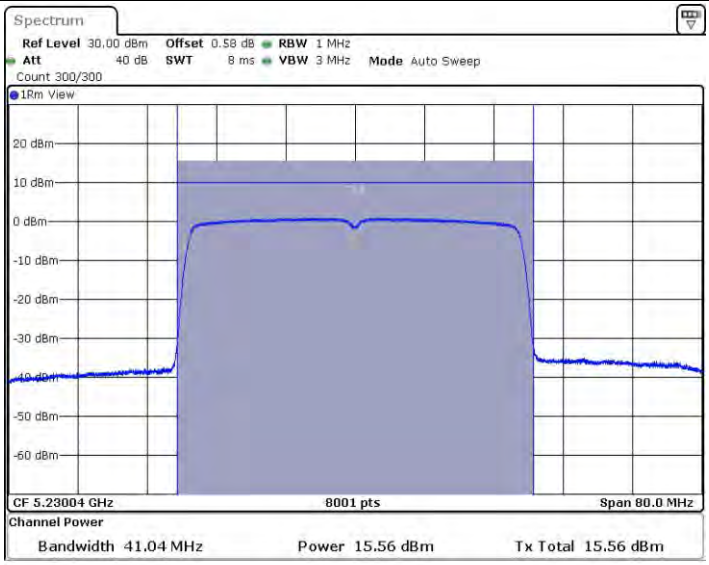

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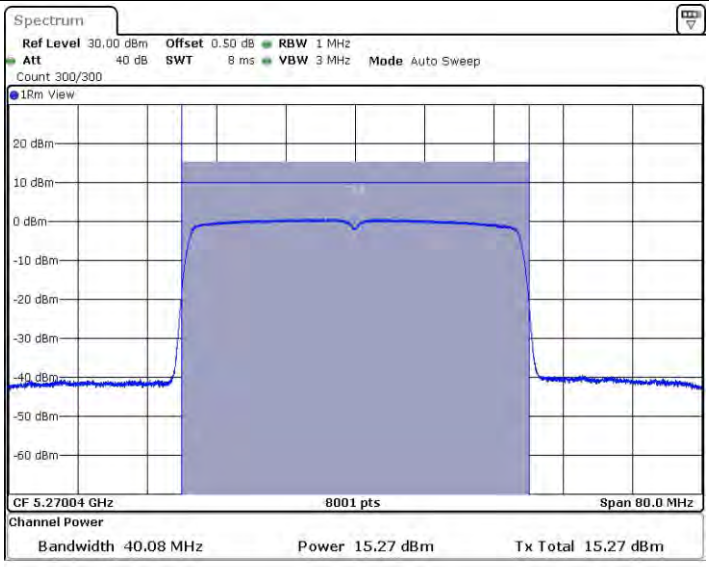

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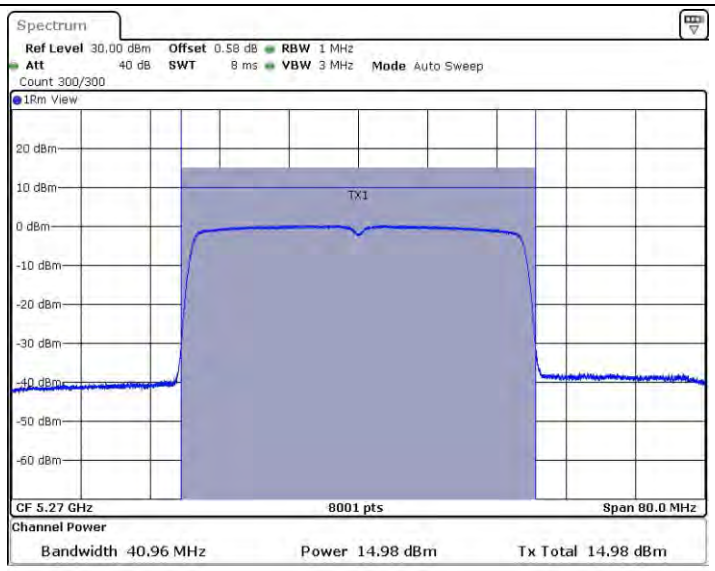

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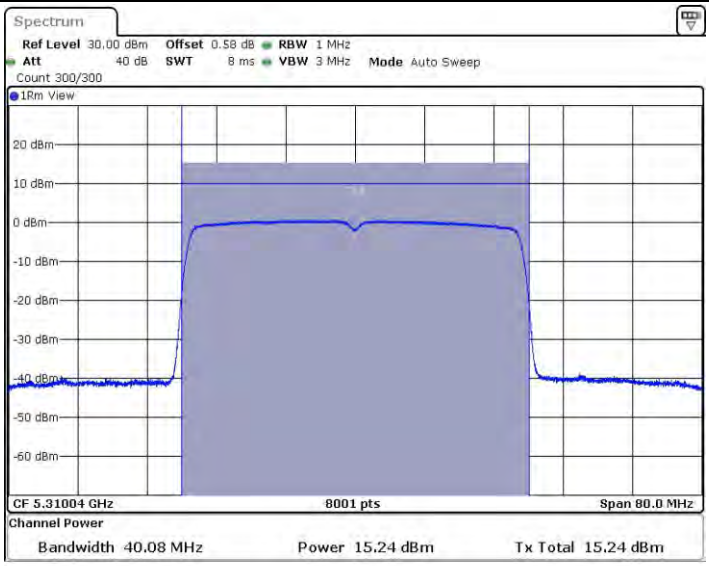

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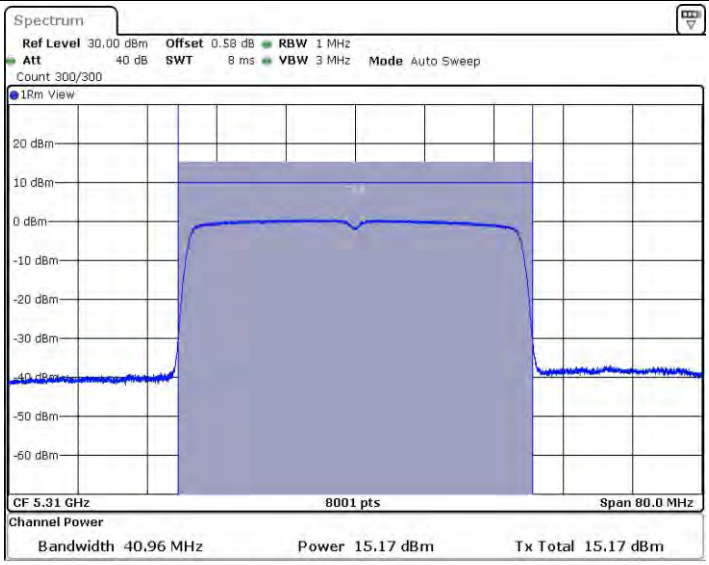

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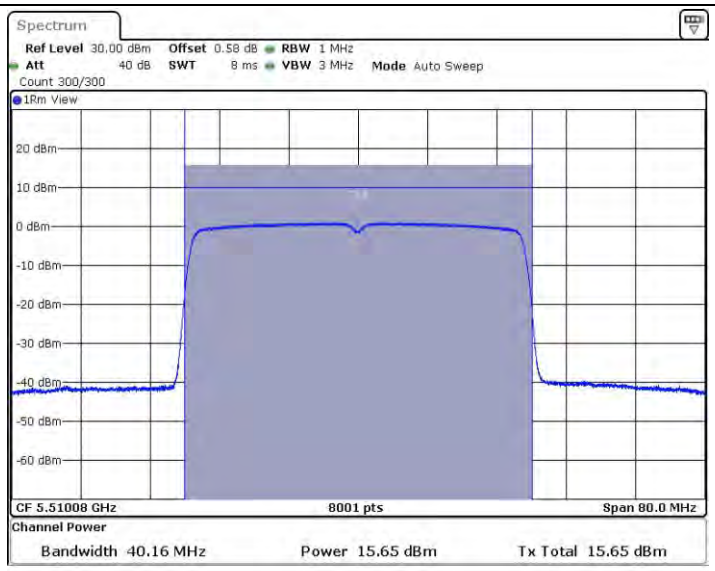

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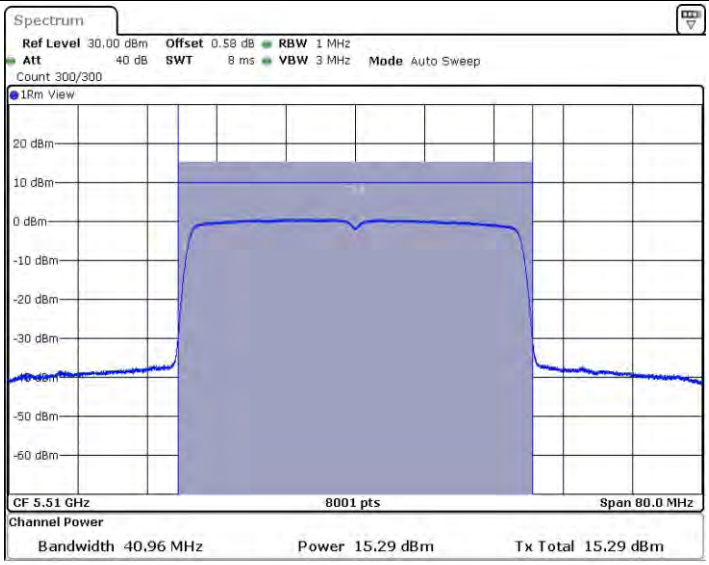

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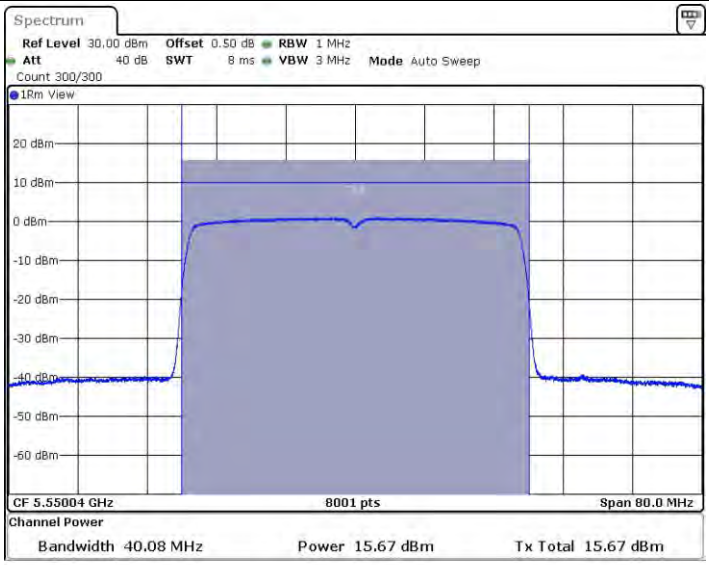

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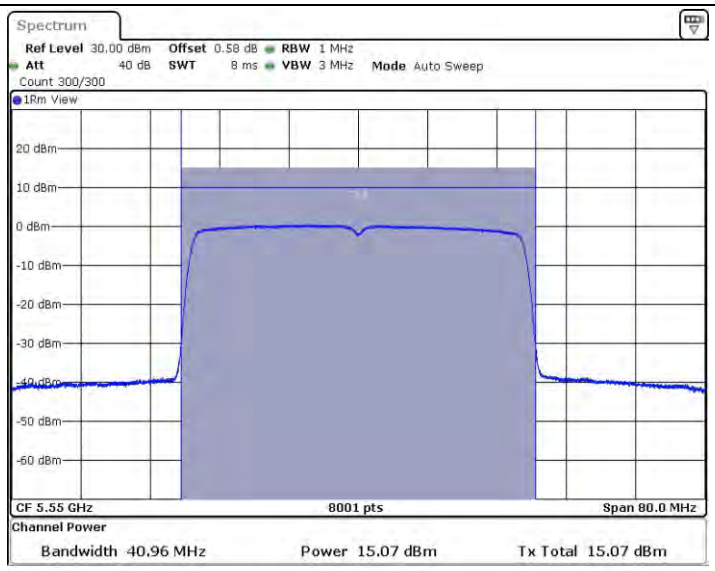

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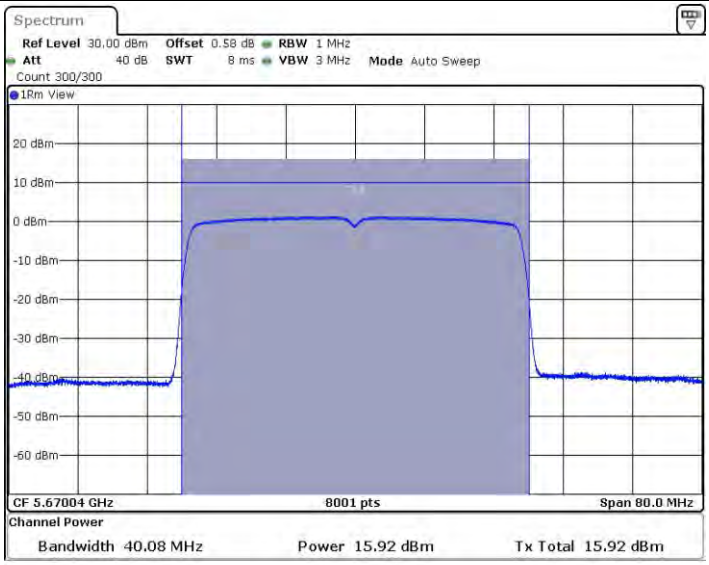

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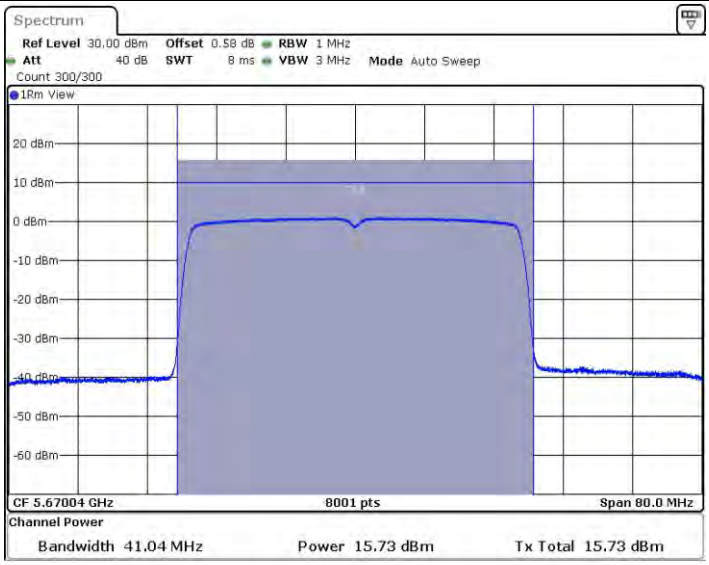

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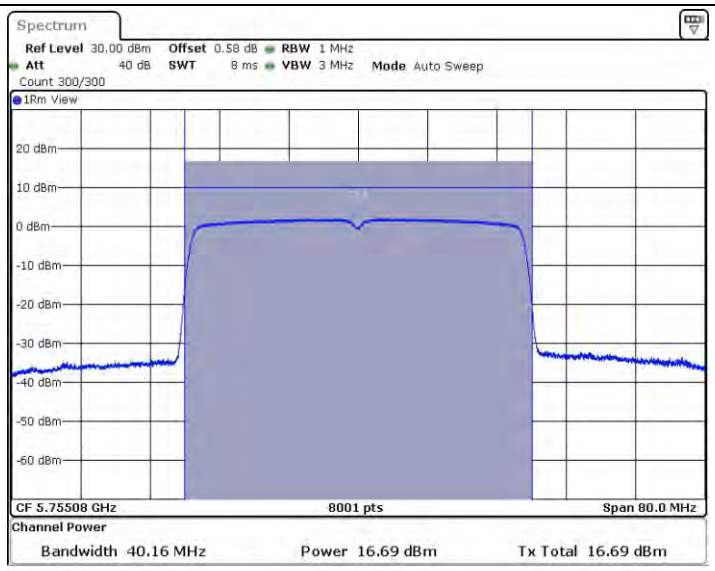

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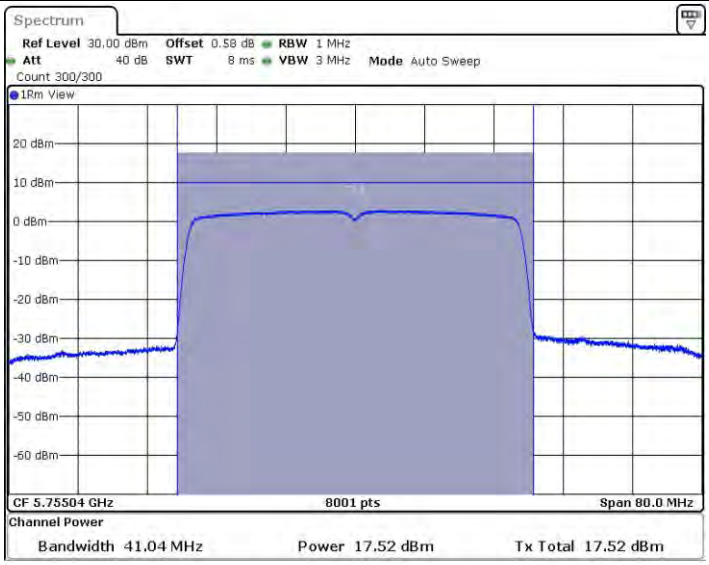

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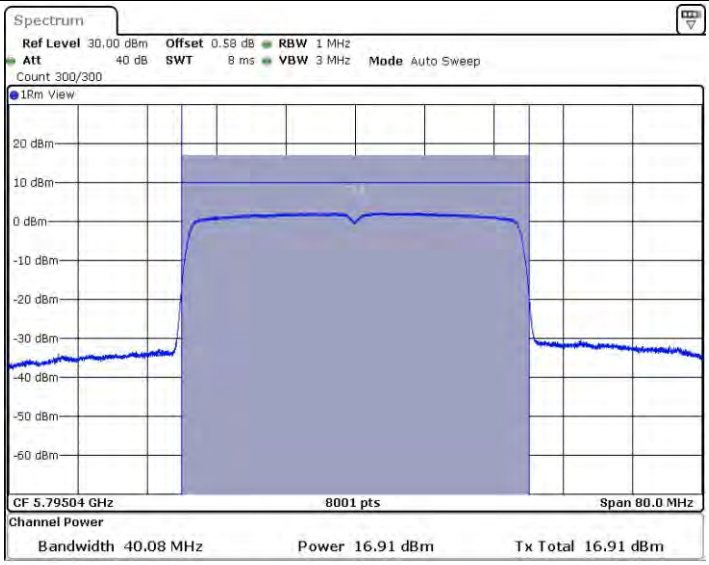

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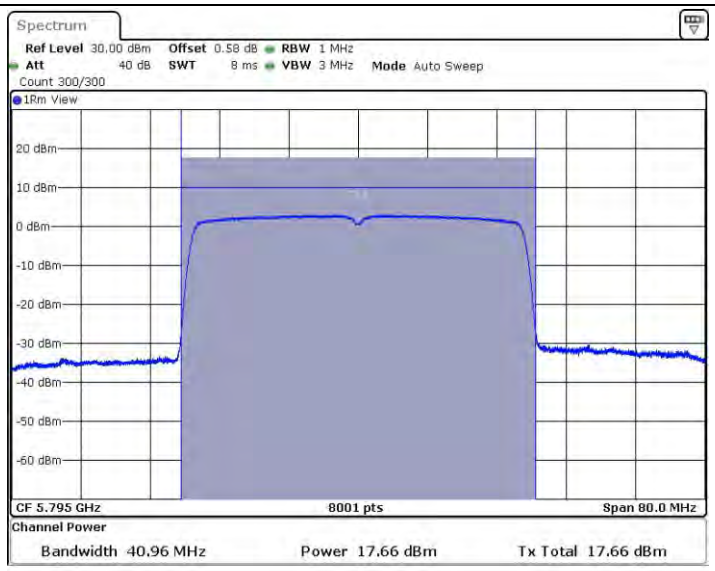

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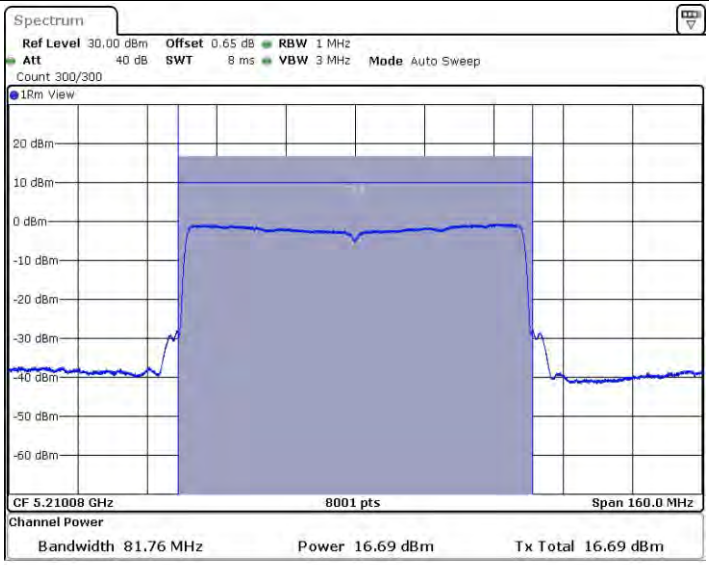

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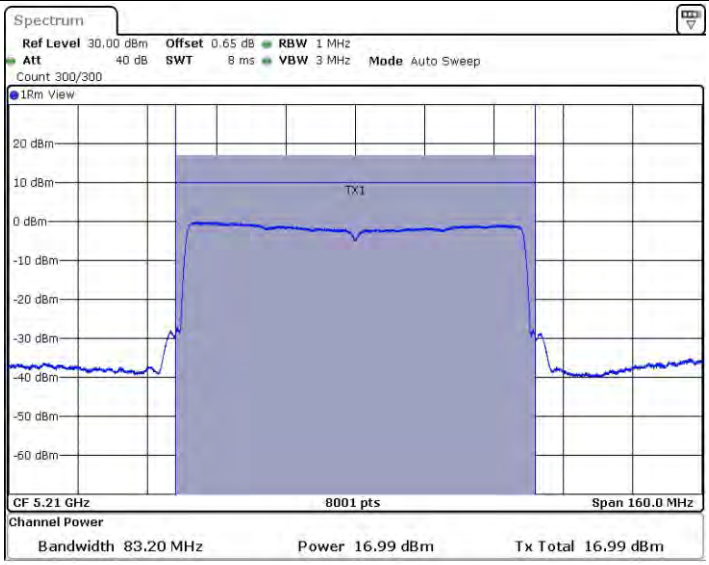

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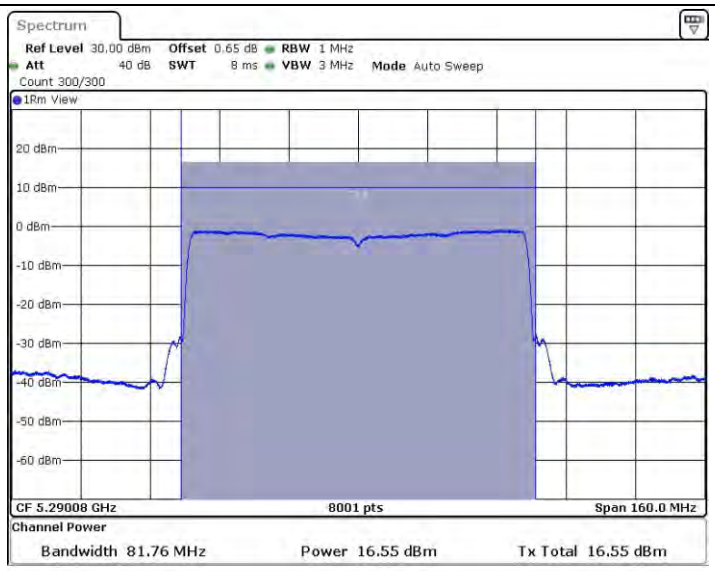

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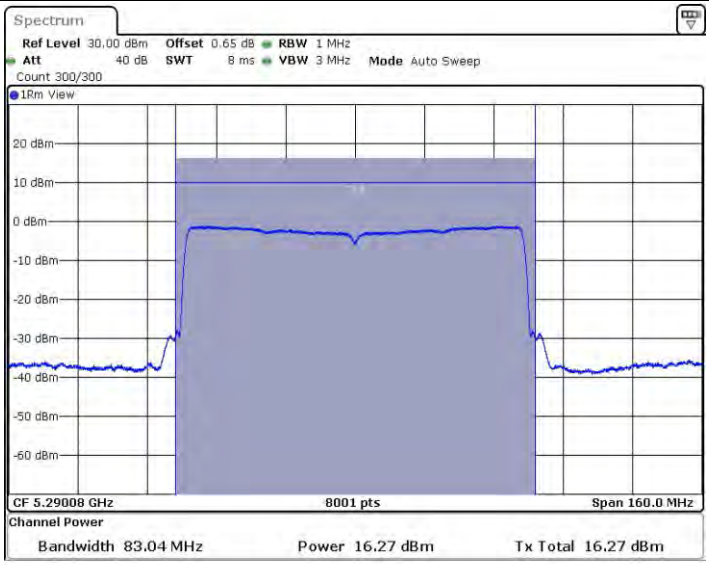

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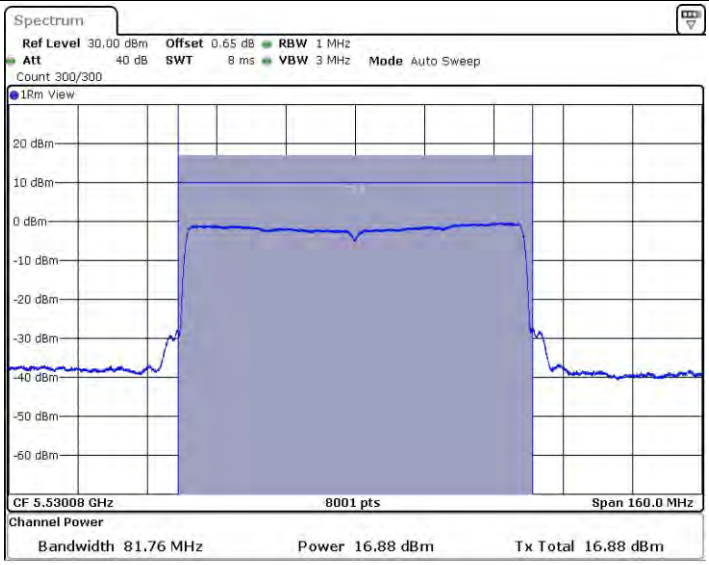

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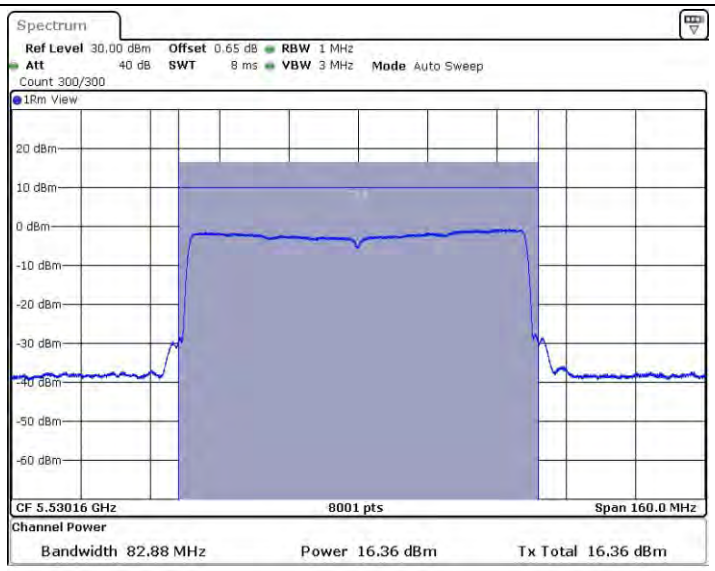

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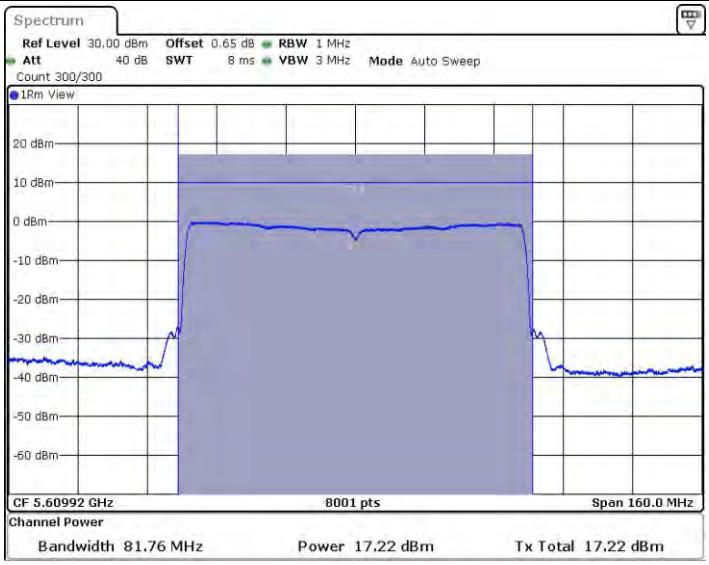

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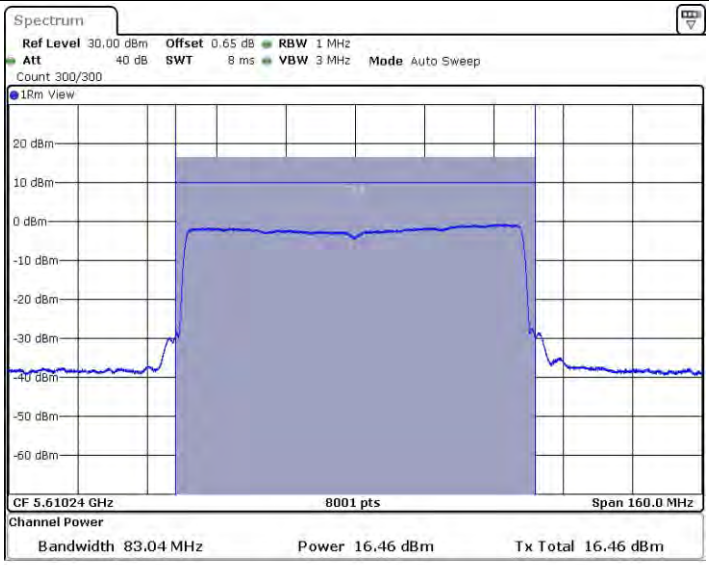

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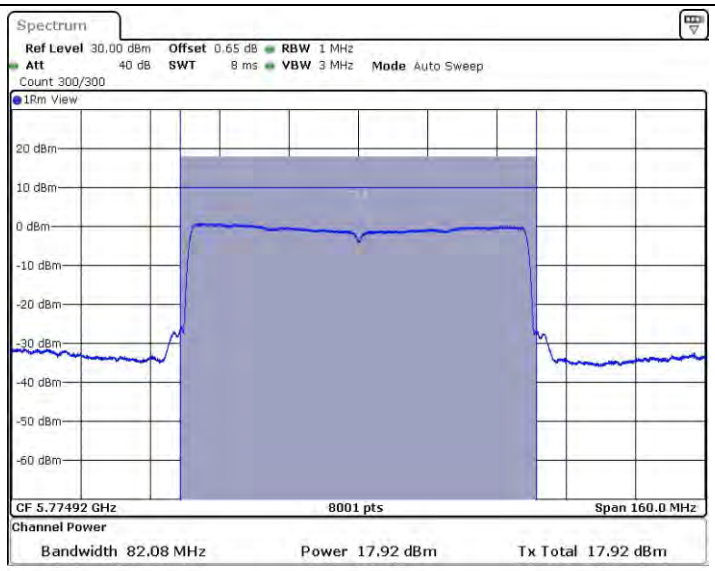

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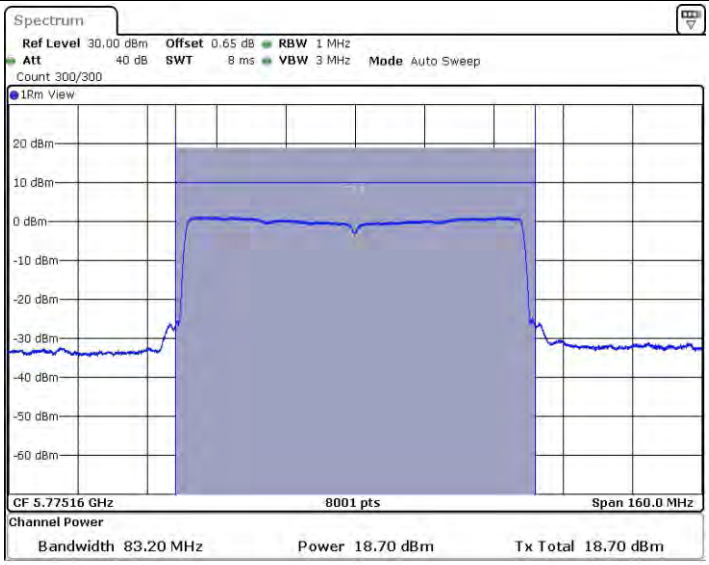

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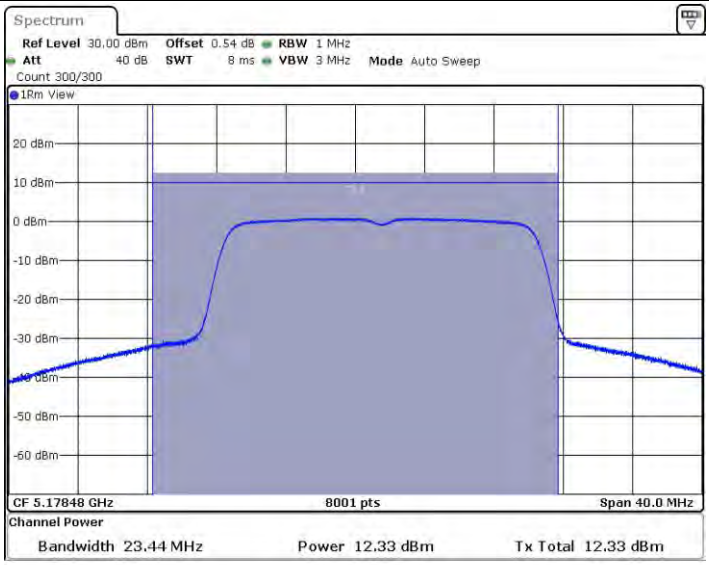

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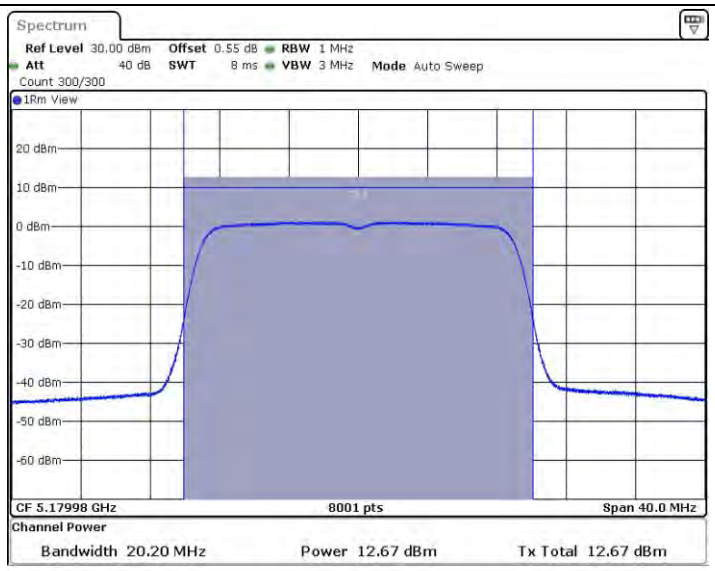

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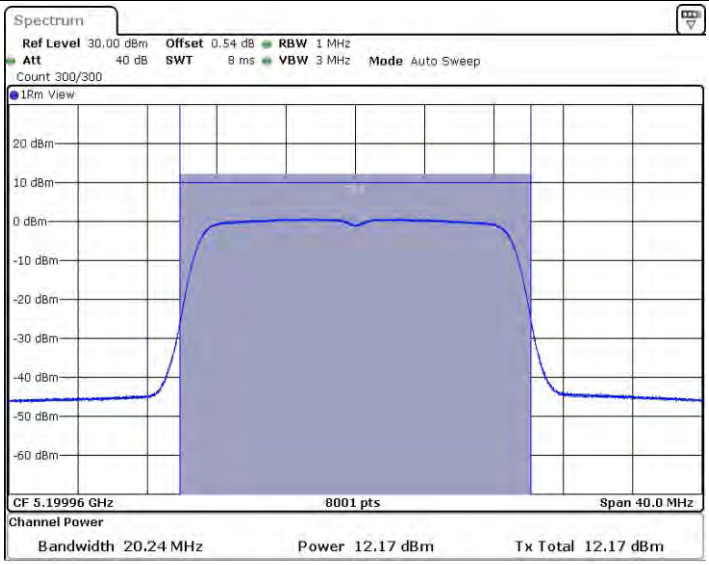

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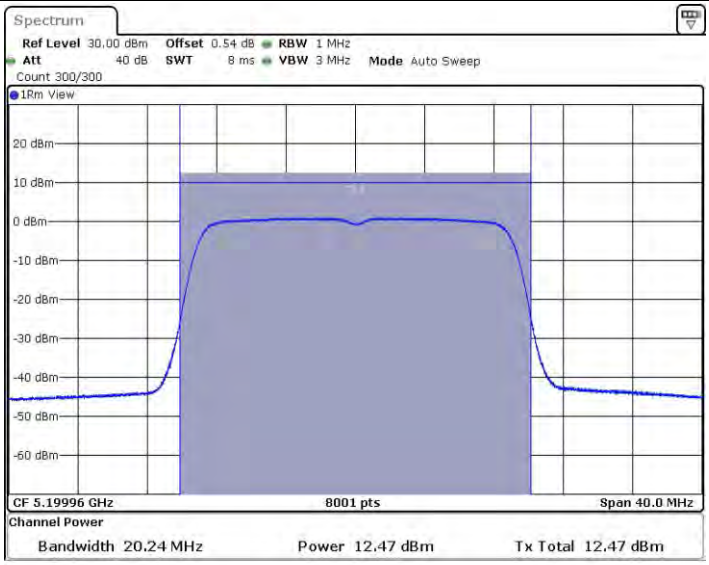

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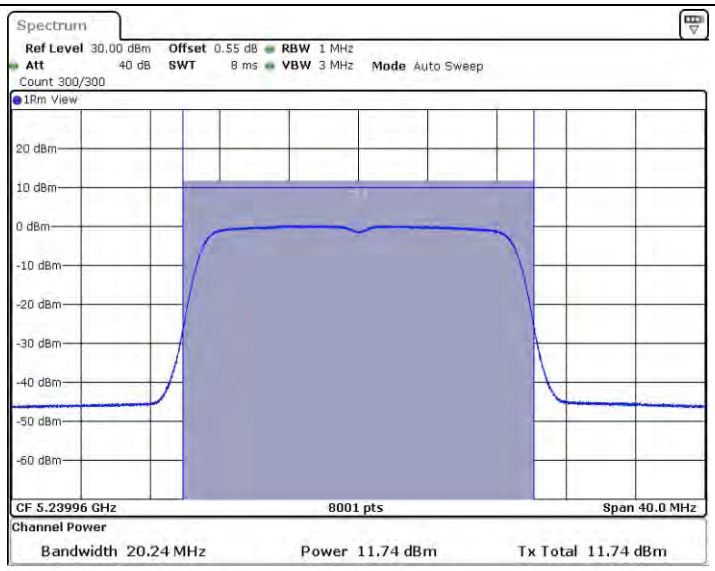

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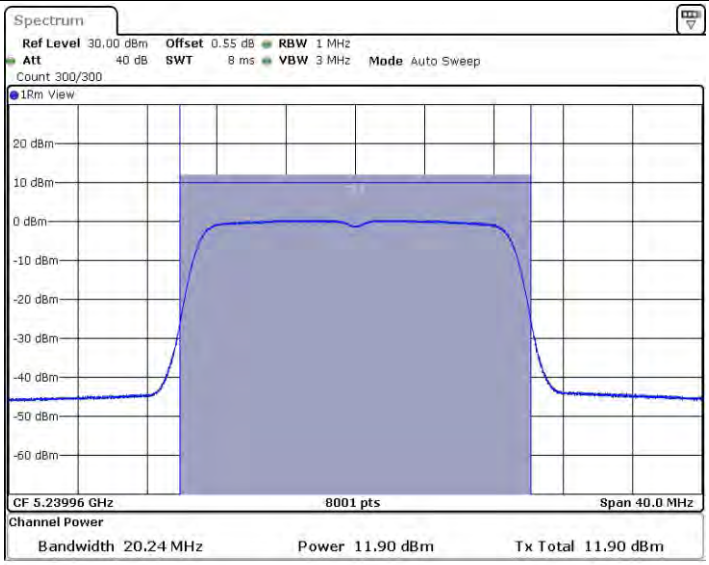

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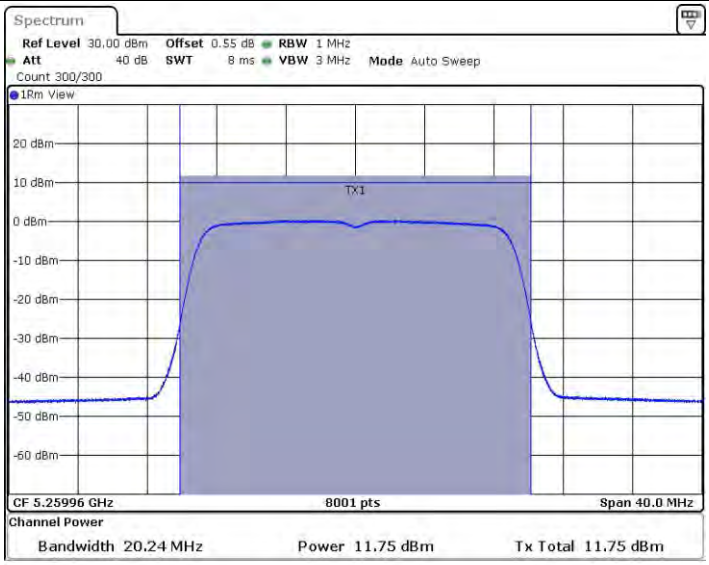

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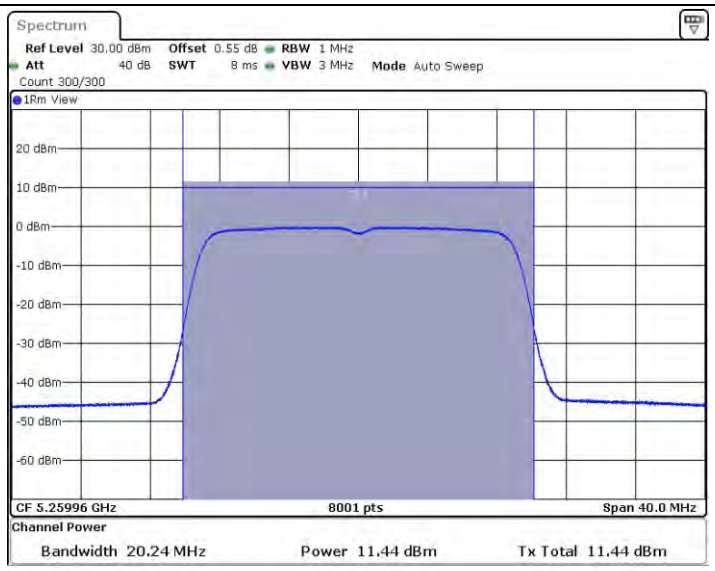

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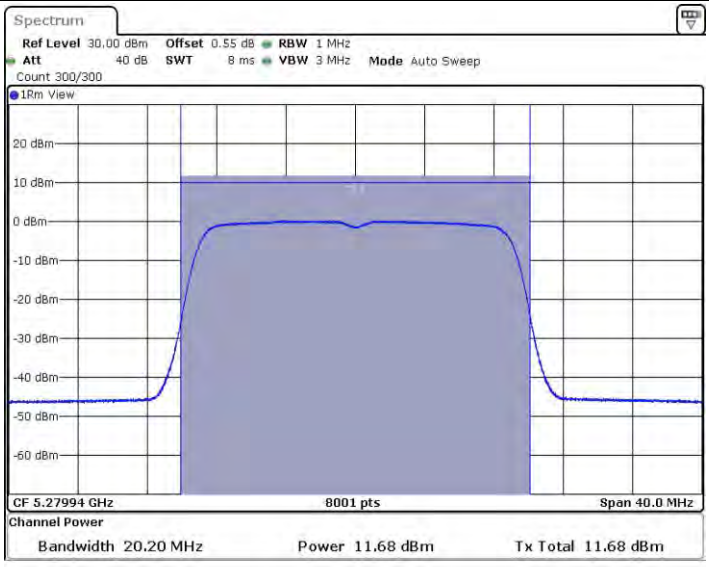

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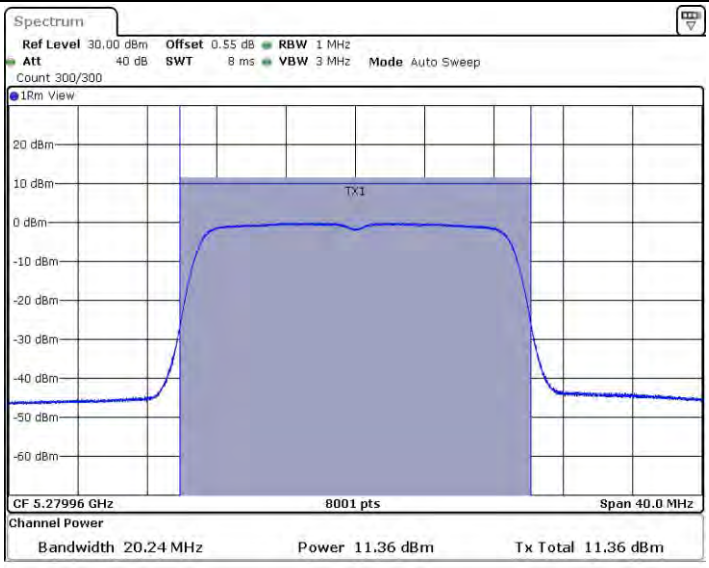

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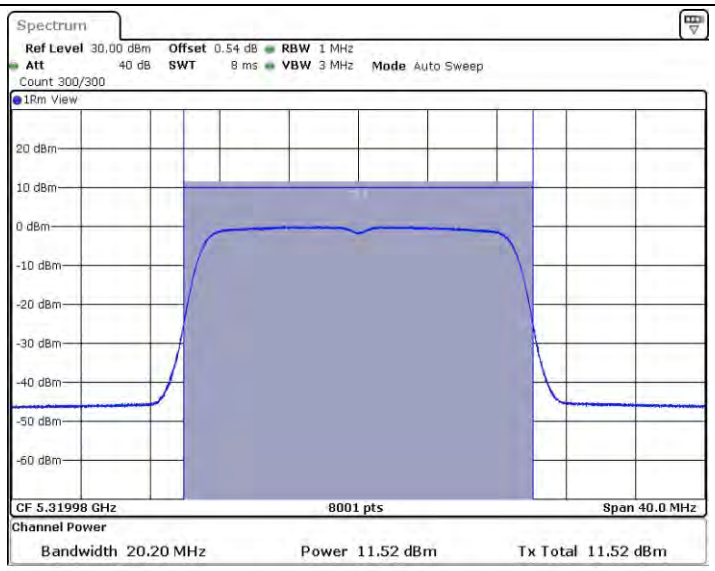

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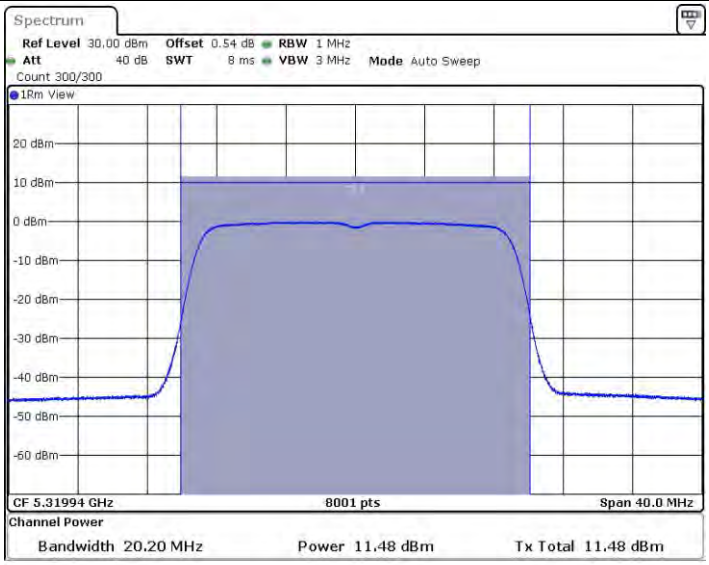

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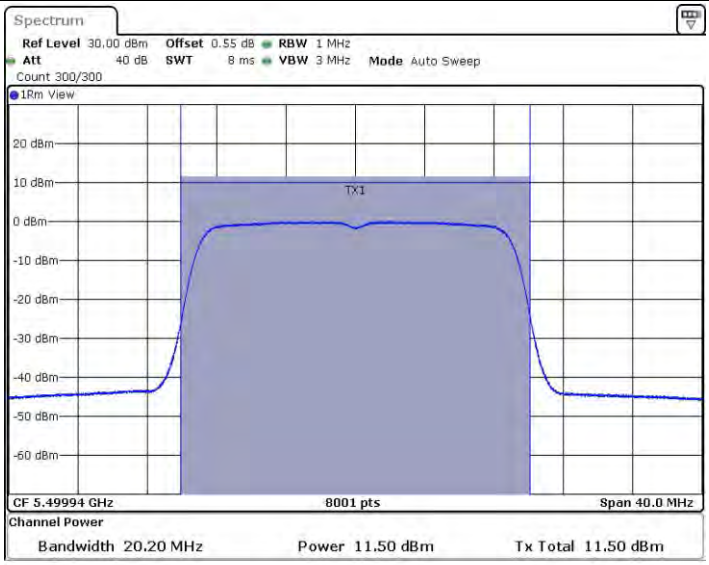
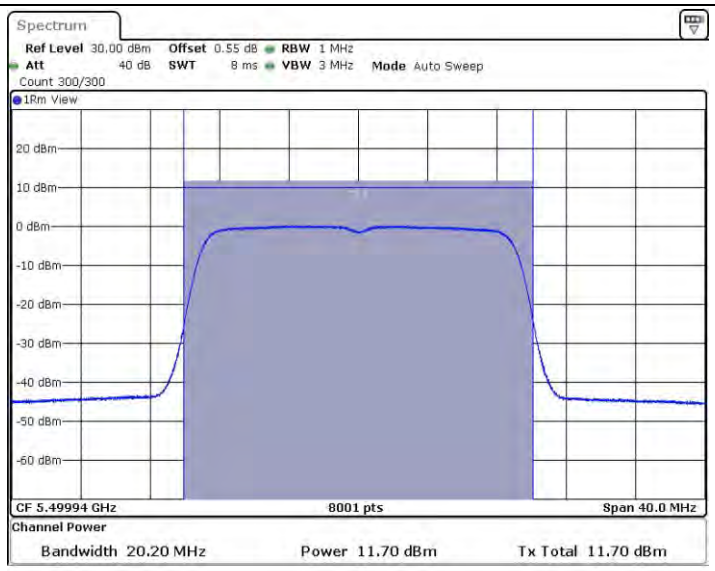


11N20MIMO_Ant0_5500



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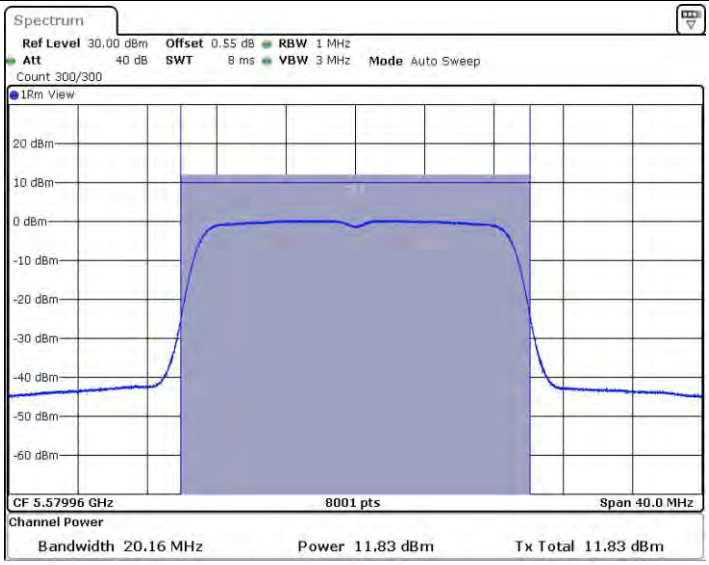

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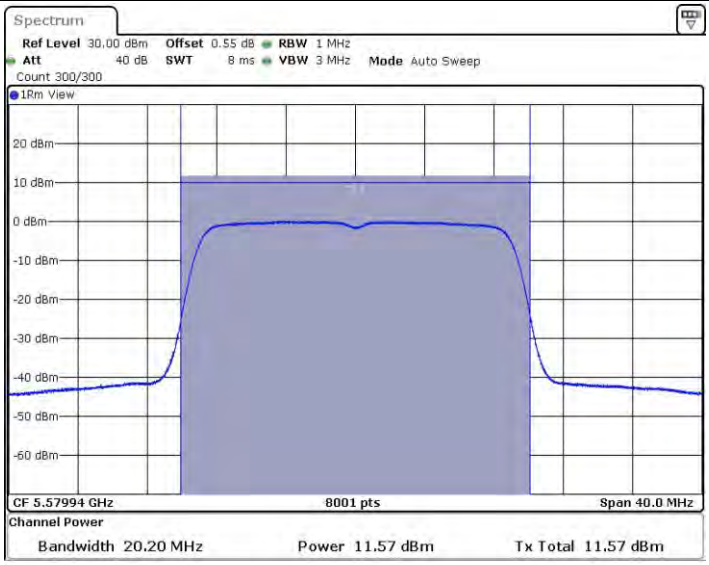

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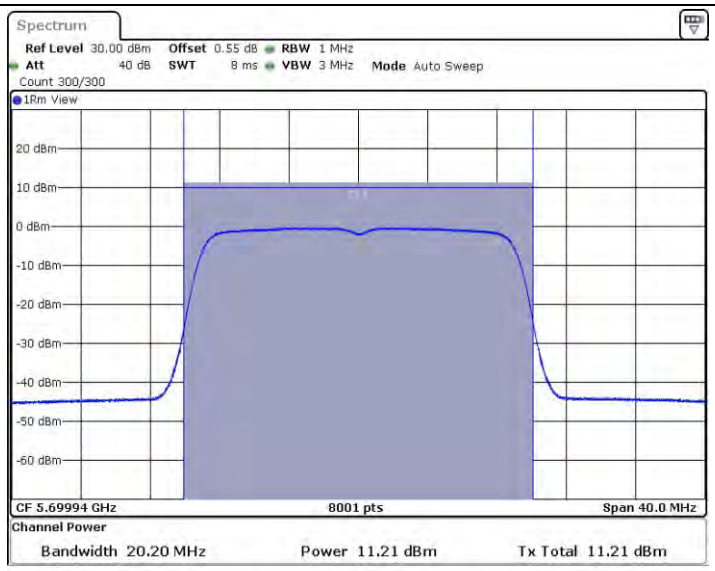

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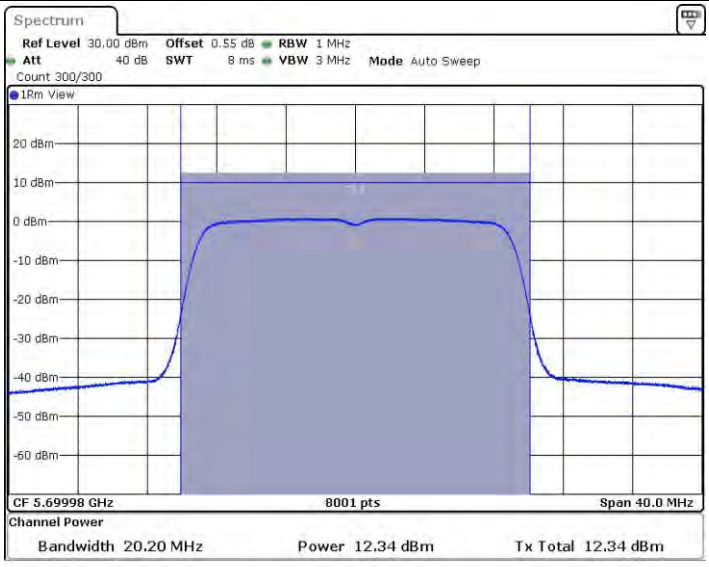

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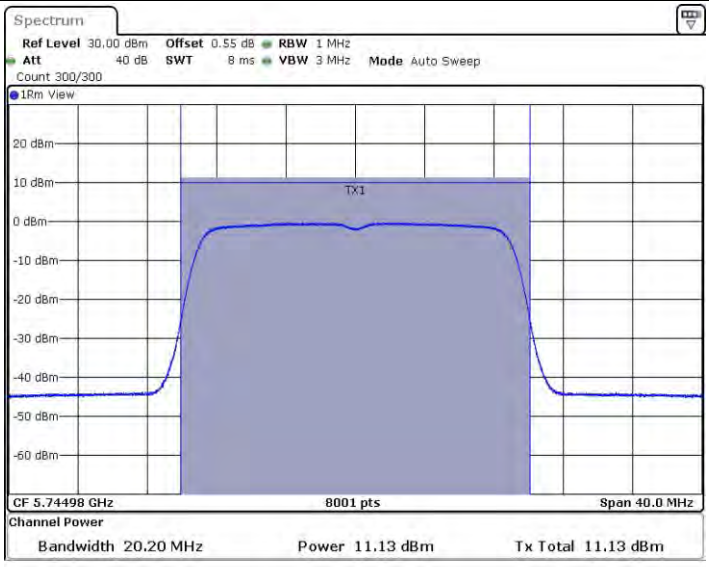

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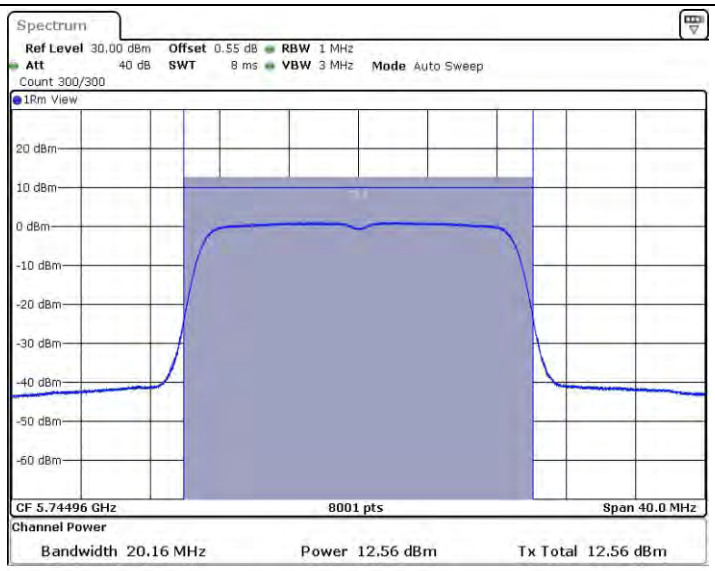

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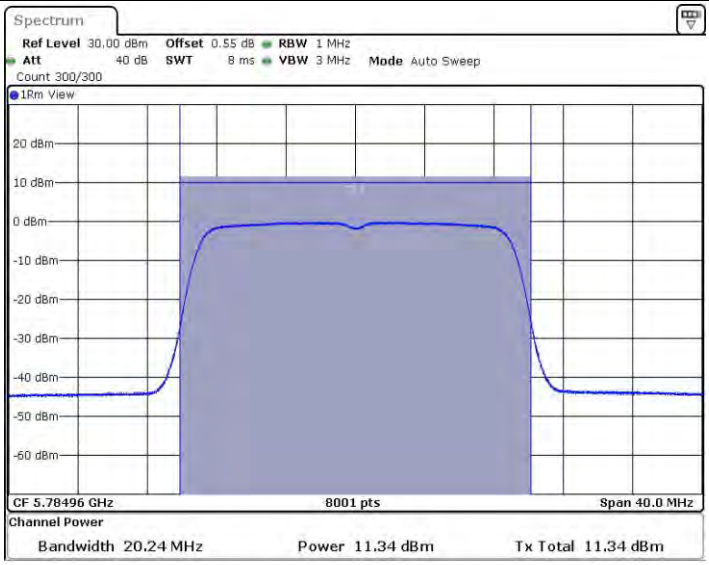

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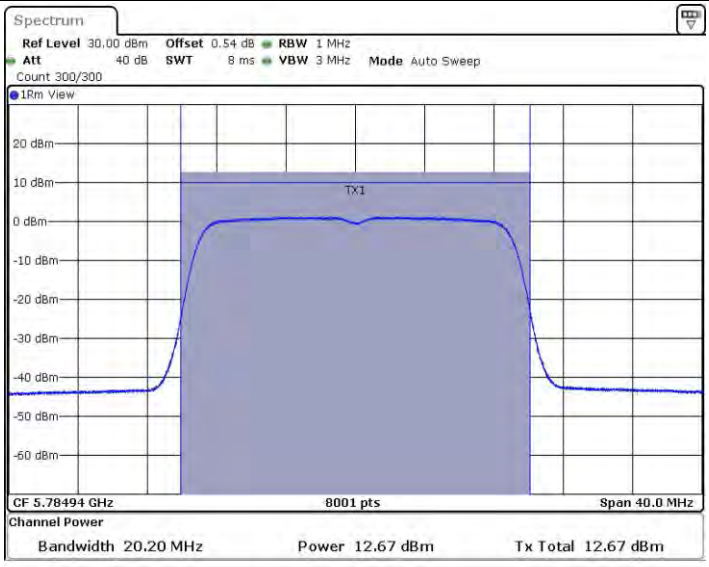

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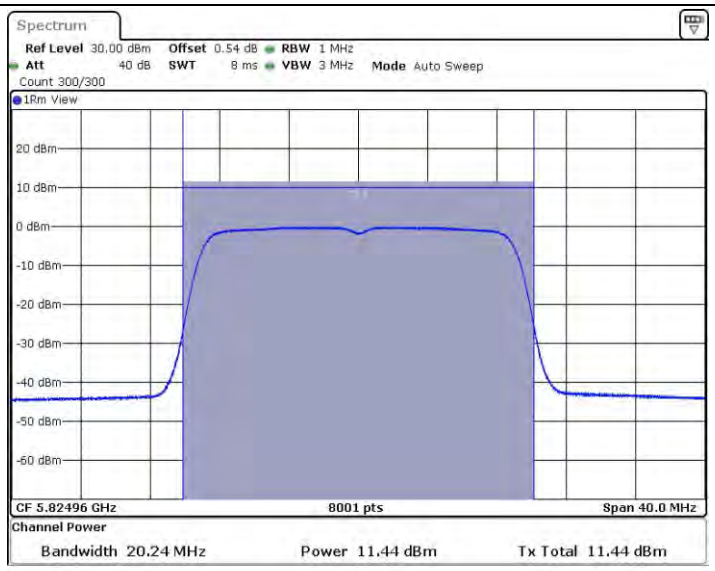

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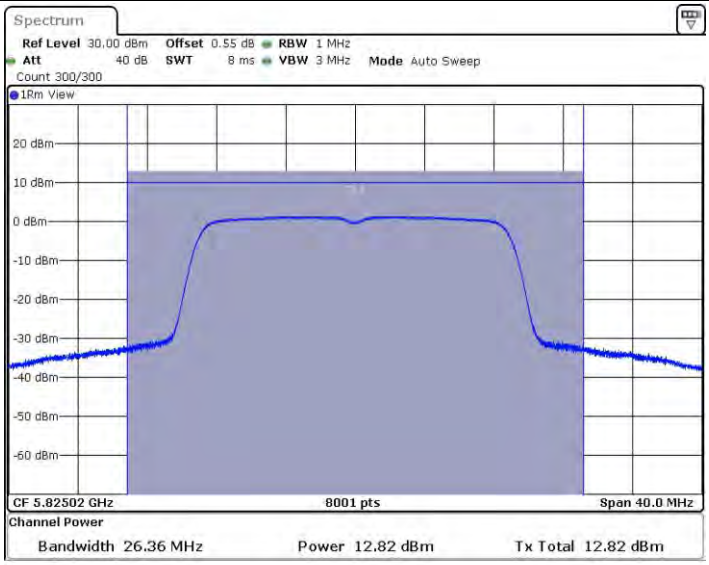

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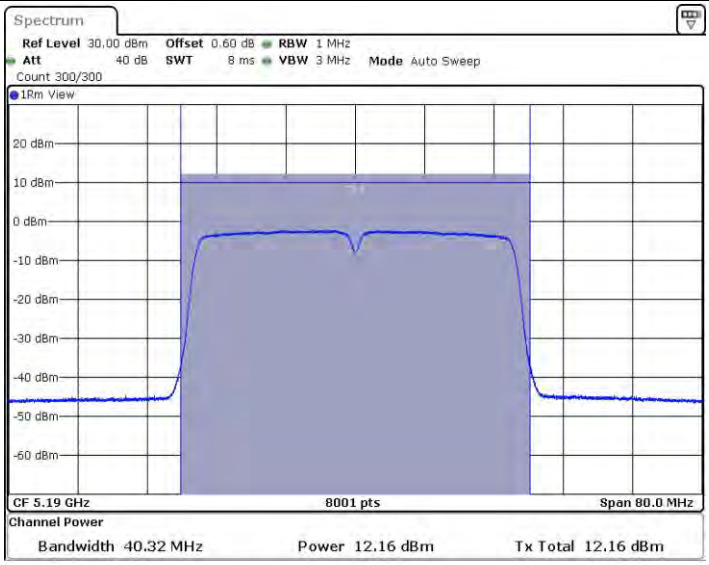

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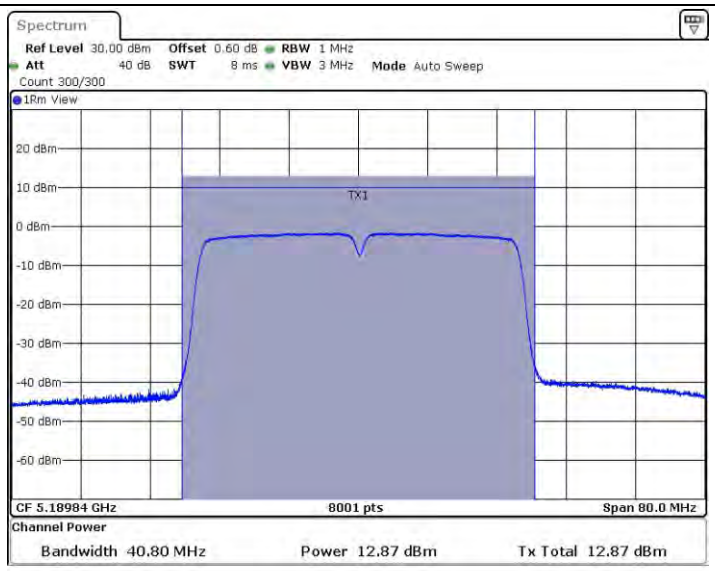

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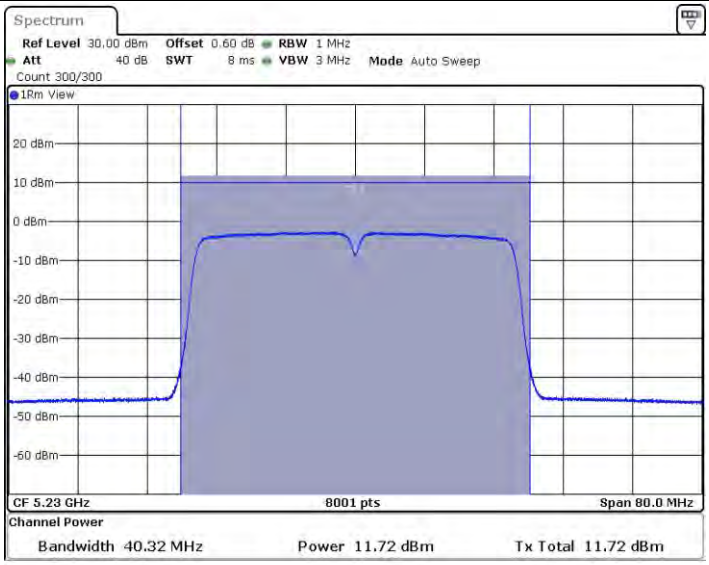

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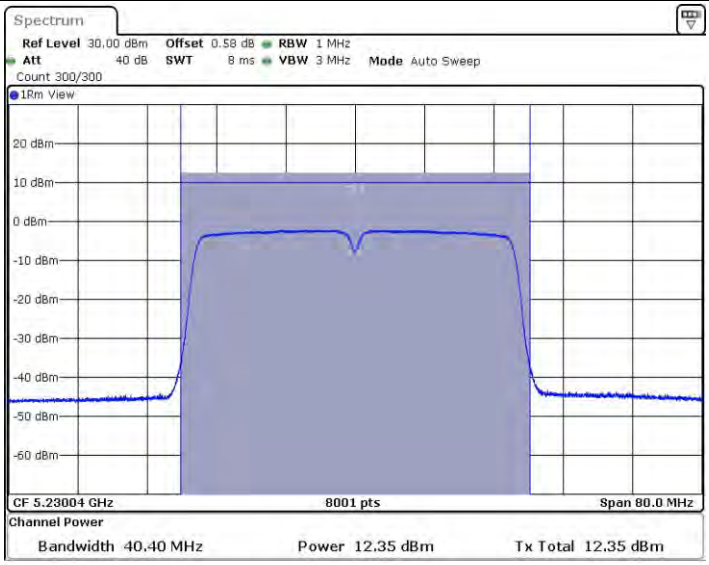

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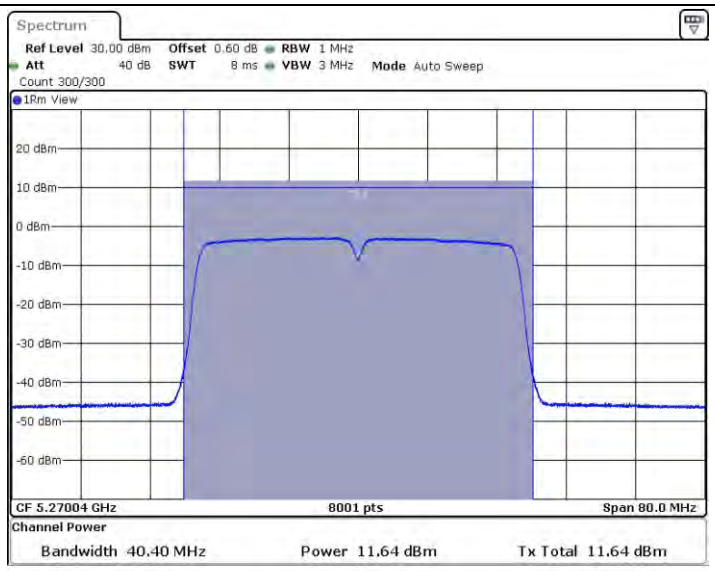

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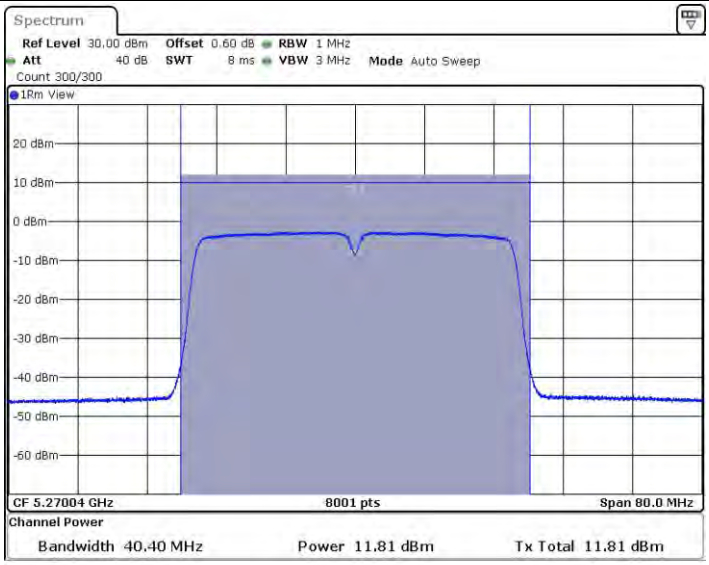

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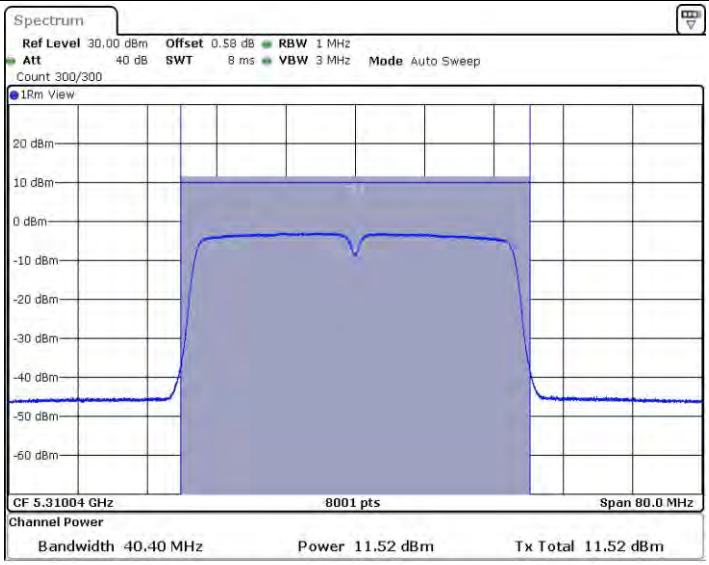

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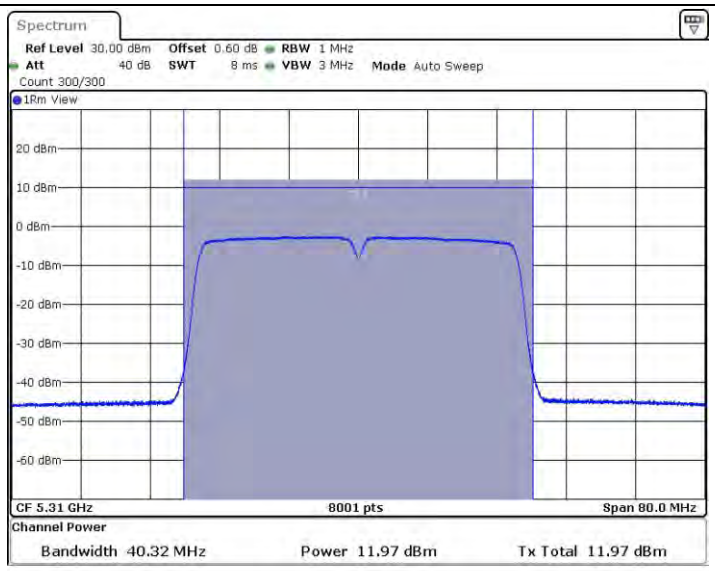

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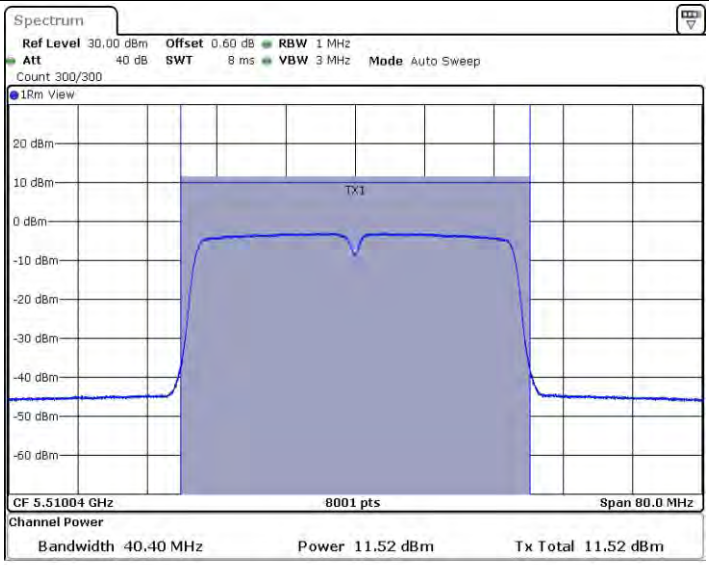

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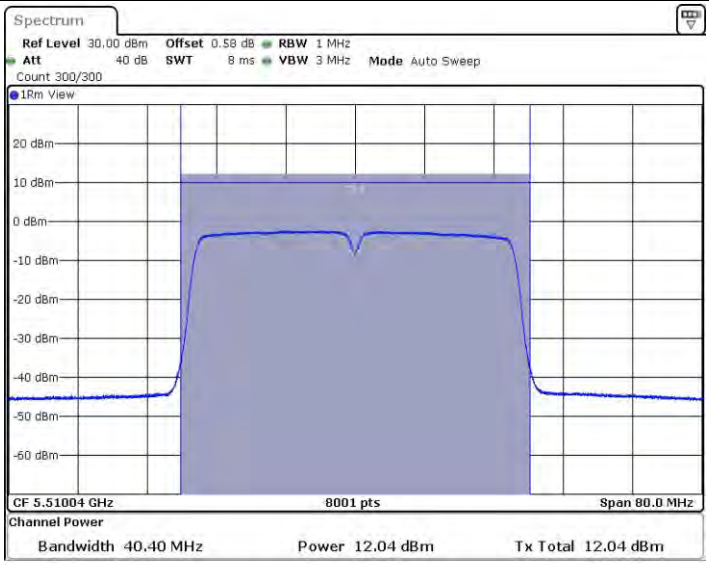

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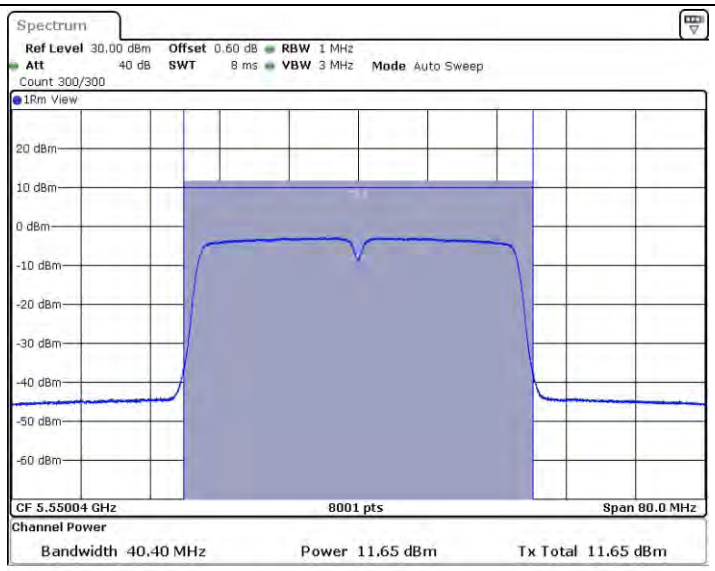

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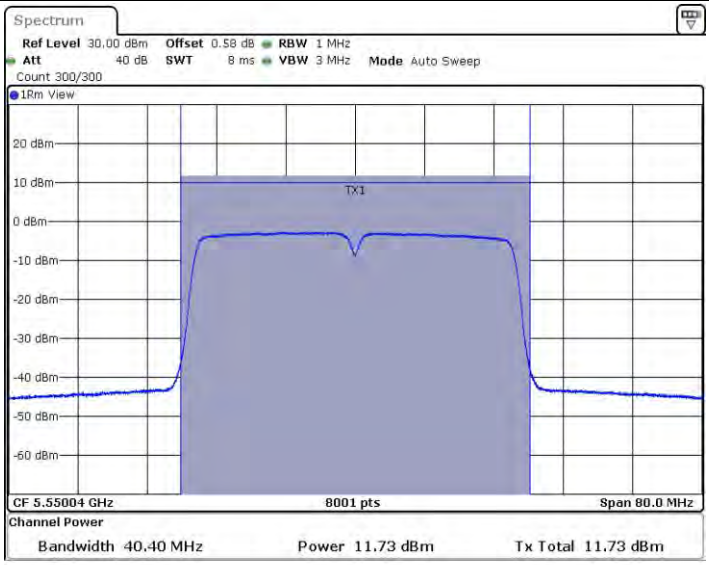

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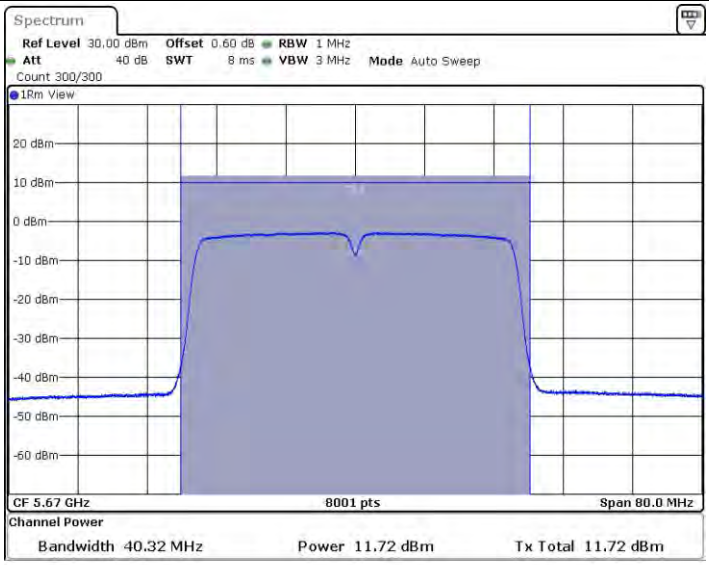

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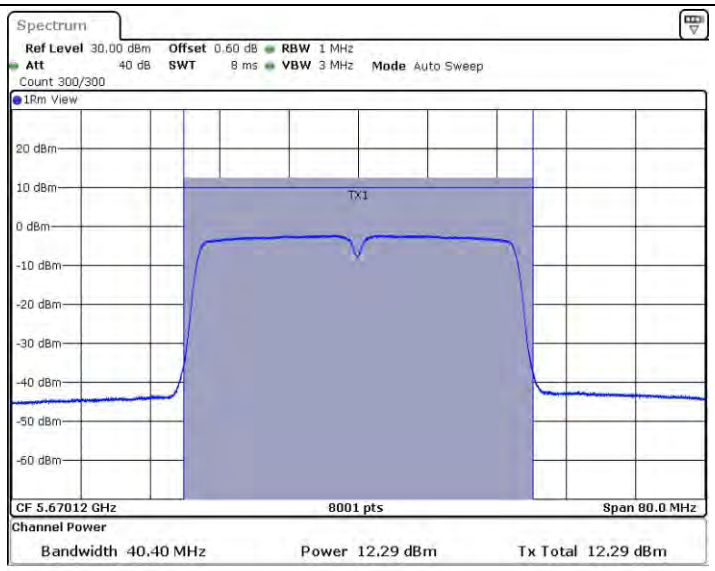

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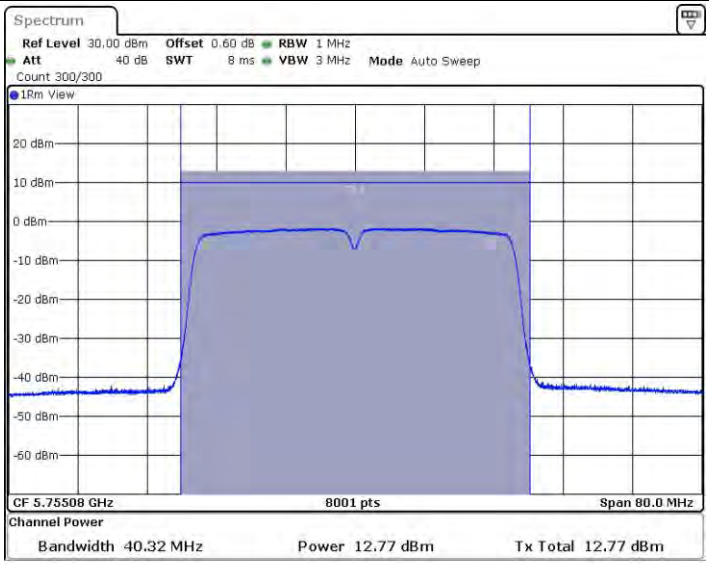

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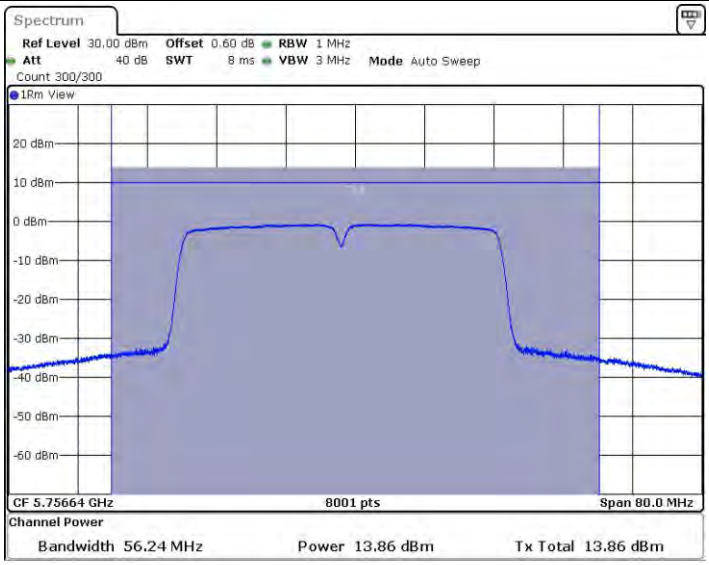

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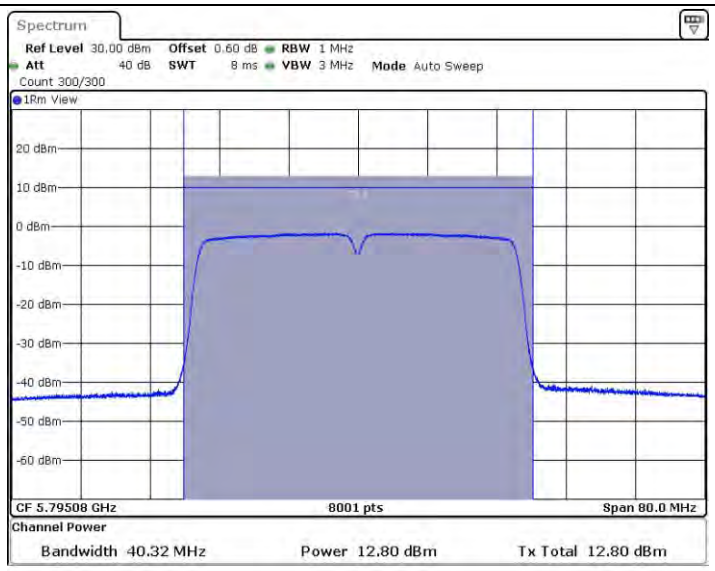

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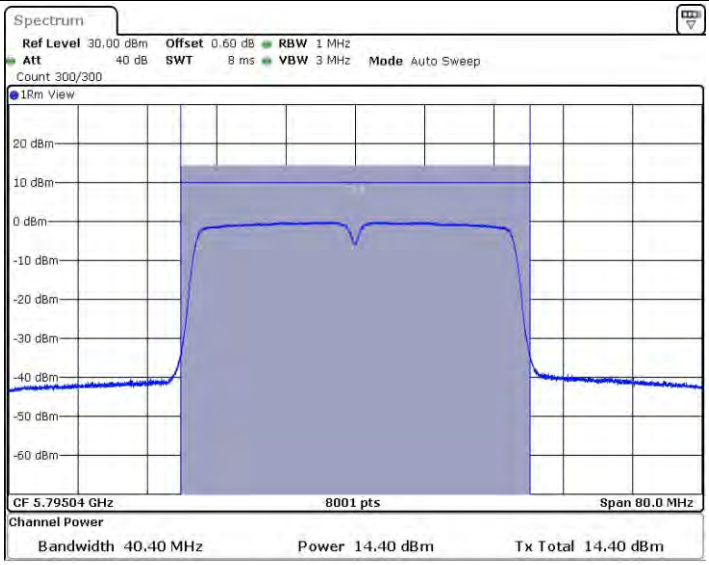

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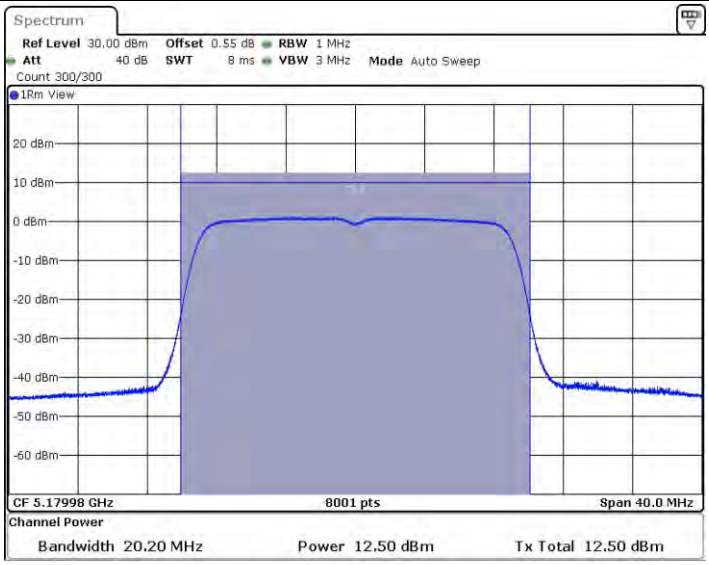

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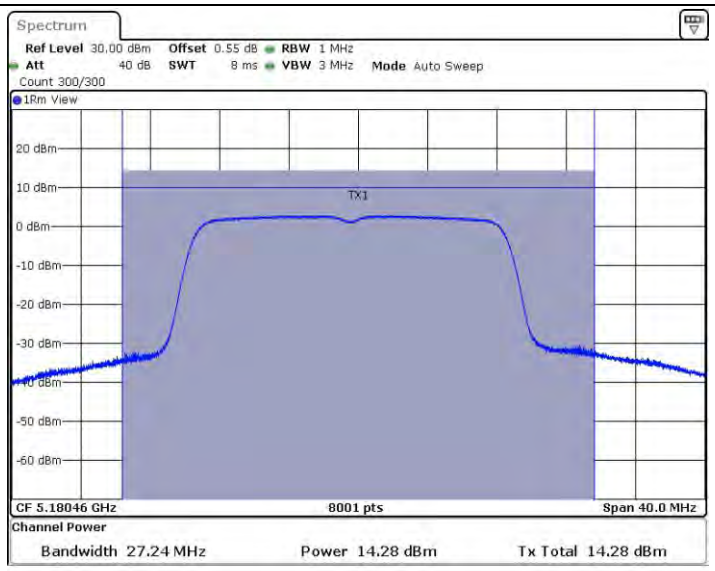

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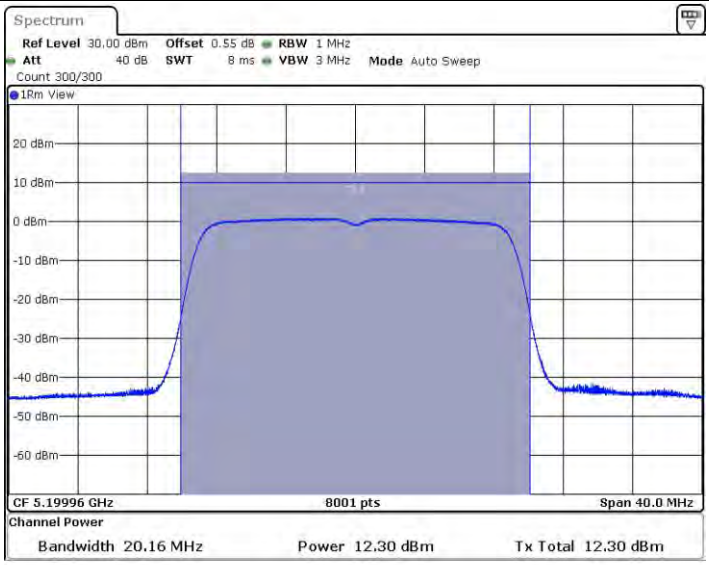

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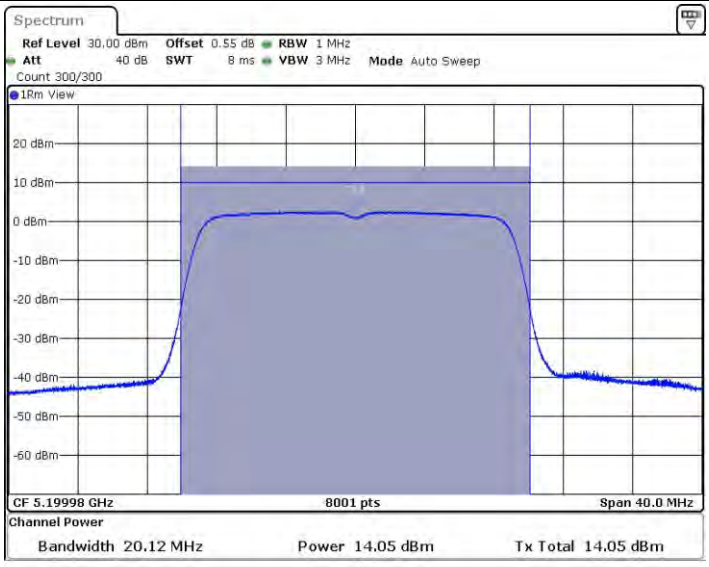

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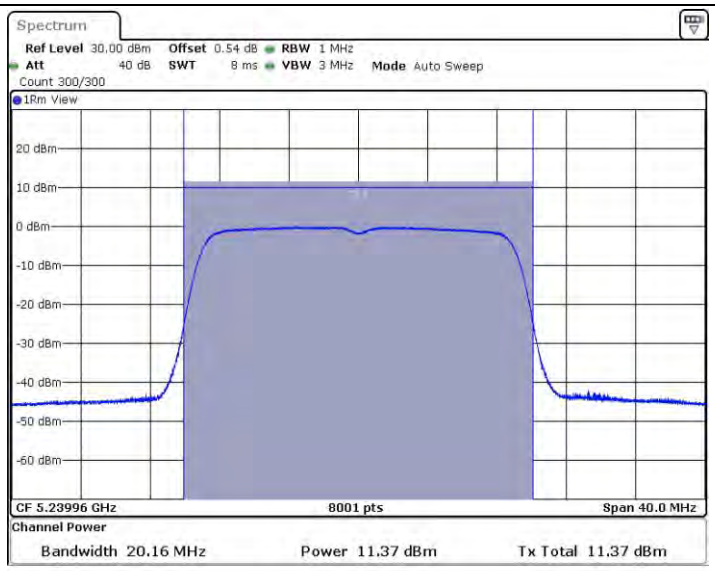

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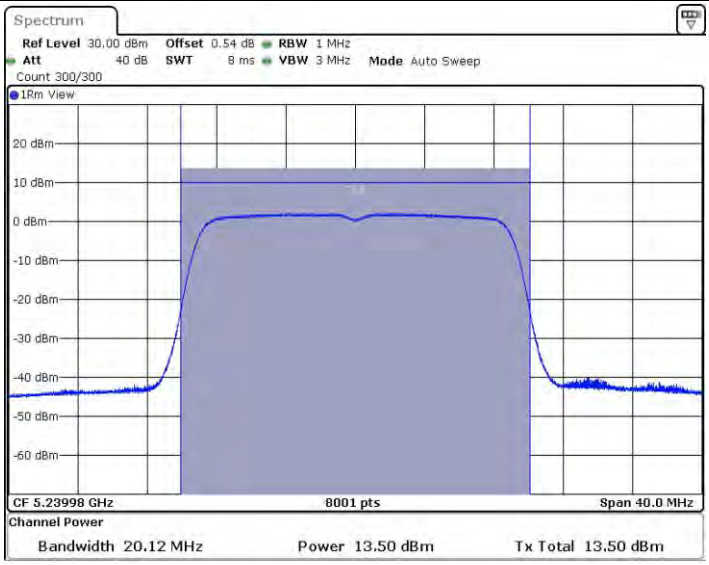

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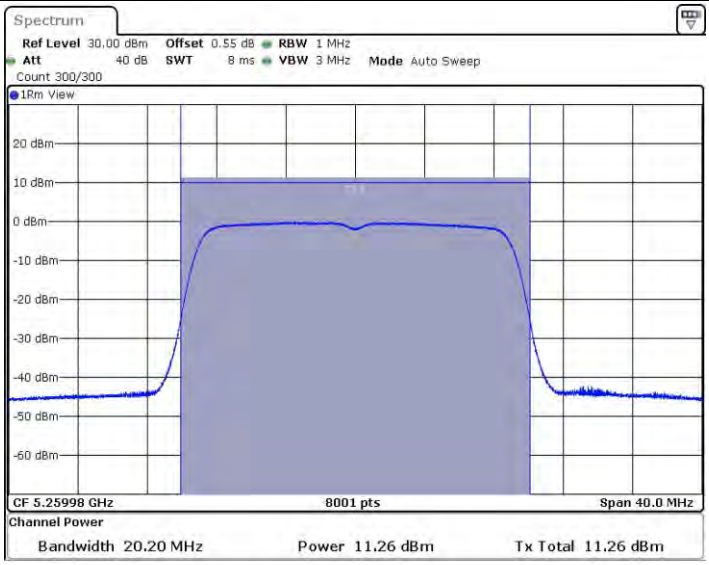

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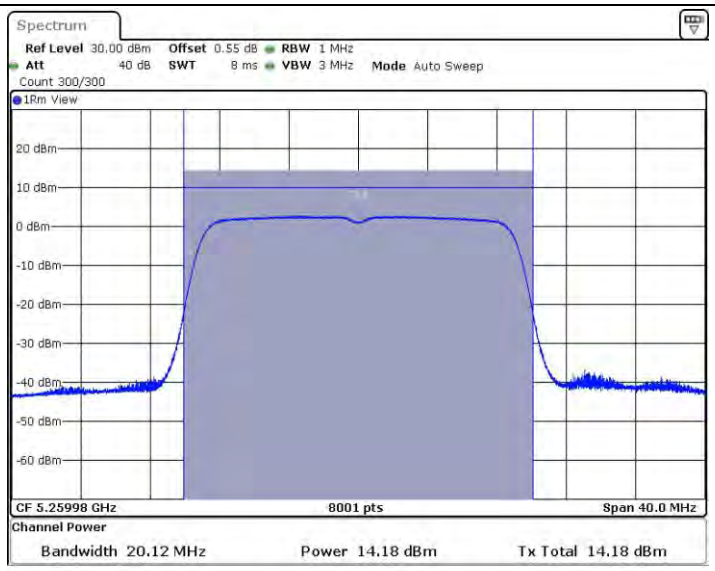

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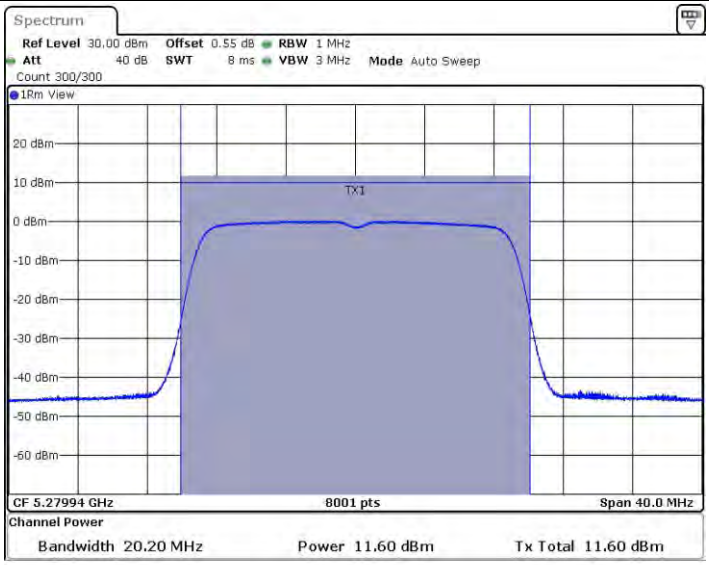

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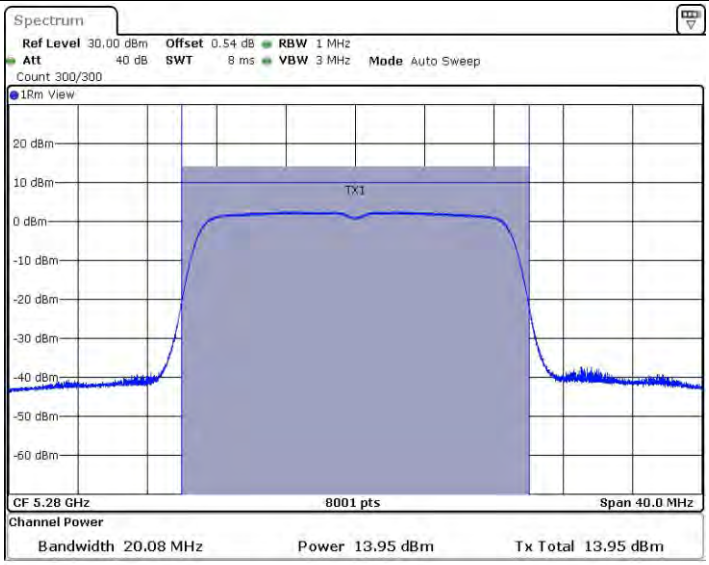

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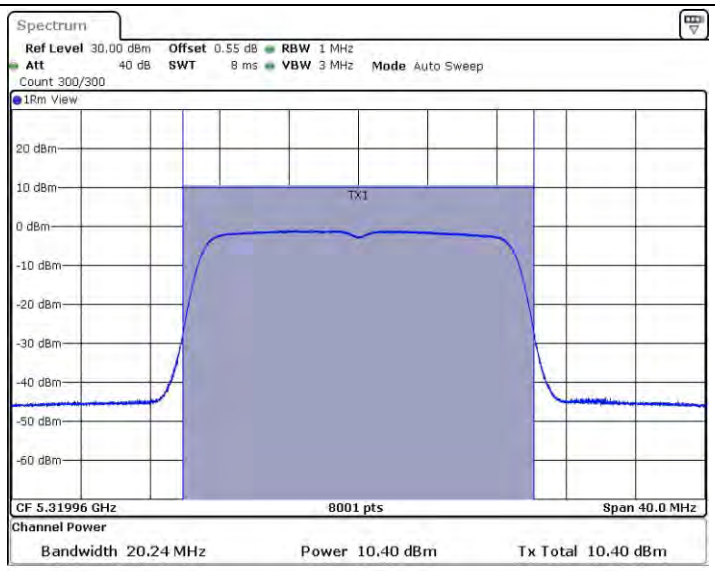

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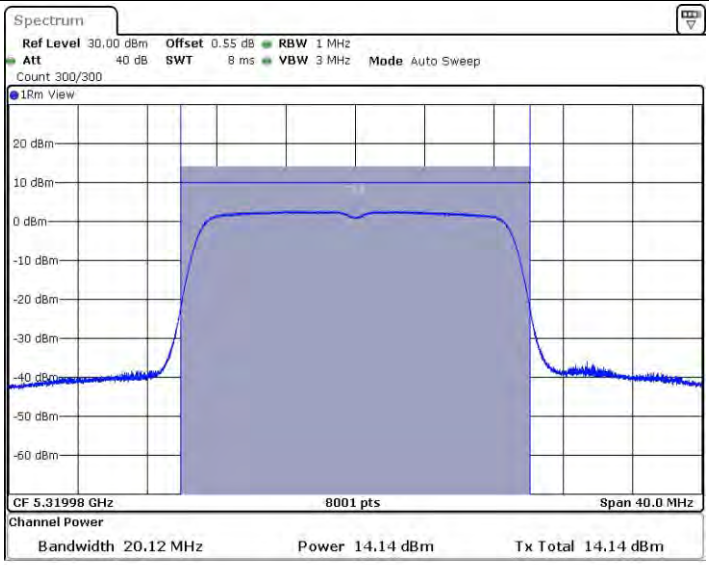

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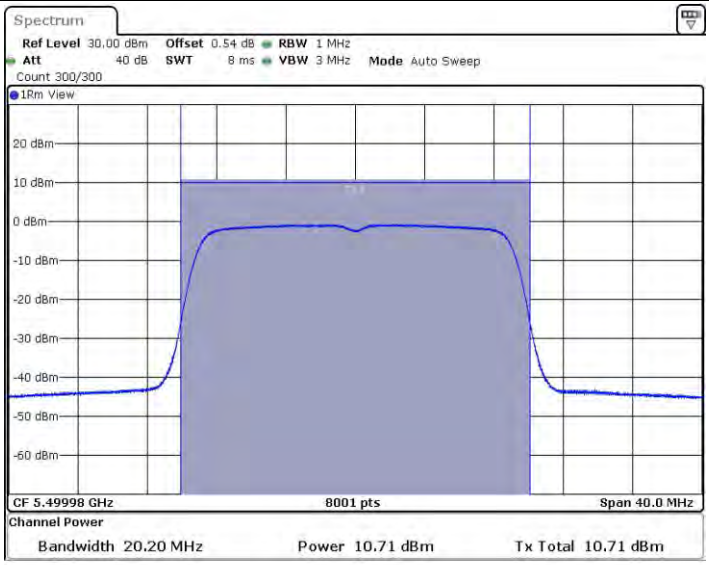

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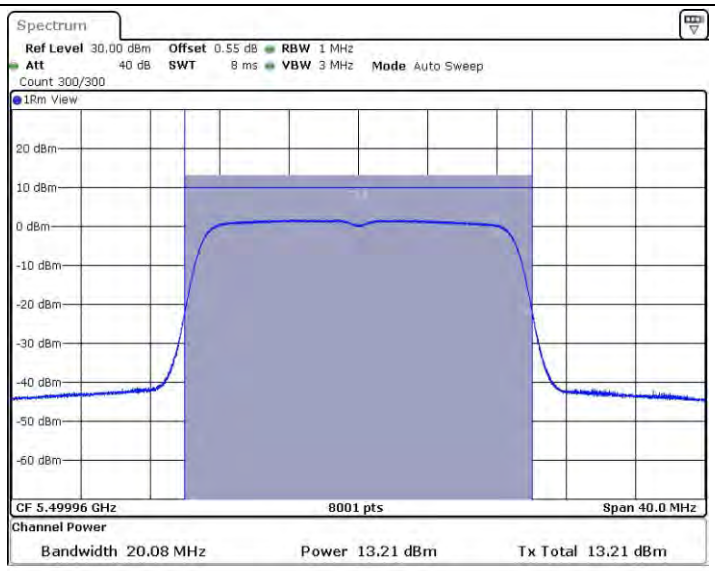

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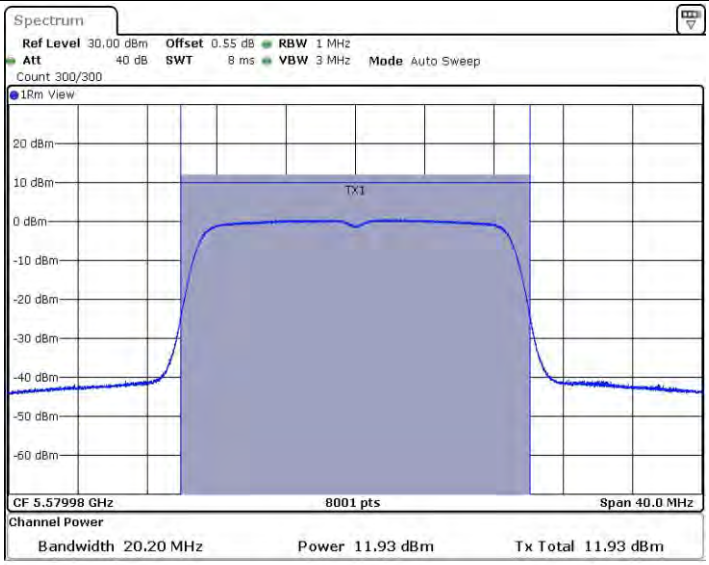

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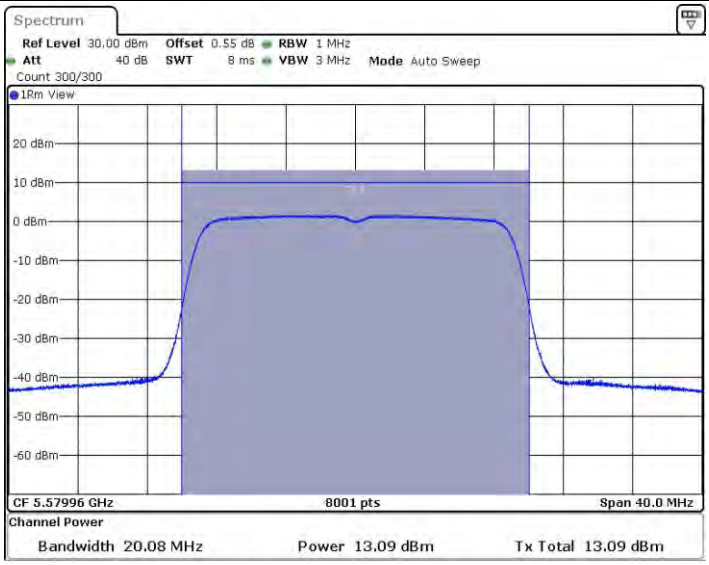

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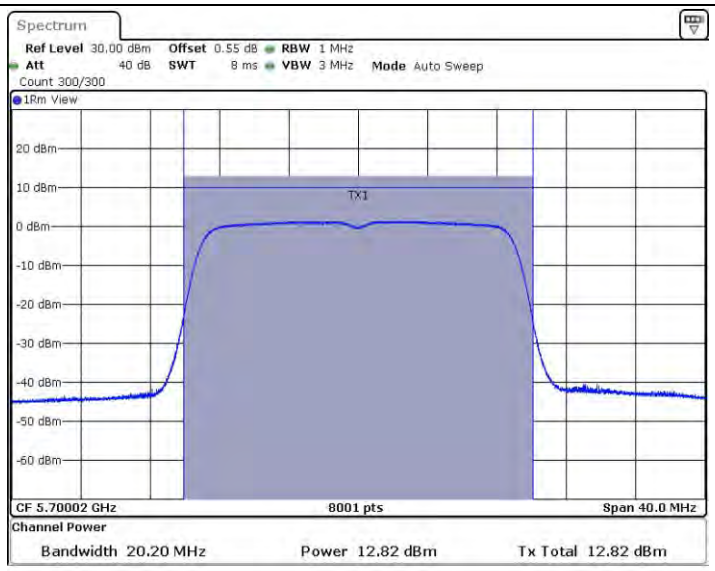

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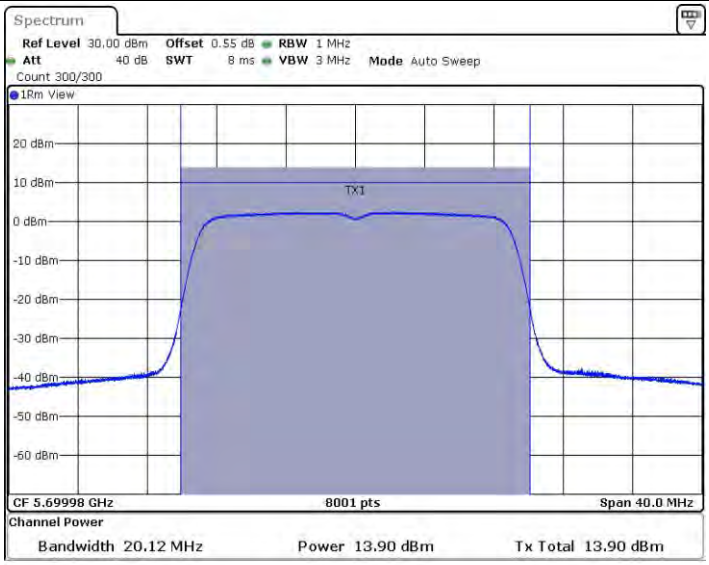

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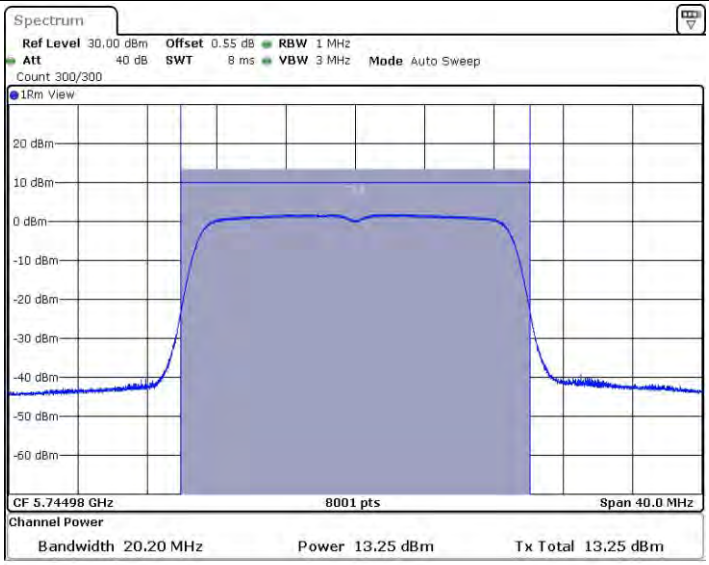

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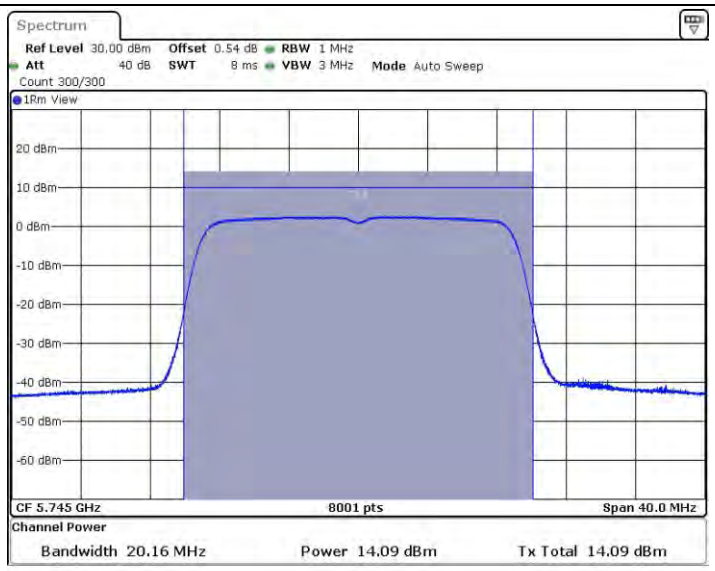

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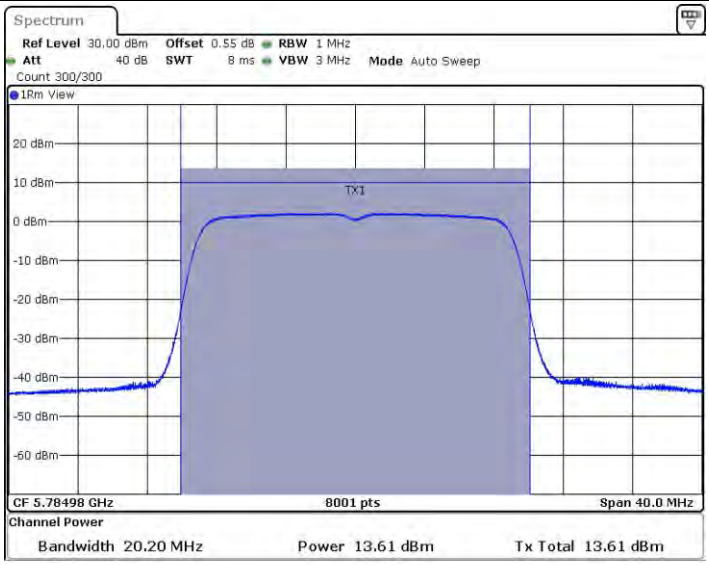

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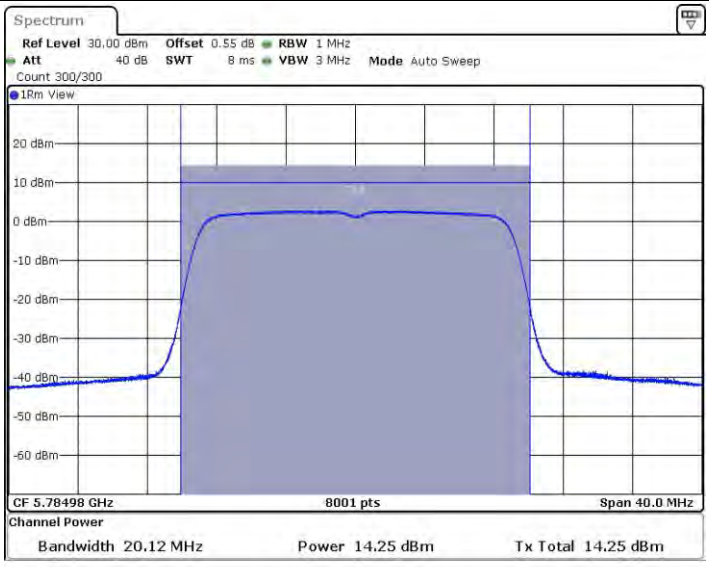

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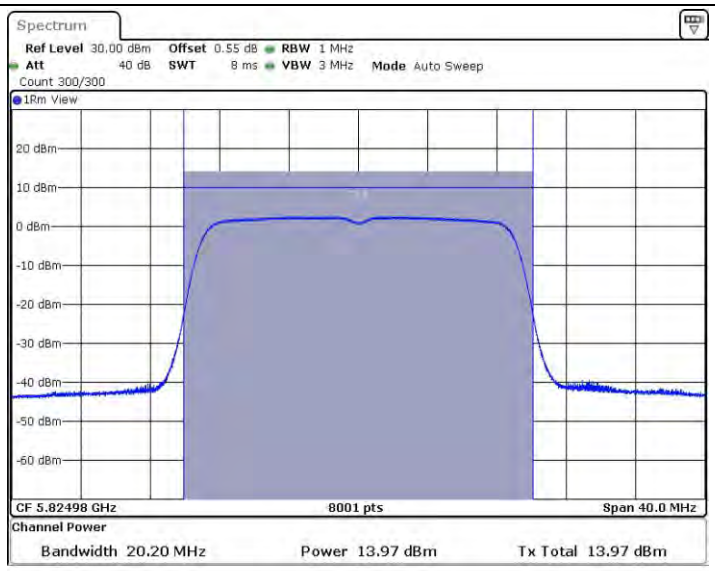

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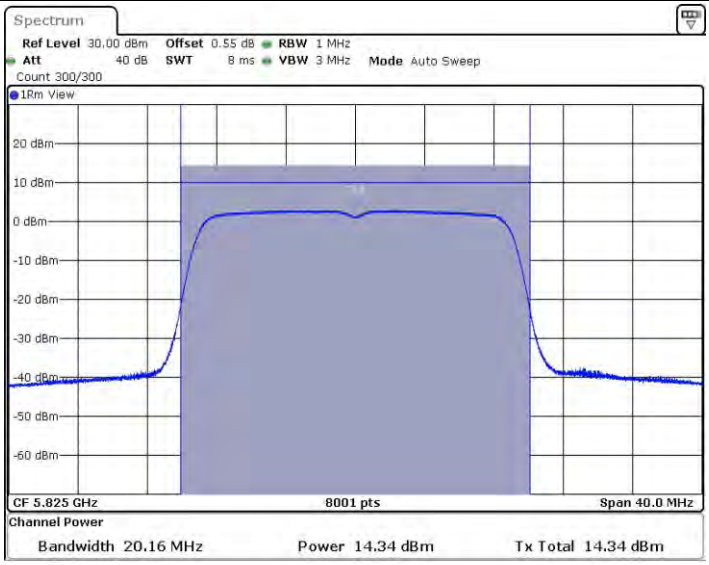

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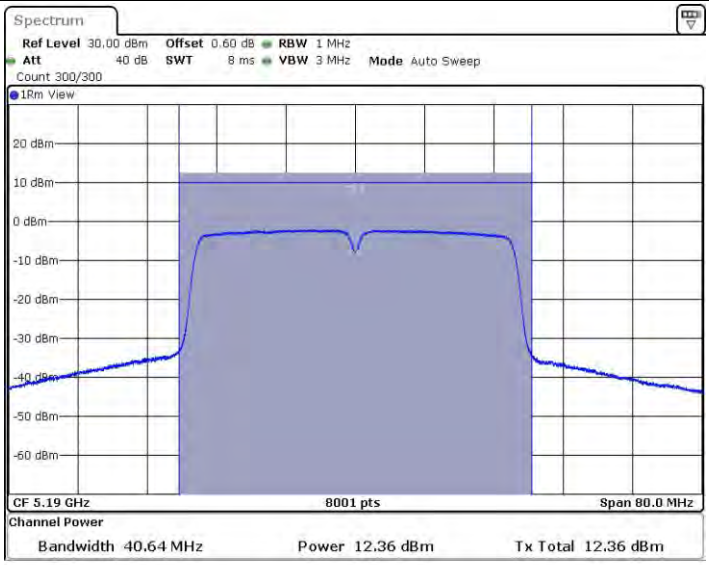

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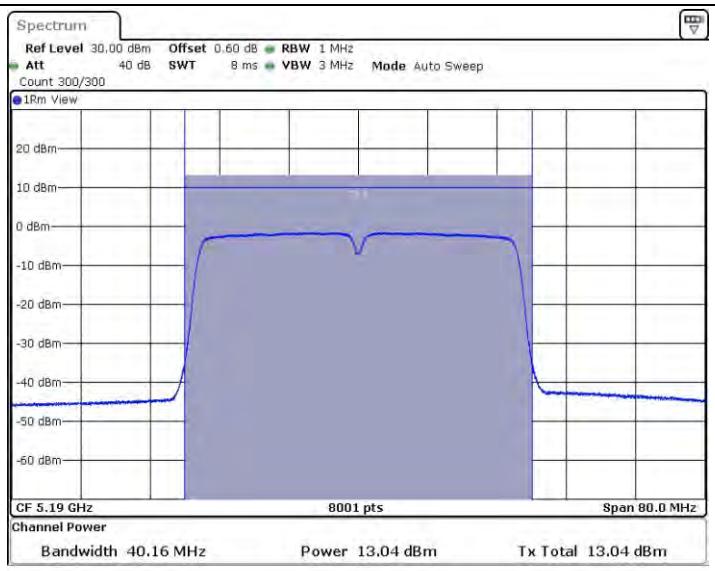

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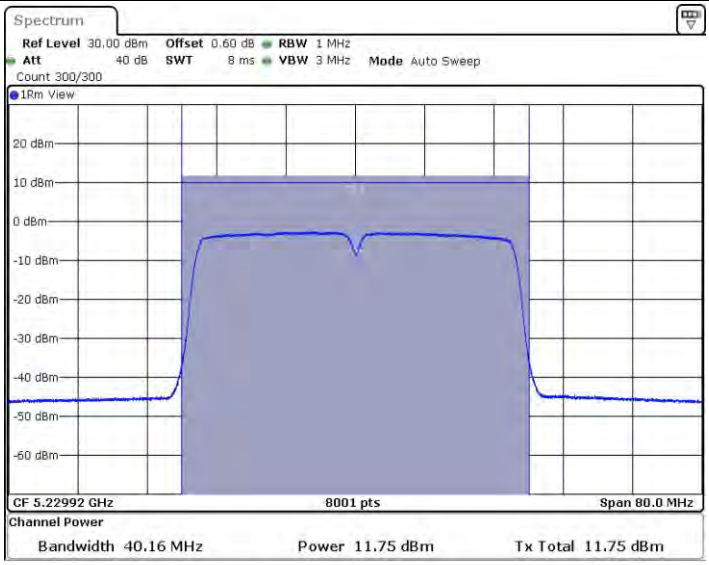
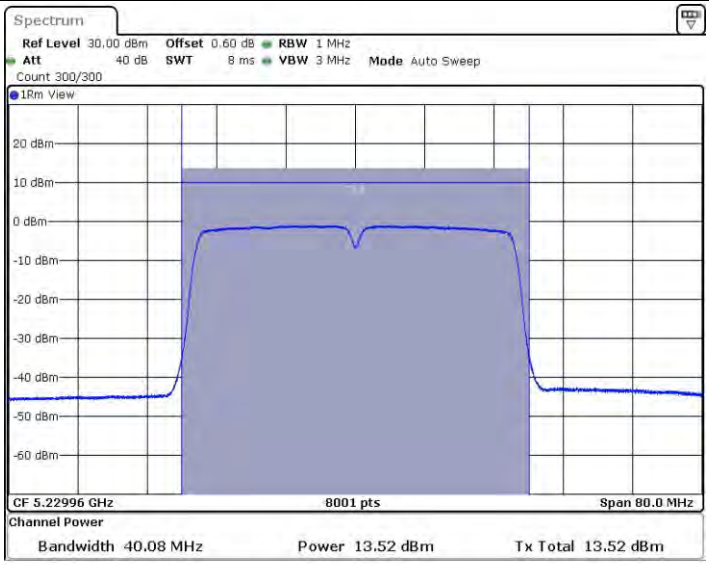


11AC40MIMO_Ant0_5230



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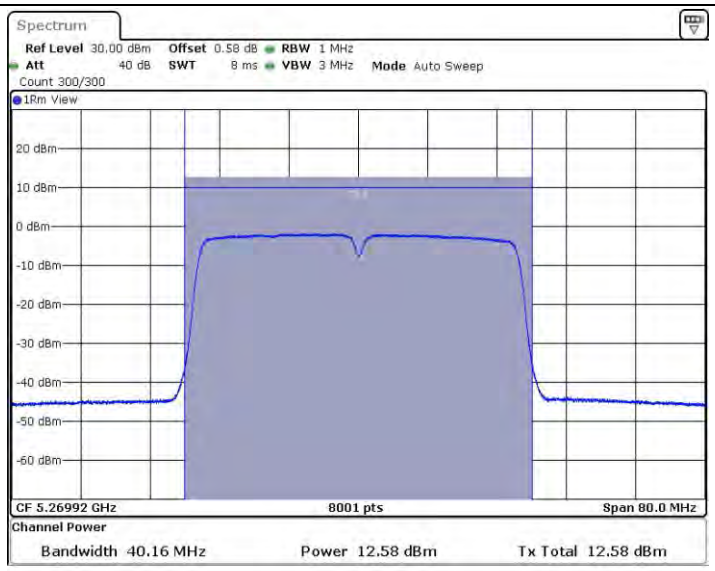

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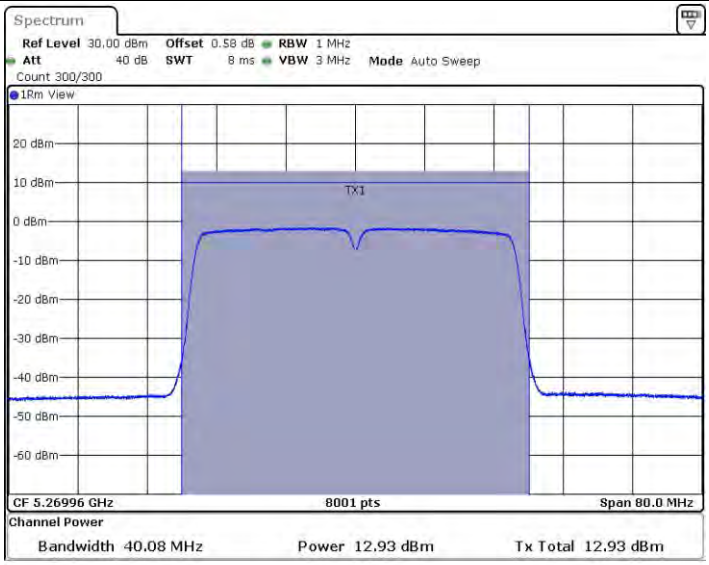

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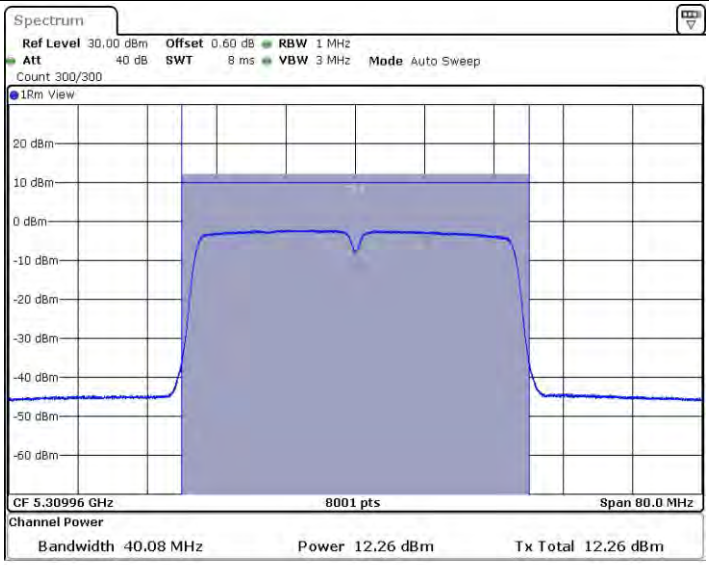

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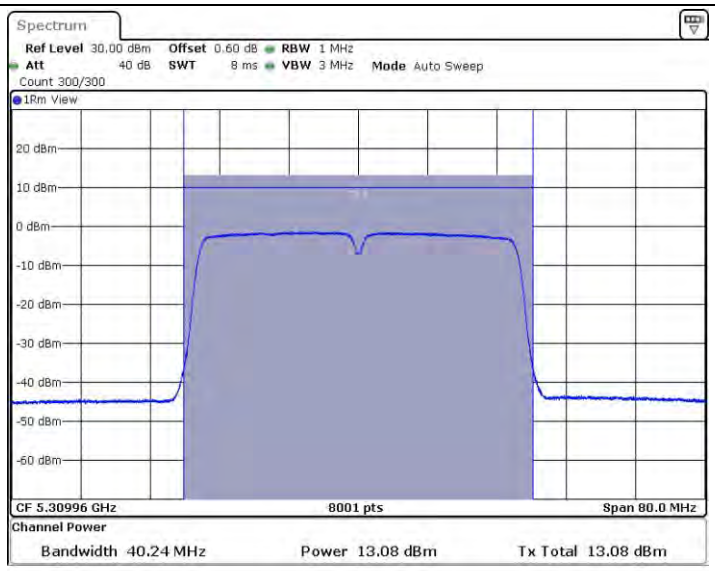

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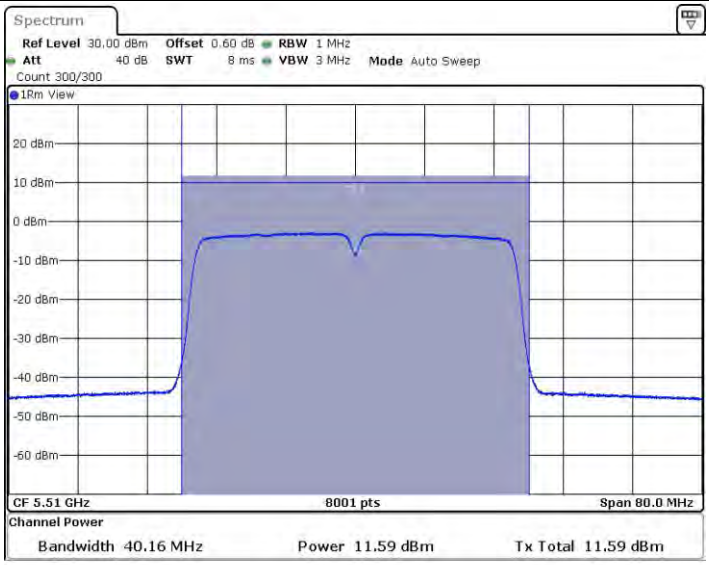

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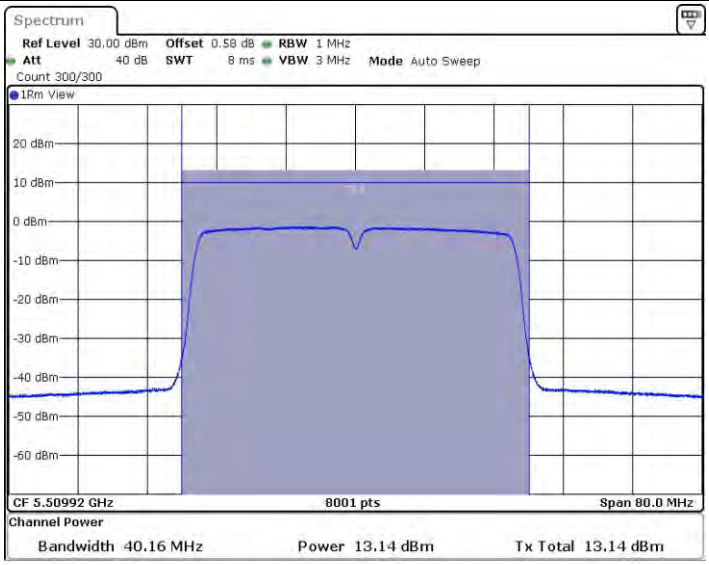

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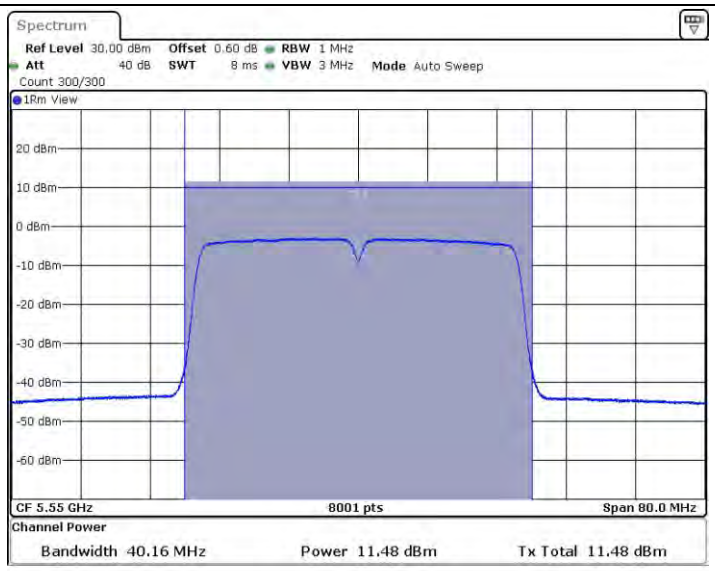

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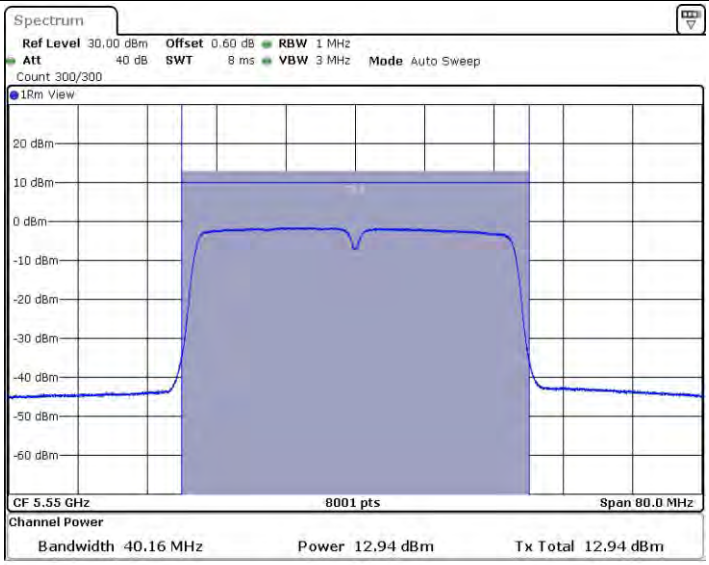

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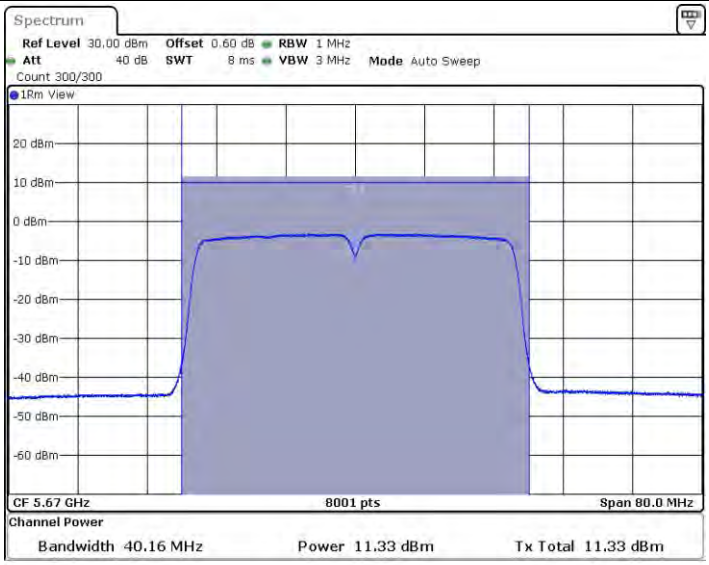

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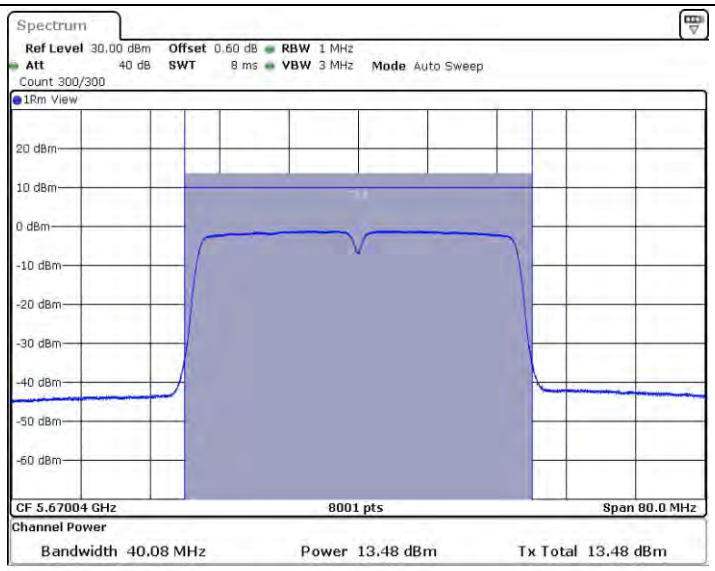

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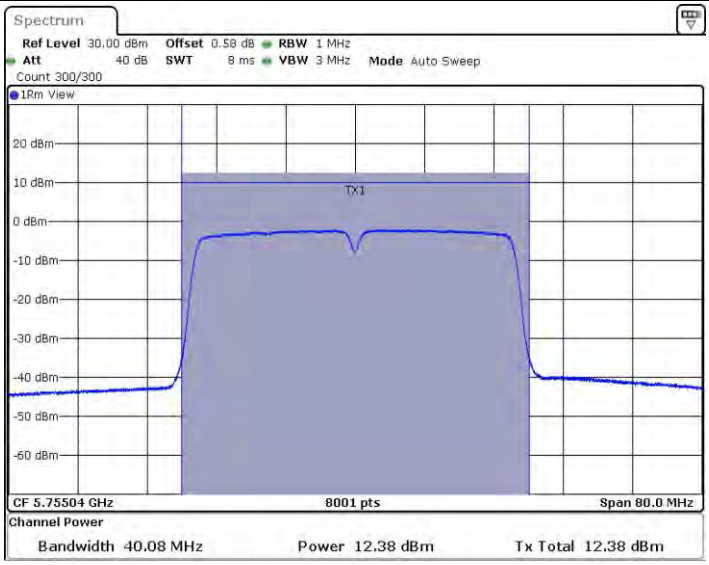

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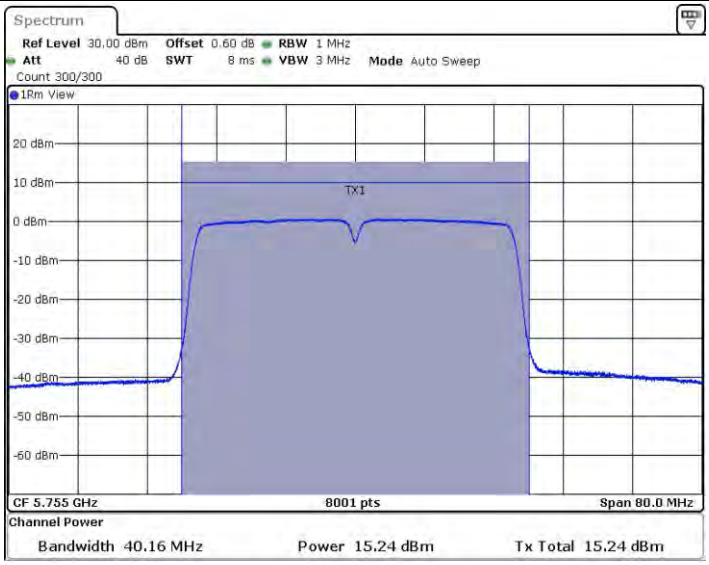

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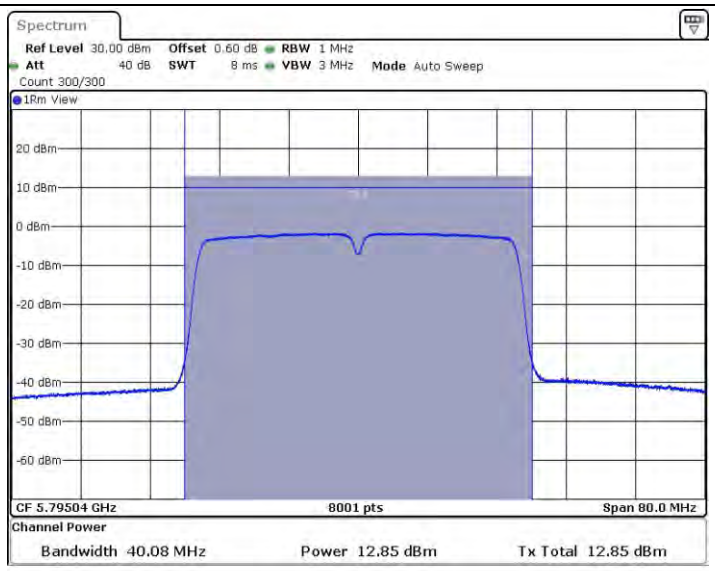

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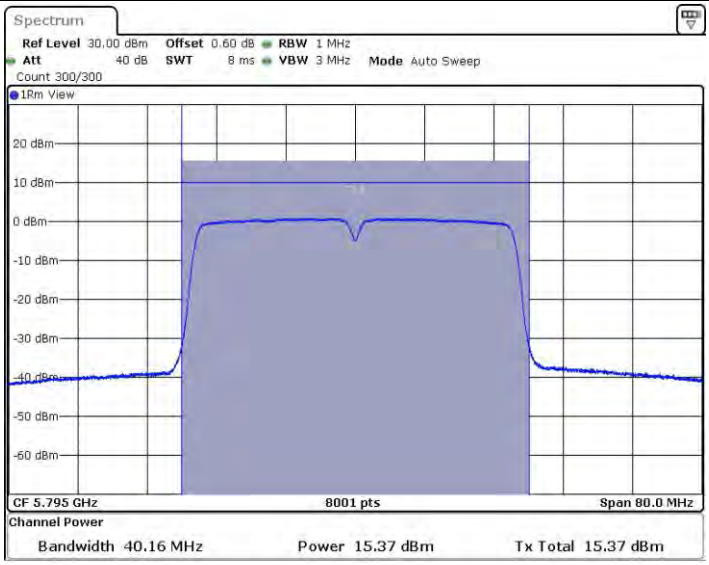

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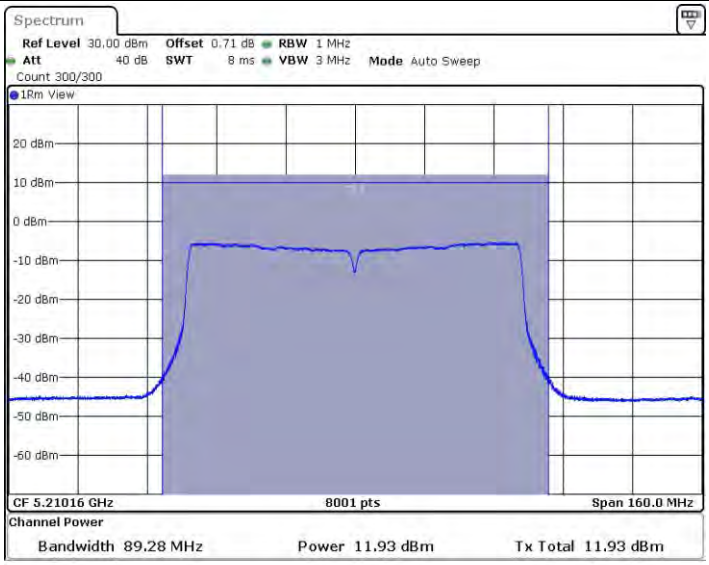

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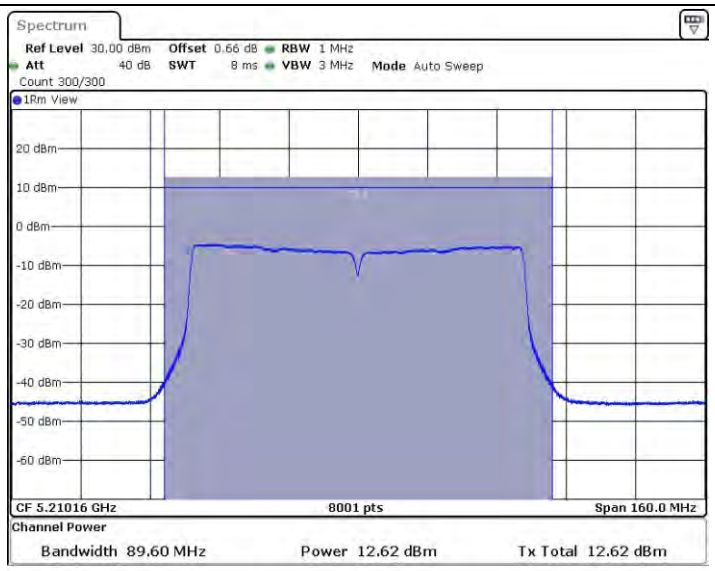

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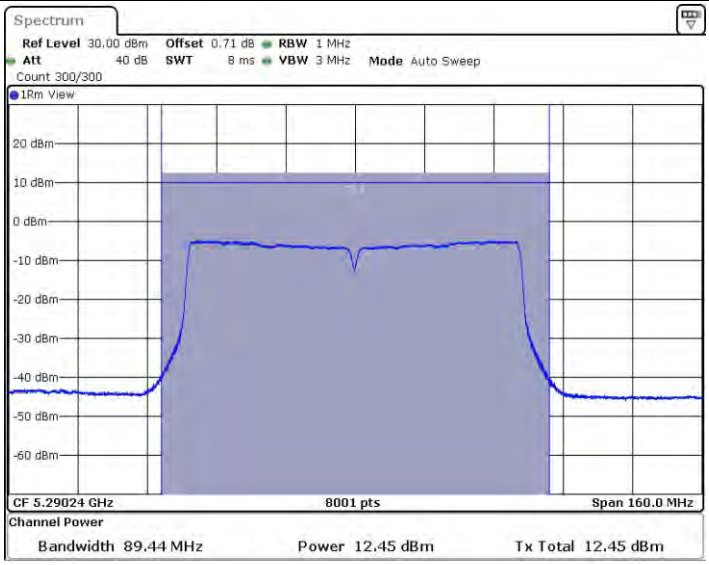

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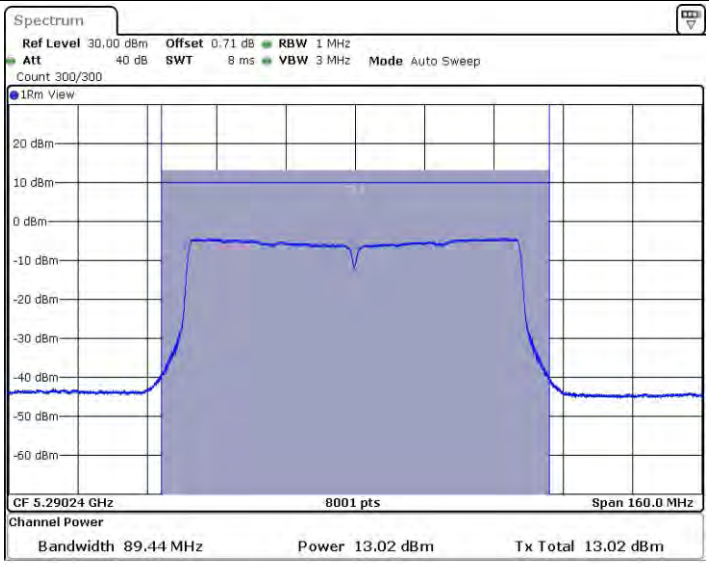

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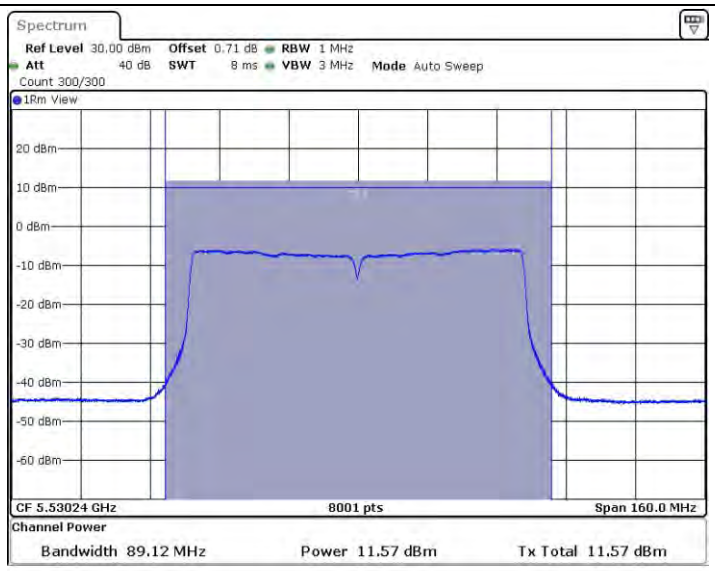

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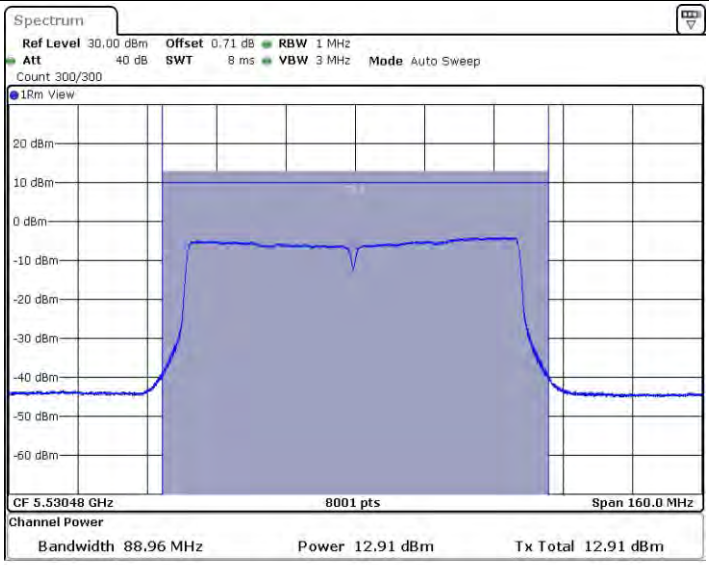

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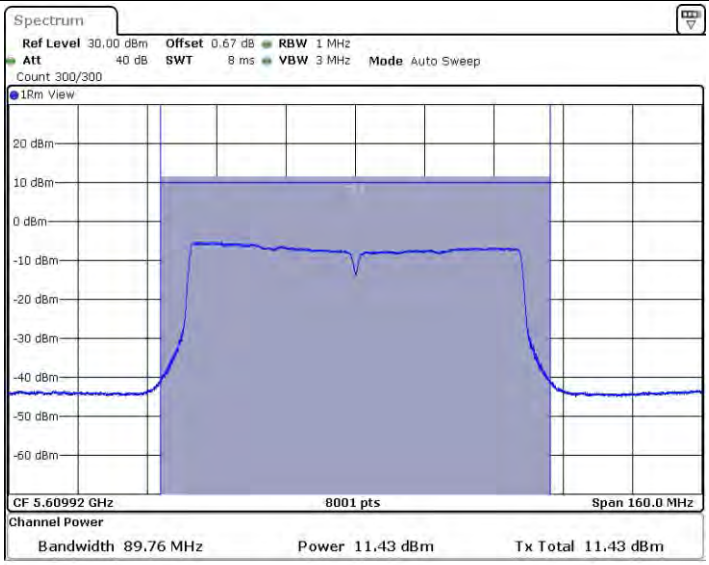

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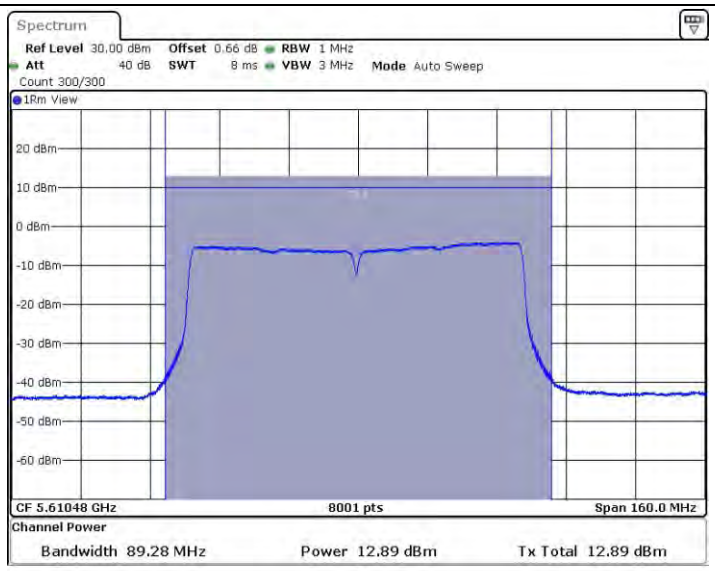

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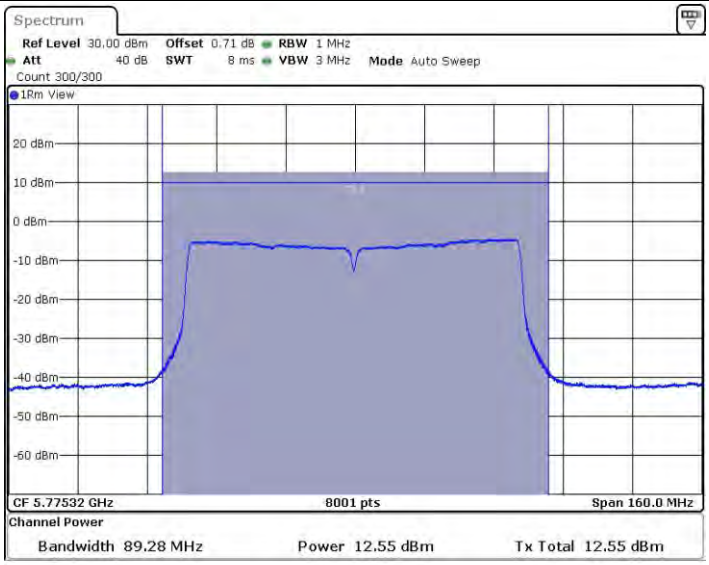

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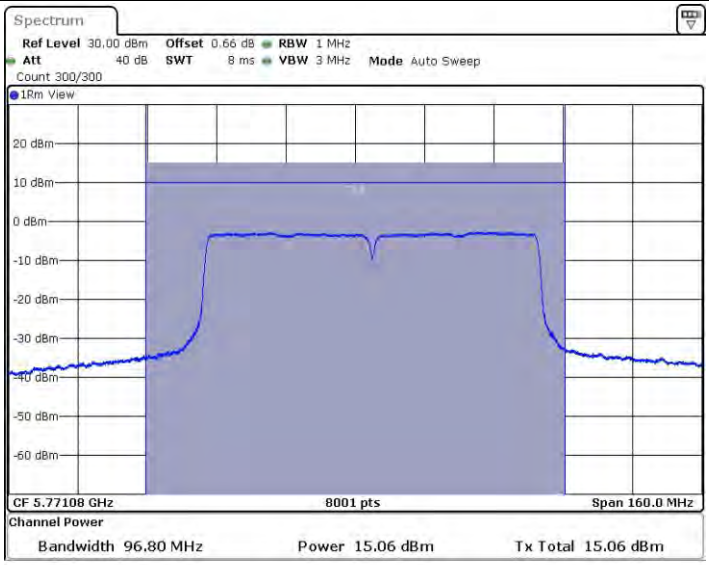

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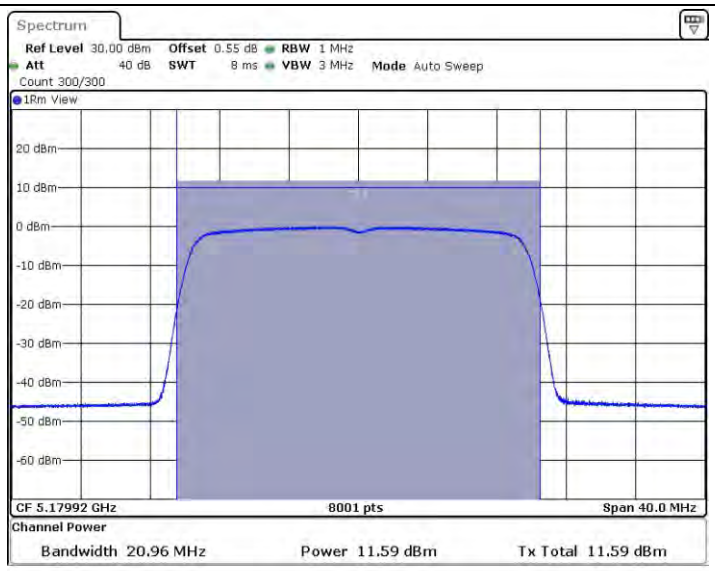
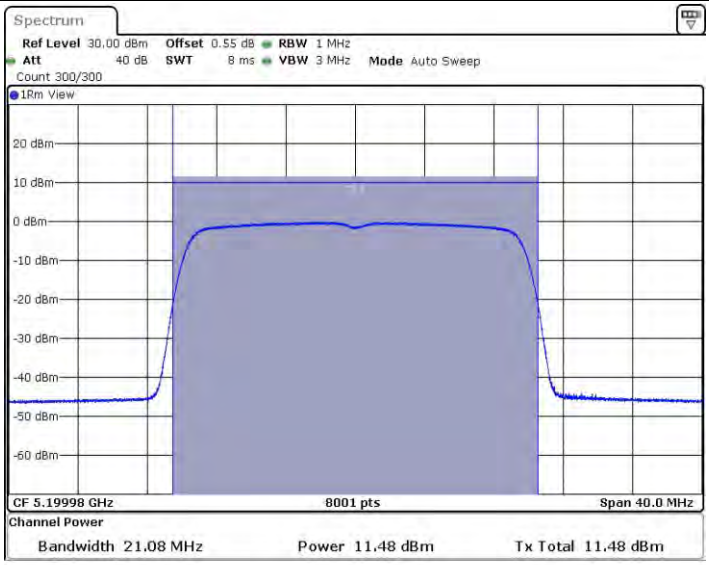


11AX20MIMO_Ant0_5180

11AX20MIMO_Ant1_5180



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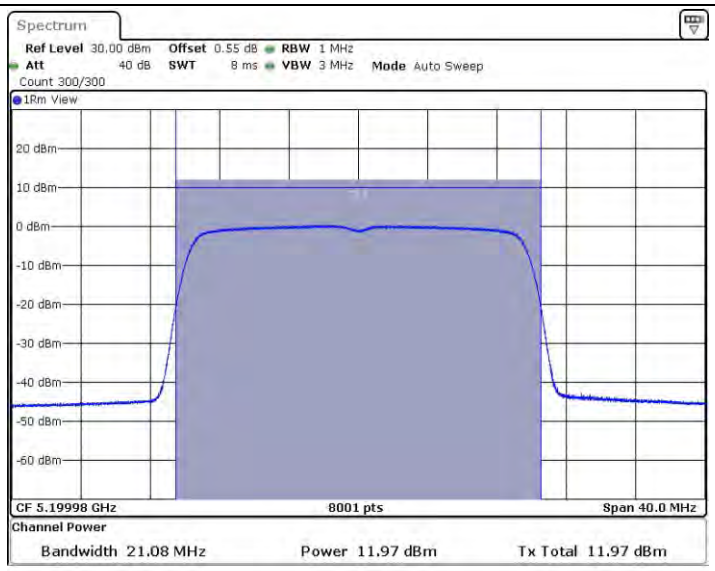
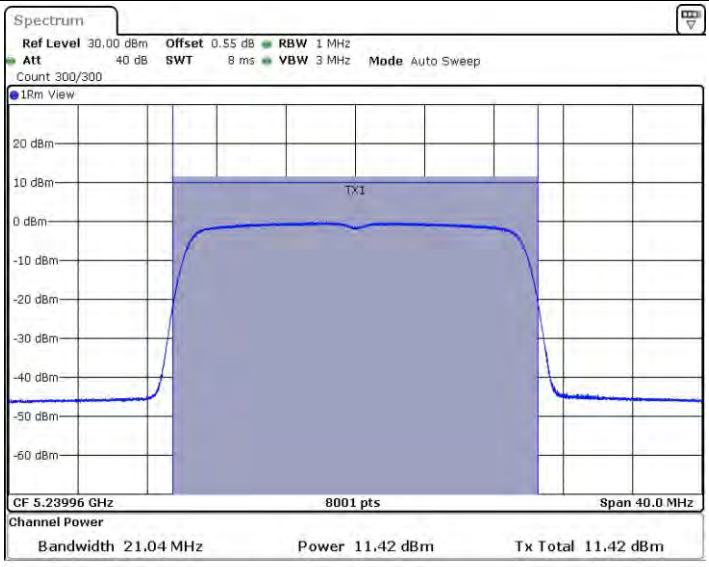


11AX20MIMO_Ant1_5200

11AX20MIMO_Ant0_5240



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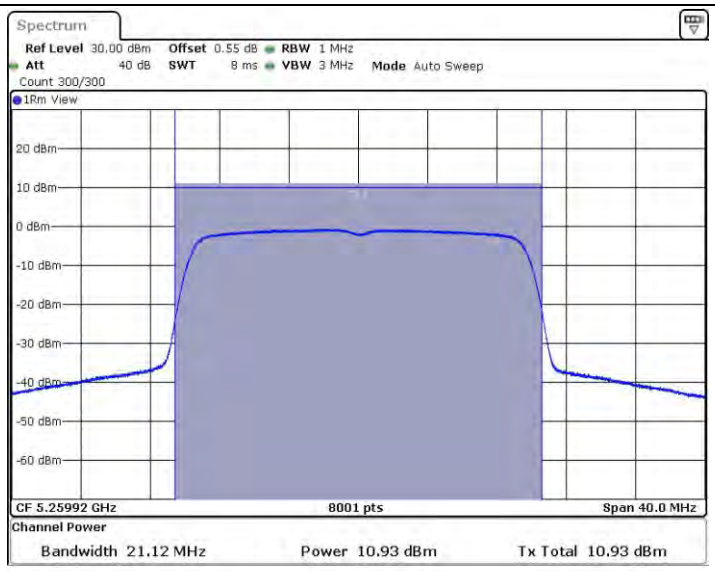

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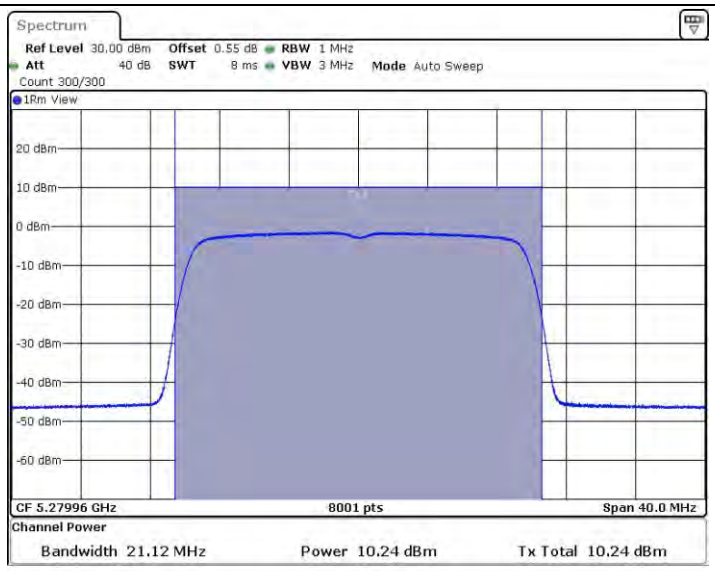

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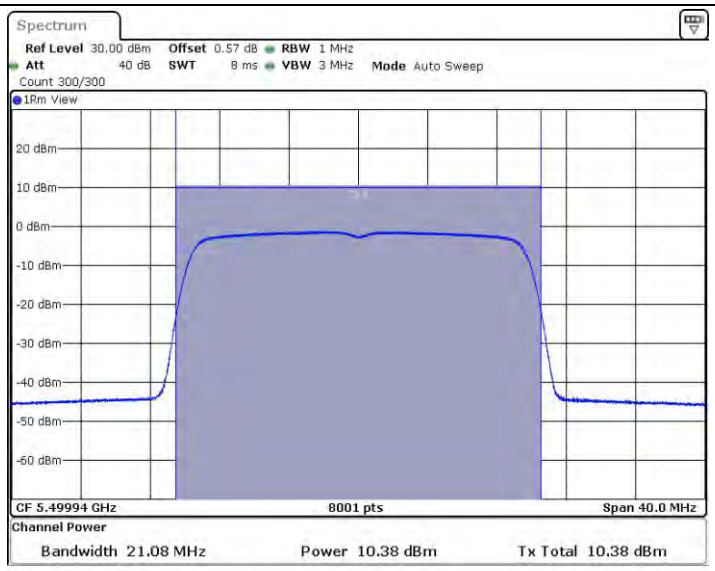

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11AX20MIMO_Ant1_5500

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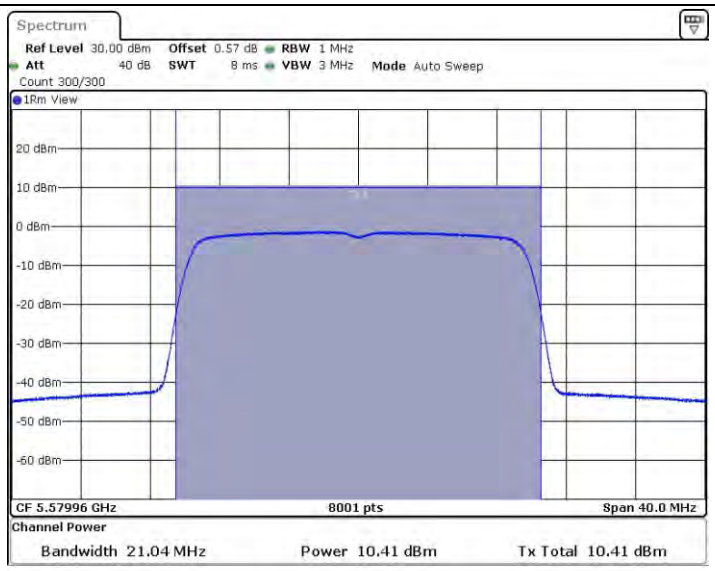

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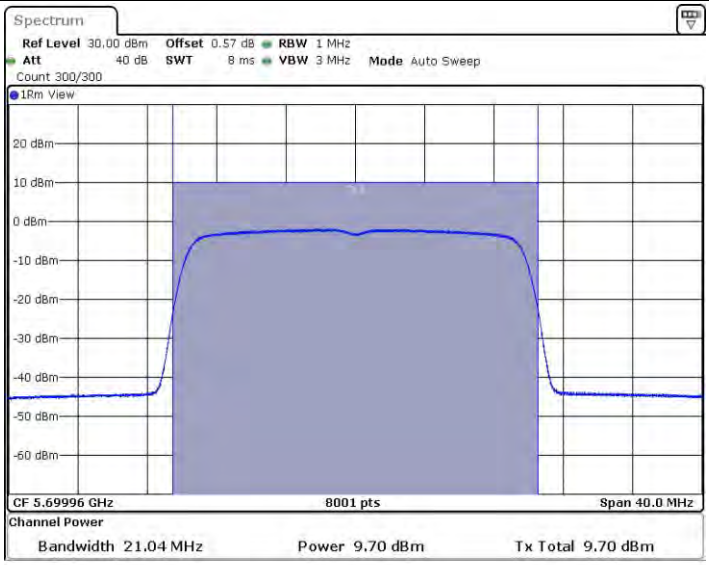

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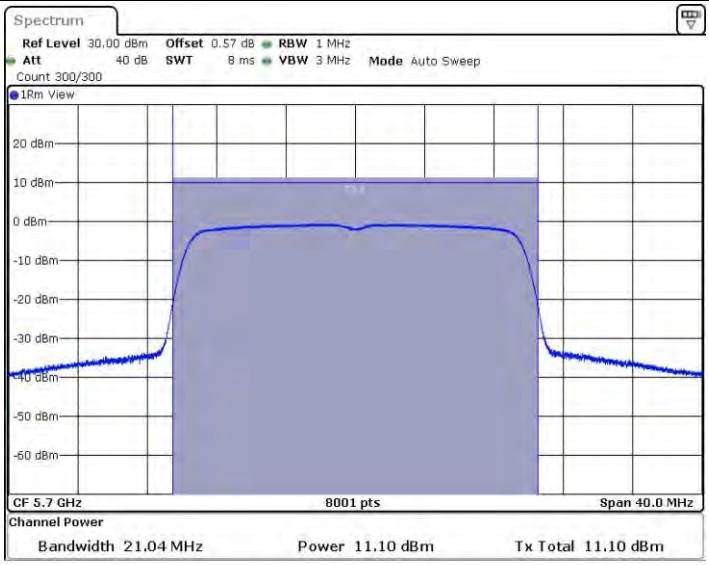

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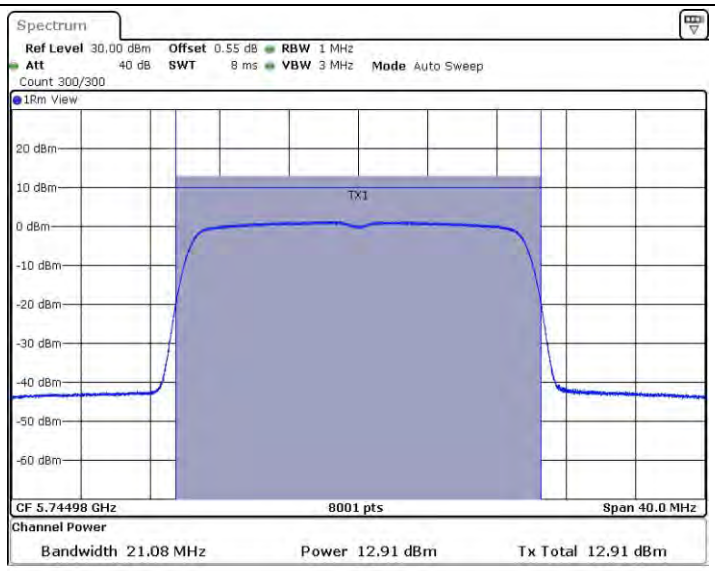

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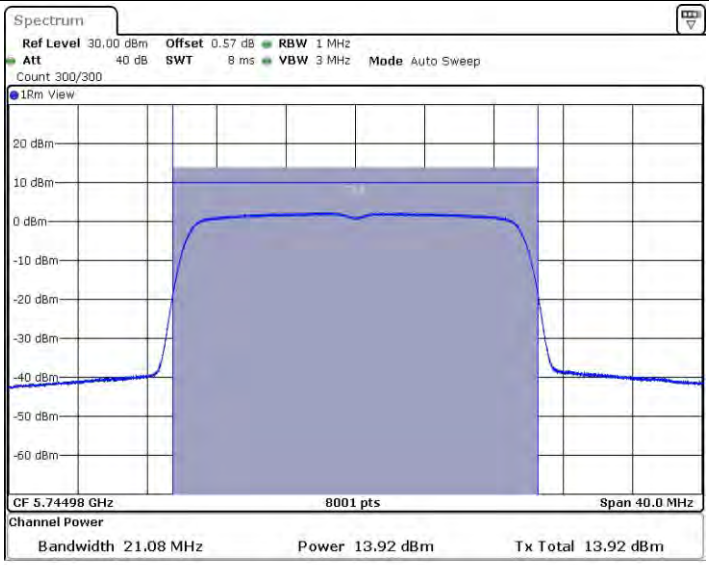

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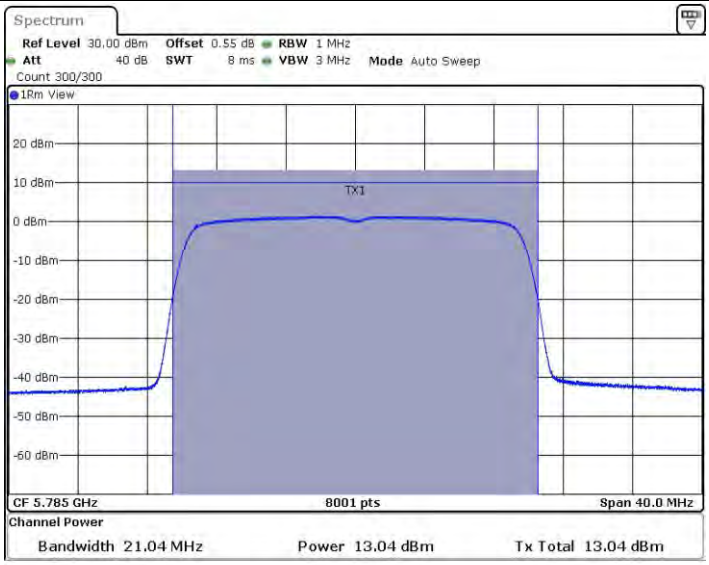

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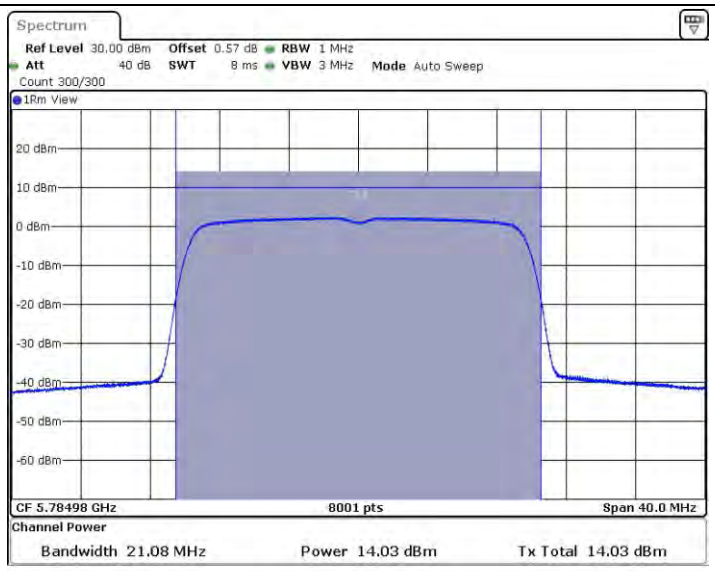

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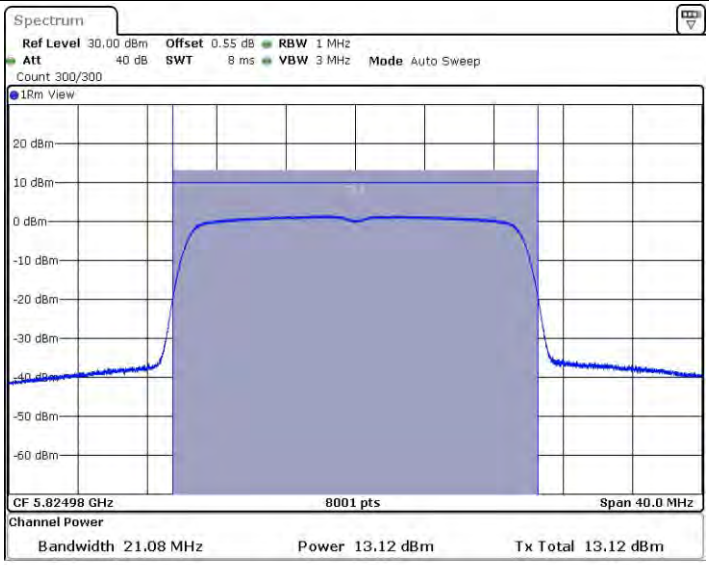

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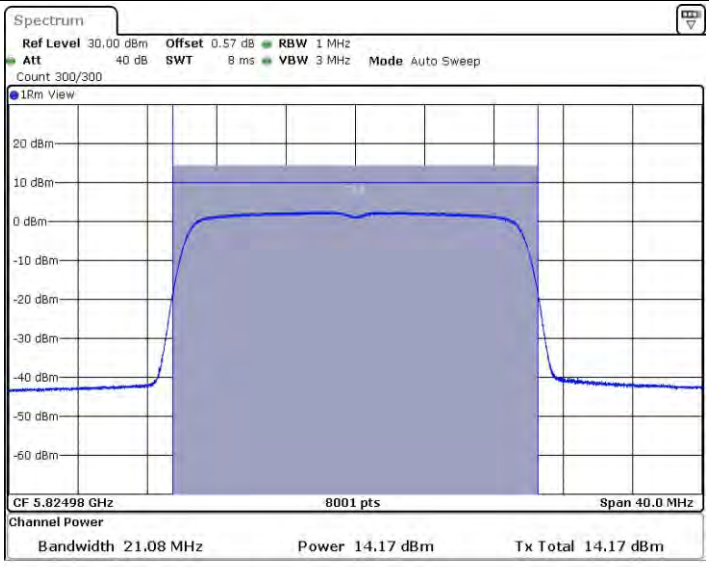

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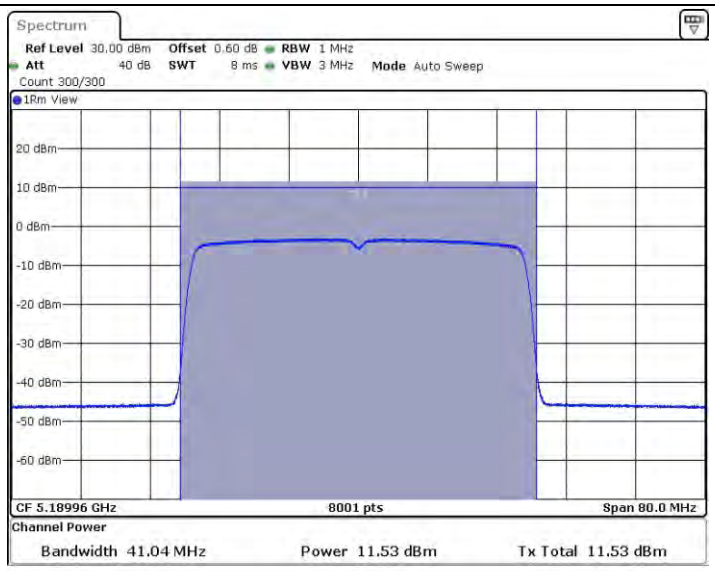

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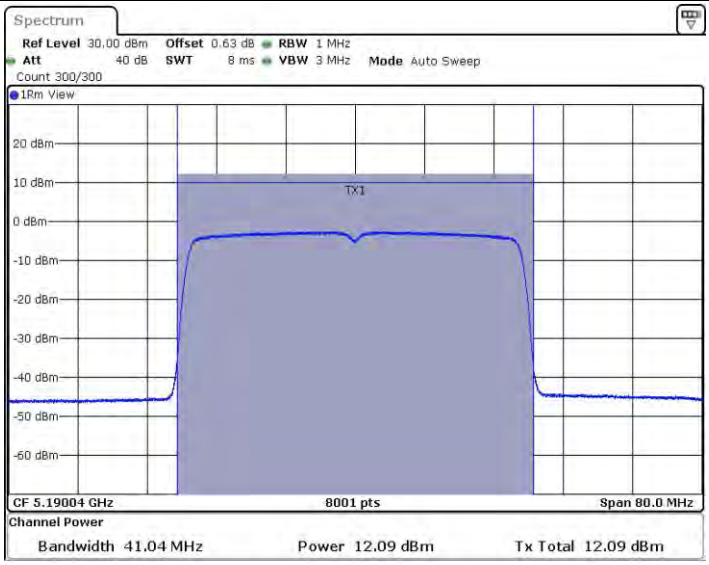

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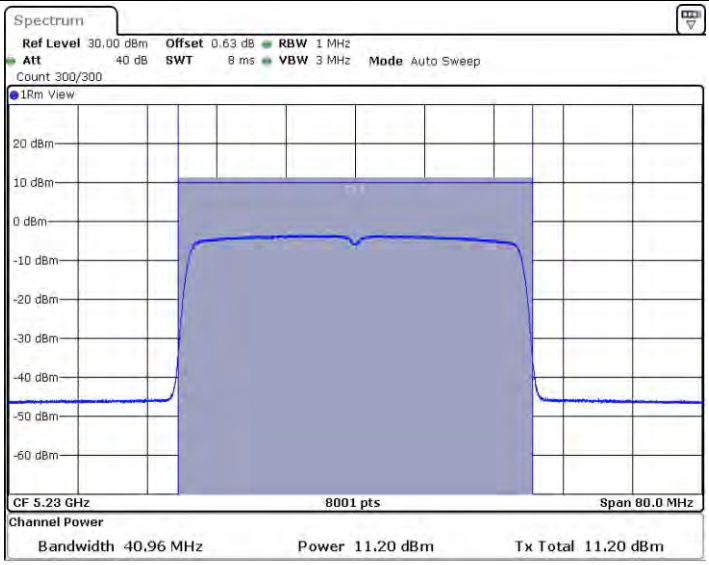

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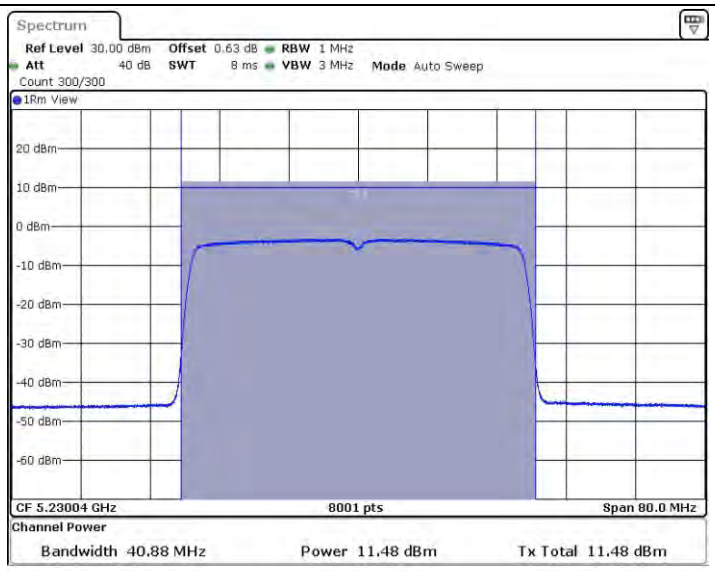

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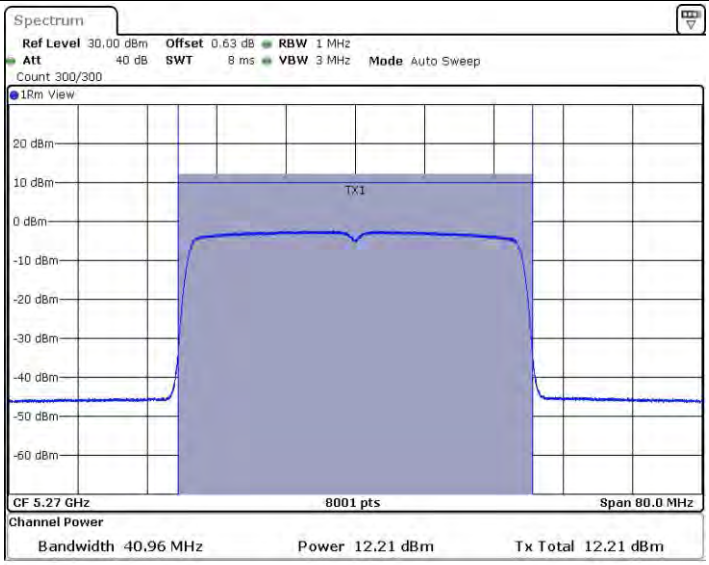

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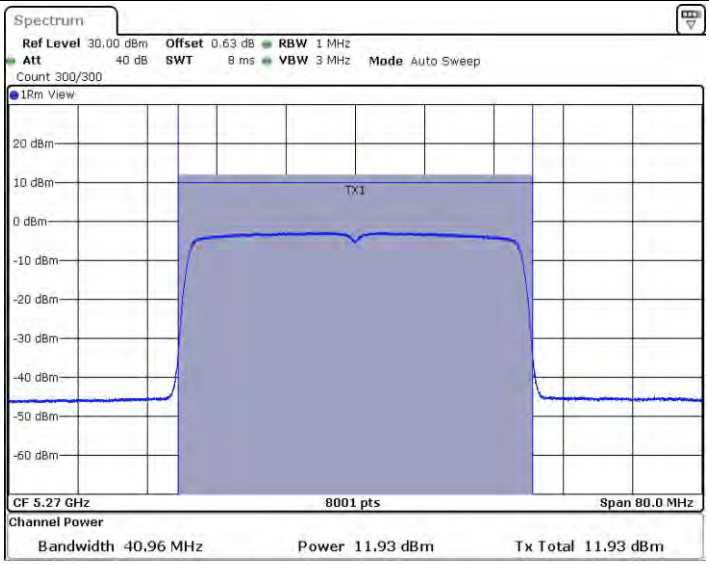

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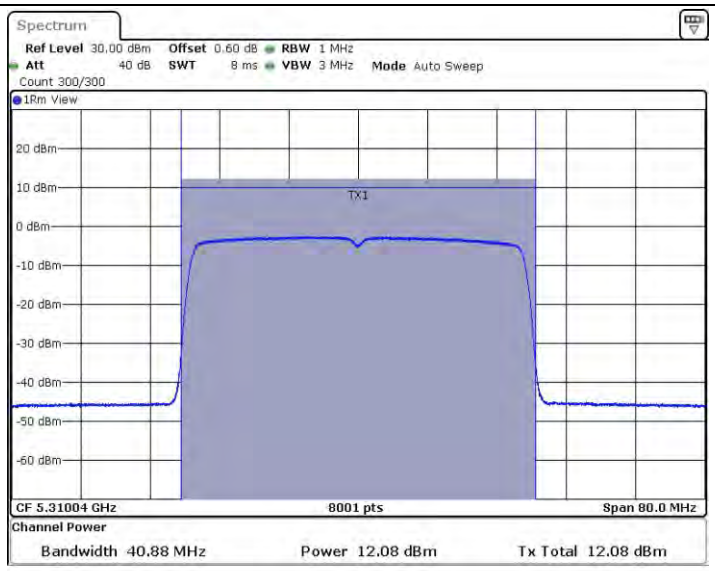

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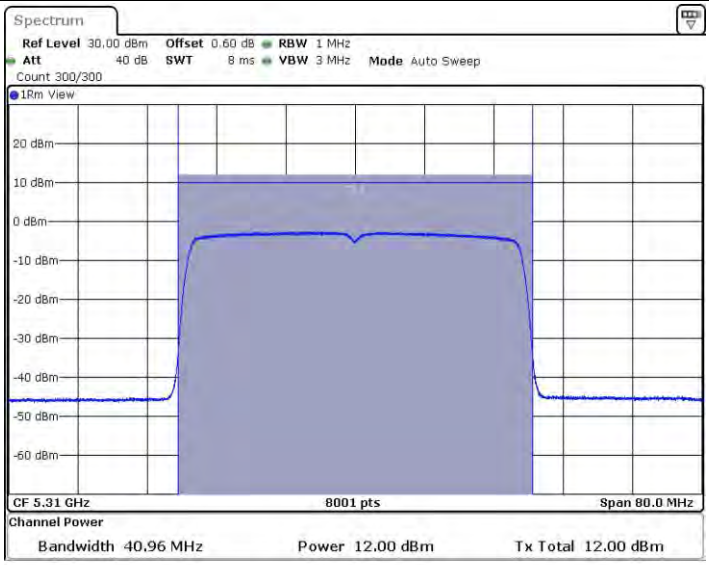

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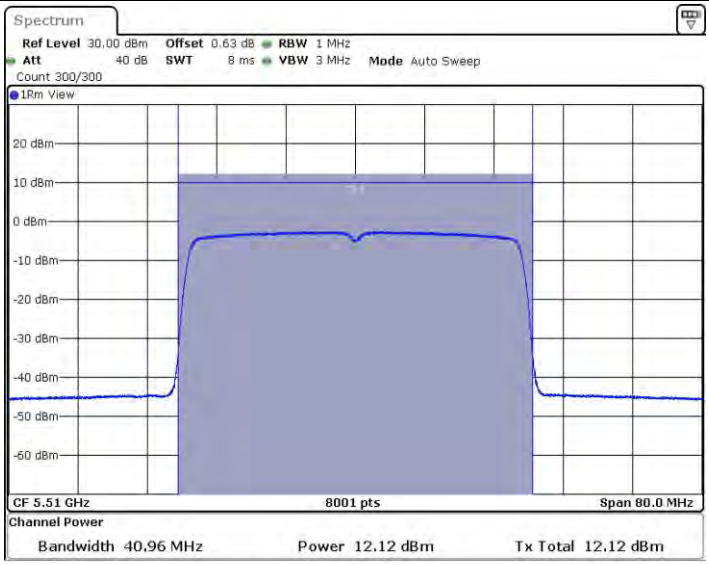

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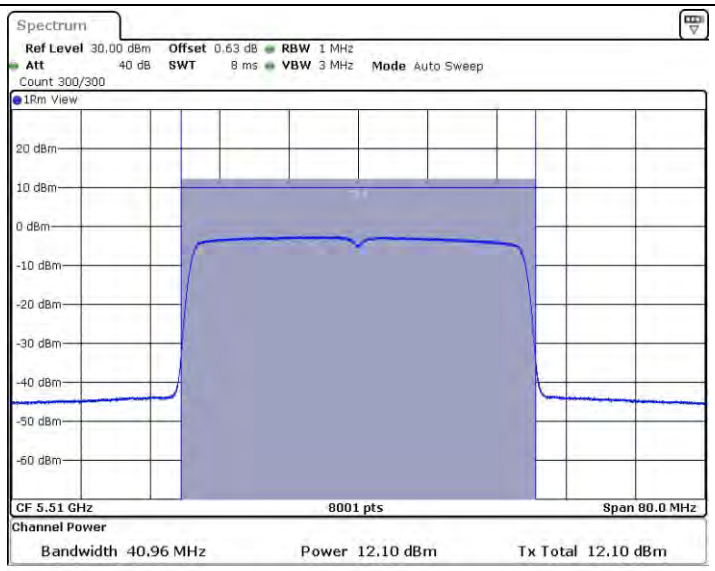

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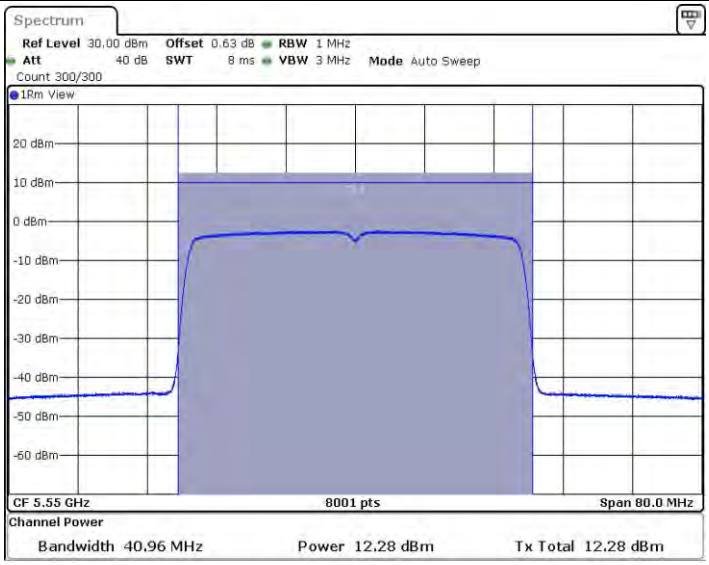

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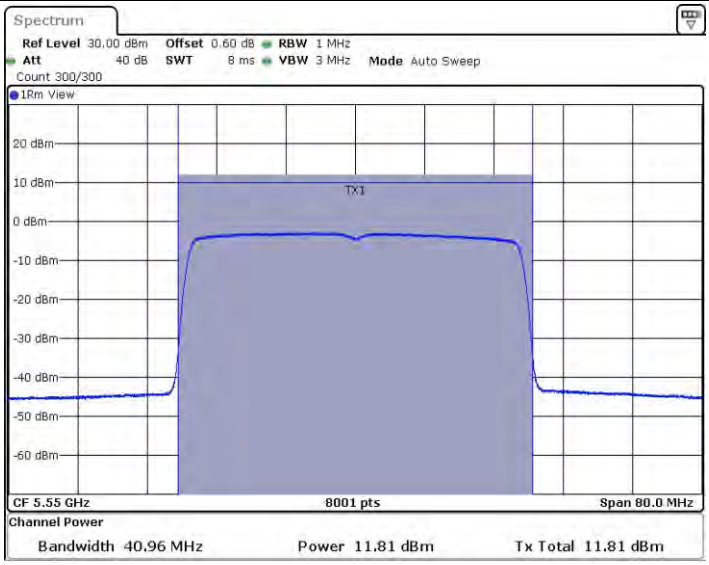

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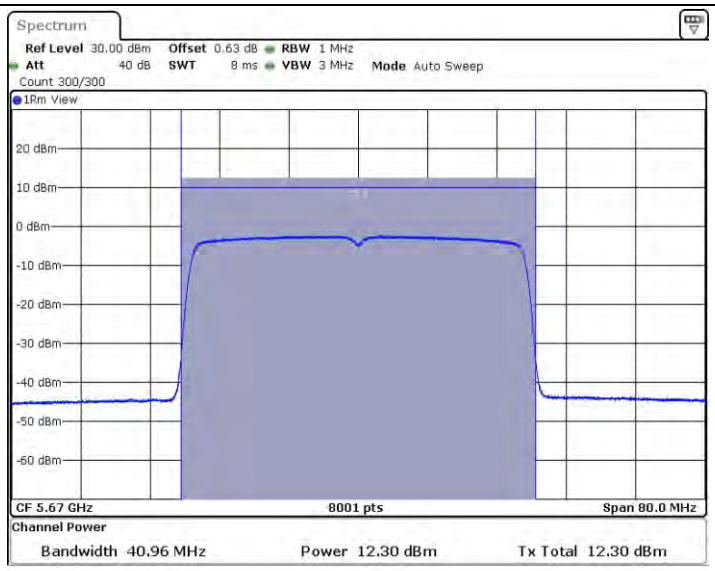

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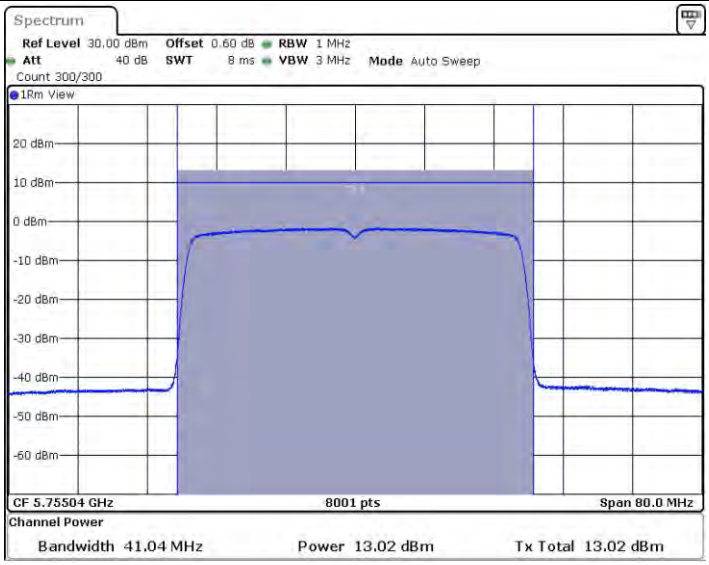

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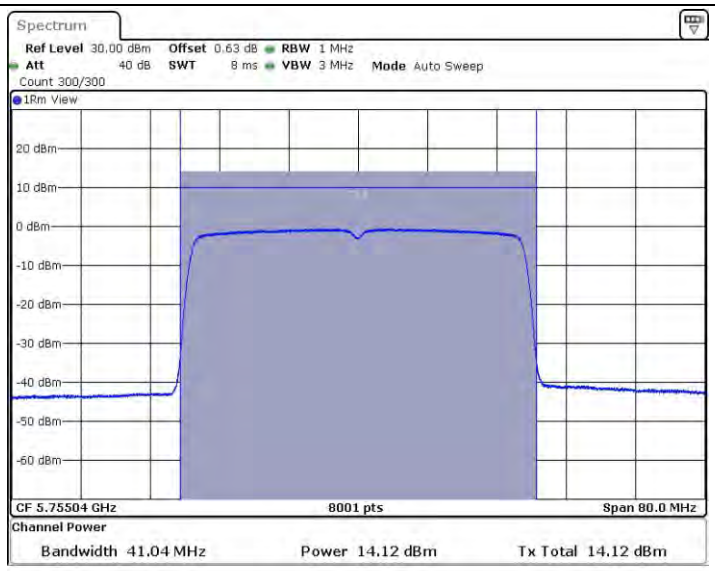
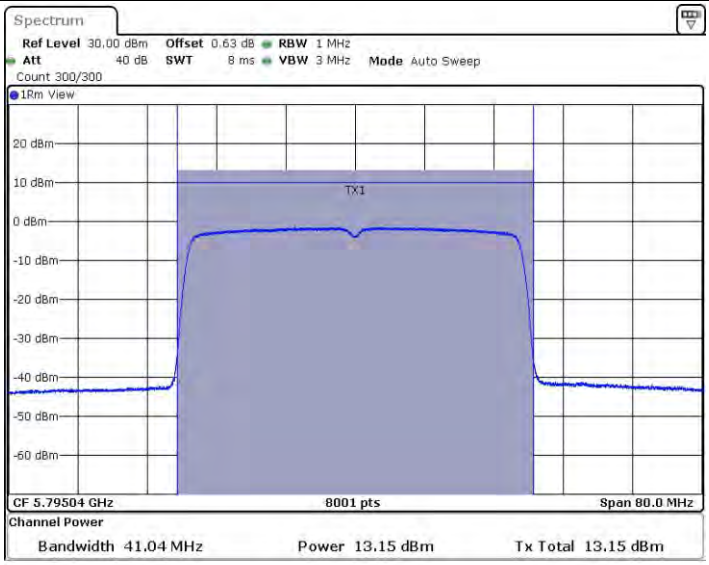
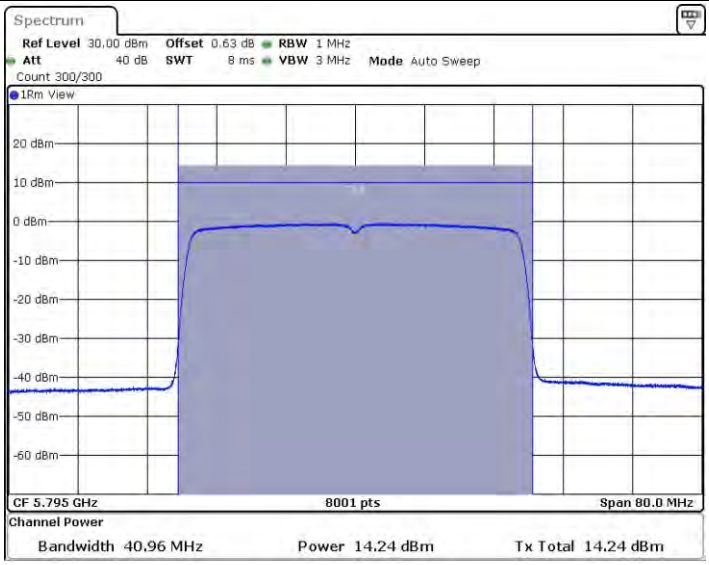


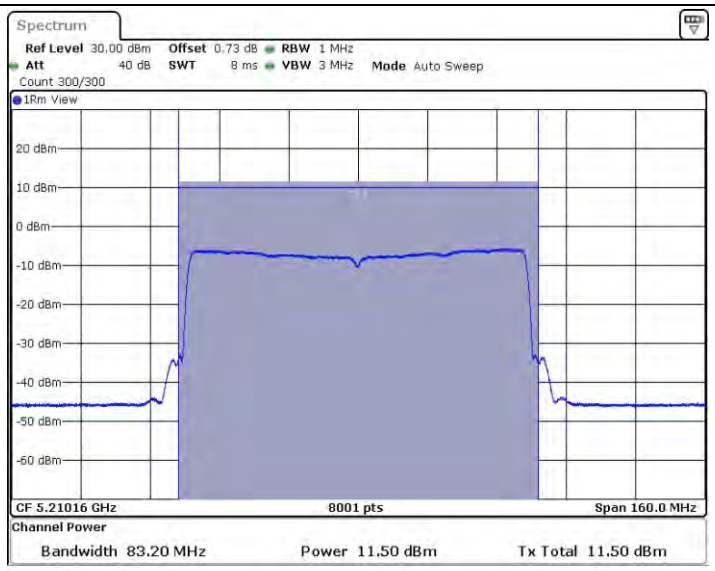
11AX40MIMO_Ant1_5755



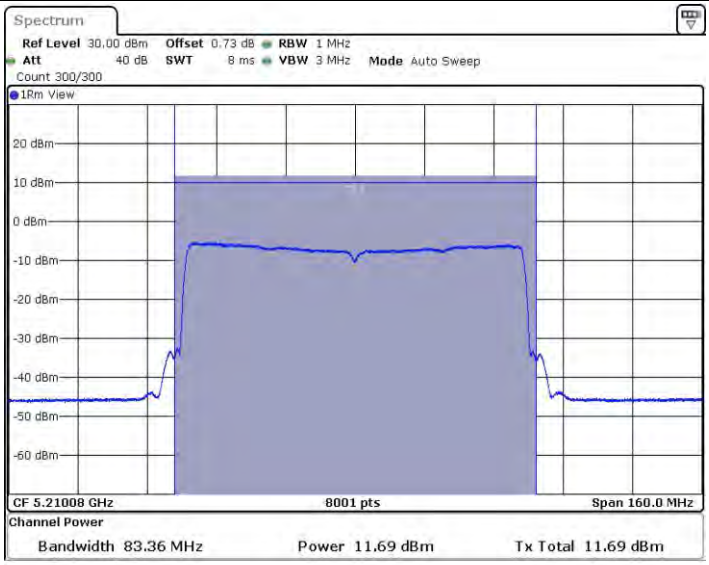
11AX40MIMO_Ant0_5795



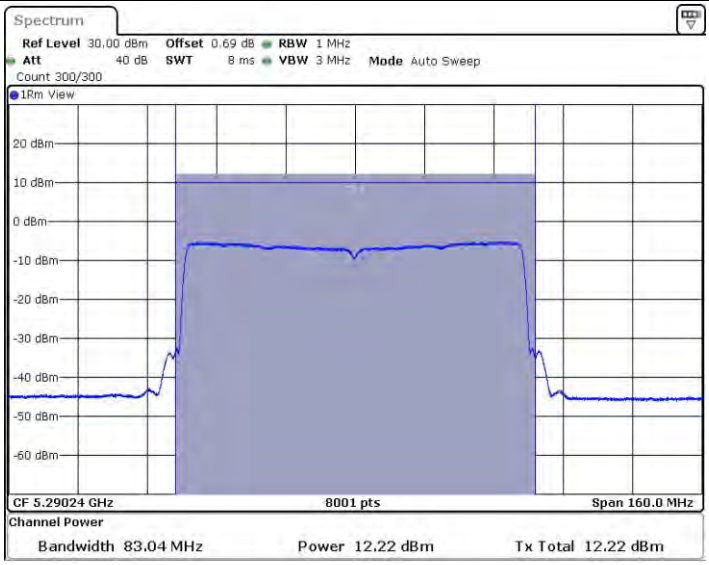
11AX40MIMO_Ant1_5795



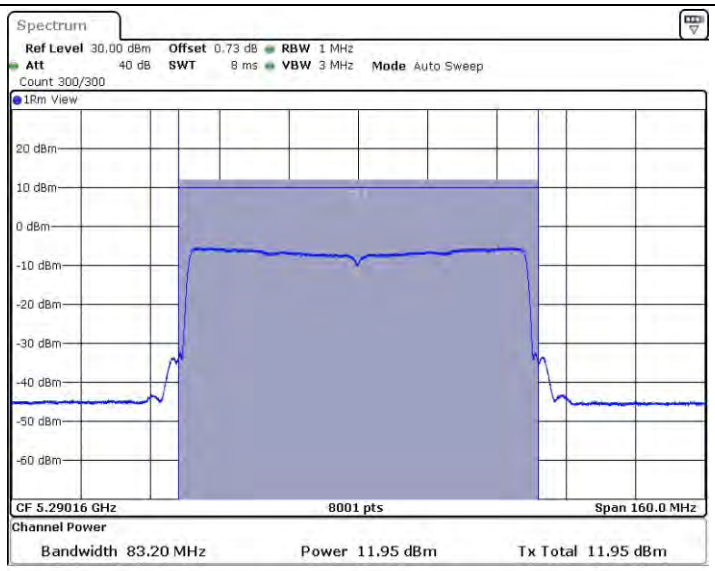
11AX80MIMO_Ant0_5210



11AX80MIMO_Ant1_5210

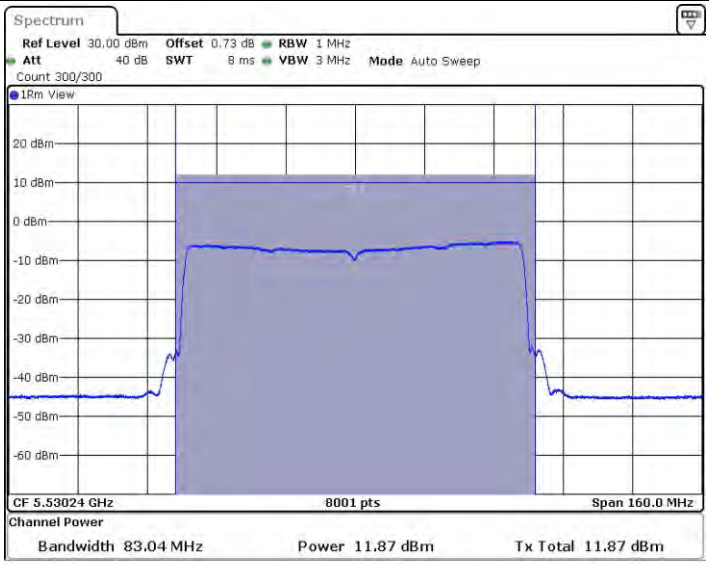


11AX80MIMO_Ant0_5290



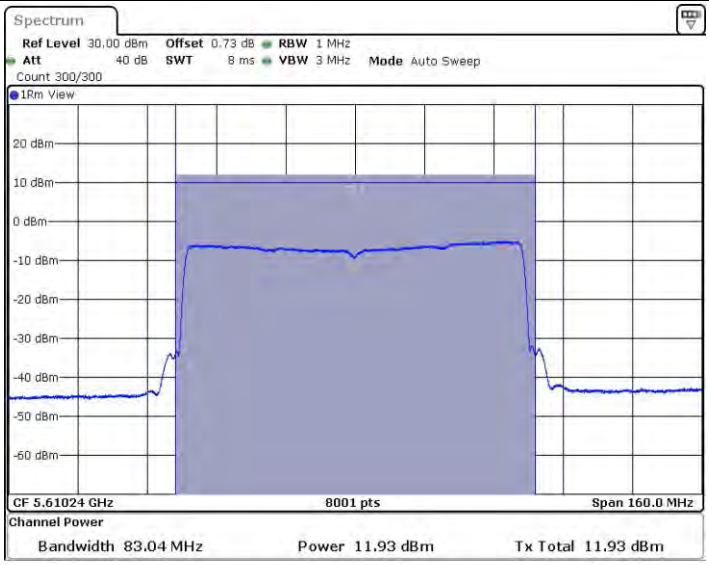
11AX80MIMO_Ant1_5290

11AX80MIMO_Ant0_5530



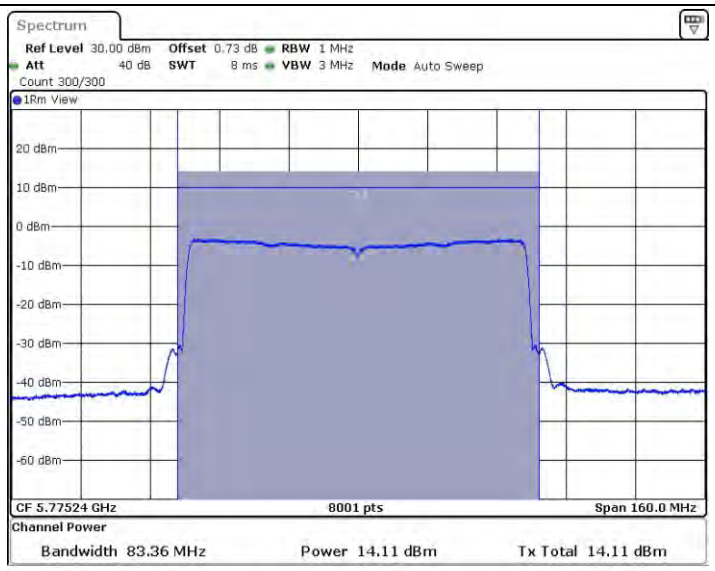
11AX80MIMO_Ant1_5530

11AX80MIMO_Ant0_5610



11AX80MIMO_Ant1_5610

11AX80MIMO_Ant0_5775



11AX80MIMO_Ant1_5775

Appendix C: Maximum power spectral density

Test Result

TestMode	Antenna	Channel	Result	Limit	Verdict
11A	Ant0	5180	9.56	<=11	PASS
	Ant1	5180	10.58	<=11	PASS
	Ant0	5200	9	<=11	PASS
	Ant1	5200	10.06	<=11	PASS
	Ant0	5240	8.64	<=11	PASS
	Ant1	5240	9.61	<=11	PASS
	Ant0	5260	9.82	<=11	PASS
	Ant1	5260	10.13	<=11	PASS
	Ant0	5280	9.91	<=11	PASS
	Ant1	5280	10.05	<=11	PASS
	Ant0	5320	9.64	<=11	PASS
	Ant1	5320	10.05	<=11	PASS
	Ant0	5500	10.23	<=11	PASS
	Ant1	5500	9.37	<=11	PASS
	Ant0	5580	10.51	<=11	PASS
	Ant1	5580	9.08	<=11	PASS
	Ant0	5700	10.17	<=11	PASS
	Ant1	5700	9.8	<=11	PASS
	Ant0	5745	10.01	<=30	PASS
	Ant1	5745	10.67	<=30	PASS
	Ant0	5785	8.92	<=30	PASS
	Ant1	5785	10.56	<=30	PASS
	Ant0	5825	9.5	<=30	PASS
	Ant1	5825	11.12	<=30	PASS
11N20SISO	Ant0	5180	9.88	<=11	PASS
	Ant1	5180	9.56	<=11	PASS
	Ant0	5200	9.48	<=11	PASS
	Ant1	5200	9.01	<=11	PASS
	Ant0	5240	9.33	<=11	PASS
	Ant1	5240	8.56	<=11	PASS
	Ant0	5260	10.48	<=11	PASS
	Ant1	5260	10.16	<=11	PASS
	Ant0	5280	10.49	<=11	PASS
	Ant1	5280	10.04	<=11	PASS
	Ant0	5320	10.03	<=11	PASS
	Ant1	5320	10.22	<=11	PASS
	Ant0	5500	9.49	<=11	PASS
	Ant1	5500	9.33	<=11	PASS
	Ant0	5580	10.3	<=11	PASS
	Ant1	5580	9.04	<=11	PASS

	Ant0	5700	9.88	<=11	PASS
	Ant1	5700	10.28	<=11	PASS
	Ant0	5745	7.83	<=30	PASS
	Ant1	5745	9.48	<=30	PASS
	Ant0	5785	8.01	<=30	PASS
	Ant1	5785	9.76	<=30	PASS
	Ant0	5825	8.22	<=30	PASS
	Ant1	5825	10.16	<=30	PASS
11N40SISO	Ant0	5190	6.36	<=11	PASS
	Ant1	5190	7.02	<=11	PASS
	Ant0	5230	6.81	<=11	PASS
	Ant1	5230	6.88	<=11	PASS
	Ant0	5270	5.42	<=11	PASS
	Ant1	5270	5.97	<=11	PASS
	Ant0	5310	5.45	<=11	PASS
	Ant1	5310	6.35	<=11	PASS
	Ant0	5510	7.33	<=11	PASS
	Ant1	5510	6.75	<=11	PASS
	Ant0	5550	6.29	<=11	PASS
	Ant1	5550	7.1	<=11	PASS
	Ant0	5670	6.07	<=11	PASS
	Ant1	5670	7.36	<=11	PASS
	Ant0	5755	4.95	<=30	PASS
	Ant1	5755	7.68	<=30	PASS
	Ant0	5795	6	<=30	PASS
	Ant1	5795	7.47	<=30	PASS
11AC20SISO	Ant0	5180	9.38	<=11	PASS
	Ant1	5180	10	<=11	PASS
	Ant0	5200	9.35	<=11	PASS
	Ant1	5200	9.45	<=11	PASS
	Ant0	5240	9.06	<=11	PASS
	Ant1	5240	8.75	<=11	PASS
	Ant0	5260	9.33	<=11	PASS
	Ant1	5260	9.58	<=11	PASS
	Ant0	5280	9.11	<=11	PASS
	Ant1	5280	9.2	<=11	PASS
	Ant0	5320	8.05	<=11	PASS
	Ant1	5320	9.22	<=11	PASS
	Ant0	5500	9.79	<=11	PASS
	Ant1	5500	10.02	<=11	PASS
	Ant0	5580	10.36	<=11	PASS
	Ant1	5580	9.79	<=11	PASS
	Ant0	5700	9.69	<=11	PASS
	Ant1	5700	10.83	<=11	PASS
	Ant0	5745	7.82	<=30	PASS

	Ant1	5745	10.58	<=30	PASS
	Ant0	5785	8.06	<=30	PASS
	Ant1	5785	10.92	<=30	PASS
	Ant0	5825	8.39	<=30	PASS
	Ant1	5825	10.51	<=30	PASS
11AC40SISO	Ant0	5190	7	<=11	PASS
	Ant1	5190	7.52	<=11	PASS
	Ant0	5230	6.45	<=11	PASS
	Ant1	5230	6.86	<=11	PASS
	Ant0	5270	7.79	<=11	PASS
	Ant1	5270	6.36	<=11	PASS
	Ant0	5310	7.56	<=11	PASS
	Ant1	5310	6.35	<=11	PASS
	Ant0	5510	7.07	<=11	PASS
	Ant1	5510	7.06	<=11	PASS
	Ant0	5550	7.37	<=11	PASS
	Ant1	5550	6.68	<=11	PASS
	Ant0	5670	7.46	<=11	PASS
	Ant1	5670	7.76	<=11	PASS
	Ant0	5755	5.61	<=30	PASS
	Ant1	5755	6.78	<=30	PASS
	Ant0	5795	5.76	<=30	PASS
	Ant1	5795	6.75	<=30	PASS
11AC80SISO	Ant0	5210	3.57	<=11	PASS
	Ant1	5210	4.71	<=11	PASS
	Ant0	5290	3.39	<=11	PASS
	Ant1	5290	3.53	<=11	PASS
	Ant0	5530	4.35	<=11	PASS
	Ant1	5530	4.56	<=11	PASS
	Ant0	5610	4.41	<=11	PASS
	Ant1	5610	4.13	<=11	PASS
	Ant0	5775	4.19	<=30	PASS
	Ant1	5775	3.84	<=30	PASS
11AX20SISO	Ant0	5180	9.39	<=11	PASS
	Ant1	5180	10.61	<=11	PASS
	Ant0	5200	9.17	<=11	PASS
	Ant1	5200	9.86	<=11	PASS
	Ant0	5240	8.98	<=11	PASS
	Ant1	5240	9.49	<=11	PASS
	Ant0	5260	8.97	<=11	PASS
	Ant1	5260	9.61	<=11	PASS
	Ant0	5280	8.8	<=11	PASS
	Ant1	5280	9.58	<=11	PASS
	Ant0	5320	8.5	<=11	PASS
	Ant1	5320	9.8	<=11	PASS

	Ant0	5500	8.04	<=11	PASS
	Ant1	5500	9.43	<=11	PASS
	Ant0	5580	8.7	<=11	PASS
	Ant1	5580	8.74	<=11	PASS
	Ant0	5700	8.04	<=11	PASS
	Ant1	5700	9.91	<=11	PASS
	Ant0	5745	8.4	<=30	PASS
	Ant1	5745	10.16	<=30	PASS
	Ant0	5785	8.68	<=30	PASS
	Ant1	5785	10.24	<=30	PASS
	Ant0	5825	9.72	<=30	PASS
	Ant1	5825	10.47	<=30	PASS
11AX40SISO	Ant0	5190	9.03	<=11	PASS
	Ant1	5190	9.48	<=11	PASS
	Ant0	5230	8.67	<=11	PASS
	Ant1	5230	8.98	<=11	PASS
	Ant0	5270	8.75	<=11	PASS
	Ant1	5270	8.36	<=11	PASS
	Ant0	5310	8.86	<=11	PASS
	Ant1	5310	8.36	<=11	PASS
	Ant0	5510	9.42	<=11	PASS
	Ant1	5510	9.27	<=11	PASS
	Ant0	5550	9.19	<=11	PASS
	Ant1	5550	8.78	<=11	PASS
	Ant0	5670	9.56	<=11	PASS
	Ant1	5670	8.9	<=11	PASS
	Ant0	5755	8.03	<=30	PASS
	Ant1	5755	8.9	<=30	PASS
	Ant0	5795	8.09	<=30	PASS
	Ant1	5795	8.93	<=30	PASS
11AX80SISO	Ant0	5210	6.47	<=11	PASS
	Ant1	5210	7.23	<=11	PASS
	Ant0	5290	6.25	<=11	PASS
	Ant1	5290	5.95	<=11	PASS
	Ant0	5530	6.54	<=11	PASS
	Ant1	5530	6.01	<=11	PASS
	Ant0	5610	7.04	<=11	PASS
	Ant1	5610	5.85	<=11	PASS
	Ant0	5775	5.97	<=30	PASS
	Ant1	5775	6.61	<=30	PASS
11N20MIMO	Ant0	5180	6.07	<=11	PASS
	Ant1	5180	6.09	<=11	PASS
	total	5180	9.09	<=11	PASS
	Ant0	5200	5.51	<=11	PASS
	Ant1	5200	5.6	<=11	PASS

	total	5200	8.57	<=11	PASS
	Ant0	5240	5.04	<=11	PASS
	Ant1	5240	5.26	<=11	PASS
	total	5240	8.16	<=11	PASS
	Ant0	5260	5.1	<=11	PASS
	Ant1	5260	4.76	<=11	PASS
	total	5260	7.94	<=11	PASS
	Ant0	5280	5.23	<=11	PASS
	Ant1	5280	4.48	<=11	PASS
	total	5280	7.88	<=11	PASS
	Ant0	5320	5.03	<=11	PASS
	Ant1	5320	5.01	<=11	PASS
	total	5320	8.03	<=11	PASS
	Ant0	5500	5.14	<=11	PASS
	Ant1	5500	5.04	<=11	PASS
	total	5500	8.10	<=11	PASS
	Ant0	5580	5.19	<=11	PASS
	Ant1	5580	5.2	<=11	PASS
	total	5580	8.21	<=11	PASS
	Ant0	5700	4.59	<=11	PASS
	Ant1	5700	6.08	<=11	PASS
	total	5700	8.41	<=11	PASS
	Ant0	5745	3.08	<=30	PASS
	Ant1	5745	4.65	<=30	PASS
	total	5745	6.95	<=30	PASS
	Ant0	5785	3.4	<=30	PASS
	Ant1	5785	4.93	<=30	PASS
	total	5785	7.24	<=30	PASS
	Ant0	5825	3.47	<=30	PASS
	Ant1	5825	4.86	<=30	PASS
	total	5825	7.23	<=30	PASS
11N40MIMO	Ant0	5190	2.6	<=11	PASS
	Ant1	5190	3.44	<=11	PASS
	total	5190	6.05	<=11	PASS
	Ant0	5230	2.42	<=11	PASS
	Ant1	5230	3.28	<=11	PASS
	total	5230	5.88	<=11	PASS
	Ant0	5270	2.36	<=11	PASS
	Ant1	5270	2.27	<=11	PASS
	total	5270	5.33	<=11	PASS
	Ant0	5310	2.19	<=11	PASS
	Ant1	5310	2.4	<=11	PASS
	total	5310	5.31	<=11	PASS
	Ant0	5510	2.22	<=11	PASS
	Ant1	5510	2.93	<=11	PASS

	total	5510	5.60	<=11	PASS
	Ant0	5550	2.35	<=11	PASS
	Ant1	5550	2.66	<=11	PASS
	total	5550	5.52	<=11	PASS
	Ant0	5670	2.8	<=11	PASS
	Ant1	5670	3.26	<=11	PASS
	total	5670	6.05	<=11	PASS
	Ant0	5755	2.01	<=30	PASS
	Ant1	5755	3.45	<=30	PASS
	total	5755	5.80	<=30	PASS
	Ant0	5795	2.42	<=30	PASS
	Ant1	5795	3.75	<=30	PASS
	total	5795	6.15	<=30	PASS
11AC20MIMO	Ant0	5180	5.86	<=11	PASS
	Ant1	5180	8.59	<=11	PASS
	total	5180	10.45	<=11	PASS
	Ant0	5200	5.57	<=11	PASS
	Ant1	5200	8.34	<=11	PASS
	total	5200	10.18	<=11	PASS
	Ant0	5240	4.7	<=11	PASS
	Ant1	5240	7.98	<=11	PASS
	total	5240	9.65	<=11	PASS
	Ant0	5260	4.64	<=11	PASS
	Ant1	5260	8.76	<=11	PASS
	total	5260	10.18	<=11	PASS
	Ant0	5280	4.14	<=11	PASS
	Ant1	5280	8.27	<=11	PASS
	total	5280	9.69	<=11	PASS
	Ant0	5320	3.58	<=11	PASS
	Ant1	5320	8.18	<=11	PASS
	total	5320	9.47	<=11	PASS
	Ant0	5500	4.37	<=11	PASS
	Ant1	5500	7.8	<=11	PASS
	total	5500	9.43	<=11	PASS
	Ant0	5580	5.78	<=11	PASS
	Ant1	5580	8.01	<=11	PASS
	total	5580	10.05	<=11	PASS
	Ant0	5700	6.75	<=11	PASS
	Ant1	5700	8.66	<=11	PASS
	total	5700	10.82	<=11	PASS
	Ant0	5745	5.46	<=30	PASS
	Ant1	5745	6.36	<=30	PASS
	total	5745	8.94	<=30	PASS
	Ant0	5785	5.69	<=30	PASS
	Ant1	5785	7.03	<=30	PASS

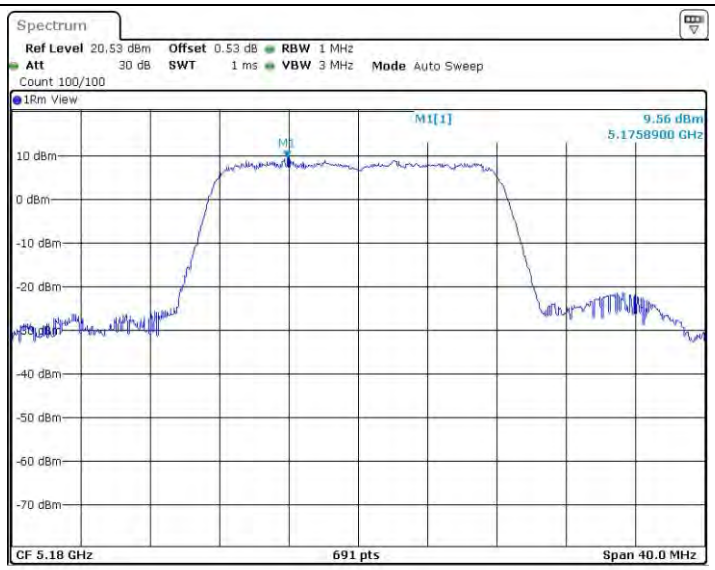
	total	5785	9.42	<=30	PASS
	Ant0	5825	6.18	<=30	PASS
	Ant1	5825	7.52	<=30	PASS
	total	5825	9.91	<=30	PASS
11AC40MIMO	Ant0	5190	2.56	<=11	PASS
	Ant1	5190	3.8	<=11	PASS
	total	5190	6.23	<=11	PASS
	Ant0	5230	1.99	<=11	PASS
	Ant1	5230	4.22	<=11	PASS
	total	5230	6.26	<=11	PASS
	Ant0	5270	3.11	<=11	PASS
	Ant1	5270	3.75	<=11	PASS
	total	5270	6.45	<=11	PASS
	Ant0	5310	2.63	<=11	PASS
	Ant1	5310	3.67	<=11	PASS
	total	5310	6.19	<=11	PASS
	Ant0	5510	1.9	<=11	PASS
	Ant1	5510	3.64	<=11	PASS
	total	5510	5.87	<=11	PASS
	Ant0	5550	2.1	<=11	PASS
	Ant1	5550	3.73	<=11	PASS
	total	5550	6.00	<=11	PASS
	Ant0	5670	2.26	<=11	PASS
	Ant1	5670	4.61	<=11	PASS
	total	5670	6.60	<=11	PASS
	Ant0	5755	1.76	<=30	PASS
	Ant1	5755	4.56	<=30	PASS
	total	5755	6.39	<=30	PASS
	Ant0	5795	2.02	<=30	PASS
	Ant1	5795	4.78	<=30	PASS
	total	5795	6.63	<=30	PASS
11AC80MIMO	Ant0	5210	-0.55	<=11	PASS
	Ant1	5210	1.14	<=11	PASS
	total	5210	3.39	<=11	PASS
	Ant0	5290	0.25	<=11	PASS
	Ant1	5290	0.96	<=11	PASS
	total	5290	3.63	<=11	PASS
	Ant0	5530	-0.56	<=11	PASS
	Ant1	5530	1.66	<=11	PASS
	total	5530	3.70	<=11	PASS
	Ant0	5610	-0.46	<=11	PASS
	Ant1	5610	1.51	<=11	PASS
	total	5610	3.65	<=11	PASS
	Ant0	5775	-1.15	<=30	PASS
	Ant1	5775	1.8	<=30	PASS

	total	5775	3.58	<=30	PASS
11AX20MIMO	Ant0	5180	7.14	<=11	PASS
	Ant1	5180	8.55	<=11	PASS
	total	5180	10.91	<=11	PASS
	Ant0	5200	7.29	<=11	PASS
	Ant1	5200	7.79	<=11	PASS
	total	5200	10.56	<=11	PASS
	Ant0	5240	7.11	<=11	PASS
	Ant1	5240	7.5	<=11	PASS
	total	5240	10.32	<=11	PASS
	Ant0	5260	6.75	<=11	PASS
	Ant1	5260	6.06	<=11	PASS
	total	5260	9.43	<=11	PASS
	Ant0	5280	6.88	<=11	PASS
	Ant1	5280	6.35	<=11	PASS
	total	5280	9.63	<=11	PASS
	Ant0	5320	6.47	<=11	PASS
	Ant1	5320	6.36	<=11	PASS
	total	5320	9.43	<=11	PASS
	Ant0	5500	6.21	<=11	PASS
	Ant1	5500	6.95	<=11	PASS
	total	5500	9.61	<=11	PASS
	Ant0	5580	6.83	<=11	PASS
	Ant1	5580	6.77	<=11	PASS
	total	5580	9.81	<=11	PASS
	Ant0	5700	5.72	<=11	PASS
	Ant1	5700	6.94	<=11	PASS
	total	5700	9.38	<=11	PASS
	Ant0	5745	6.91	<=30	PASS
	Ant1	5745	7.95	<=30	PASS
	total	5745	10.47	<=30	PASS
	Ant0	5785	7	<=30	PASS
	Ant1	5785	7.9	<=30	PASS
	total	5785	10.48	<=30	PASS
	Ant0	5825	7.12	<=30	PASS
	Ant1	5825	8.29	<=30	PASS
	total	5825	10.75	<=30	PASS
11AX40MIMO	Ant0	5190	4.89	<=11	PASS
	Ant1	5190	5.29	<=11	PASS
	total	5190	8.10	<=11	PASS
	Ant0	5230	4.66	<=11	PASS
	Ant1	5230	4.91	<=11	PASS
	total	5230	7.80	<=11	PASS
	Ant0	5270	5.52	<=11	PASS
	Ant1	5270	5.22	<=11	PASS

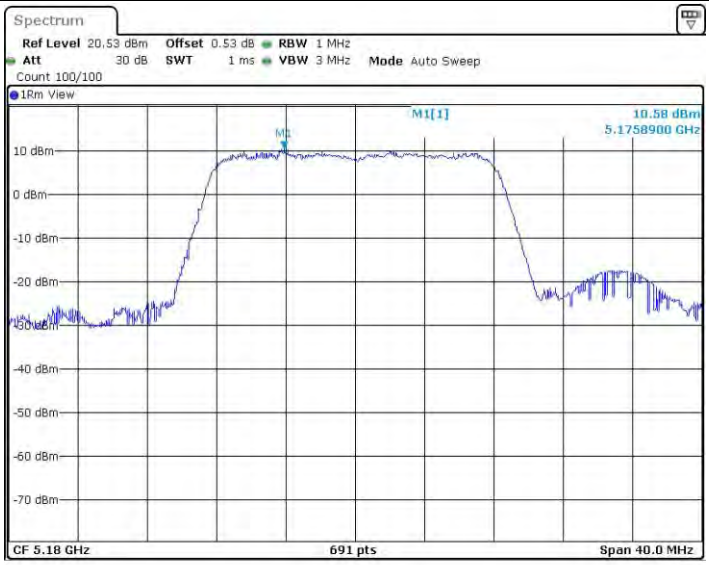
	total	5270	8.38	<=11	PASS
	Ant0	5310	5.46	<=11	PASS
	Ant1	5310	5.33	<=11	PASS
	total	5310	8.41	<=11	PASS
	Ant0	5510	5.64	<=11	PASS
	Ant1	5510	6.11	<=11	PASS
	total	5510	8.89	<=11	PASS
	Ant0	5550	5.76	<=11	PASS
	Ant1	5550	5.67	<=11	PASS
	total	5550	8.73	<=11	PASS
	Ant0	5670	6.13	<=11	PASS
	Ant1	5670	6.77	<=11	PASS
	total	5670	9.47	<=11	PASS
	Ant0	5755	4.44	<=30	PASS
	Ant1	5755	5.48	<=30	PASS
	total	5755	8.00	<=30	PASS
	Ant0	5795	4.32	<=30	PASS
	Ant1	5795	5.47	<=30	PASS
	total	5795	7.94	<=30	PASS
11AX80MIMO	Ant0	5210	1.31	<=11	PASS
	Ant1	5210	1.22	<=11	PASS
	total	5210	4.28	<=11	PASS
	Ant0	5290	1.97	<=11	PASS
	Ant1	5290	1.62	<=11	PASS
	total	5290	4.81	<=11	PASS
	Ant0	5530	1.66	<=11	PASS
	Ant1	5530	1.47	<=11	PASS
	total	5530	4.58	<=11	PASS
	Ant0	5610	2.84	<=11	PASS
	Ant1	5610	1.51	<=11	PASS
	total	5610	5.24	<=11	PASS
	Ant0	5775	1.6	<=30	PASS
	Ant1	5775	1.98	<=30	PASS
	total	5775	4.80	<=30	PASS

Note：The Duty Cycle Factor is compensated in the graph.

Test Graphs



11A_Ant0_5180



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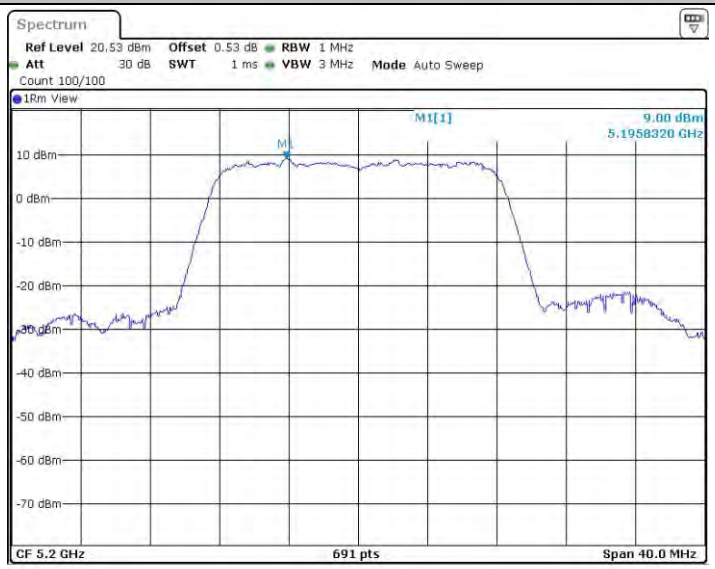

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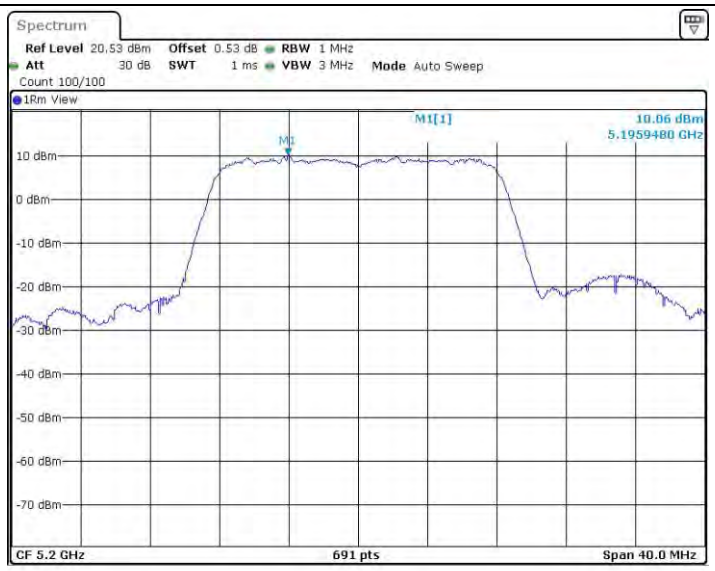
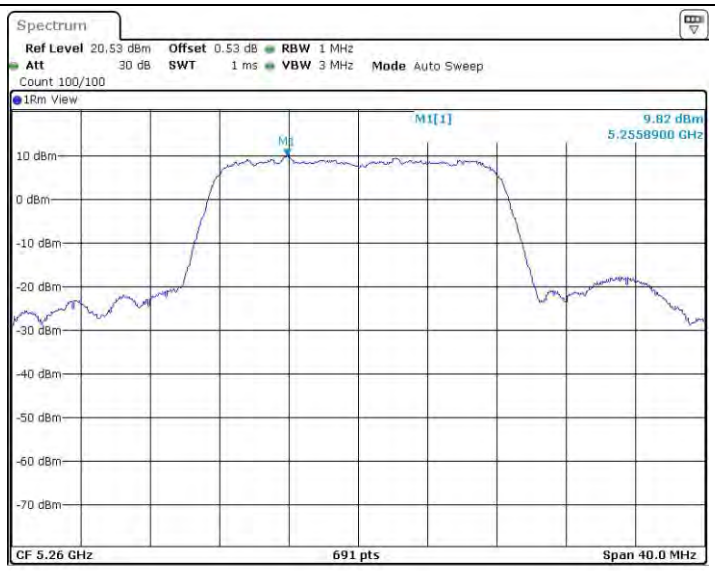


11A_Ant1_5200

11A_Ant0_5240

11A_Ant1_5240



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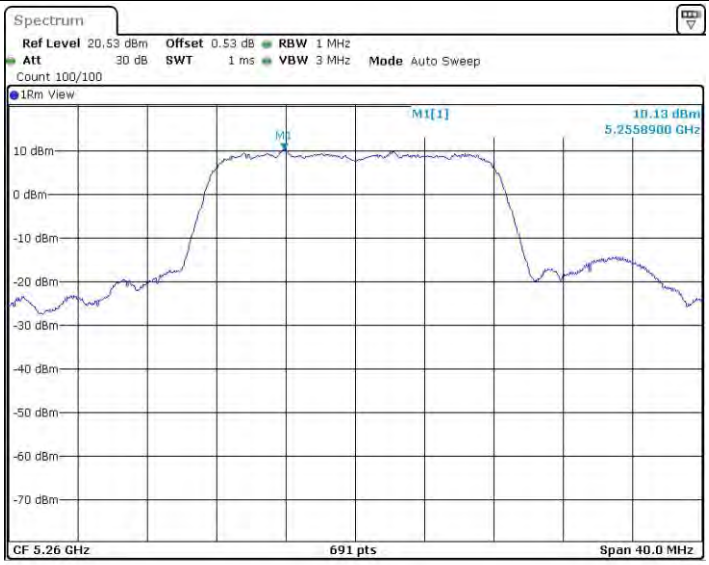

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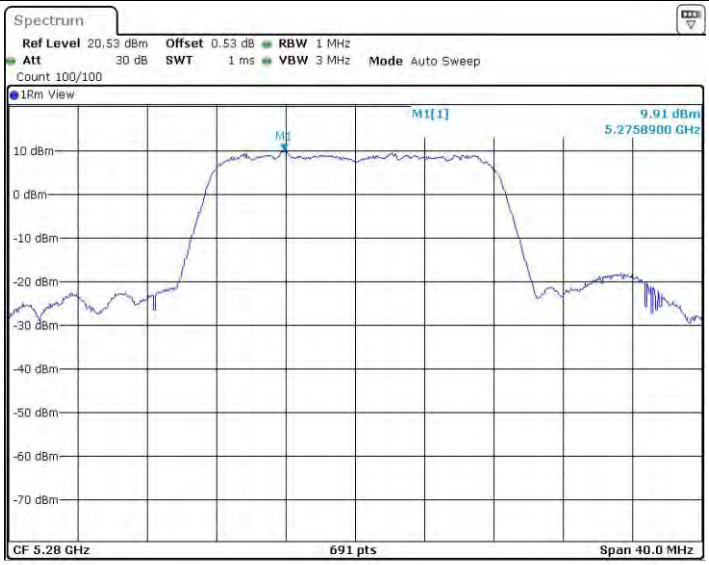
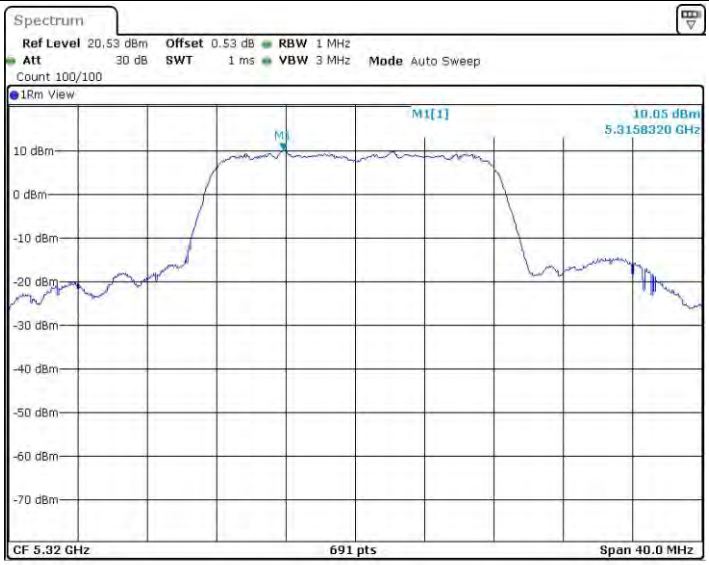


11A_Ant0_5280

11A_Ant1_5280

11A_Ant0_5320



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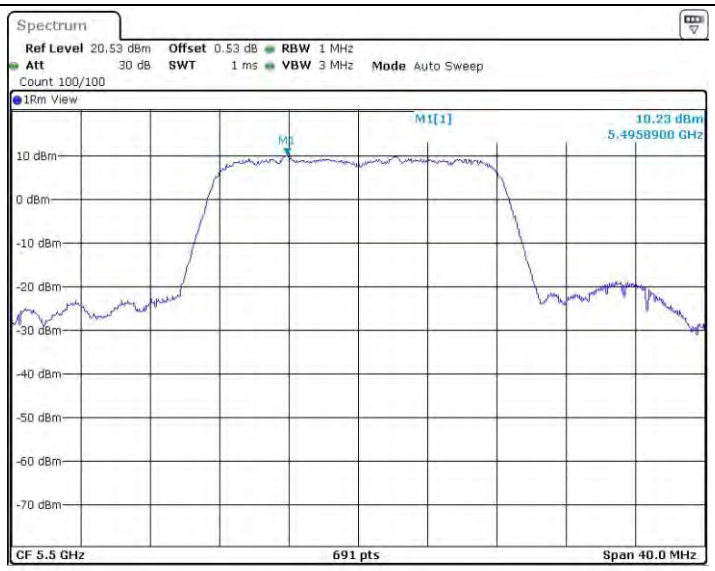

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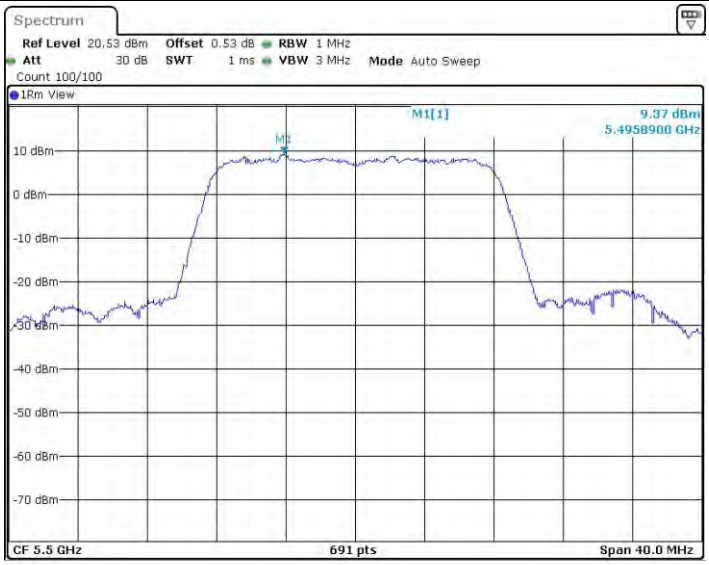

11A_Ant1_5500

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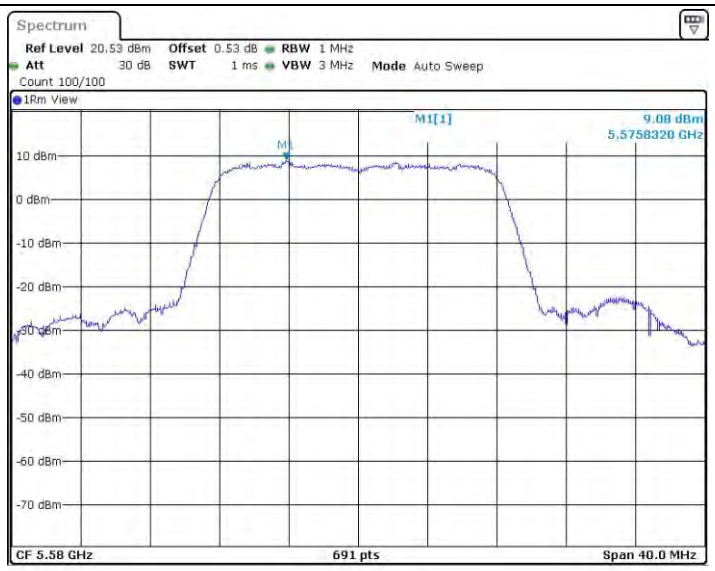
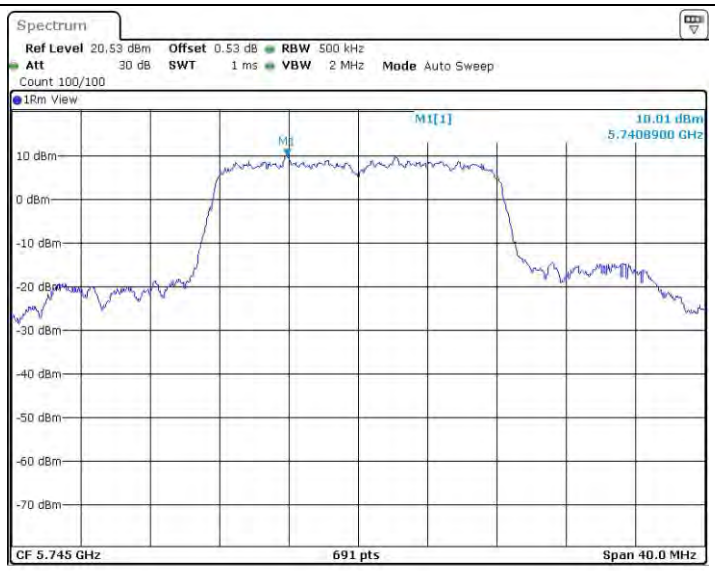


11A_Ant1_5580

11A_Ant0_5700

11A_Ant1_5700



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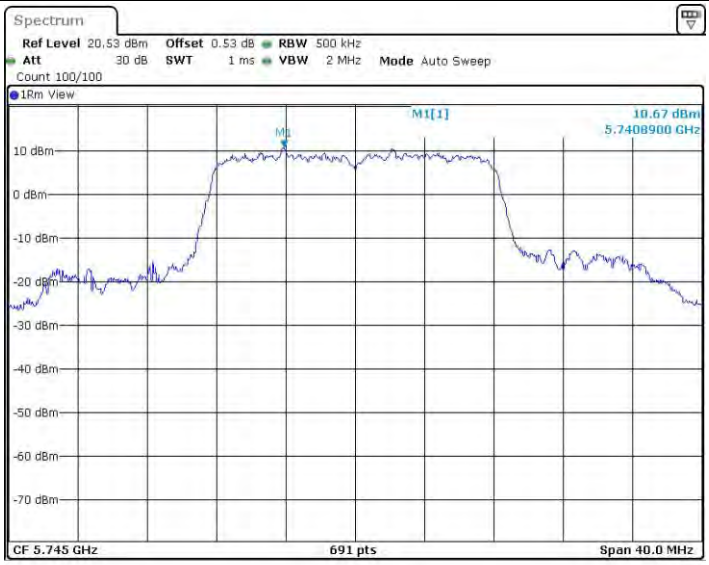
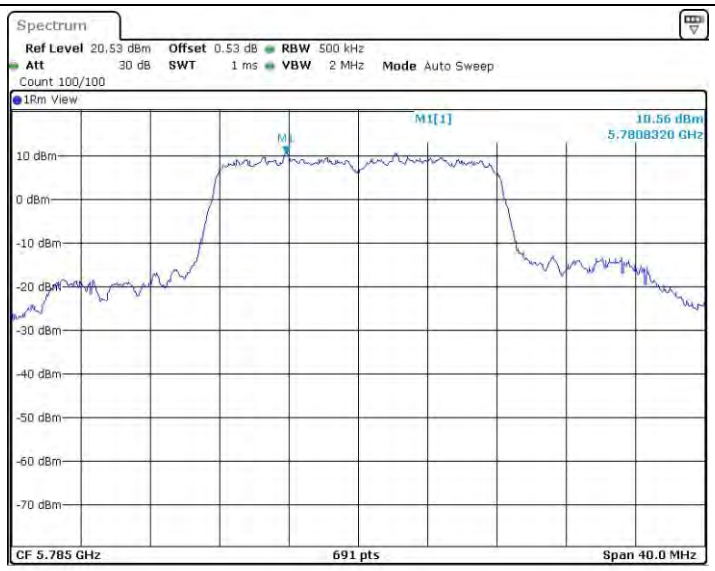


11A_Ant1_5745

11A_Ant0_5785



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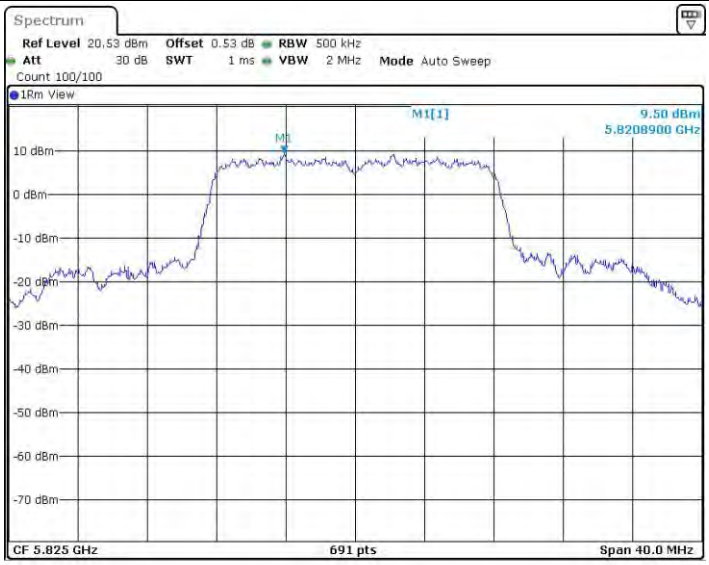

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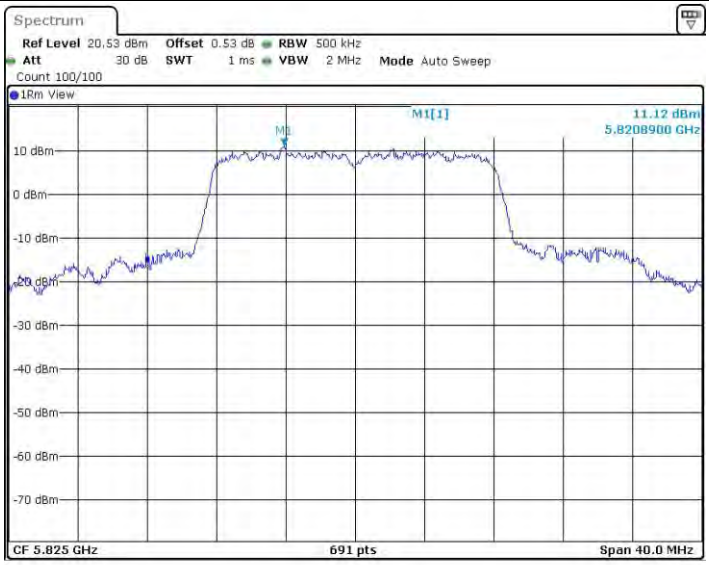
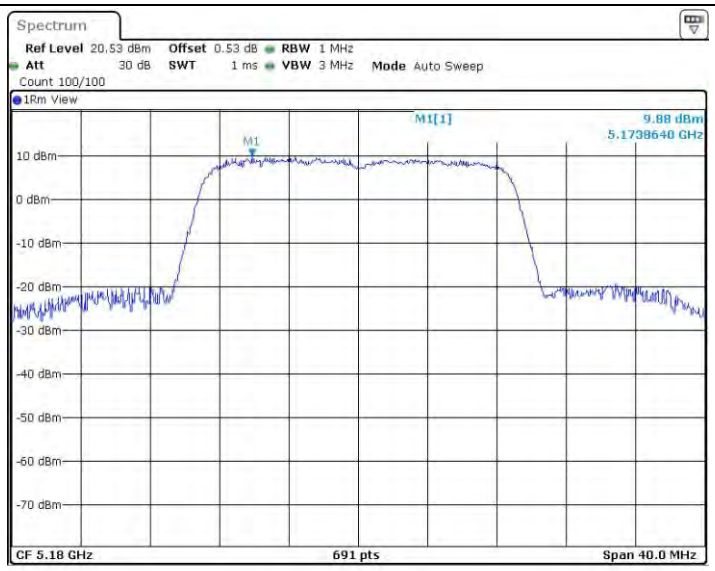
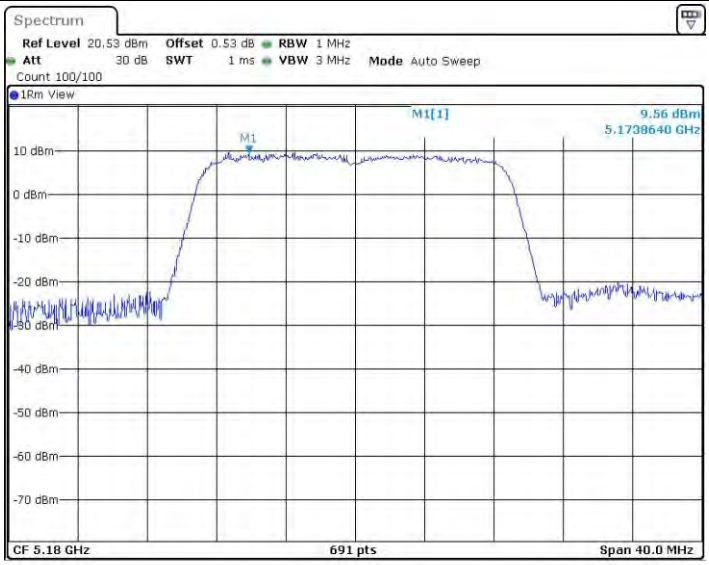


11A_Ant1_5825



11N20SISO_Ant0_5180



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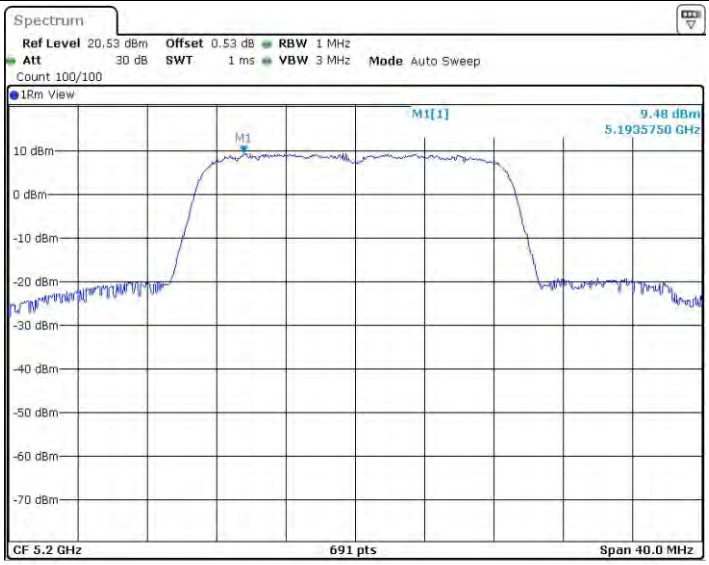

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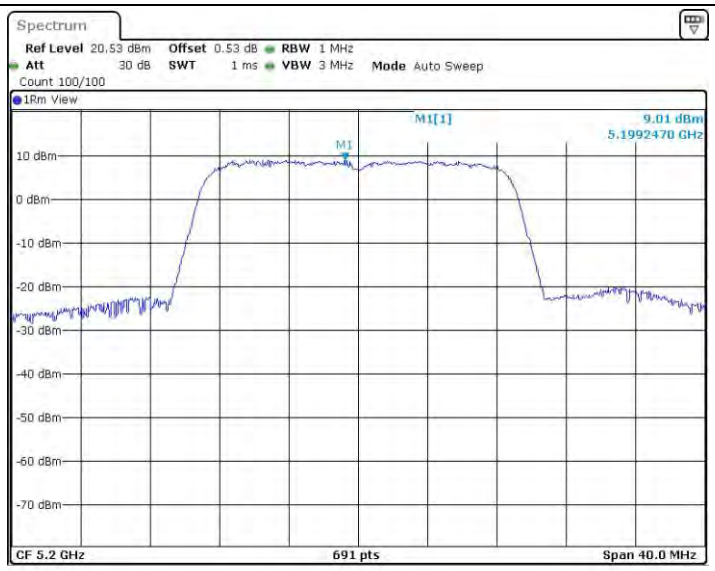

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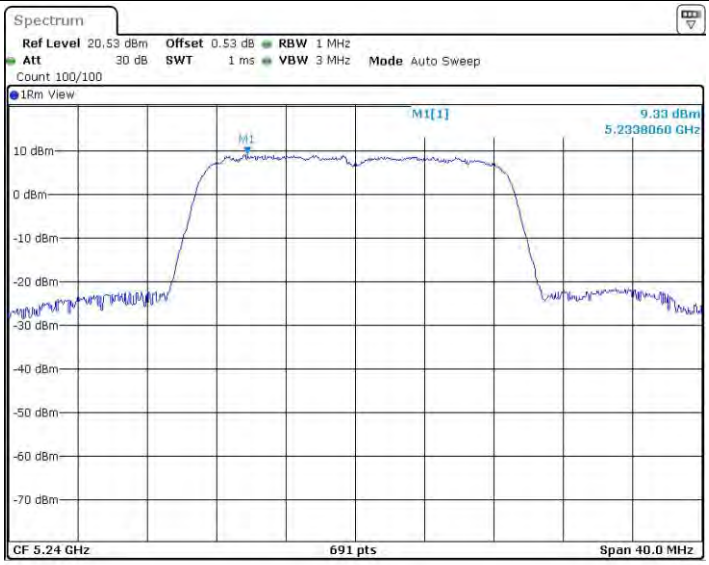

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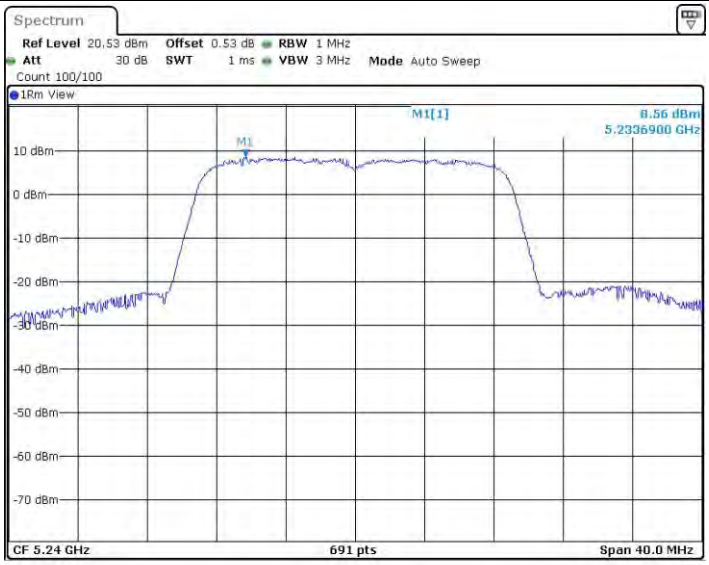
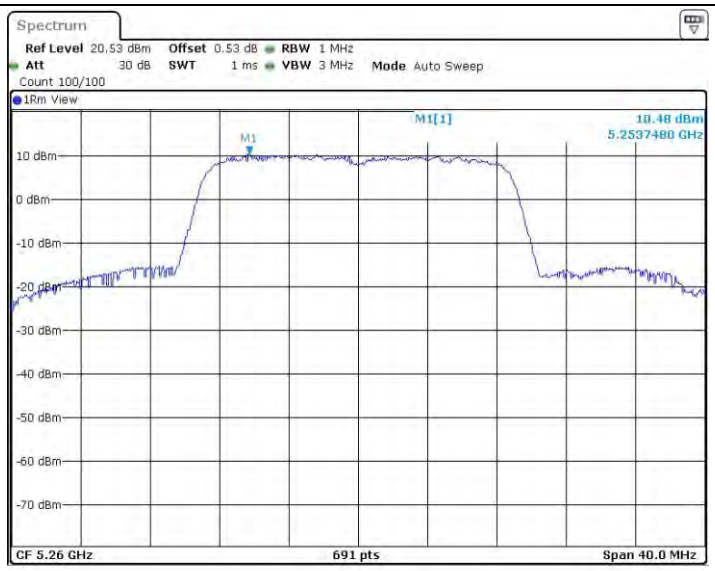


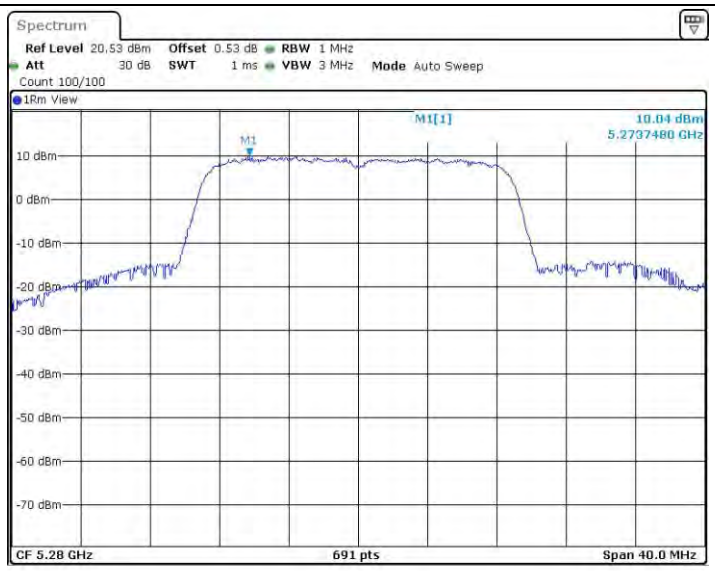
11N20SISO_Ant1_5240



11N20SISO_Ant0_5260

11N20SISO_Ant1_5260

11N20SISO_Ant0_5280



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11N20SISO_Ant0_5320

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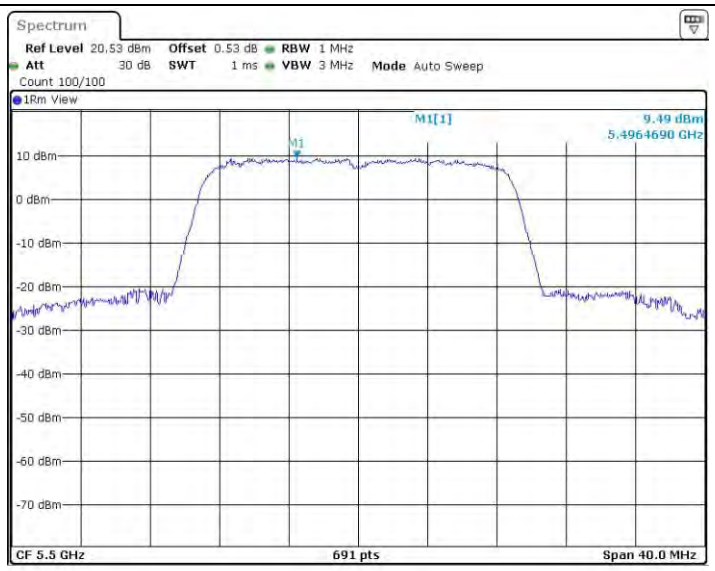

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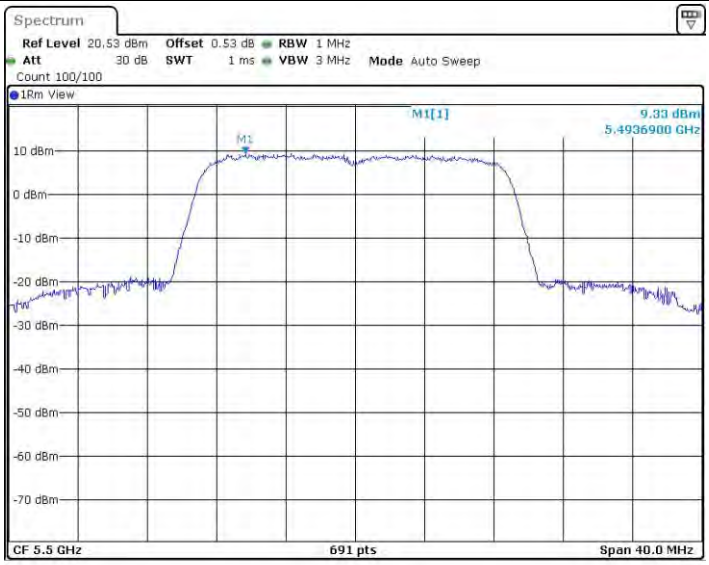

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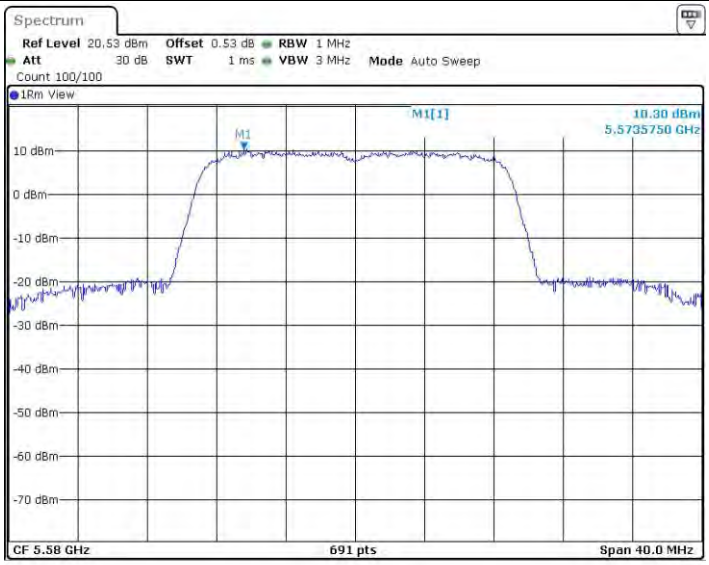

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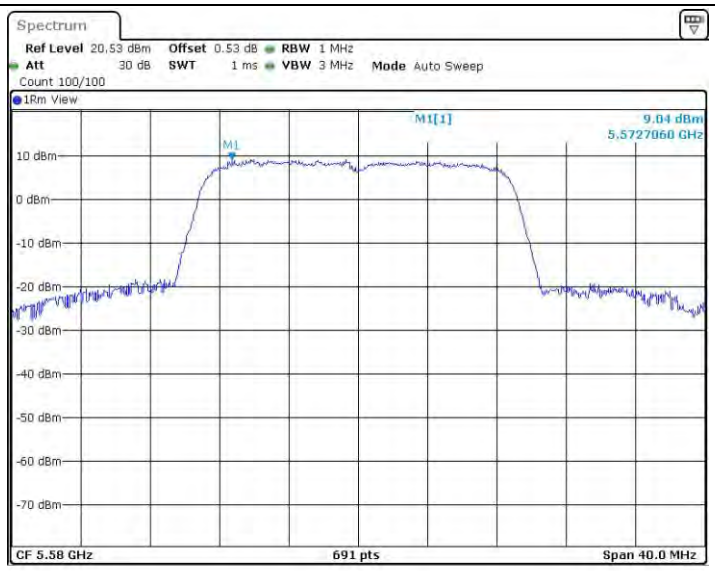

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11N20SISO_Ant0_5700

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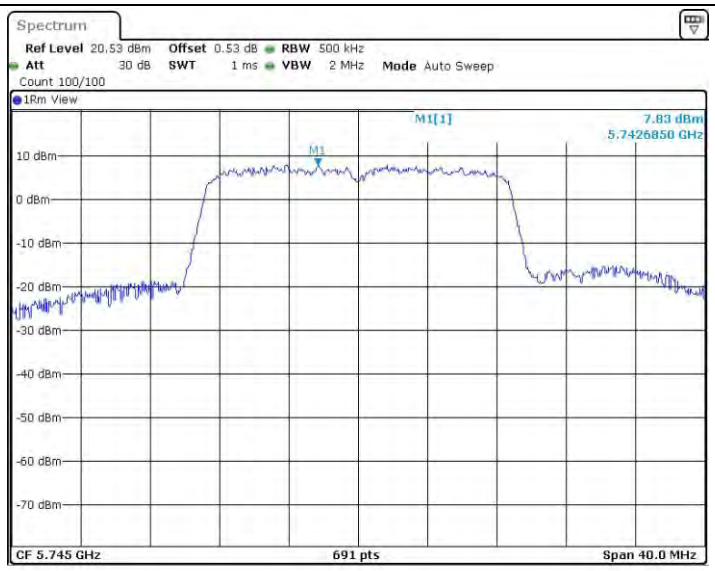

11N20SISO_Ant0_5745

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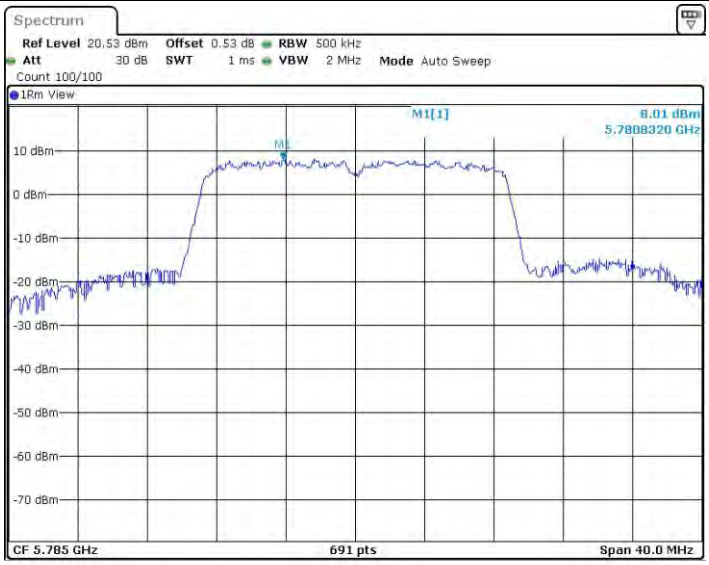

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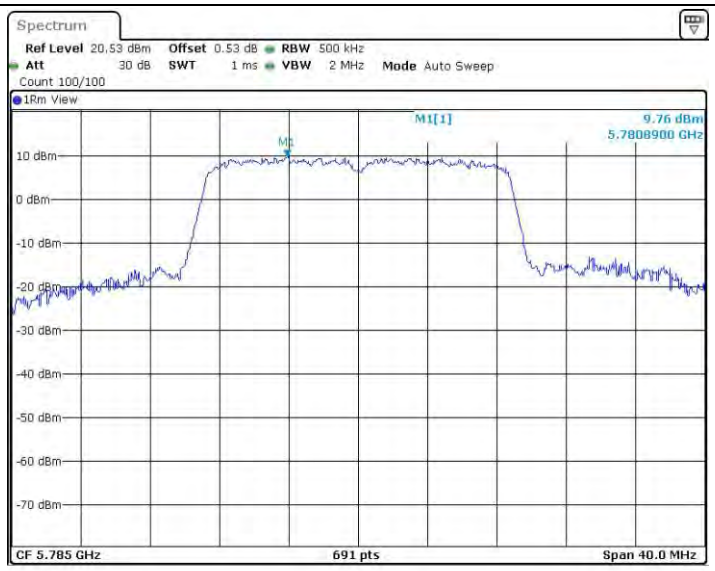

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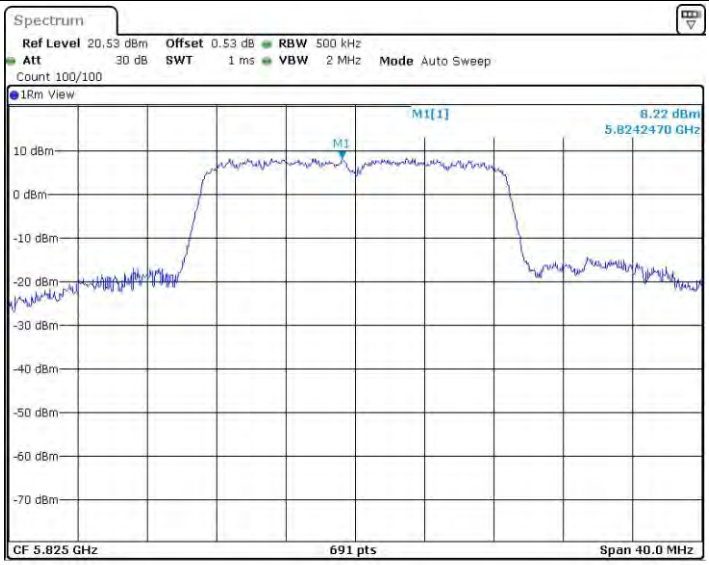

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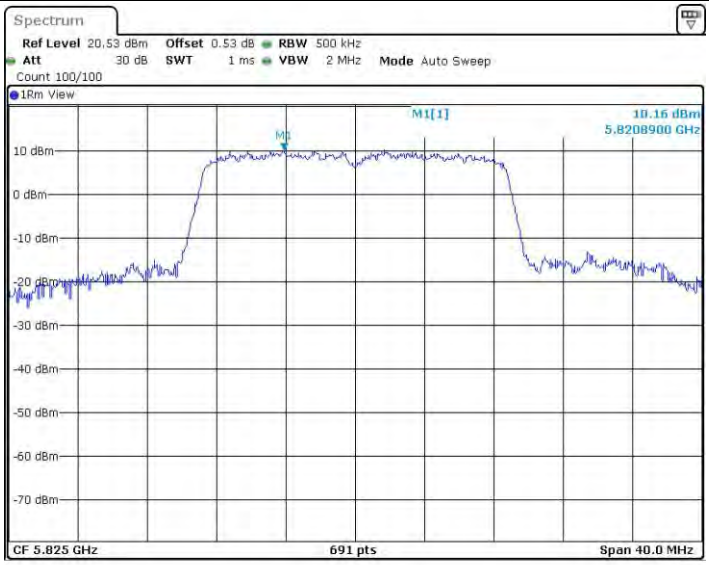

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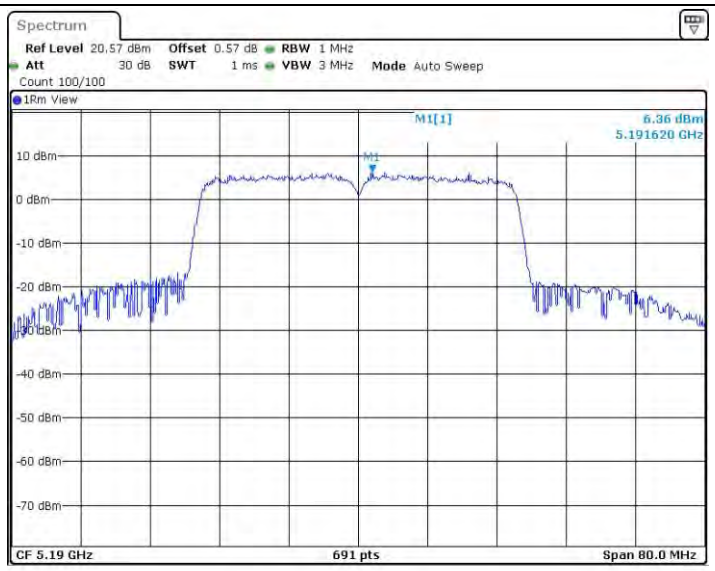

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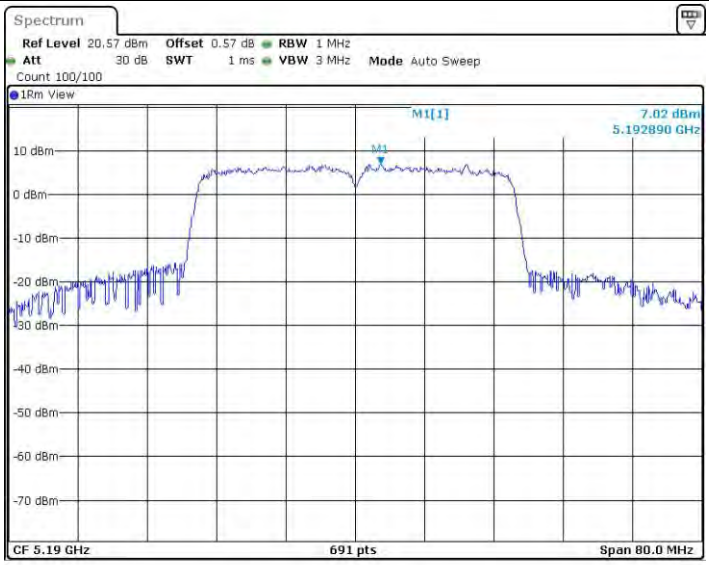
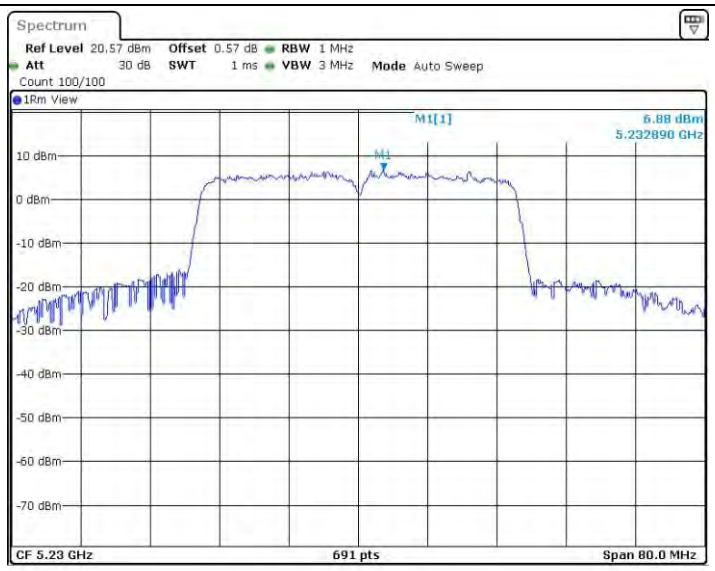


11N40SISO_Ant1_5190

11N40SISO_Ant0_5230



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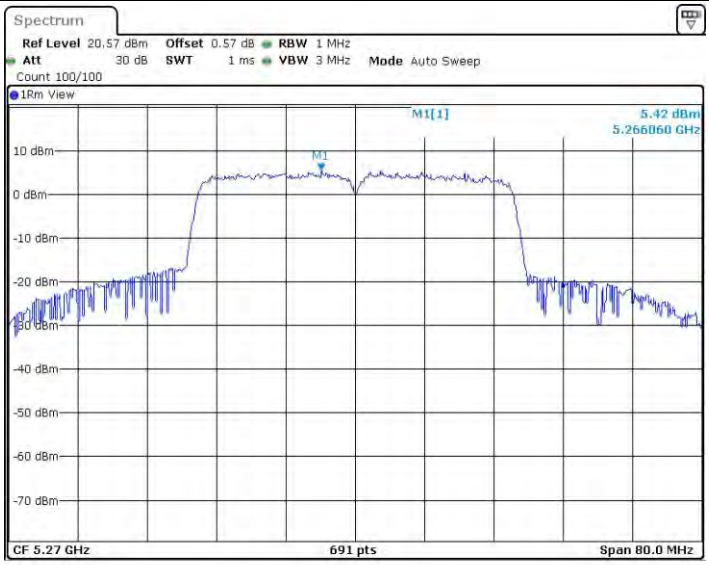

11N40SISO_Ant0_5270

11N40SISO_Ant1_5270