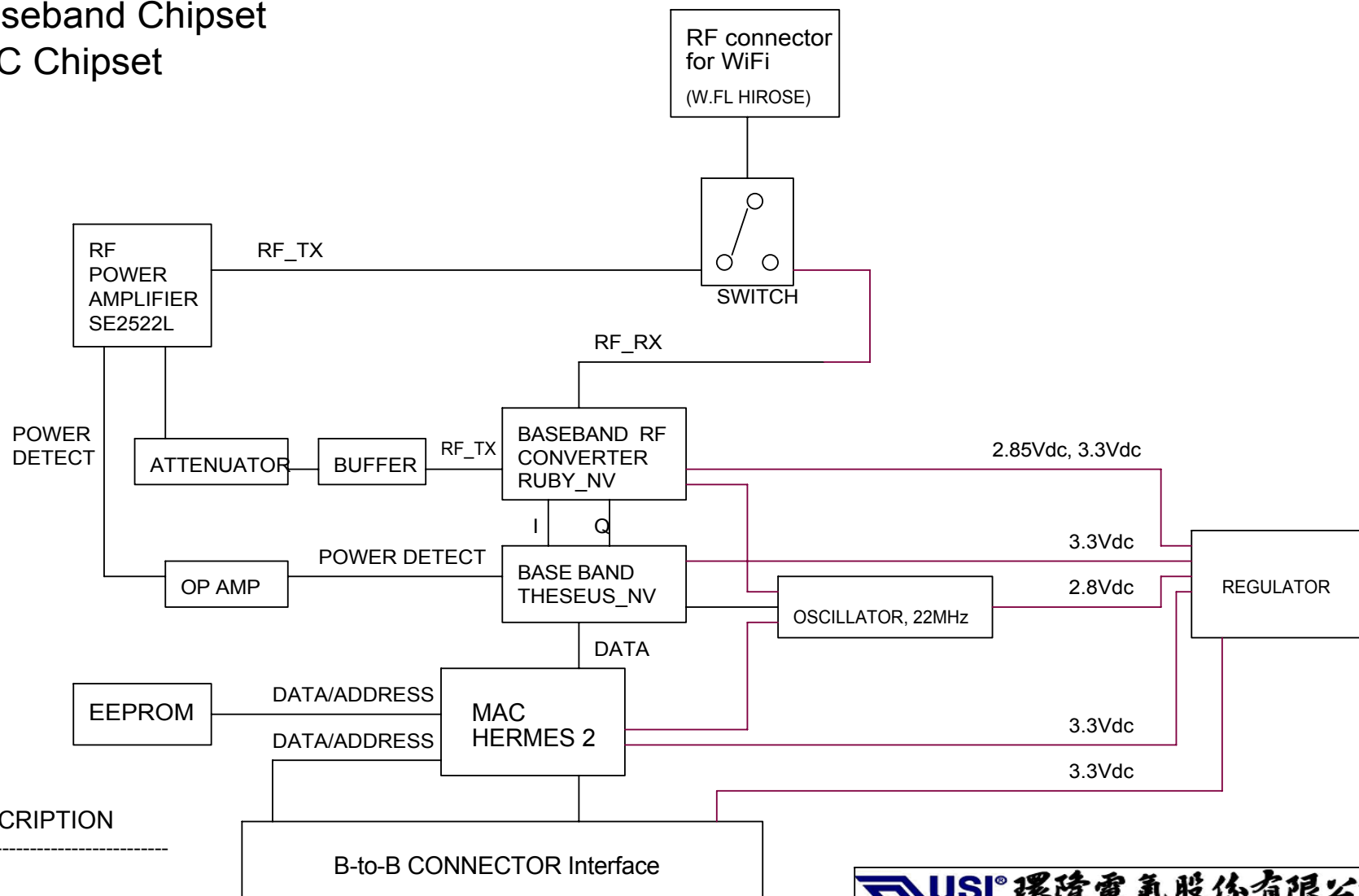


SiP Wireless LAN 802.11b - Only Module

Designed with AGERE RUBY2 RF Chipset
& THESEUS2 Baseband Chipset
& HERMES2 MAC Chipset



PAGE FUNCTIONAL DESCRIPTION

- 0 Blockdiagram
- 1 Host Interface & Power Supply
- 2 Hermes II
- 3 Theseus II
- 4 Base Band RF Converter
- 5 RF Power Amplifier & RF input/output

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