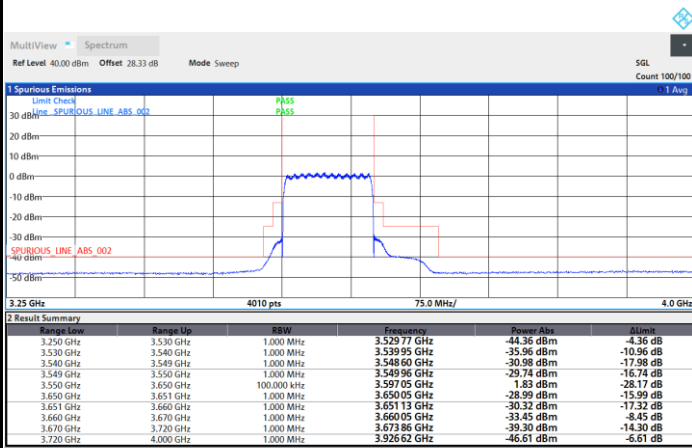




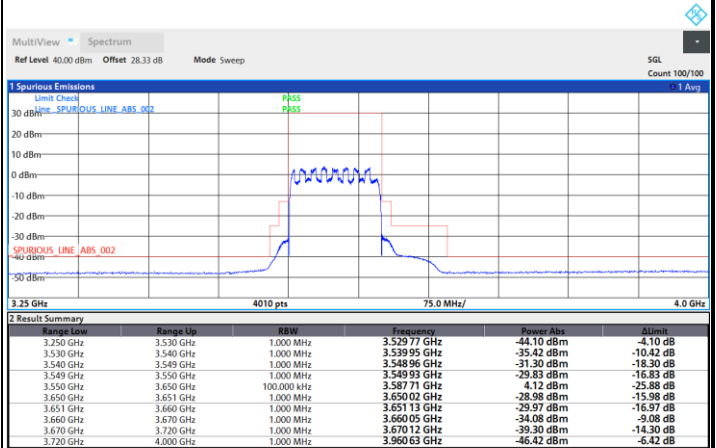
FR1 n48 / 100MHz / Lowest Channel / MASK

QPSK



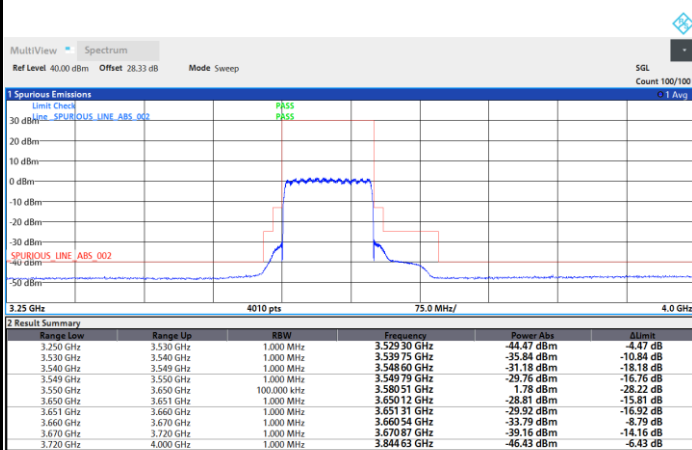
21:57:32 23.05.2023

16QAM



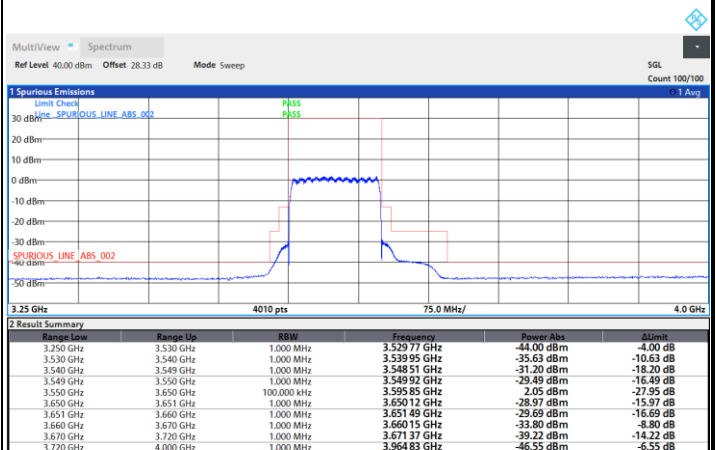
22:05:26 23.05.2023

64QAM



22:58:58 23.05.2023

256QAM

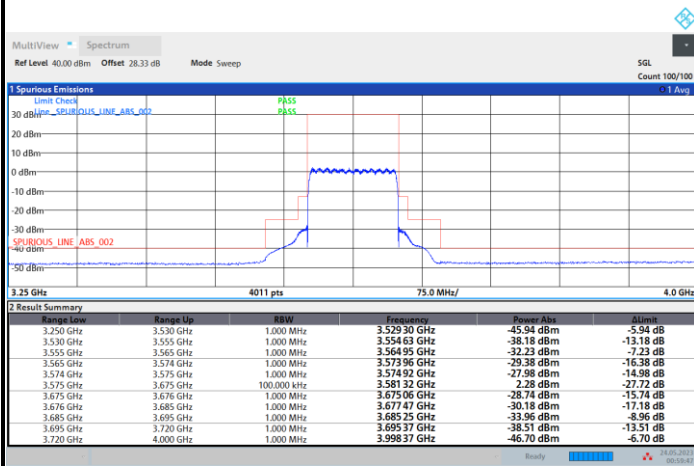


23:06:35 23.05.2023



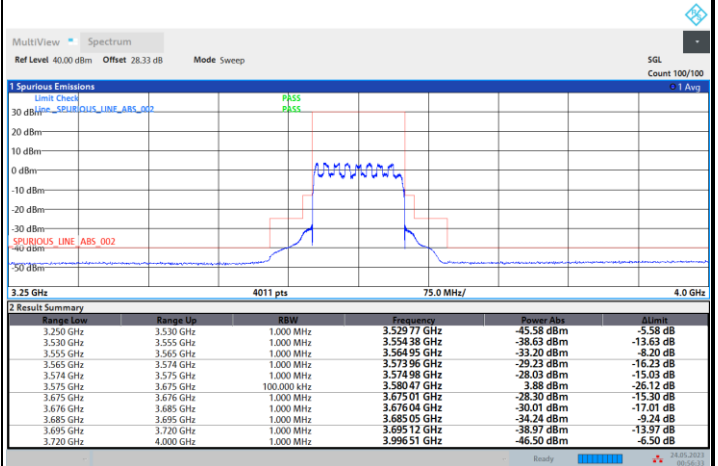
FR1 n48 / 100MHz / Middle Channel / MASK

QPSK



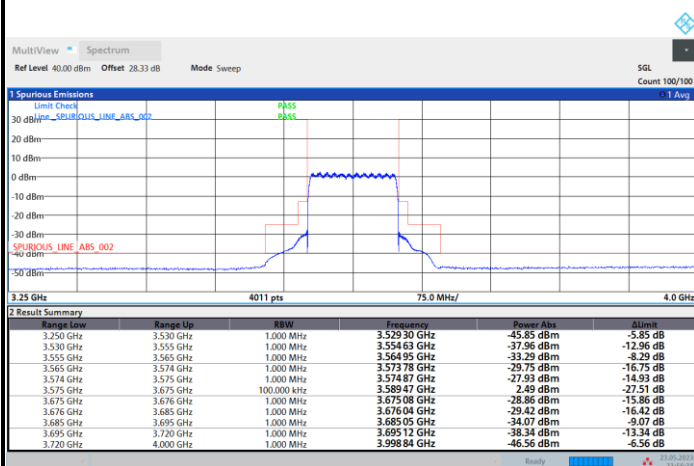
00:59:47 24.05.2023

16QAM



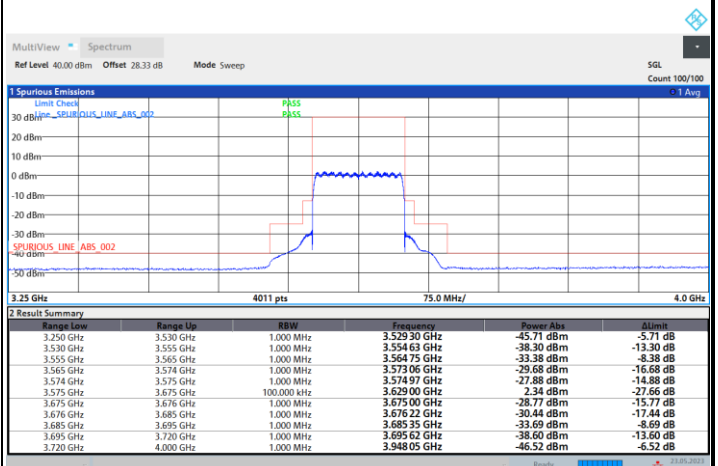
00:56:33 24.05.2023

64QAM



23:56:34 23.05.2023

256QAM

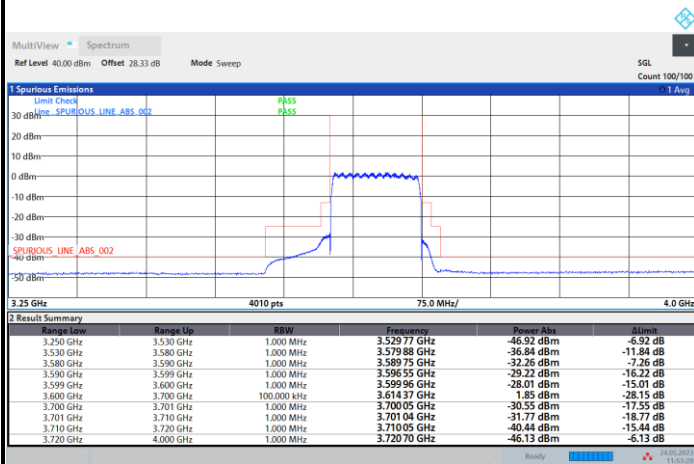


23:48:34 23.05.2023



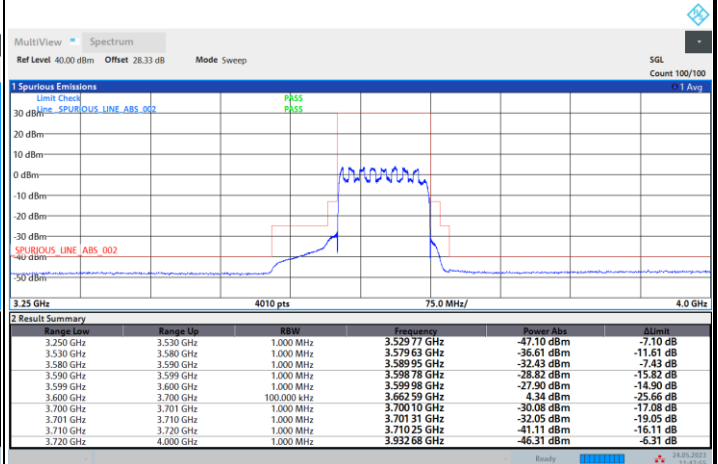
FR1 n48 / 100MHz / Highest Channel / MASK

QPSK



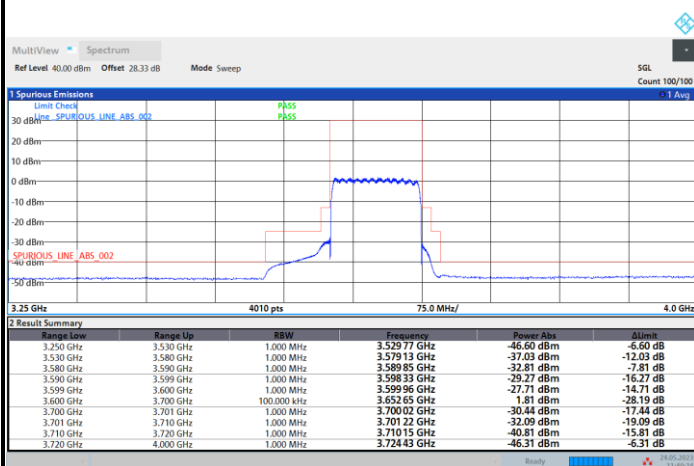
11:53:29 24.05.2023

16QAM



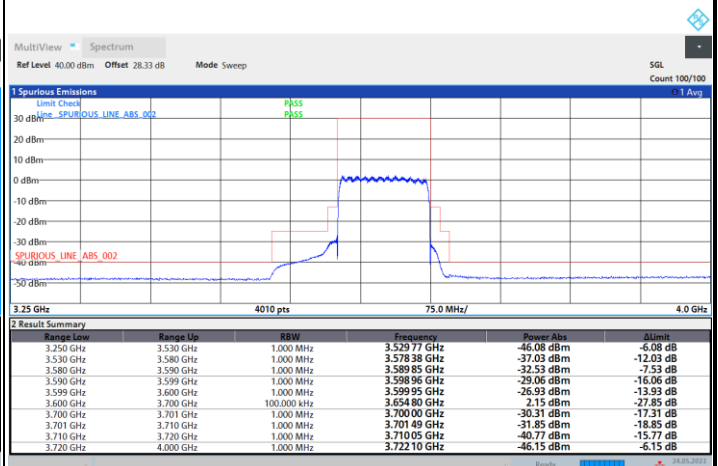
11:47:56 24.05.2023

64QAM



11:40:25 24.05.2023

256QAM



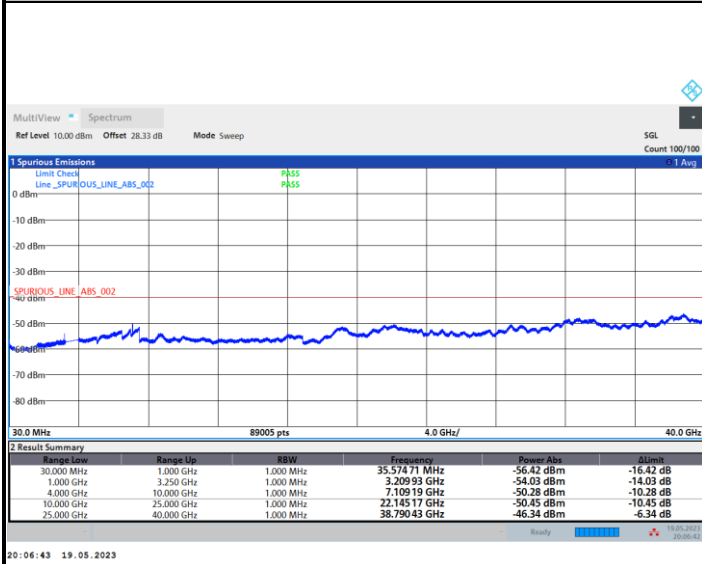
11:27:33 24.05.2023



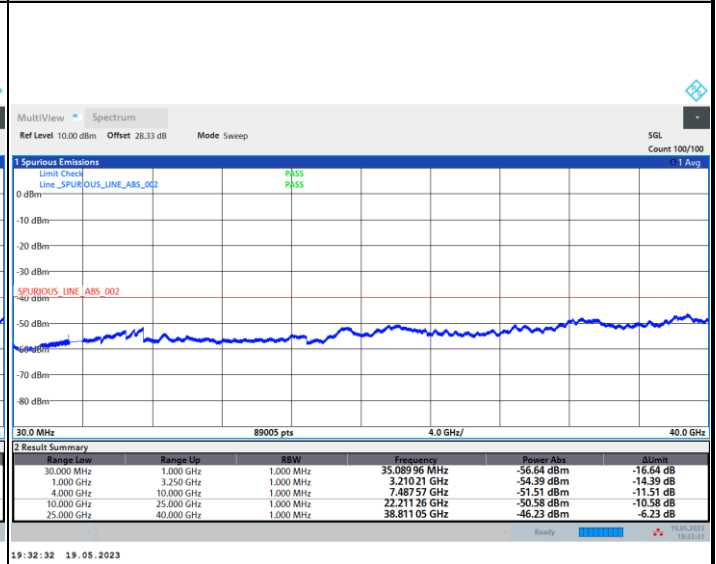
Conducted Spurious Emission

FR1 n48 / 10MHz / QPSK / CSE

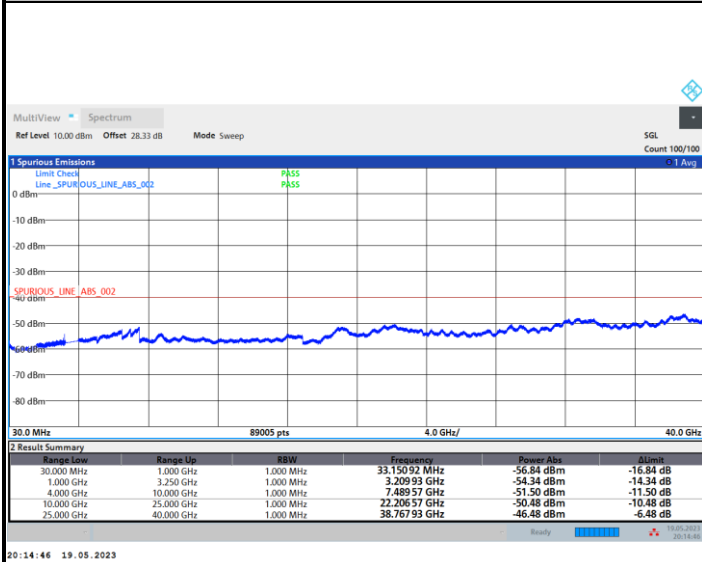
Lowest Channel



Middle Channel



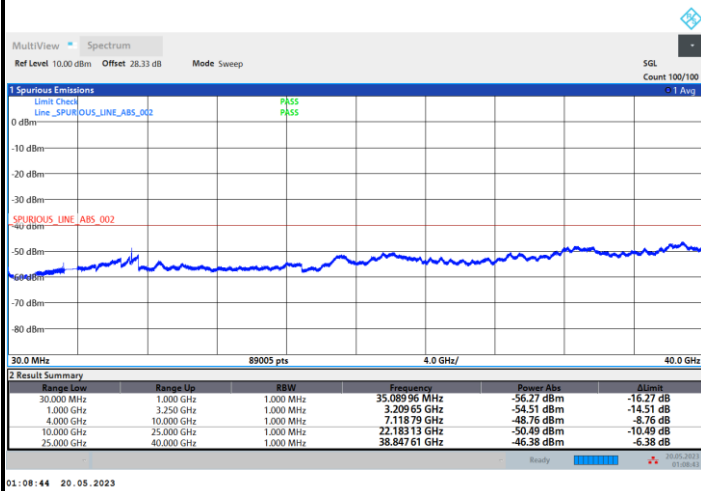
Highest Channel



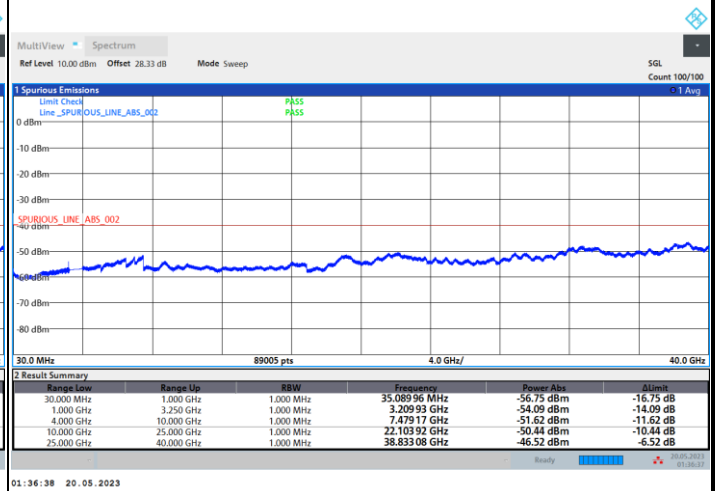


FR1 n48 / 20MHz / QPSK / CSE

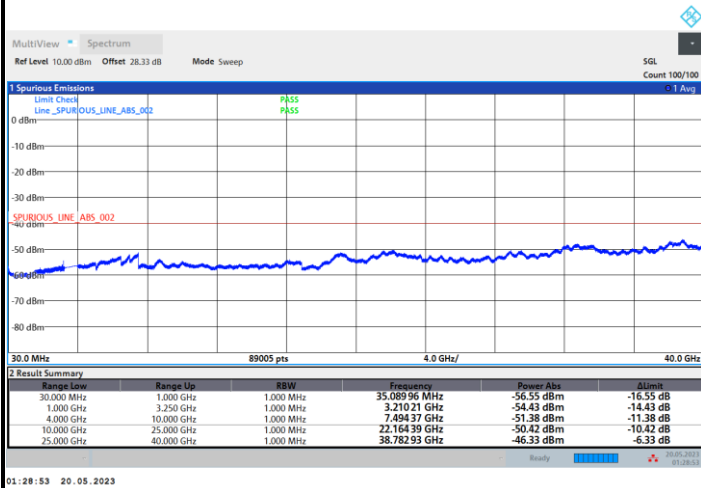
Lowest Channel



Middle Channel



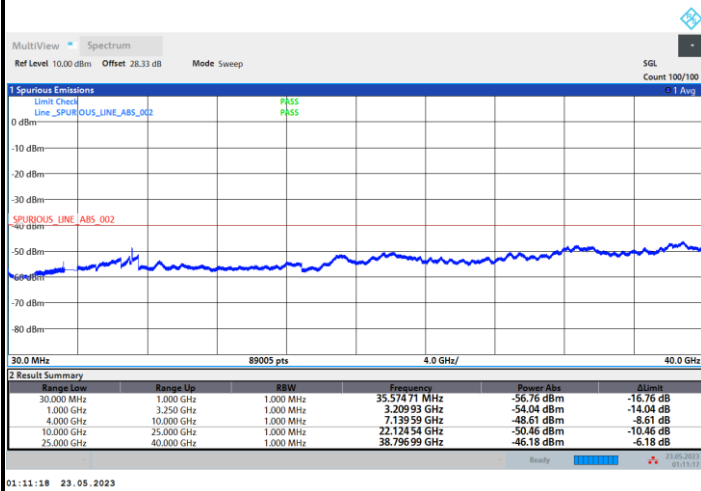
Highest Channel



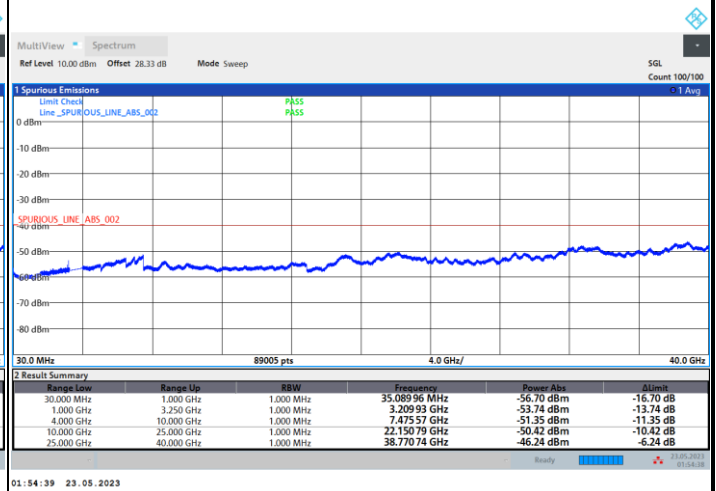


FR1 n48 / 40MHz / QPSK / CSE

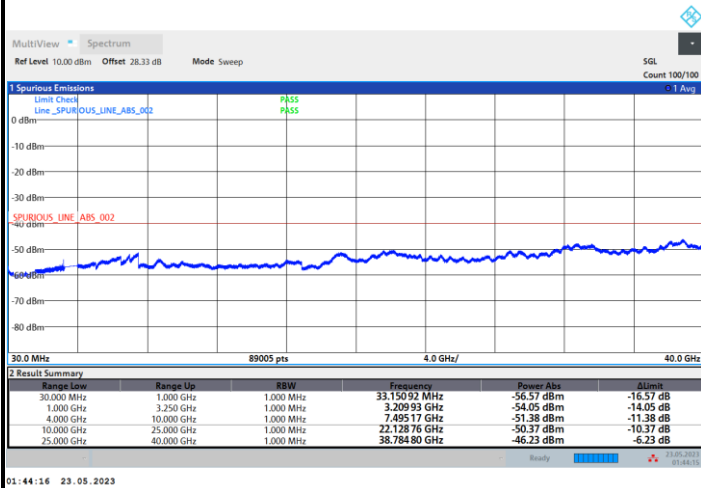
Lowest Channel



Middle Channel



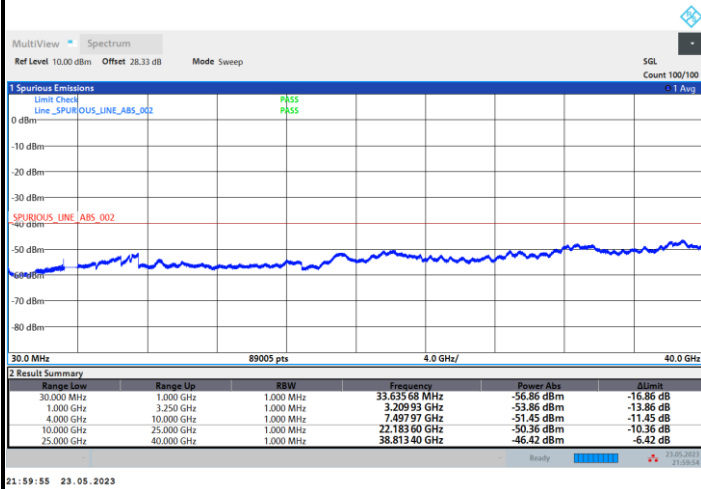
Highest Channel



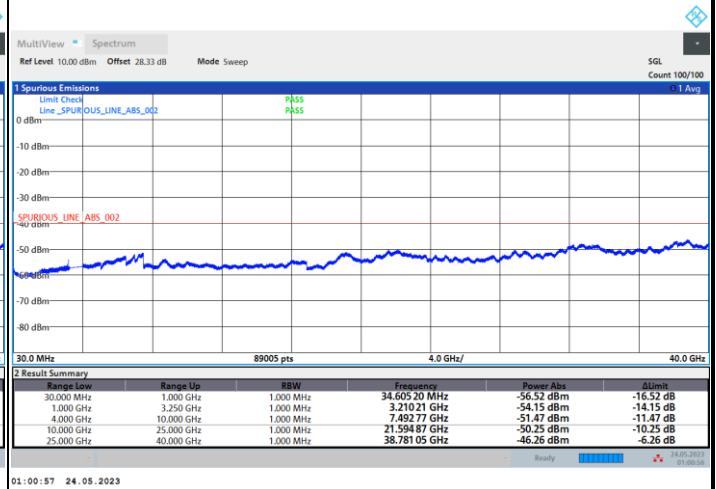


FR1 n48 / 100MHz / QPSK / CSE

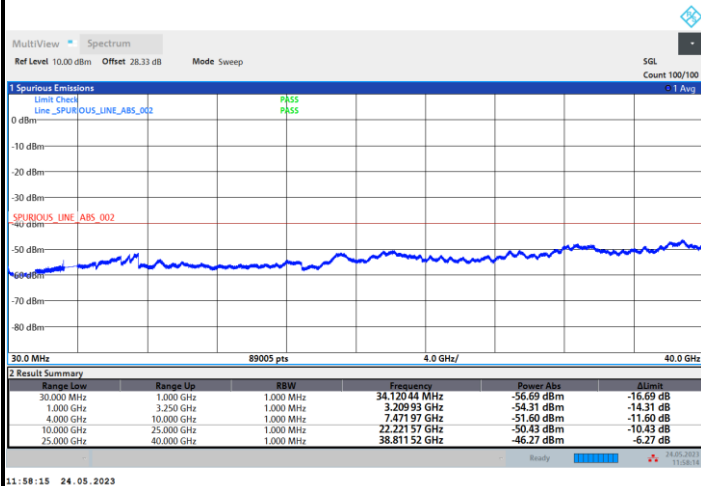
Lowest Channel



Middle Channel



Highest Channel



Frequency Stability

Test Conditions		FR1 n48 (QPSK) / Middle Channel	Limit
Temperature (°C)	Voltage (Volt)	BW 10MHz	Note 2.
		Frequency offset (ppm)	Result
50	Normal Voltage	0.6621	PASS
40	Normal Voltage	0.8828	
30	Normal Voltage	0.7172	
20(Ref.)	Normal Voltage	0.0000	
10	Normal Voltage	0.7172	
0	Normal Voltage	0.9931	
-10	Normal Voltage	0.9379	
-20	Normal Voltage	1.2138	
-30	Normal Voltage	1.4345	
20	Maximum Voltage	0.2207	
20	Normal Voltage	0.6621	
20	Minimum Voltage	0.2759	

Note:

1. Normal Voltage = 12 V. ; Minimum Voltage = 11.2 V. ; Maximum Voltage = 13.2 V.
2. The frequency fundamental emissions stay within the authorized frequency block.

<MIMO ANT 2>

Maximum EIRP (dBm/10MHz)

Mode	FR1 n48 : Conducted (dBm/10MHz) <SISO> Lowest Channel							
BW	10MHz		20MHz		40MHz		50MHz	
Mod.	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM
Lowest CH	16.63	15.65	17.67	16.14	17.25	16.05	-	-
Mod.	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM
Lowest CH	16.53	16.54	17.49	18.11	17.41	17.35	-	-
BW	60MHz		80MHz		90MHz		100MHz	
Mod.	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM
Lowest CH	-	-	-	-	-	-	14.04	14.59
Mod.	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM
Lowest CH	-	-	-	-	-	-	13.91	13.95

Mode	FR1 n48 : Maximum EIRP (dBm/10MHz) <MIMO 4TX> Lowest Channel							
BW	10MHz		20MHz		40MHz		50MHz	
Mod.	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM
Lowest CH	27.95	26.97	28.99	27.46	28.57	27.37	-	-
Mod.	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM
Lowest CH	27.85	27.86	28.81	29.43	28.73	28.67	-	-
BW	60MHz		80MHz		90MHz		100MHz	
Mod.	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM
Lowest CH	-	-	-	-	-	-	25.36	25.91
Mod.	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM
Lowest CH	-	-	-	-	-	-	25.23	25.27
Limit	30dBm/10MHz							
Result	PASS							

Note

1. The measured conducted result has included duty cycle offset factor.
2. The Maximum EIRP = conducted result + 6.02dB (4TX) + 5.3dBi MIMO antenna gain.

Mode	FR1 n48 : Conducted (dBm/10MHz) <SISO> Middle Channel							
BW	10MHz		20MHz		40MHz		50MHz	
Mod.	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM
Middle CH	16.36	15.78	17.97	16.27	17.09	15.84	-	-
Mod.	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM
Middle CH	16.76	16.27	18.01	18.16	17.03	17.04	-	-
BW	60MHz		80MHz		90MHz		100MHz	
Mod.	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM
Middle CH	-	-	-	-	-	-	14.31	14.65
Mod.	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM
Middle CH	-	-	-	-	-	-	14.03	14.23

Mode	FR1 n48 : Maximum EIRP (dBm/10MHz) <MIMO 4TX> Middle Channel							
BW	10MHz		20MHz		40MHz		50MHz	
Mod.	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM
Middle CH	27.68	27.10	29.29	27.59	28.41	27.16	-	-
Mod.	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM
Middle CH	28.08	27.59	29.33	29.48	28.35	28.36	-	-
BW	60MHz		80MHz		90MHz		100MHz	
Mod.	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM
Middle CH	-	-	-	-	-	-	25.63	25.97
Mod.	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM
Middle CH	-	-	-	-	-	-	25.35	25.55
Limit	30dBm/10MHz							
Result	PASS							

Note

1. The measured conducted result has included duty cycle offset factor.
2. The Maximum EIRP = conducted result + 6.02dB (4TX) + 5.3dBi MIMO antenna gain.

Mode	FR1 n48 : Conducted (dBm/10MHz) <SISO> Highest Channel							
BW	10MHz		20MHz		40MHz		50MHz	
Mod.	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM
Highest CH	16.10	15.70	18.27	16.43	16.84	15.89	-	-
Mod.	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM
Highest CH	15.93	16.01	17.84	17.77	17.19	16.94	-	-
BW	60MHz		80MHz		90MHz		100MHz	
Mod.	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM
Highest CH	-	-	-	-	-	-	14.34	14.82
Mod.	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM
Highest CH	-	-	-	-	-	-	14.55	14.31

Mode	FR1 n48 : Maximum EIRP (dBm/10MHz) <MIMO 4TX> Highest Channel							
BW	10MHz		20MHz		40MHz		50MHz	
Mod.	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM
Highest CH	27.42	27.02	29.59	27.75	28.16	27.21	-	-
Mod.	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM
Highest CH	27.25	27.33	29.16	29.09	28.51	28.26	-	-
BW	60MHz		80MHz		90MHz		100MHz	
Mod.	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM
Highest CH	-	-	-	-	-	-	25.66	26.14
Mod.	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM
Highest CH	-	-	-	-	-	-	25.87	25.63
Limit	30dBm/10MHz							
Result	PASS							

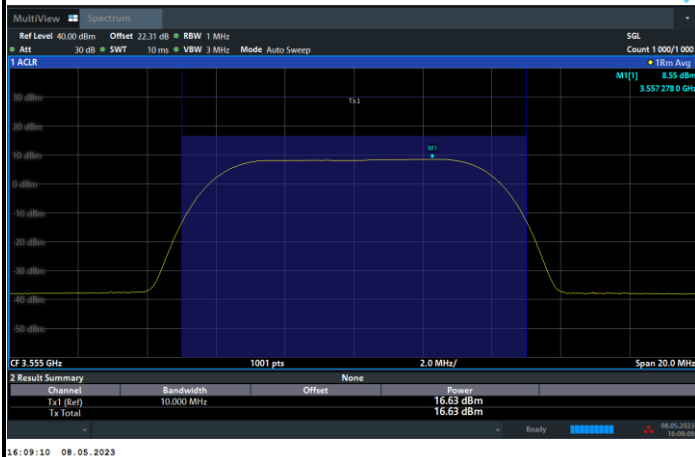
Note

1. The measured conducted result has included duty cycle offset factor.
2. The Maximum EIRP = conducted result + 6.02dB (4TX) + 5.3dBi MIMO antenna gain.

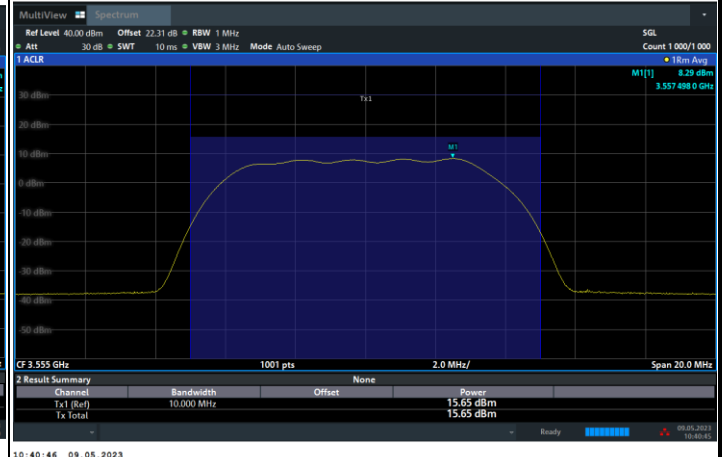


FR1 n48 / 10MHz / Lowest Channel / Conducted (dBm/10MHz)

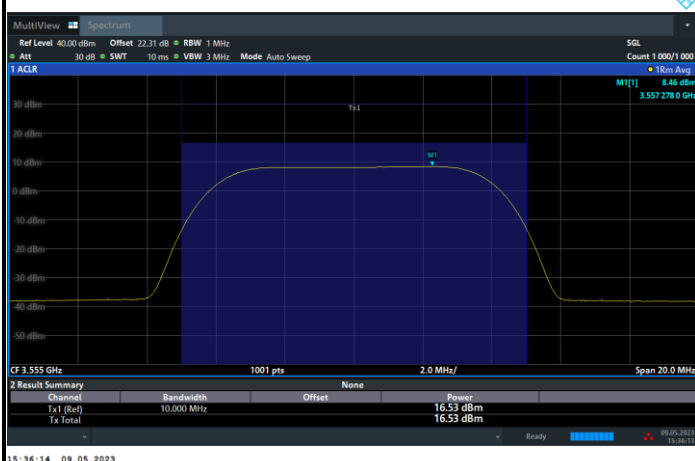
QPSK



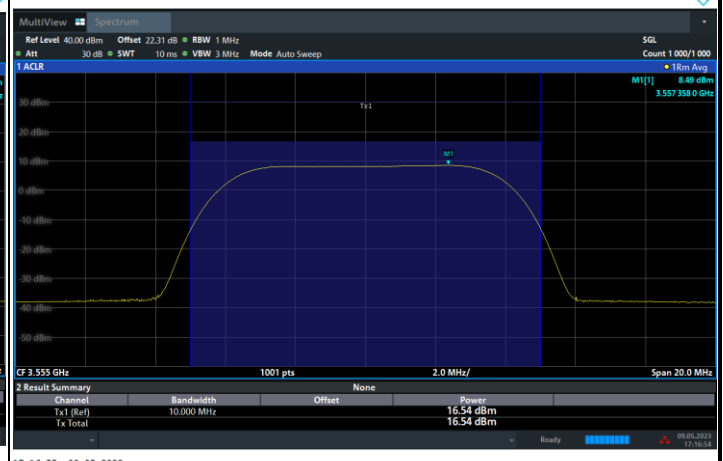
16QAM



64QAM



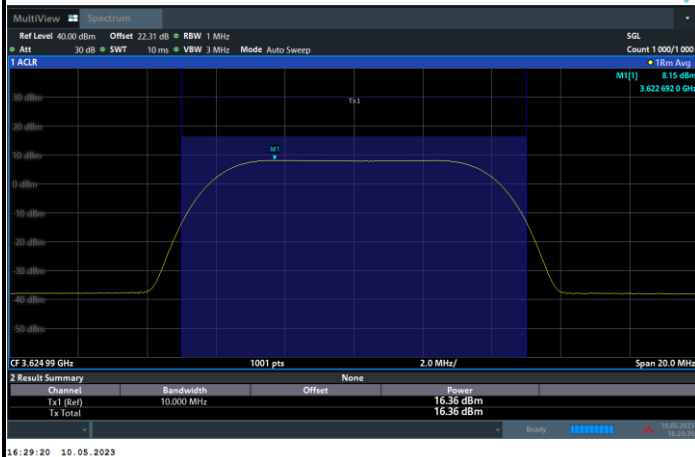
256QAM



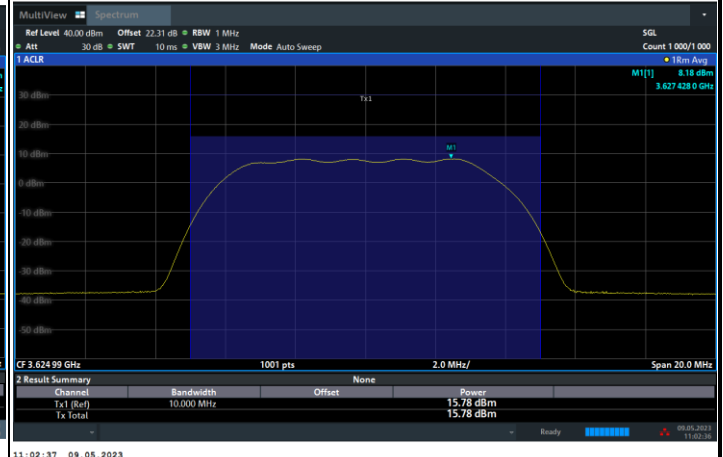


FR1 n48 / 10MHz / Middle Channel / Conducted (dBm/10MHz)

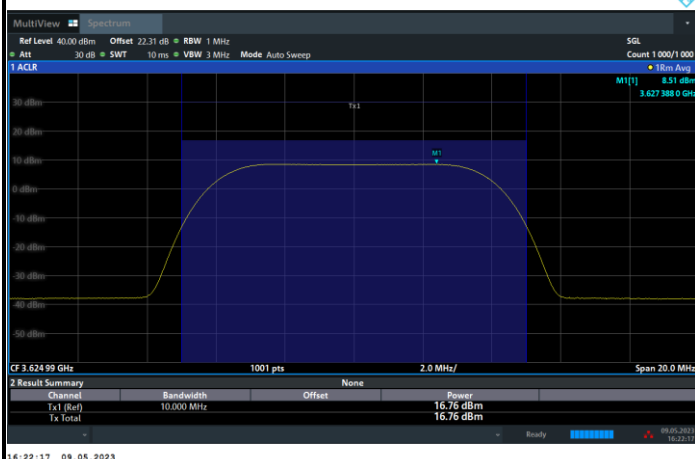
QPSK



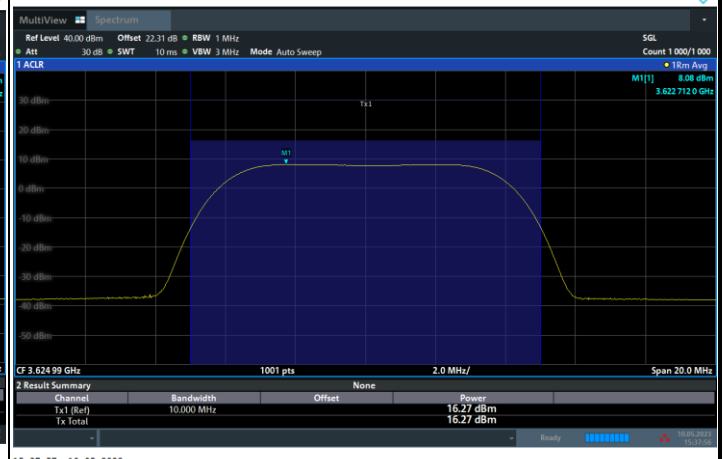
16QAM



64QAM



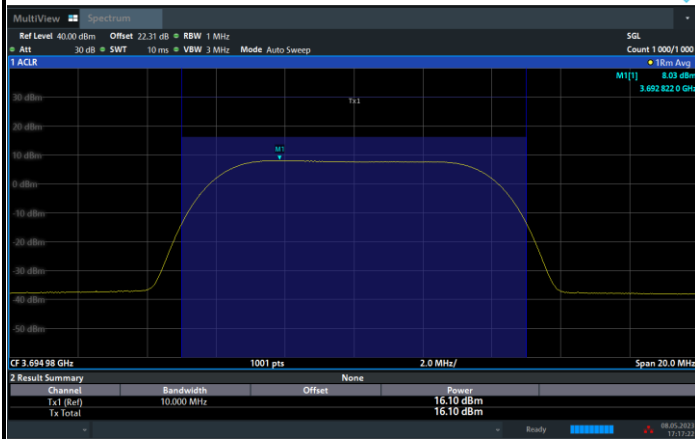
256QAM





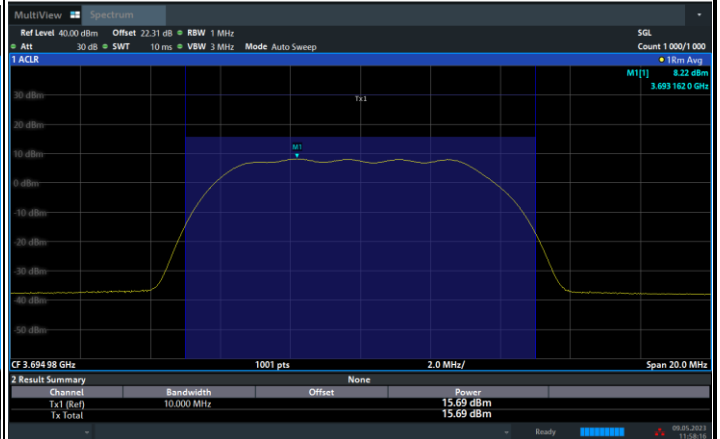
FR1 n48 / 10MHz / Highest Channel / Conducted (dBm/10MHz)

QPSK



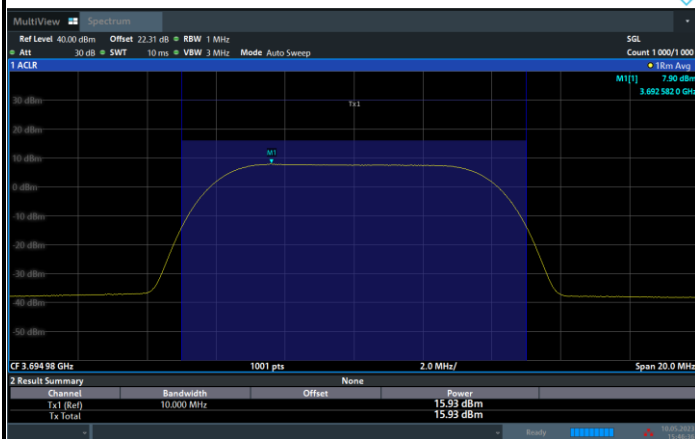
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16QAM



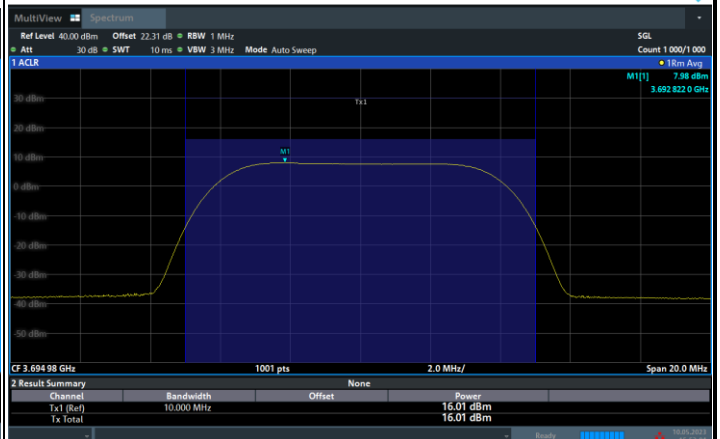
11:58:16 08.05.2023

64QAM



15:46:37 10.05.2023

256QAM

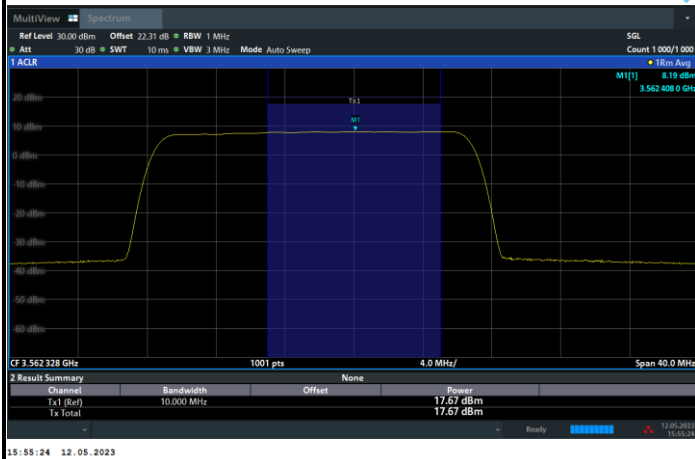


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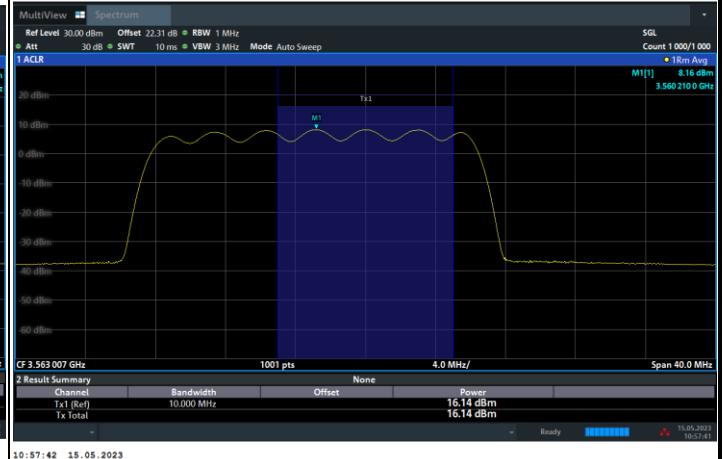


FR1 n48 / 20MHz / Lowest Channel / Conducted (dBm/10MHz)

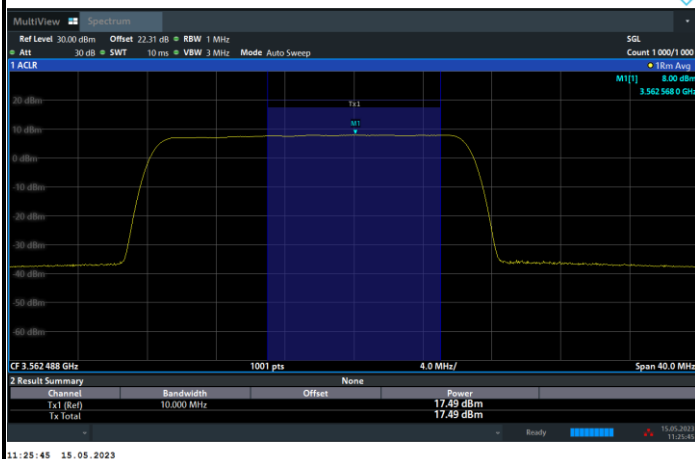
QPSK



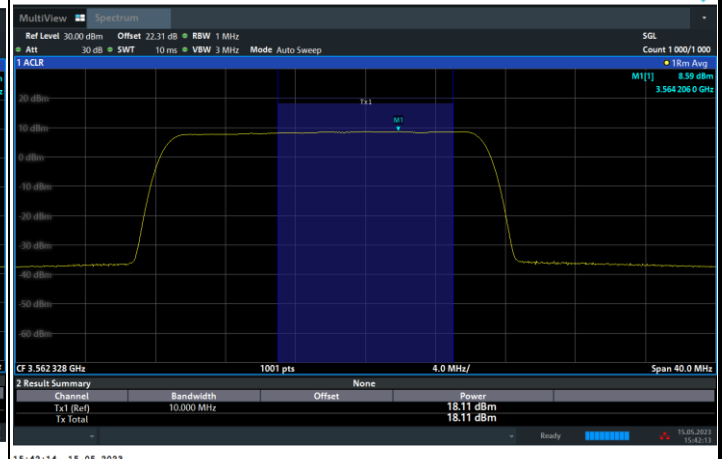
16QAM



64QAM



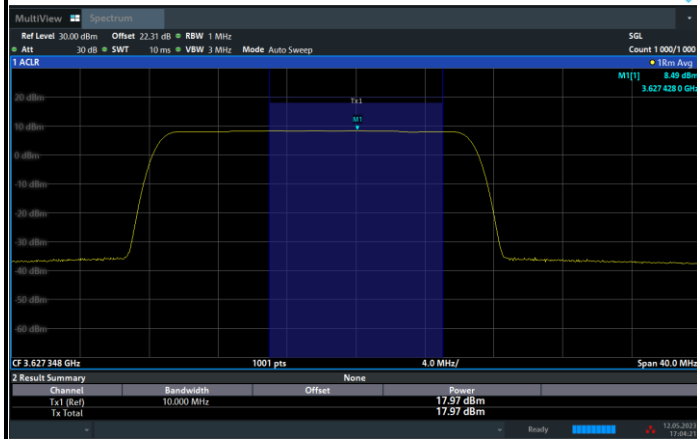
256QAM





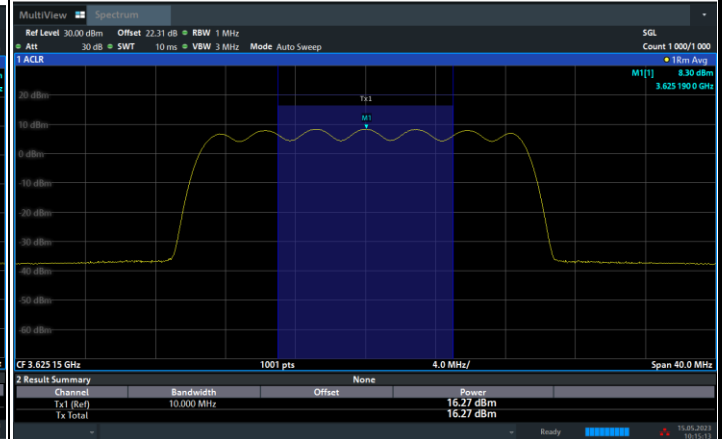
FR1 n48 / 20MHz / Middle Channel / Conducted (dBm/10MHz)

QPSK



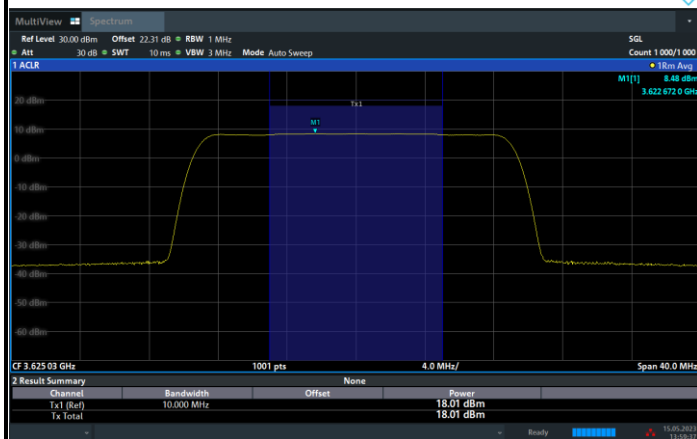
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16QAM



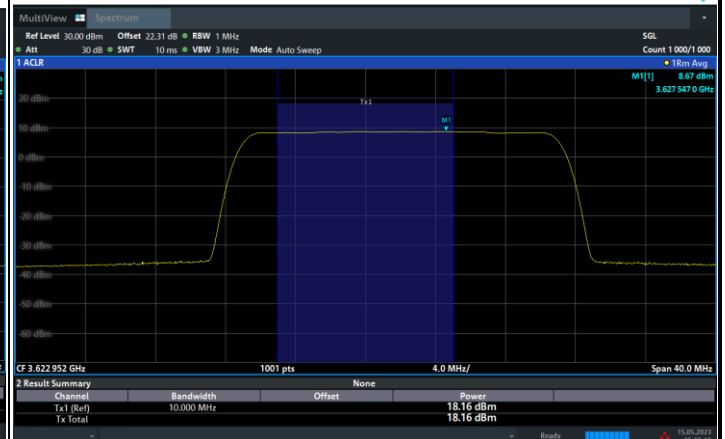
10:15:13 15.05.2023

64QAM



13:59:38 15.05.2023

256QAM

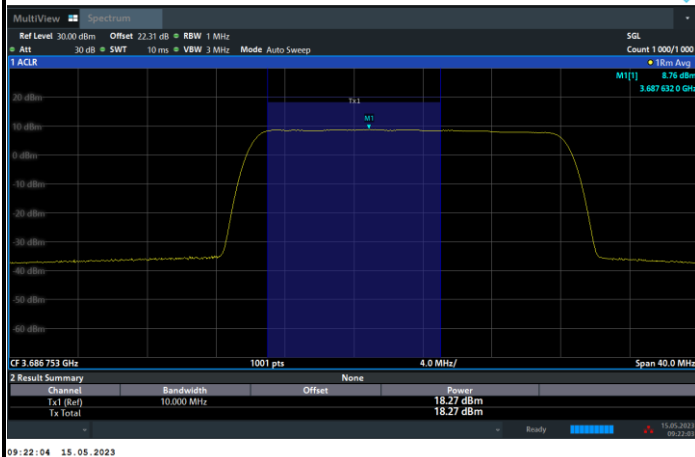


15:02:15 15.05.2023



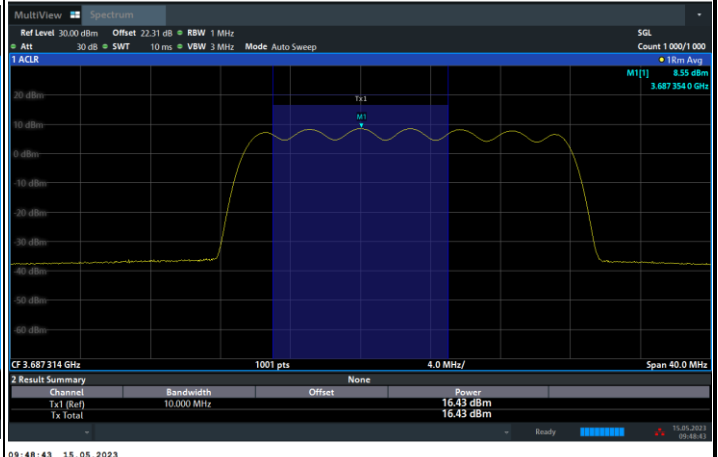
FR1 n48 / 20MHz / Highest Channel / Conducted (dBm/10MHz)

QPSK



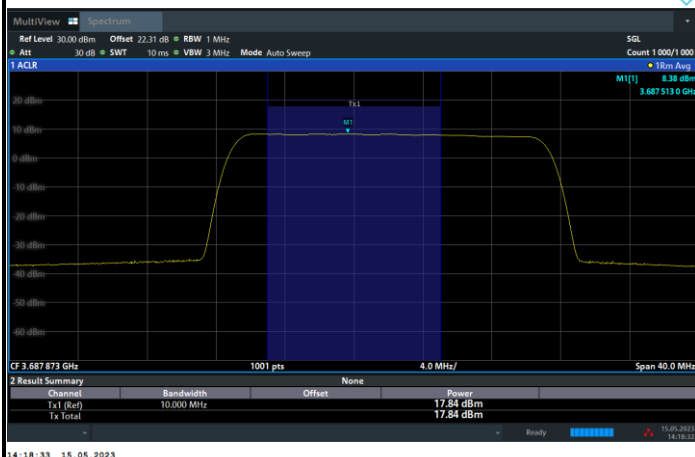
09:22:04 15.05.2023

16QAM



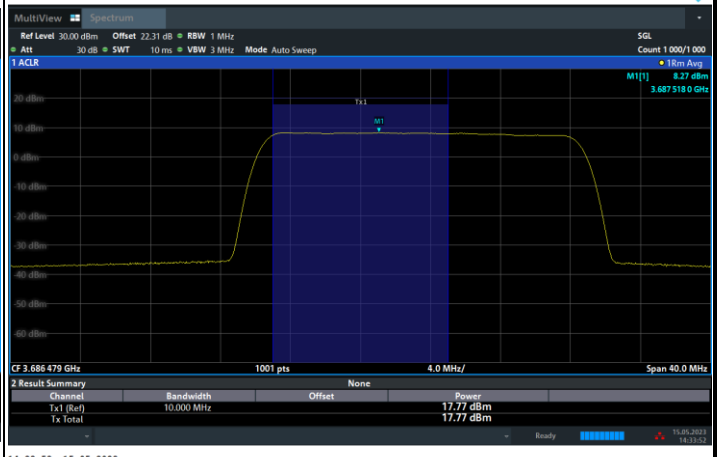
09:48:43 15.05.2023

64QAM



14:18:33 15.05.2023

256QAM

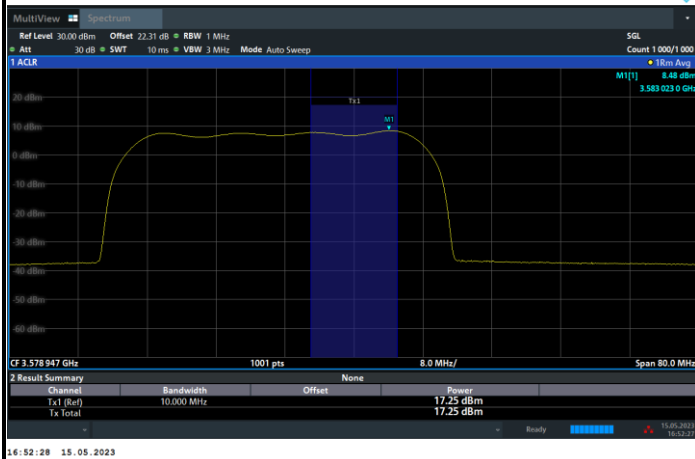


14:33:53 15.05.2023



FR1 n48 / 40MHz / Lowest Channel / Conducted (dBm/10MHz)

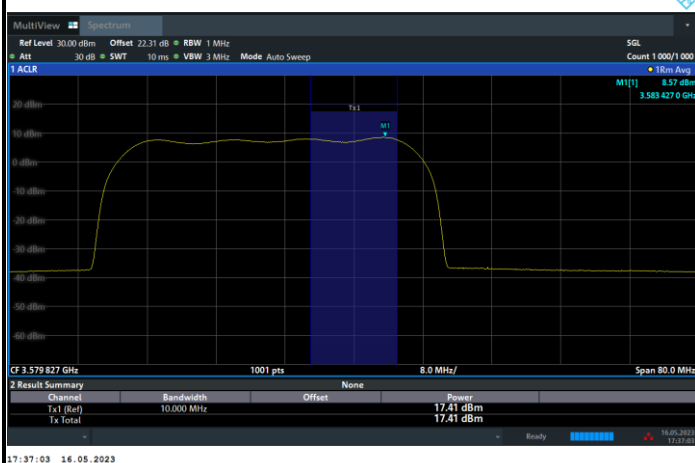
QPSK



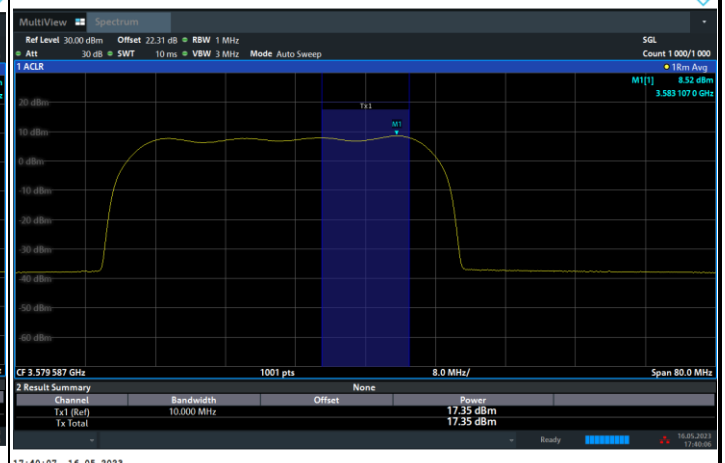
16QAM



64QAM



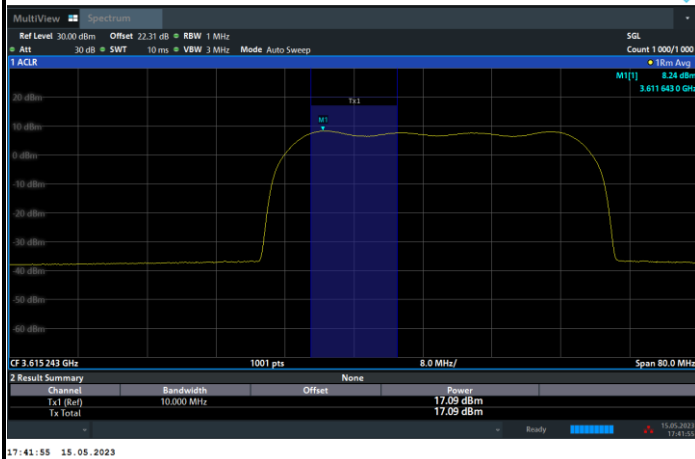
256QAM





FR1 n48 / 40MHz / Middle Channel / Conducted (dBm/10MHz)

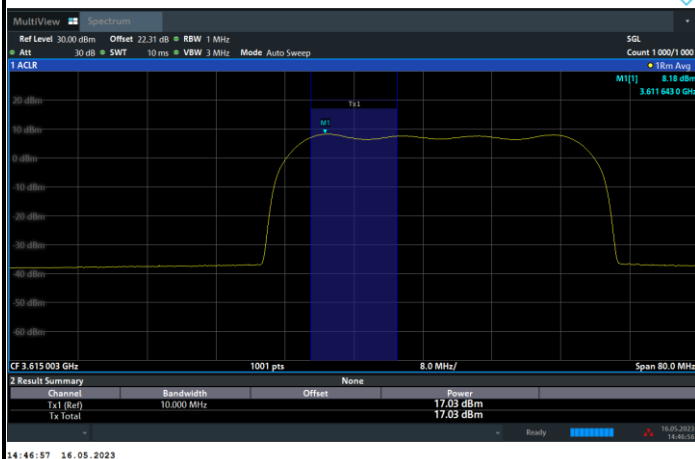
QPSK



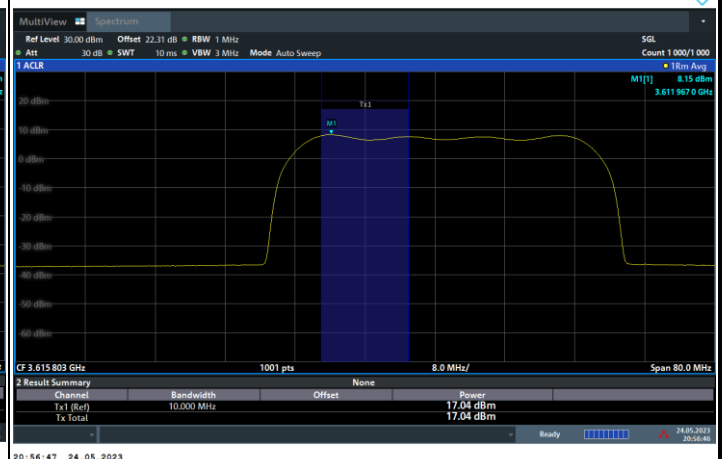
16QAM



64QAM



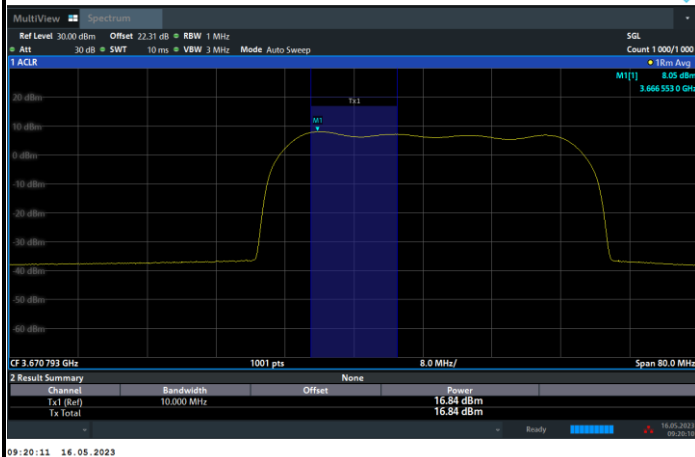
256QAM





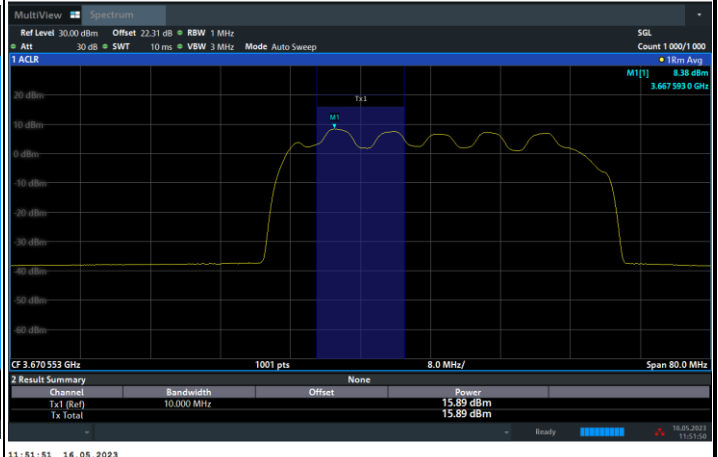
FR1 n48 / 40MHz / Highest Channel / Conducted (dBm/10MHz)

QPSK



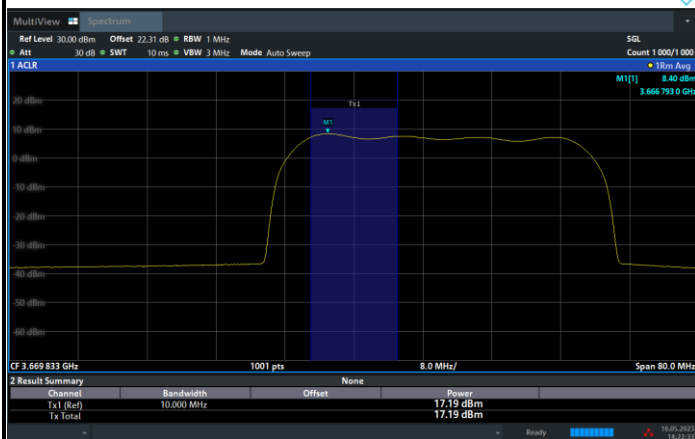
09:20:11 16.05.2023

16QAM



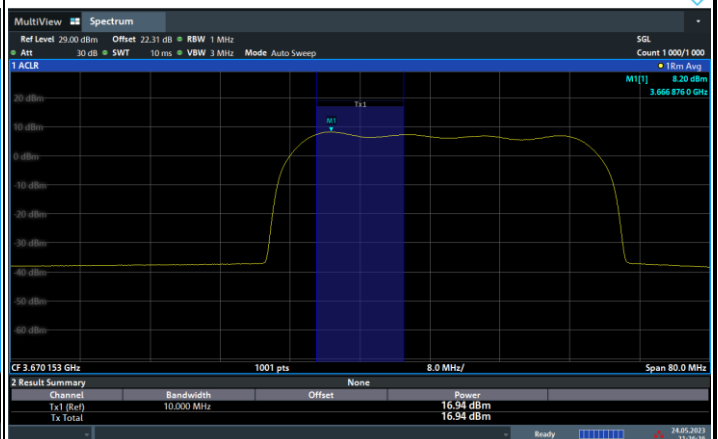
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64QAM



14:22:33 16.05.2023

256QAM

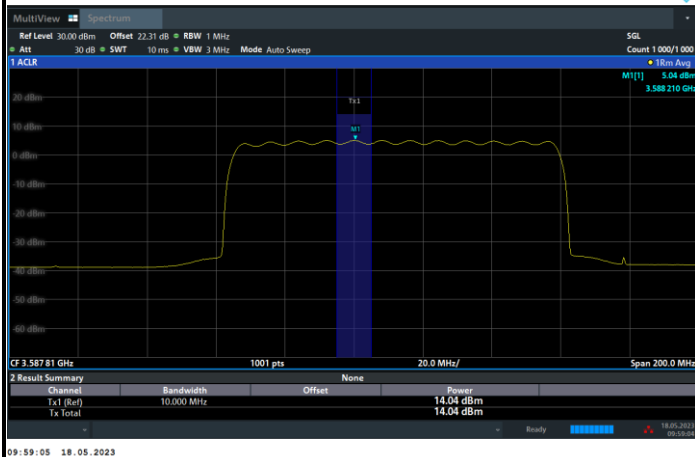


21:26:27 16.05.2023

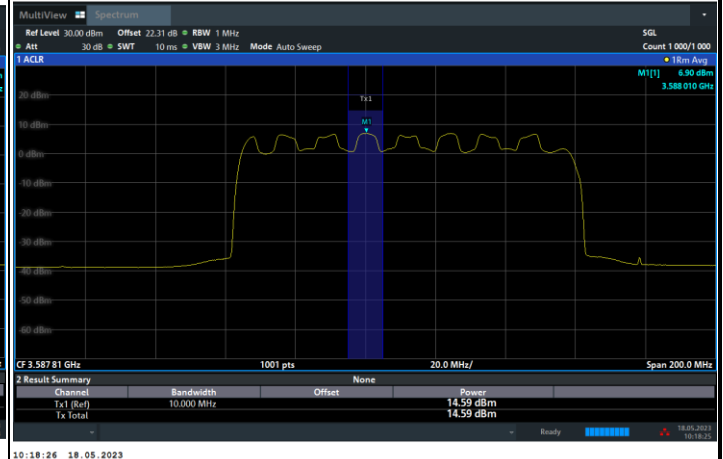


FR1 n48 / 100MHz / Lowest Channel / Conducted (dBm/10MHz)

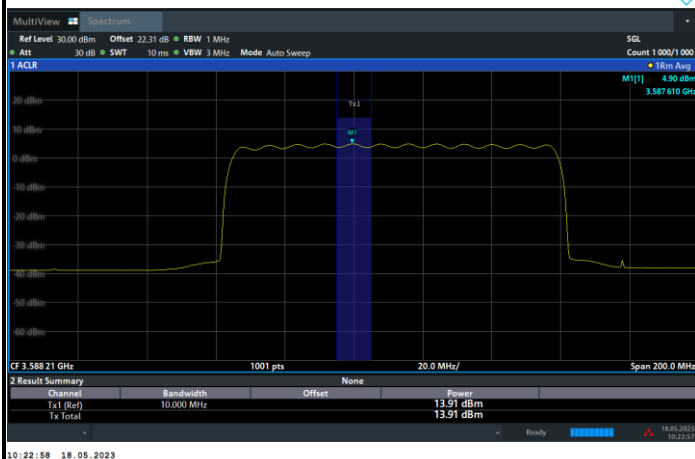
QPSK



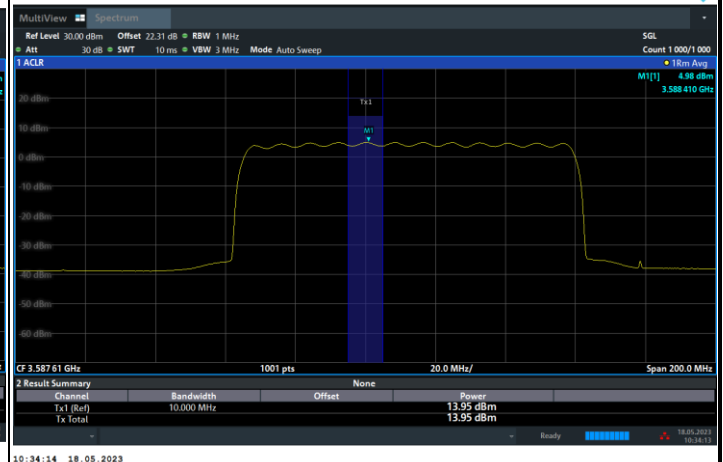
16QAM



64QAM



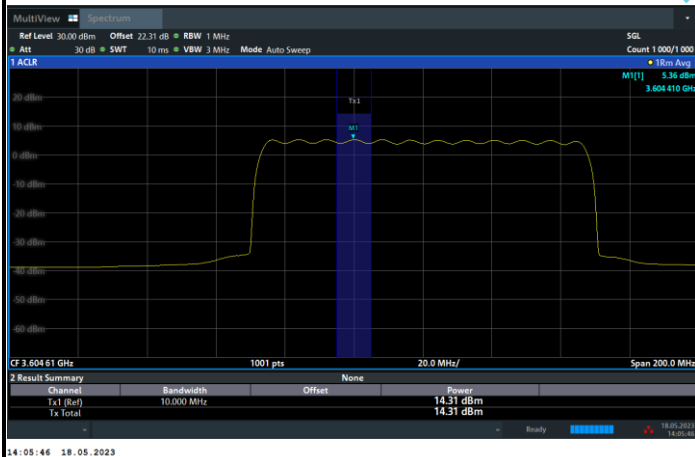
256QAM





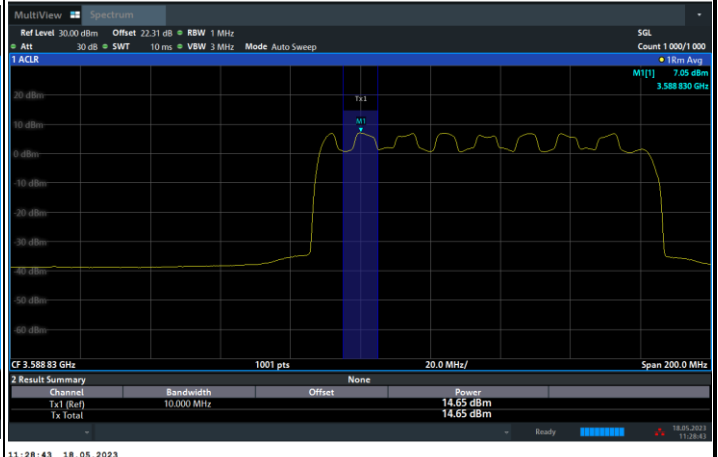
FR1 n48 / 100MHz / Middle Channel / Conducted (dBm/10MHz)

QPSK



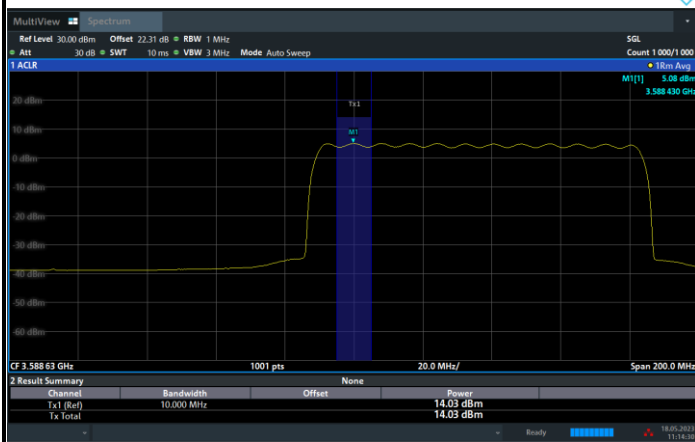
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16QAM



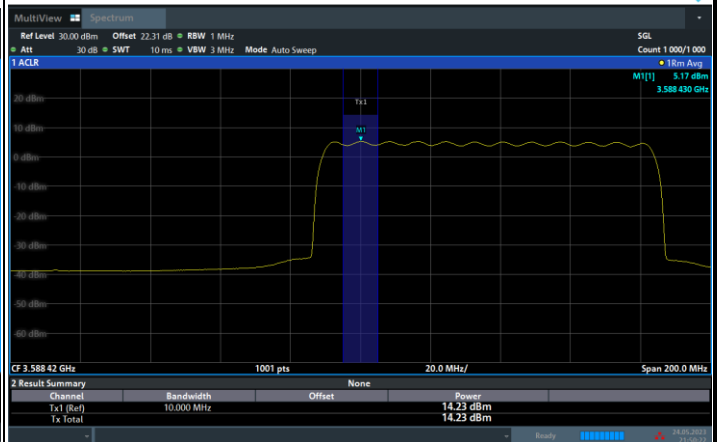
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64QAM



11:14:30 18.05.2023

256QAM

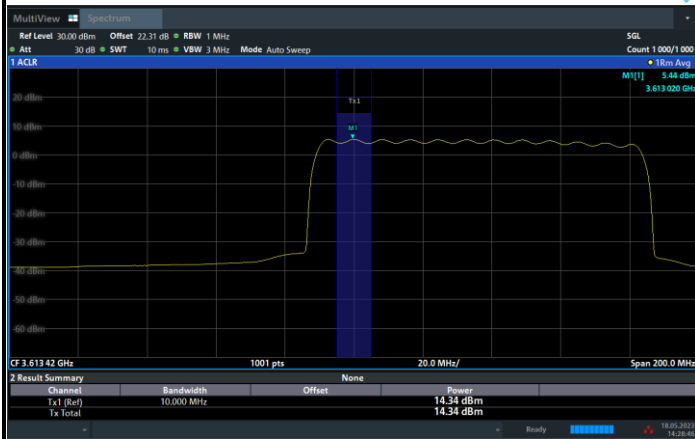


21:50:22 24.05.2023

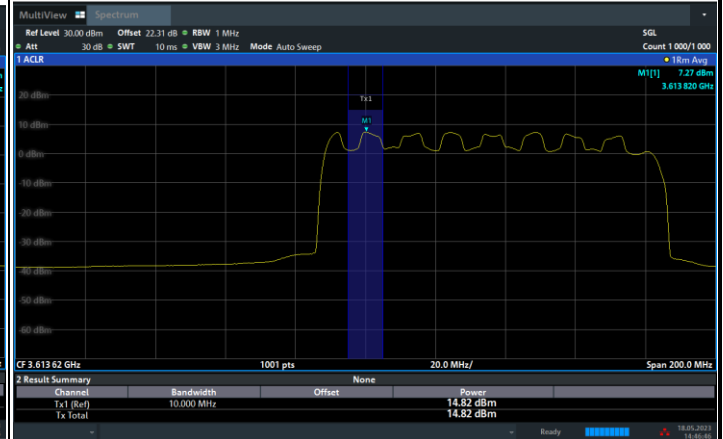


FR1 n48 / 100MHz / Highest Channel / Conducted (dBm/10MHz)

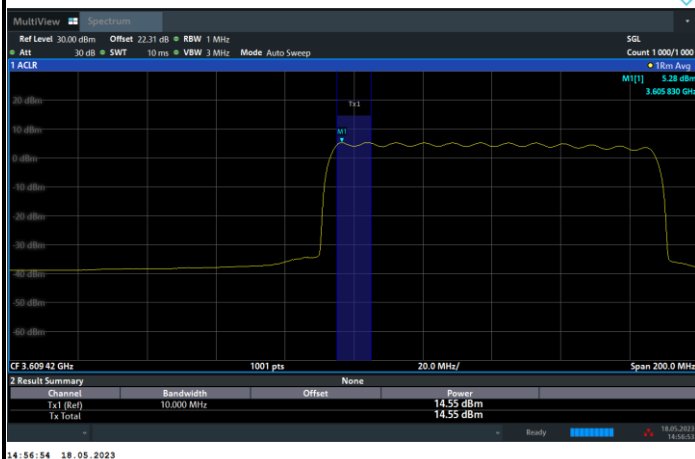
QPSK



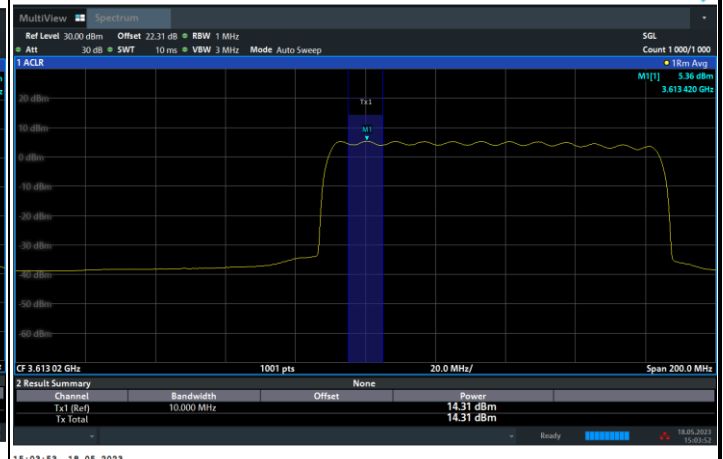
16QAM



64QAM



256QAM



Power Spectral Density

Mode	FR1 n48 : Conducted PSD (dBm/MHz) <SISO> Lowest Channel							
BW	10MHz		20MHz		40MHz		50MHz	
Mod.	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM
Lowest CH	8.58	8.50	8.18	8.15	8.46	8.30	-	-
Mod.	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM
Lowest CH	8.63	8.66	8.03	8.61	8.61	8.56	-	-
BW	60MHz		80MHz		90MHz		100MHz	
Mod.	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM
Lowest CH	-	-	-	-	-	-	5.11	6.92
Mod.	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM
Lowest CH	-	-	-	-	-	-	4.98	4.95

Mode	FR1 n48 : EIRP PSD (dBm/MHz) <MIMO 4TX> Lowest Channel							
BW	10MHz		20MHz		40MHz		50MHz	
Mod.	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM
Lowest CH	19.90	19.82	19.50	19.47	19.78	19.62	-	-
Mod.	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM
Lowest CH	19.95	19.98	19.35	19.93	19.93	19.88	-	-
BW	60MHz		80MHz		90MHz		100MHz	
Mod.	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM
Lowest CH	-	-	-	-	-	-	16.43	18.24
Mod.	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM
Lowest CH	-	-	-	-	-	-	16.30	16.27
Limit	20dBm/MHz							
Result	PASS							

Note

1. The measured conducted PSD result has included duty cycle offset factor.
2. The EIRP PSD = conducted PSD result + 6.02dB (4TX) + 5.3dBi MIMO antenna gain.

Mode	FR1 n48 : Conducted PSD (dBm/MHz) <SISO> Middle Channel							
BW	10MHz		20MHz		40MHz		50MHz	
Mod.	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM
Middle CH	8.34	8.41	8.49	8.32	8.36	8.30	Data_PSD_50M_30k_CP_M_Full_QPSK	Data_PSD_50M_30k_CP_M_Full_16Q
Mod.	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM
Middle CH	8.68	8.20	8.54	8.64	8.27	8.03	Data_PSD_50M_30k_CP_M_Full_64Q	Data_PSD_50M_30k_CP_M_Full_256Q
BW	60MHz		80MHz		90MHz		100MHz	
Mod.	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM
Middle CH	-	-	-	-	-	-	5.36	7.04
Mod.	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM
Middle CH	-	-	-	-	-	-	5.07	5.16

Mode	FR1 n48 : EIRP PSD (dBm/MHz) <MIMO 4TX> Middle Channel							
BW	10MHz		20MHz		40MHz		50MHz	
Mod.	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM
Middle CH	19.66	19.73	19.81	19.64	19.68	19.62	-	-
Mod.	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM
Middle CH	20.00	19.52	19.86	19.96	19.59	19.35	-	-
BW	60MHz		80MHz		90MHz		100MHz	
Mod.	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM
Middle CH	-	-	-	-	-	-	16.68	18.36
Mod.	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM
Middle CH	-	-	-	-	-	-	16.39	16.48
Limit	20dBm/MHz							
Result	PASS							

Note

1. The measured conducted PSD result has included duty cycle offset factor.
2. The EIRP PSD = conducted PSD result + 6.02dB (4TX) + 5.3dBi MIMO antenna gain.

Mode	FR1 n48 : Conducted PSD (dBm/MHz) <SISO> Highest Channel							
BW	10MHz		20MHz		40MHz		50MHz	
Mod.	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM
Highest CH	8.25	8.59	8.26	8.63	8.14	8.46	-	-
Mod.	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM
Highest CH	8.09	8.06	8.35	8.31	8.46	8.21	-	-
BW	60MHz		80MHz		90MHz		100MHz	
Mod.	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM
Highest CH	-	-	-	-	-	-	5.43	7.31
Mod.	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM
Highest CH	-	-	-	-	-	-	5.34	5.32

Mode	FR1 n48 : EIRP PSD (dBm/MHz) <MIMO 4TX> Highest Channel							
BW	10MHz		20MHz		40MHz		50MHz	
Mod.	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM
Highest CH	19.57	19.91	19.58	19.95	19.46	19.78	-	-
Mod.	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM
Highest CH	19.41	19.38	19.67	19.63	19.78	19.53	-	-
BW	60MHz		80MHz		90MHz		100MHz	
Mod.	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM
Highest CH	-	-	-	-	-	-	16.75	18.63
Mod.	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM
Highest CH	-	-	-	-	-	-	16.66	16.64
Limit	20dBm/MHz							
Result	PASS							

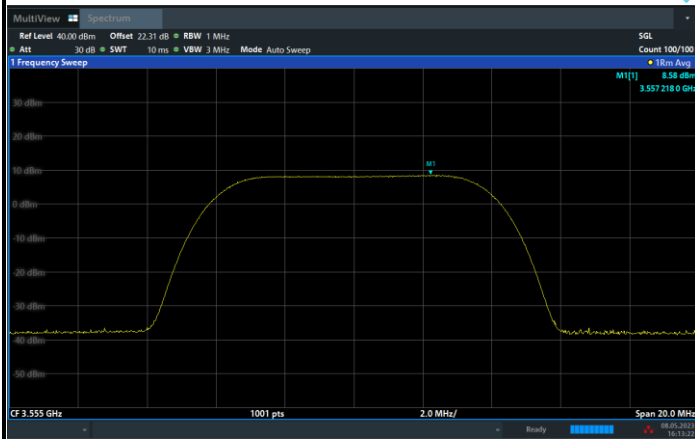
Note

1. The measured conducted PSD result has included duty cycle offset factor.
2. The EIRP PSD = conducted PSD result + 6.02dB (4TX) + 5.3dBi MIMO antenna gain.



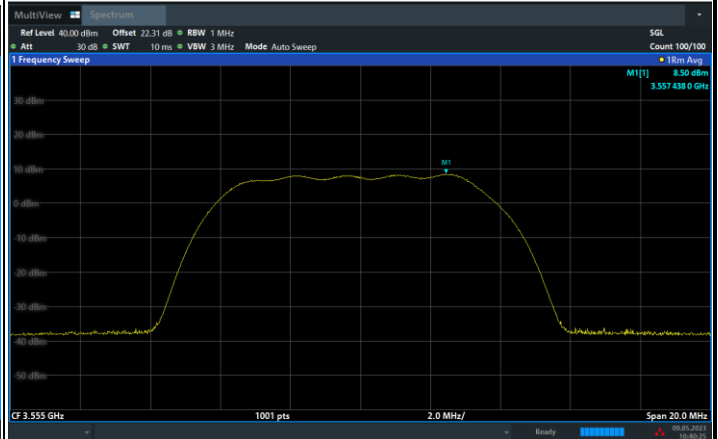
FR1 n48 / 10MHz / Lowest Channel / PSD

QPSK



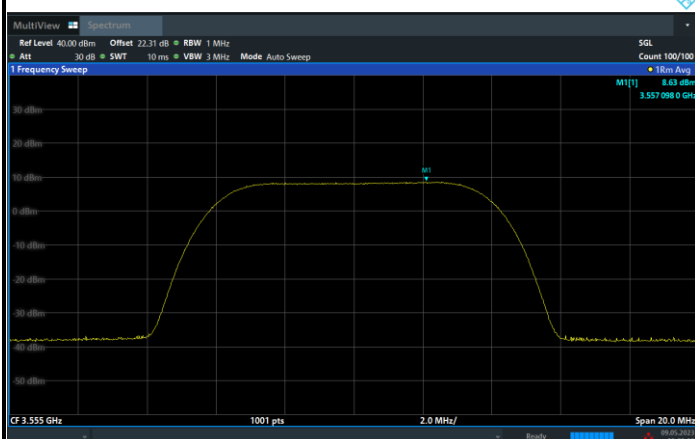
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16QAM



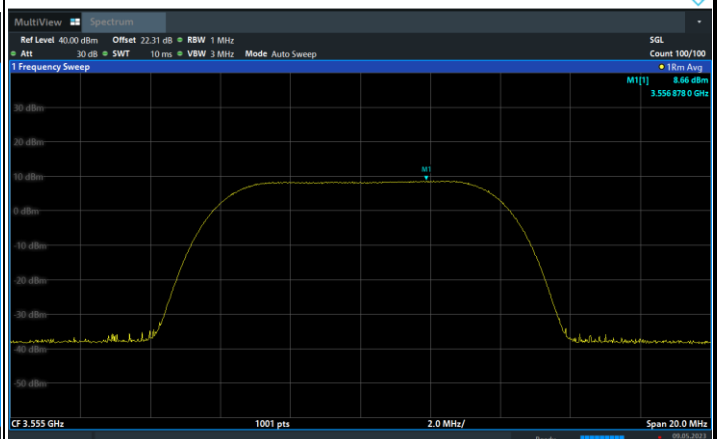
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64QAM



15:35:20 09.05.2023

256QAM

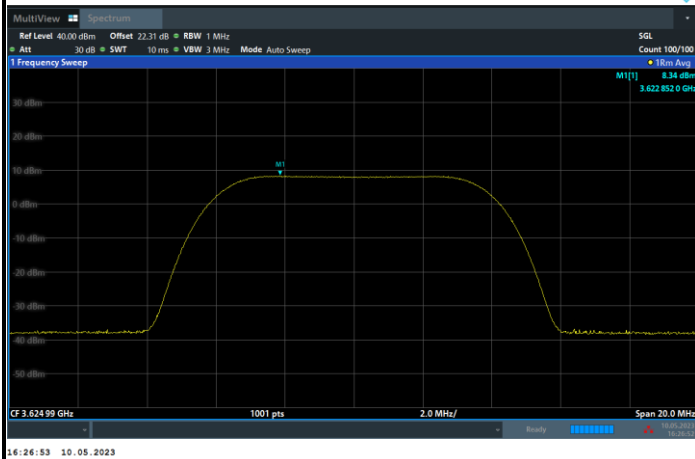


17:16:37 09.05.2023

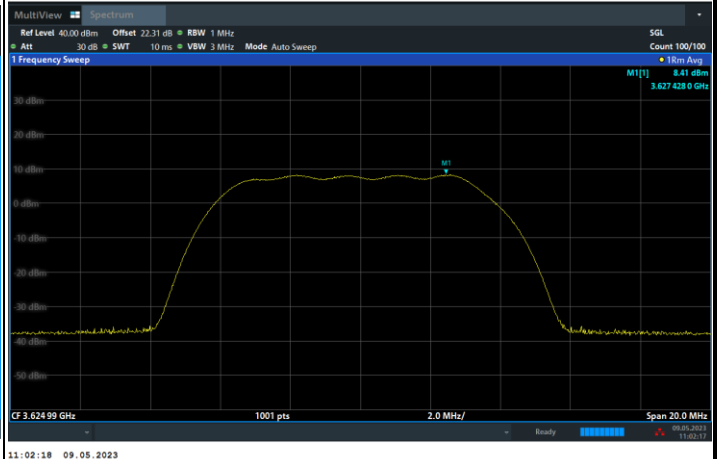


FR1 n48 / 10MHz / Middle Channel / PSD

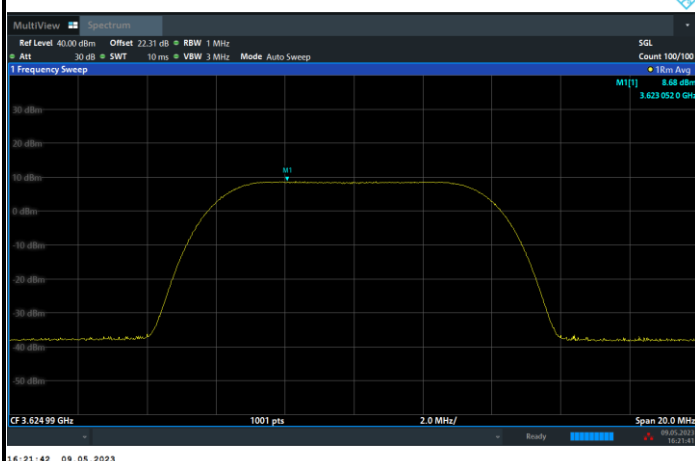
QPSK



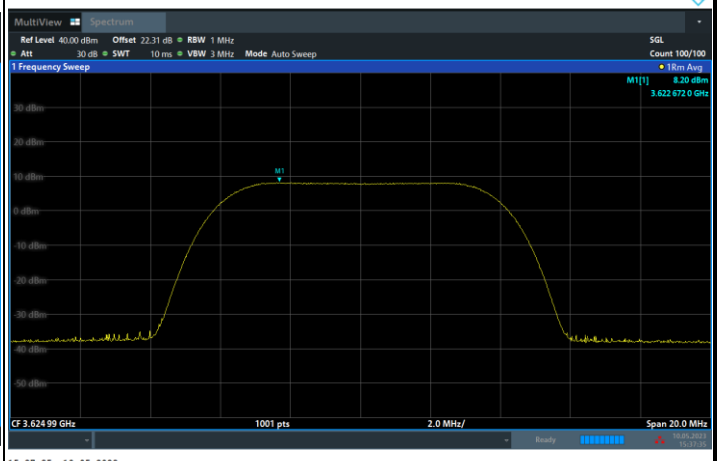
16QAM



64QAM



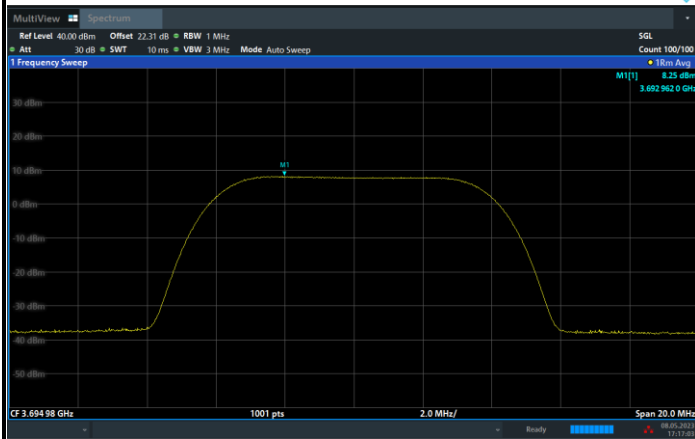
256QAM



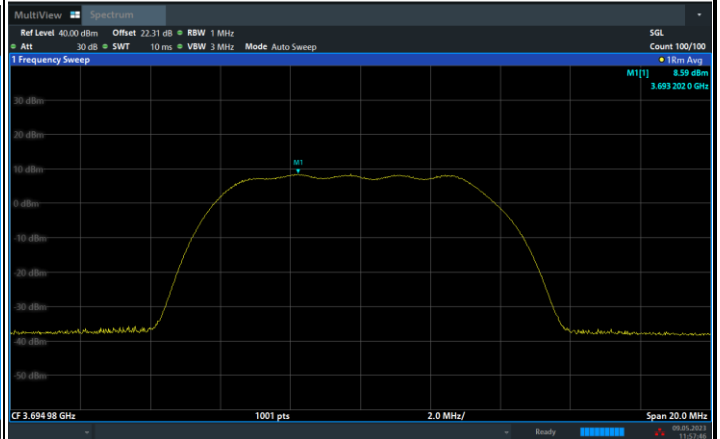


FR1 n48 / 10MHz / Highest Channel / PSD

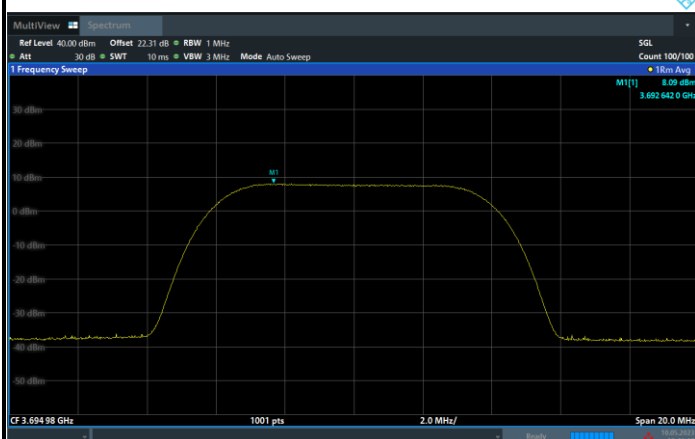
QPSK



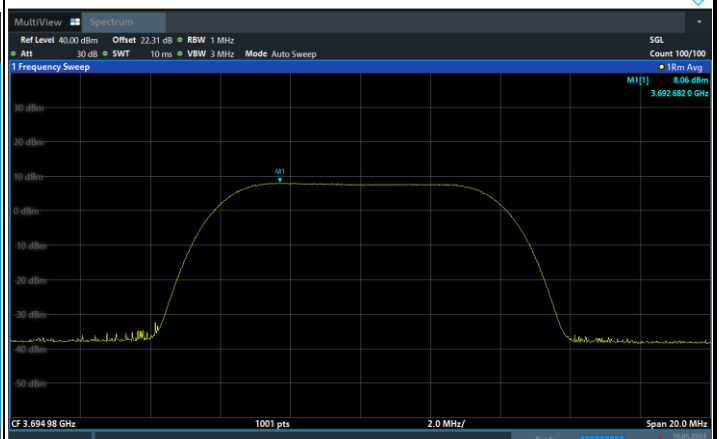
16QAM



64QAM



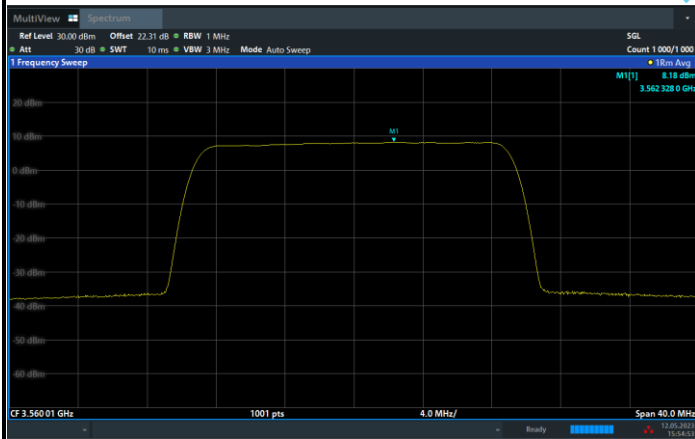
256QAM



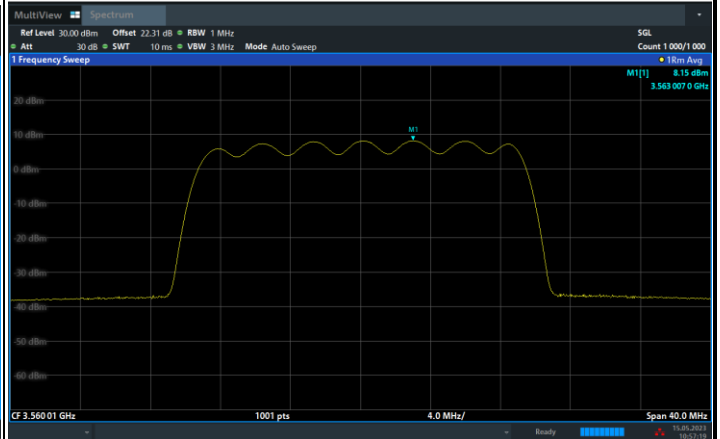


FR1 n48 / 20MHz / Lowest Channel / PSD

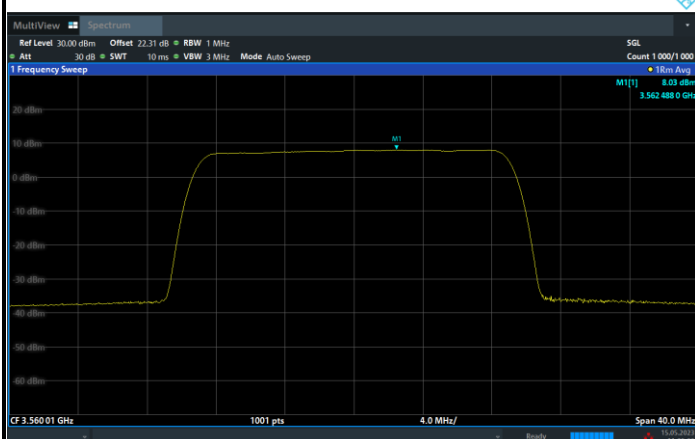
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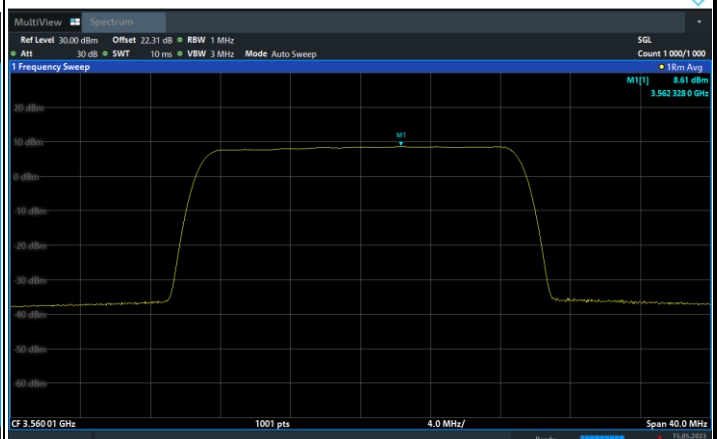
16QAM



64QAM



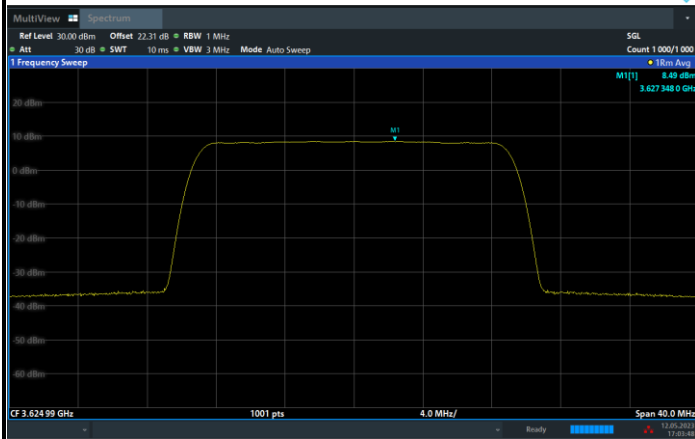
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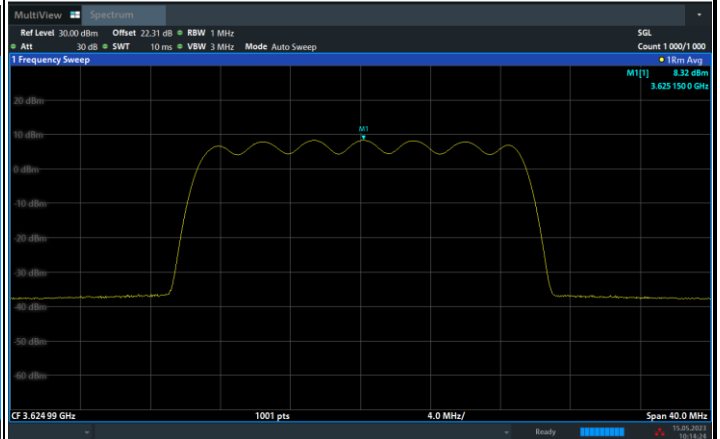


FR1 n48 / 20MHz / Middle Channel / PSD

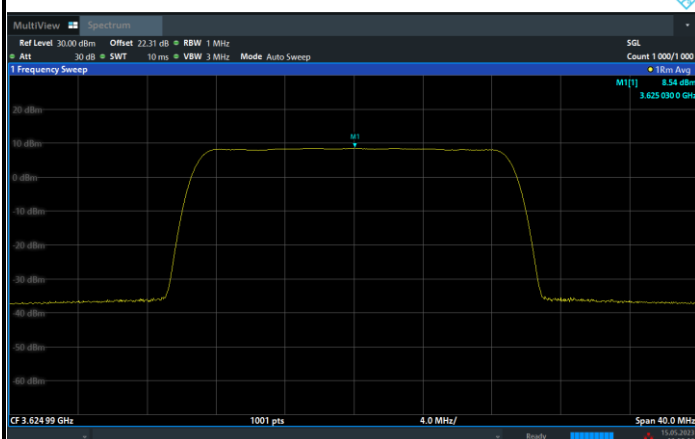
QPSK



16QAM



64QAM



256QAM

