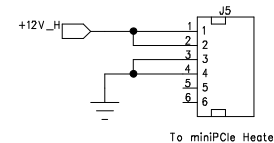
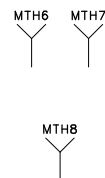
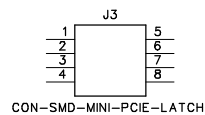
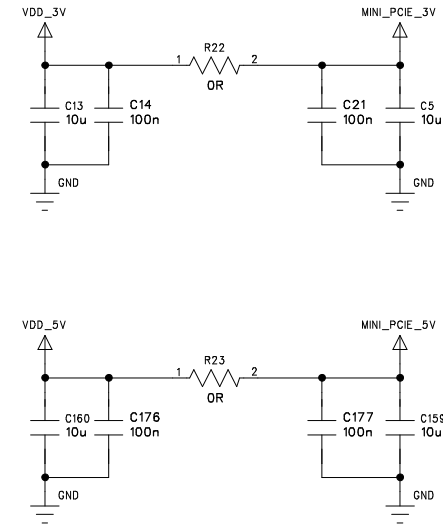
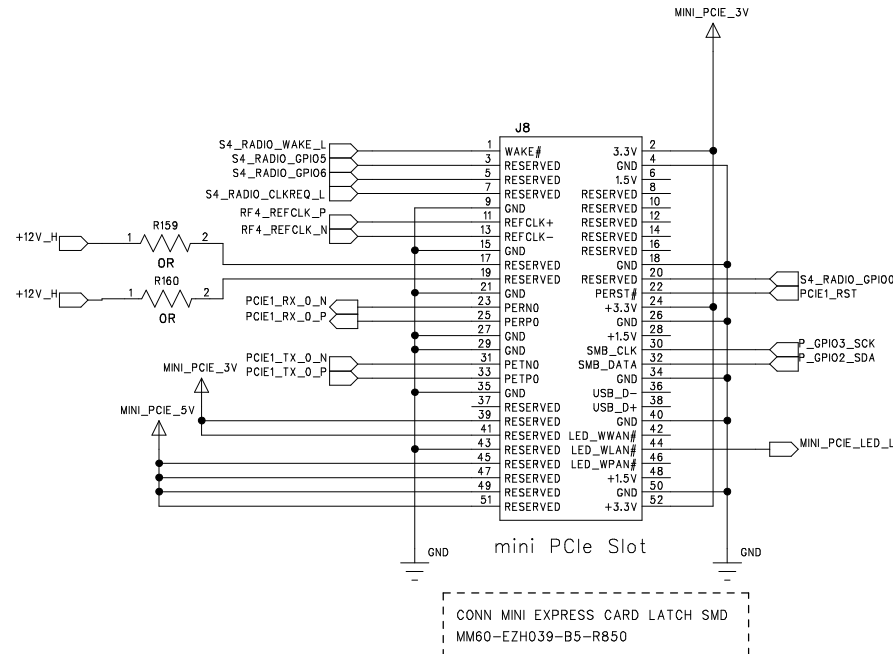
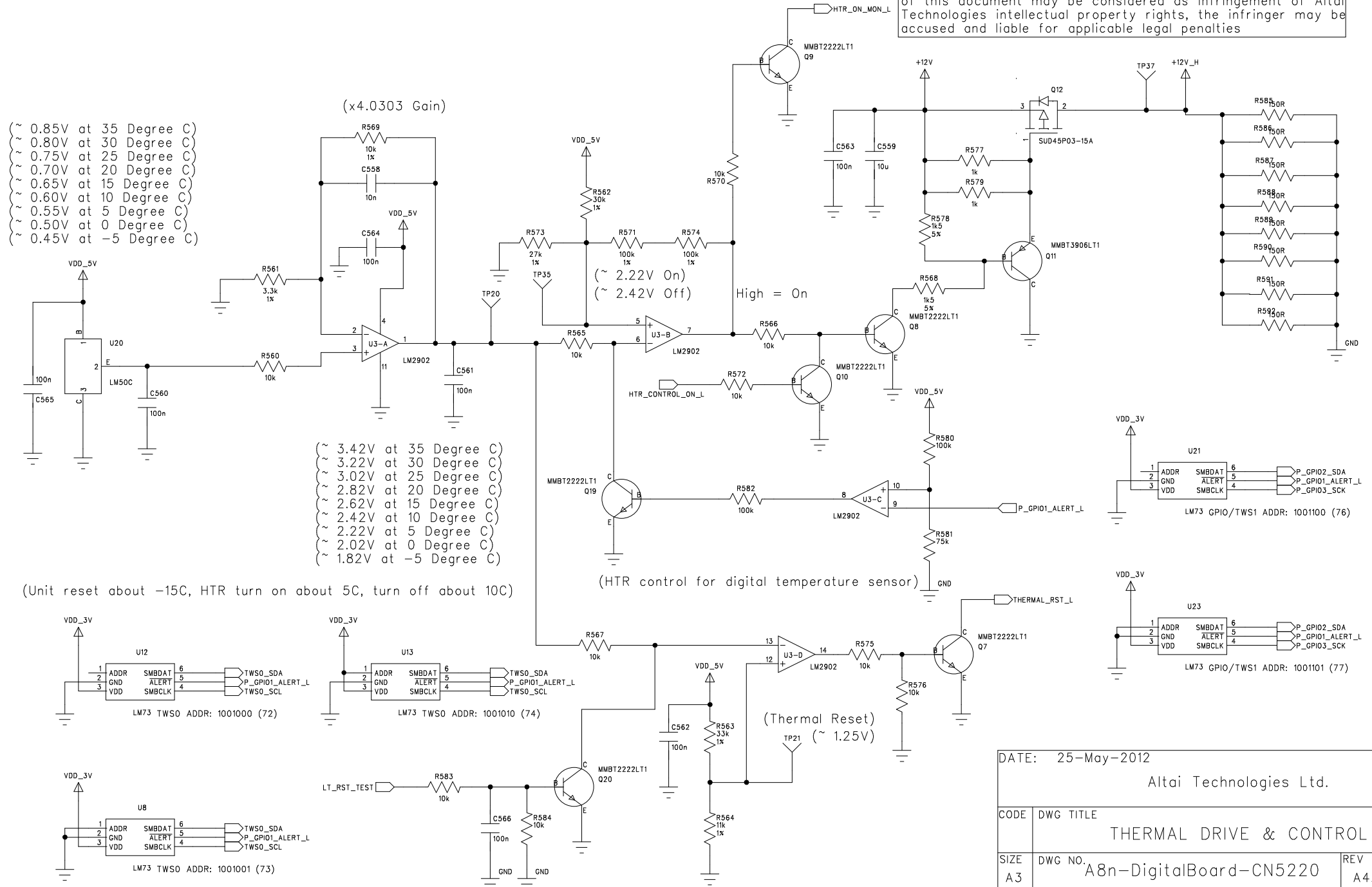


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DATE: 25-May-2012			
Altai Technologies Ltd.			
CODE	DWG TITLE		
	MINI PCI-E		
SIZE	DWG NO.		REV
A3	A8n-DigitalBoard-CN5220		A4.2
SCALE 1/1		SHEET 31 of 32	



6		5	4	3	2	1		
PART NUMBER BLOCK		Altai Technologies Limited Proprietary Information Any unauthorized use, duplication, reproduction, or disclosure of this document may be considered as infringement of Altai Technologies intellectual property rights, the infringer may be accused and liable for applicable legal penalties	REVISIONS					
PART NUMBER	REV		ZONE	REV	DESCRIPTION	DATE	APPROVED	
	A1			A0	First Draft (Integrated in MB)	October,2010		
	A2			A1	EVT First Draft Modification (Integrated in MB)	March,2011		
	A3			A2	DVT (Split from MB, added Choke & POE+)	July,2011		
					A3	DVT (Replace LM5116 by Isolated Power Supply)	August,2011	
					A4	MVT (Modify PCB outline & holes for assembly)	October,2011	

Altai Technologies Ltd.
WA8011NAC GbE Power Board Schematic

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		MATERIAL:					CODE	DWG TITLE			
		FINISH:						A8n GbE Power Board Schematic			
		PROCESS:									
		THIRD ANGLE PROJECTION					SIZE	DWG NO.			REV
							A3	A8n-GbE-Power-Board			A5.1
		DRAWN					SCALE	1/1			SHEET
		ENGR									1 of 6
		APVD									

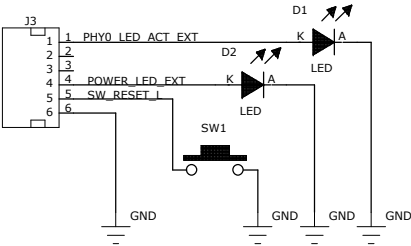
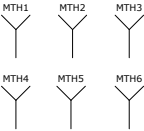
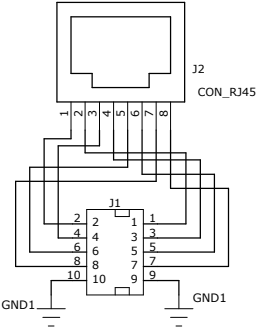
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PART NUMBER	REV		ZONE	REV	DESCRIPTION	DATE	APPROVED
	A1			A0	First Draft (Integrated in MB)	October,2010	
	A2			A1	EVT First Draft Modification (Integrated in MB)	March,2011	
	A3			A2	DVT/MVT (Modified MB & RF, added Interface Board)	May,2011	

Altai Technologies Ltd.
WA8011NAC Interface Board Schematic
CN5220 Based

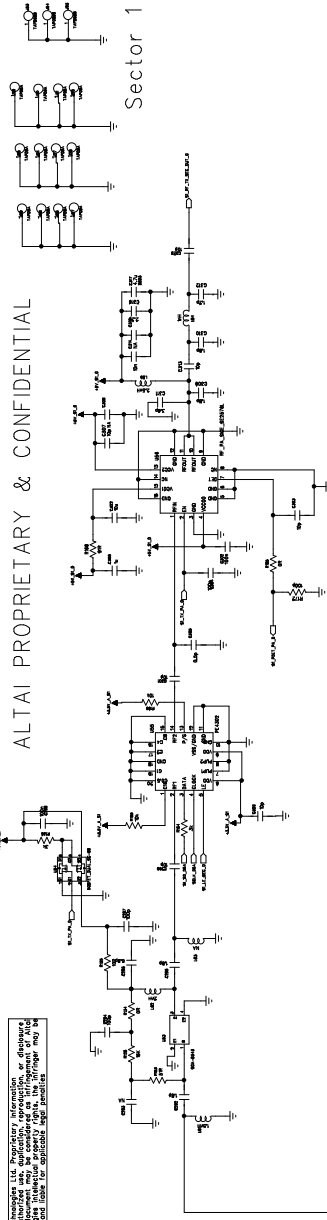
ALTAI CONFIDENTIAL

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		PROCESS:					
		THIRD ANGLE PROJECTION			DRAWN	Kenneth Tsui	19-May-2011
					ENGR	Kenneth Tsui	19-May-2011
					APVD		
NEXT ASSY		USED ON		SIZE	DWG NO.		REV
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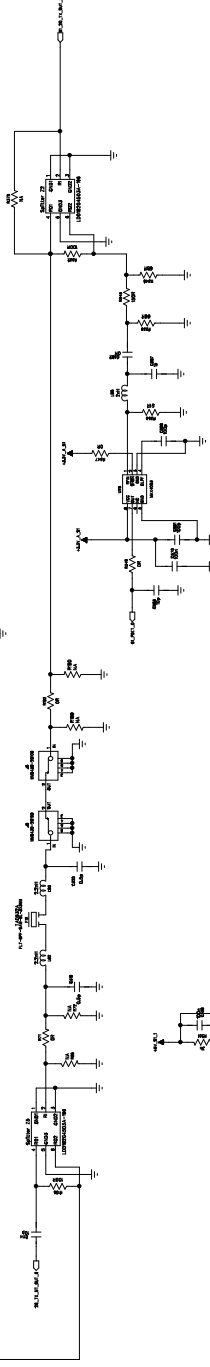
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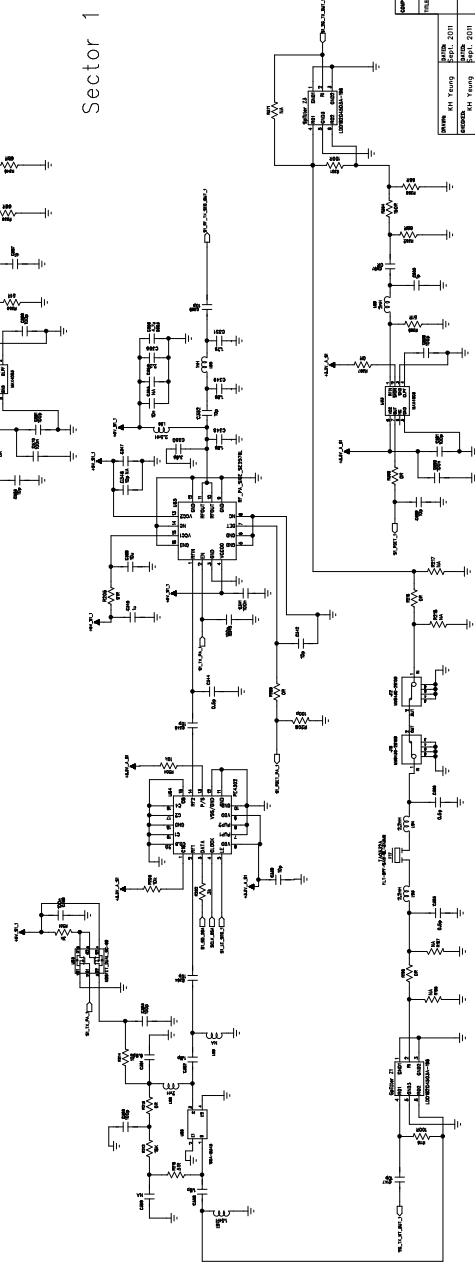
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CODE	DWG TITLE		
	LED & ETH connector		
SIZE	DWG NO.		REV
A3	A8n_ETH_PCB		A0
SCALE	1/1	SHEET 1 of 1	



Sector 1 Stream 0



Sector 1 Stream 1

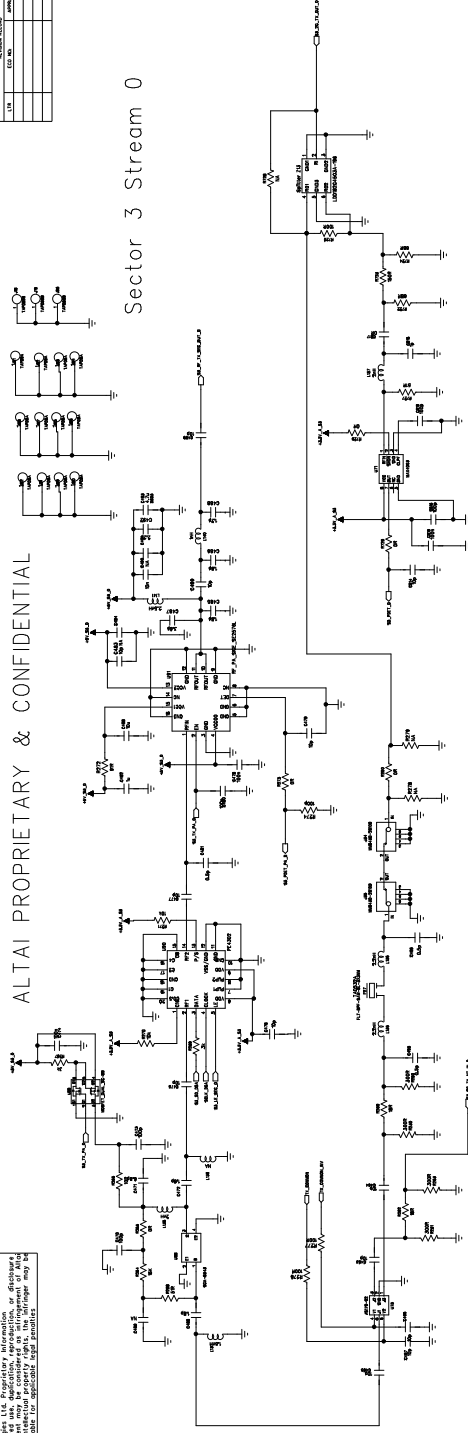


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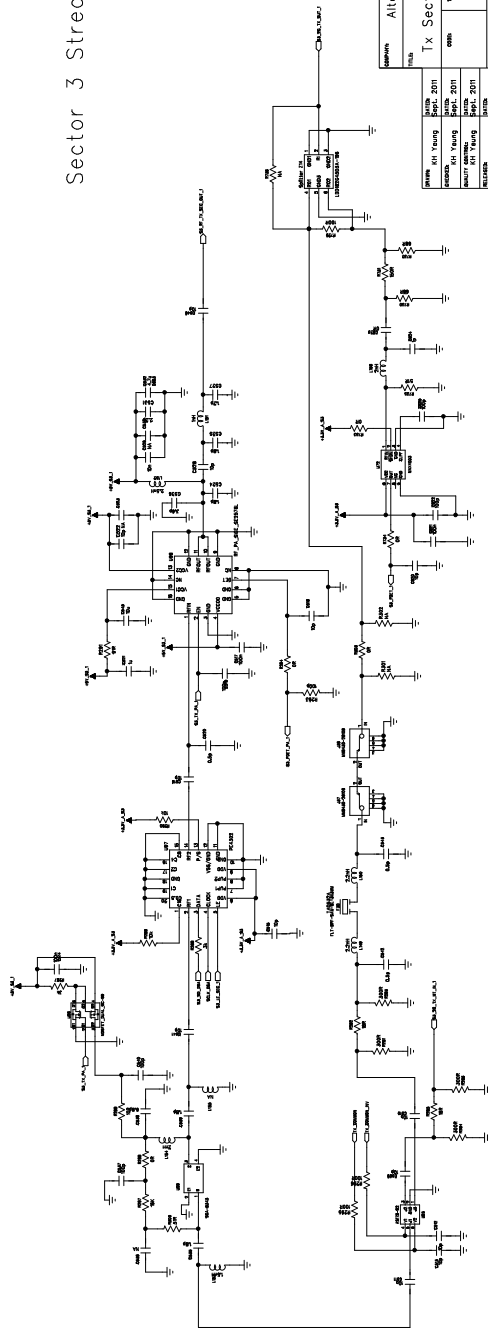
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Sector 3 Stream 0



Sector 3 Stream 1



Revision Record			
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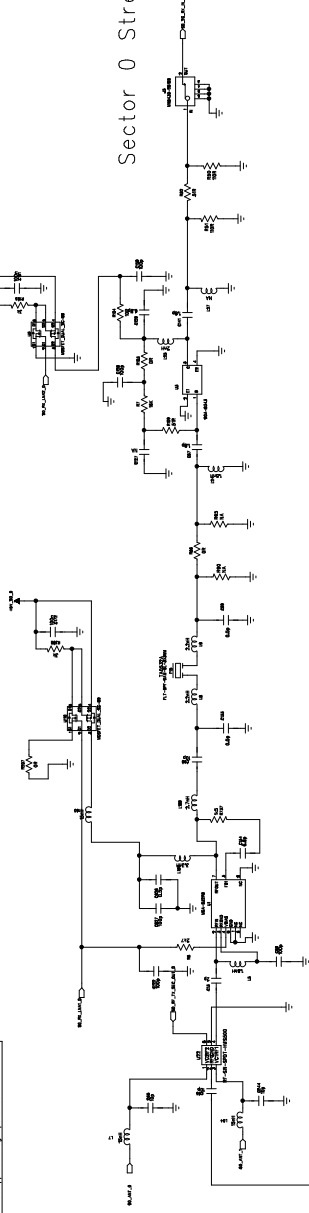
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Revision History			
Rev.	Rev. No.	Changes	Date
1	1.0	Initial Design	2011
2	1.1	Design Change	2011
3	1.2	Design Change	2011
4	1.3	Design Change	2011
5	1.4	Design Change	2011
6	1.5	Design Change	2011

Sector 0 Stream 0

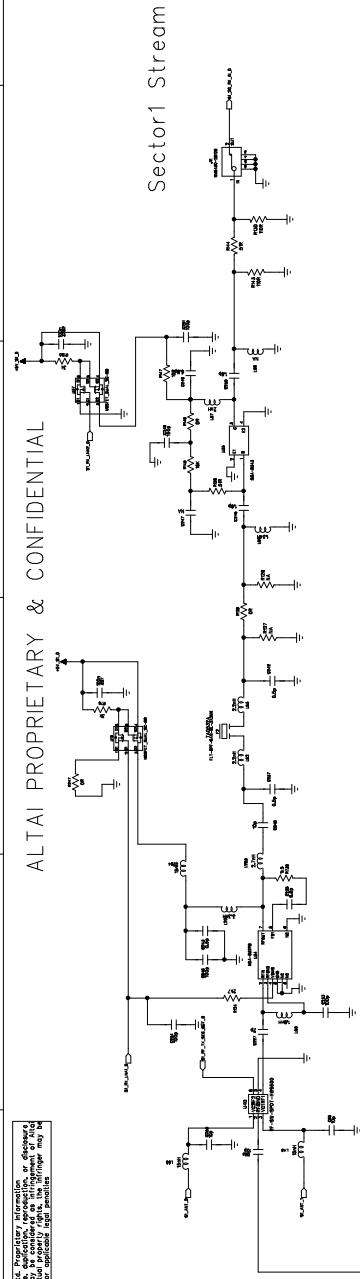


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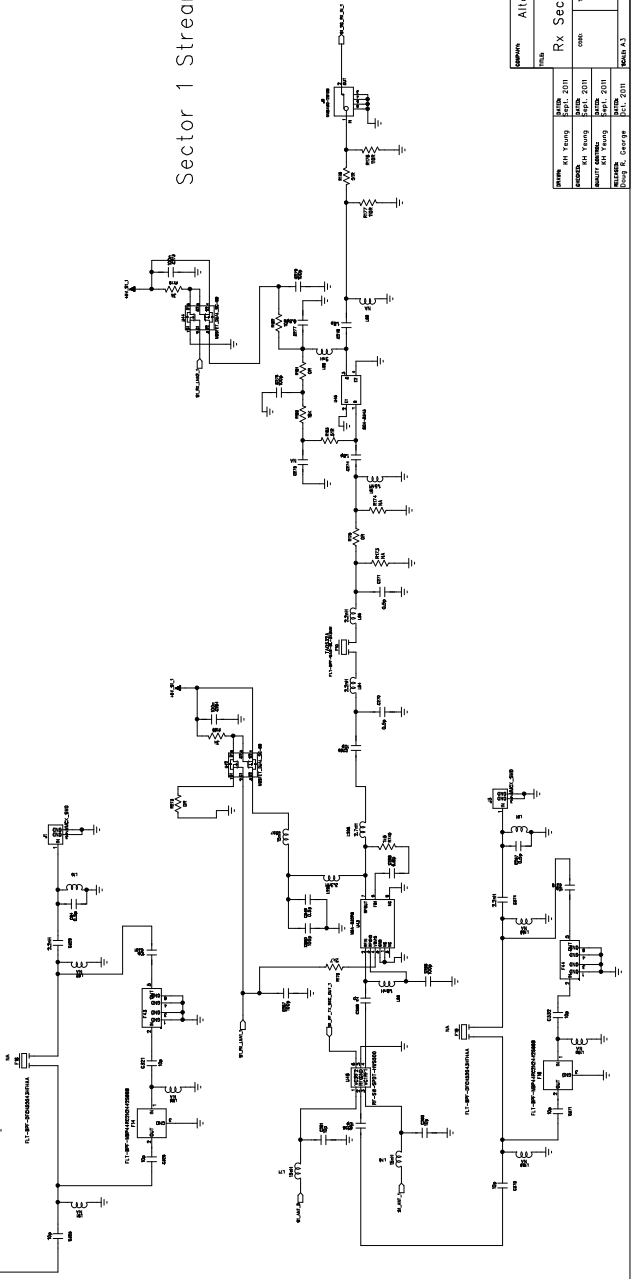
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Revision History		
Rev	Rev No	Comments
1.0		

Sector1 Stream 0



Sector 1 Stream 1



Altai Technologies Ltd.		
Rev	Rev No	Comments
1.0		

DESIGNED BY	DATE	DESIGNED BY	DATE	DESIGNED BY	DATE
ALT	2011	ALT	2011	ALT	2011
ALT	2011	ALT	2011	ALT	2011
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REV	REV No	REV
Rx Sector 1 Stream 0 & 1	D	R4.2

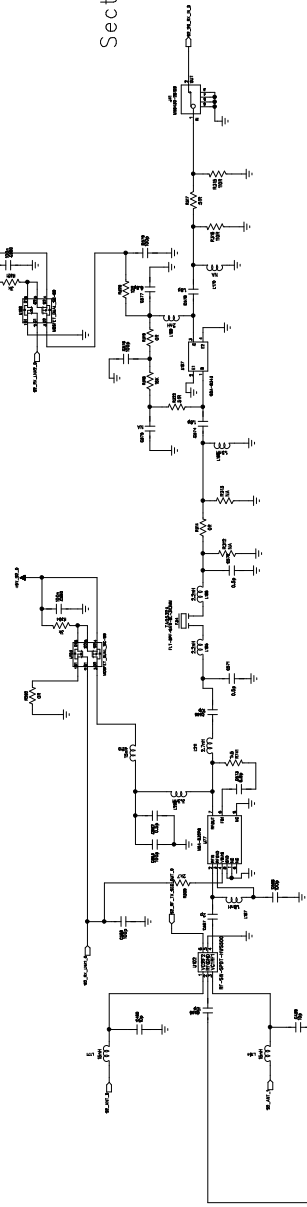
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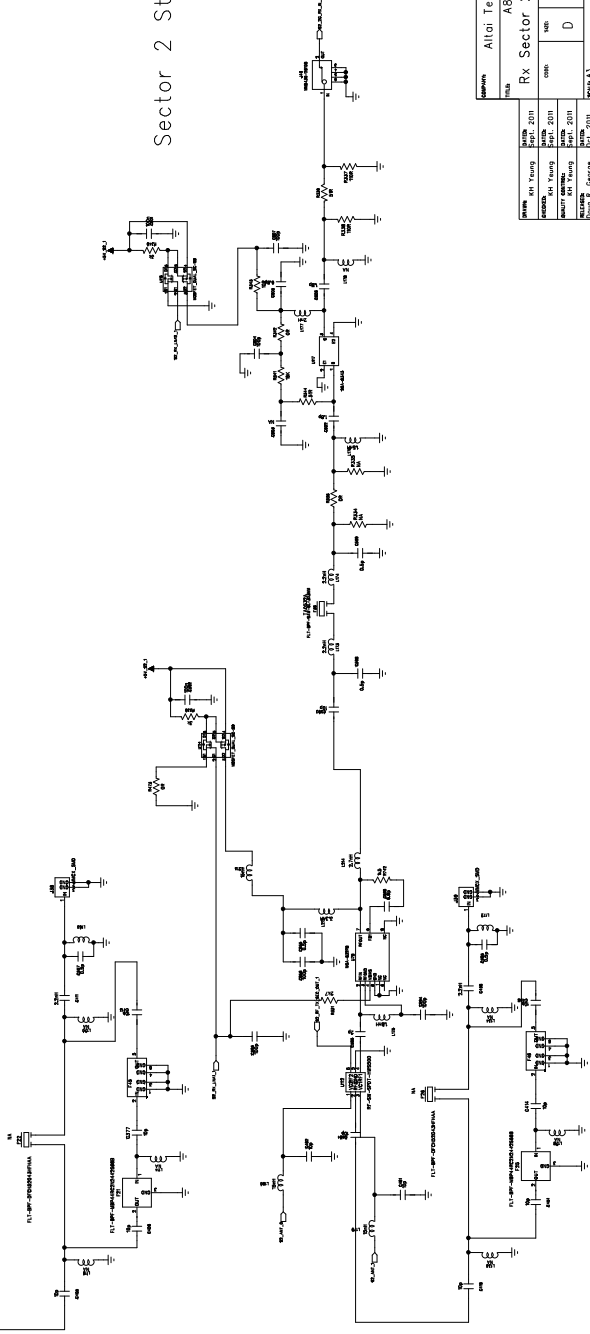
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Revision History		
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Sector 2 Stream 0



Sector 2 Stream 1

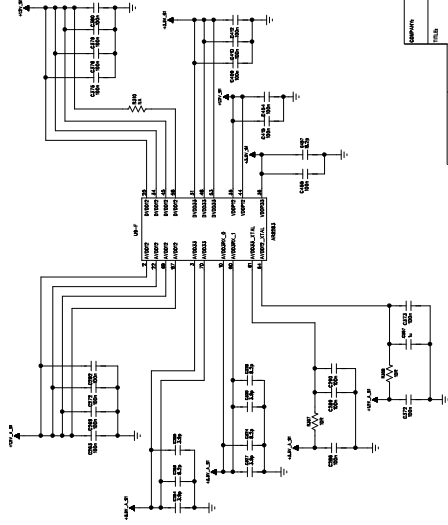
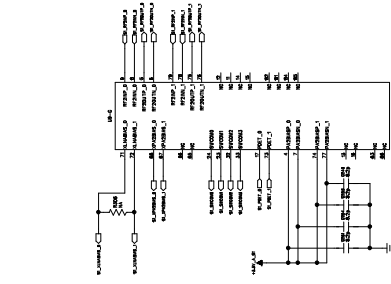
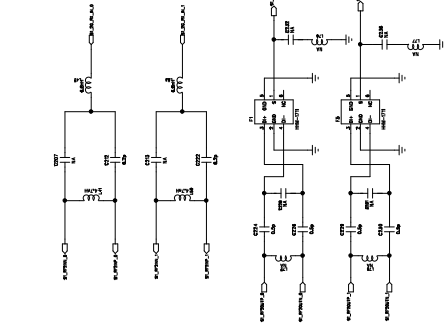
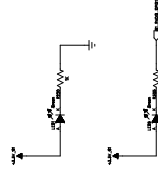
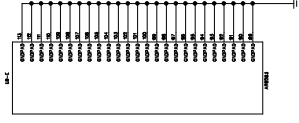
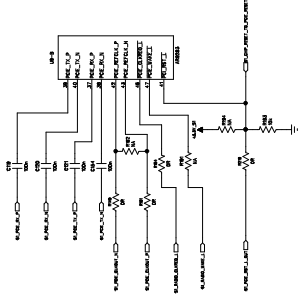
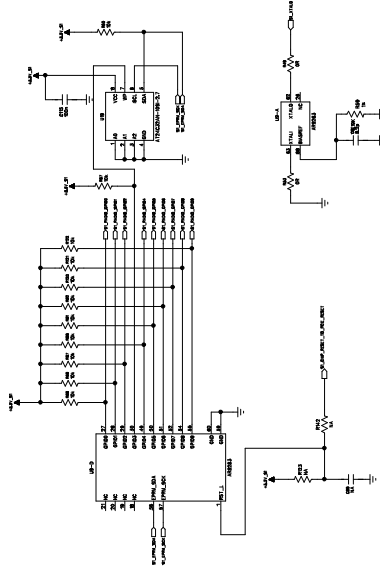


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A8N-BS-RF		
Rx Sector 2 Stream 0 & 1		
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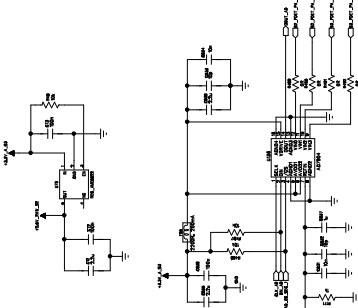
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Revision History		
Rev.	Rev. No.	Comments
1	1.0	Initial Release



Altai Technologies Ltd.		
ASX-BS-RF		
Radio Sector 1		
Rev.	Rev. No.	Rev.
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Company	Altai Technologies Ltd.			
Title	A8N-BS-RF SECTOR ADC			
costs	100N	DRIVING 50	100N	R4.2
Serial A3				ISSUES 14 OF 23

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Virtual Channel Resource Sharing

For SLO1_SLOT1

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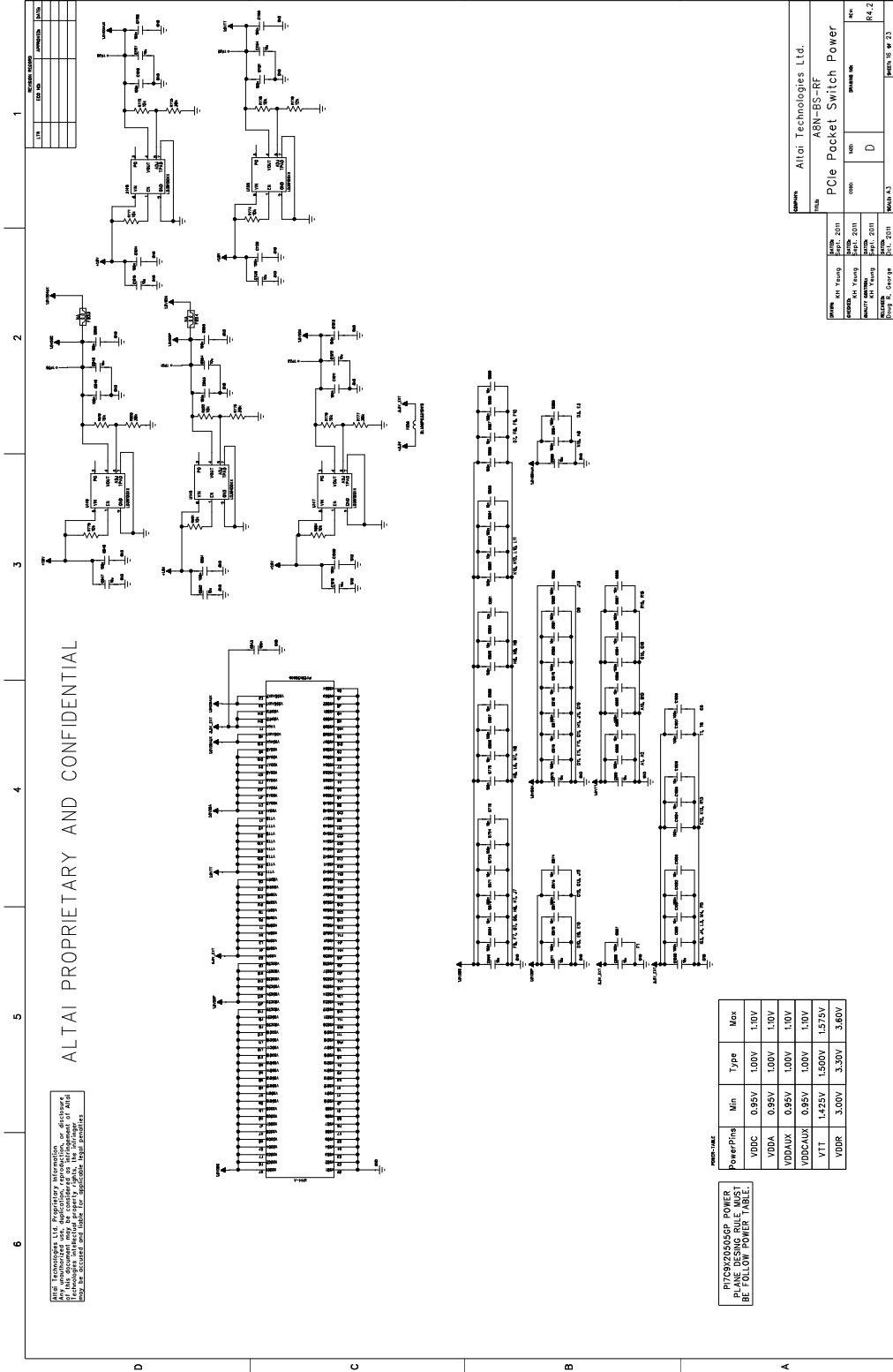
For SLO1_SLOT299

For SLO1_SLOT300

For SLO1_SLOT301

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P17C8V205056P POWER PLANE DESIGN RULE MUST BE FOLLOW POWER TABLE

POWER TABLE

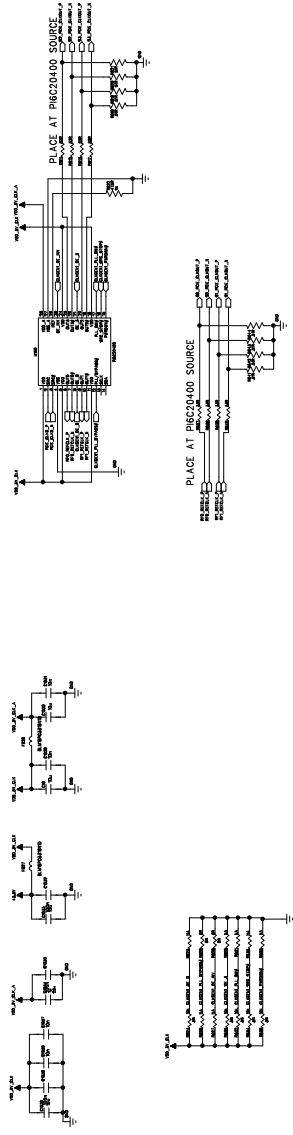
PowerPin	Min	Type	Max
VDDC	0.95V	1.00V	1.10V
VDDA	0.95V	1.00V	1.10V
VDDAUX	0.95V	1.00V	1.10V
VDDCAUX	0.95V	1.00V	1.10V
VTT	1.425V	1.500V	1.575V
VDDR	3.00V	3.30V	3.60V

Altai Technologies Ltd.			
Part No.	Rev.	Rev.	Rev.
A8N-BS-RF	A	A	A
PCle Packet Switch Power	1.0	1.0	1.0
Rev.	Rev.	Rev.	Rev.
R4.2	R4.2	R4.2	R4.2

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Revision History	
Rev.	Description
1.0	Initial Release



Company		Altai Technologies Ltd.	
Product		ARX-BS-RF	
Version		PCIe Clock Buffer	
Author		D	
Date		R4.2	
Page		Page 17 of 23	

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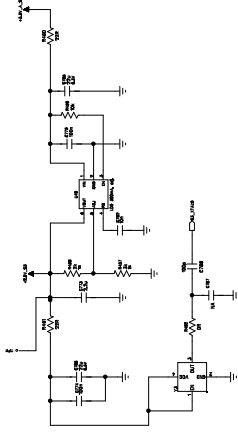
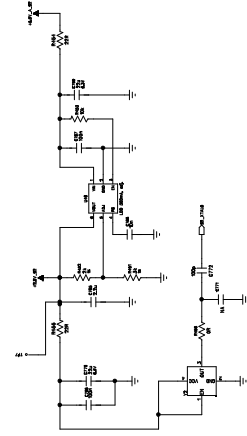
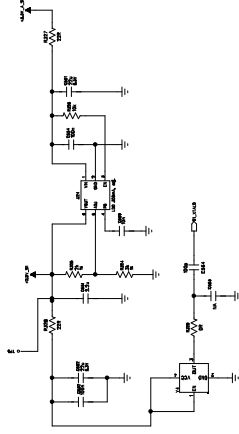
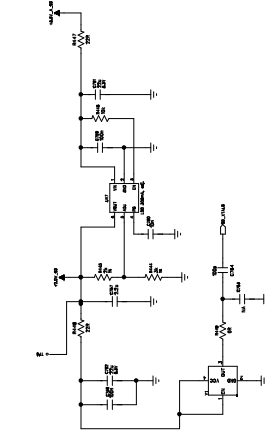
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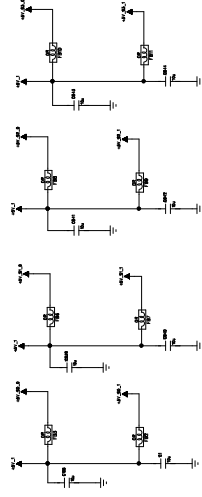
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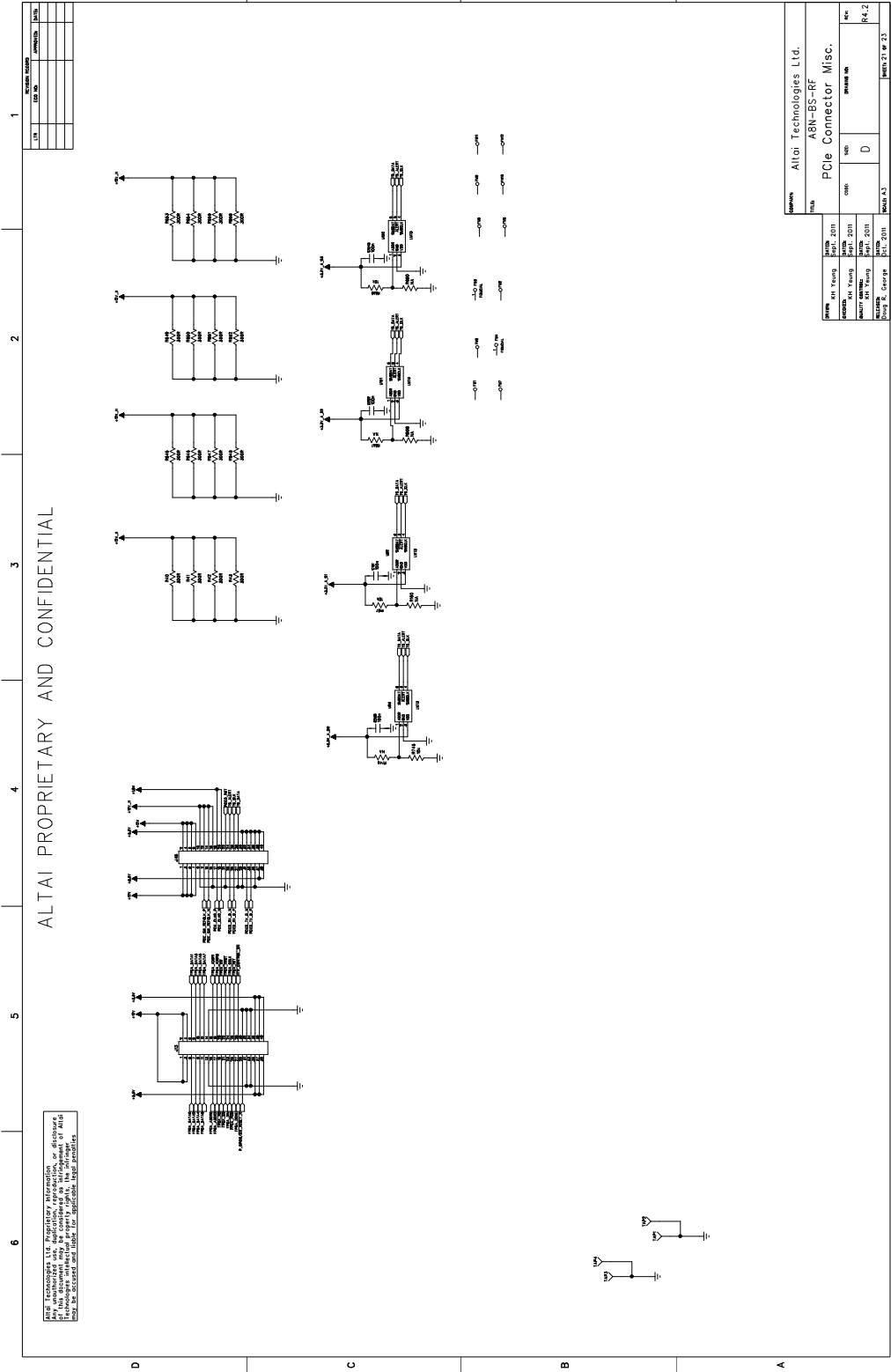
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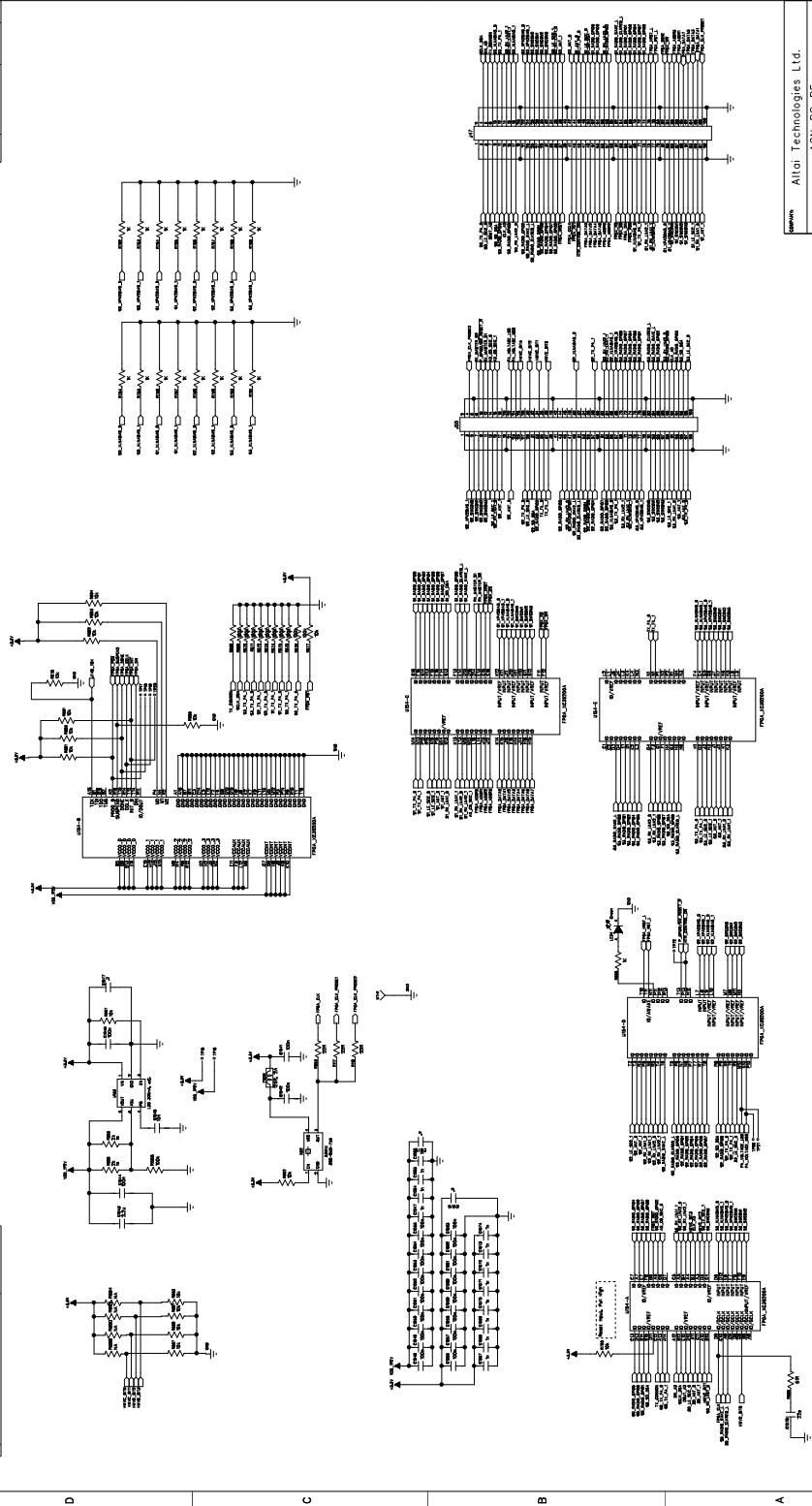


Company		Altai Technologies Ltd.	
Part No.		A8N-BS-RF	
Version		Rev. 1.0	
Date		2011-01-01	
Author		PCle Connector Misc.	
Checked		D	
Approved		R4.2	
Drawing No.		A8N-BS-RF	
Rev.		R4.2	
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REVISION HISTORY	
REV.	DESCRIPTION
1.0	Initial Release



COMPANY		Altair Technologies Ltd.	
NAME	DATE	NAME	DATE
DESIGNED BY	2018	DESIGNED BY	2018
CHECKED BY	2018	CHECKED BY	2018
APPROVED BY	2018	APPROVED BY	2018
REVIEWED BY	2018	REVIEWED BY	2018
DATE	2018	DATE	2018
REV.	R4.2	REV.	R4.2

