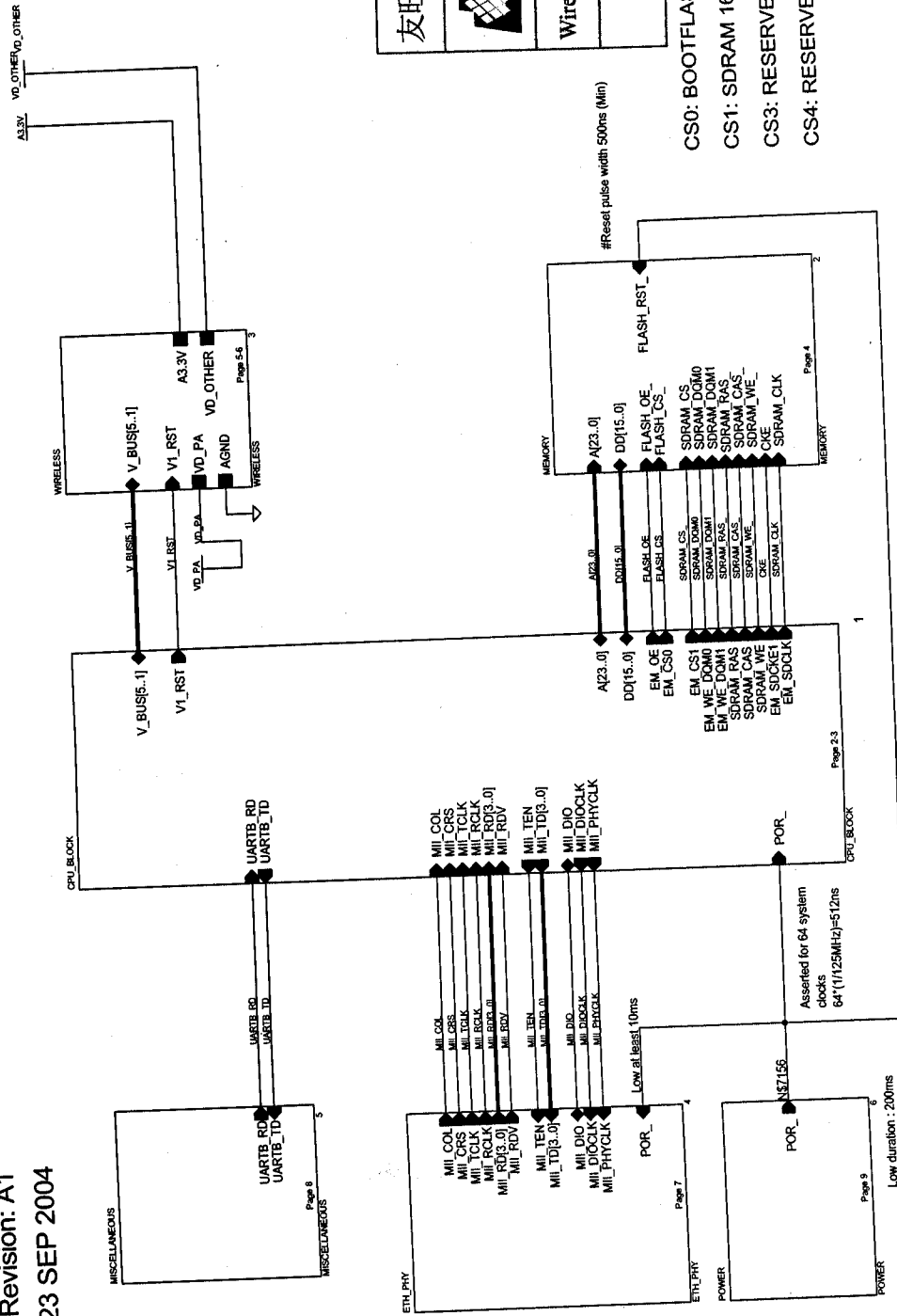


Date: 23 SEP 2004

External LED :

- POWER LED (pg.3)
- StatusErr (pg.3)
- Wireless Link/act (pg.3)
- LAN Link (pg.4)



友旺科技股份有限公司



Wireless 802.11b+g Access Point

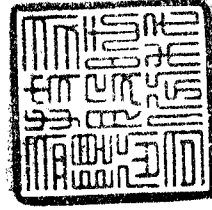
M/N : WAP404

CS0: BOOTFLASH

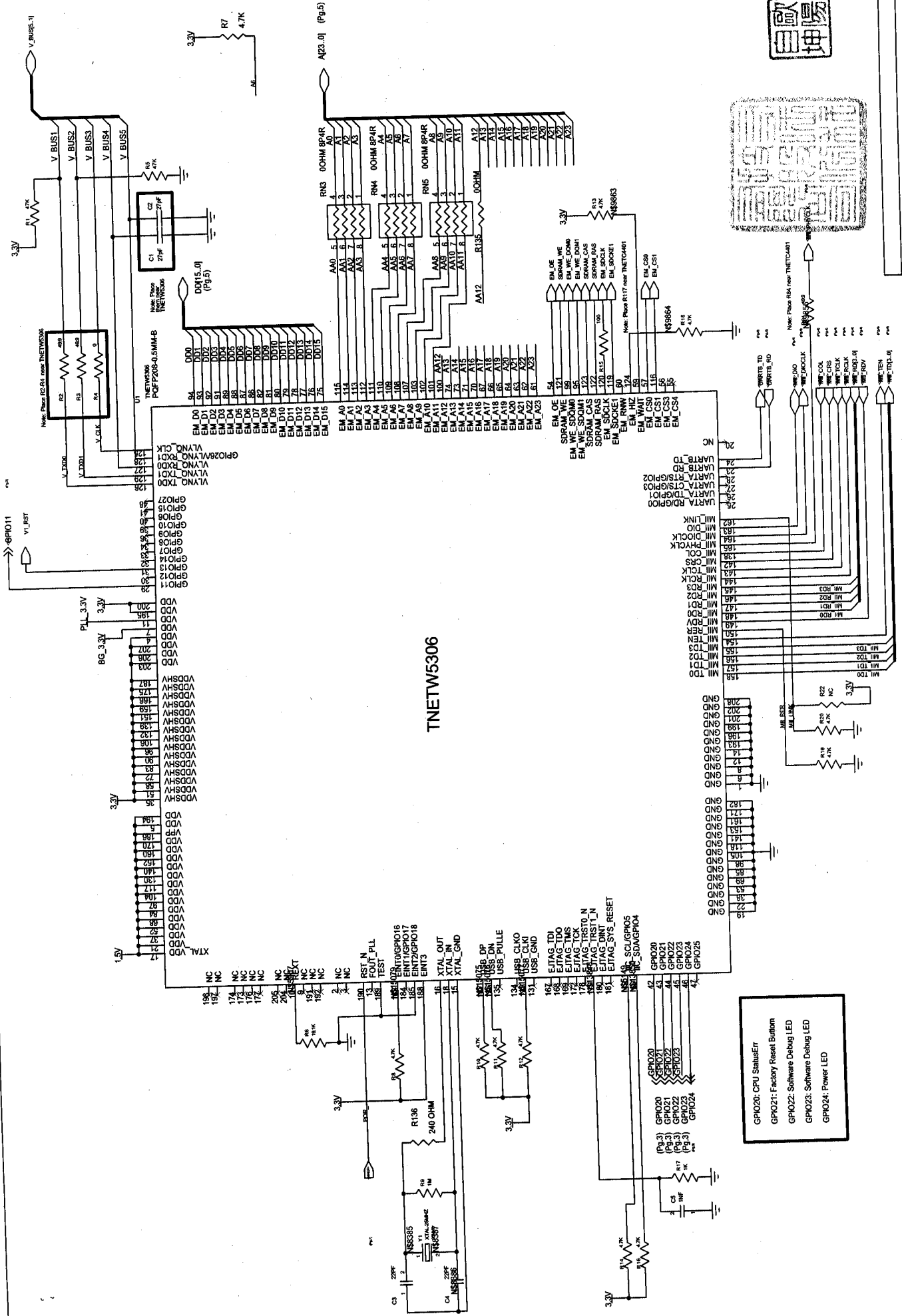
C.S1: SDRAM 16Bit

CS3: RESERVED

CS4: RESERVED



Title		WAP404 Wireless 802.11g AP		Rev	A1
Size	Document Number	WAP404_DIAGRAM		Sheet	1 of 9
Cust#m	Date		2004 年 09 月 23 日		

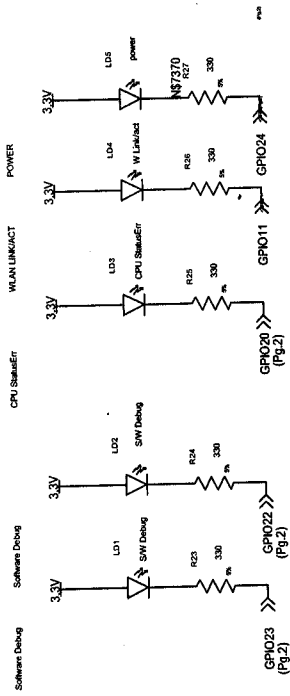


Title		WAP404 Wireless 802.11g AP	
Document Number		CPU TNETW5306-SHEET1	
Size		A3	
Date		2004.11.14	
Rev		X1	
Sheet		2 of 9	

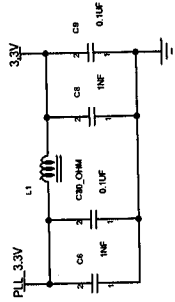
GPIO20: CPU Status LED
 GPIO21: Factory Reset Button
 GPIO22: Software Debug LED
 GPIO23: Software Debug LED
 GPIO24: Power LED

BOOTS_EM_A[18:16]
 000 - Reserved
 001 - Boot from CS0 - Flash
 010 - Boot from CS1 - SDRAM
 010 - Boot from CS3 - Asynchronous Memory
 100 - Boot from small (up to 2K) EEPROM through I2C
 101 - Boot from large (up to 64K) EEPROM through I2C
 110 - Reserved
 111 - Boot from VLYNQ

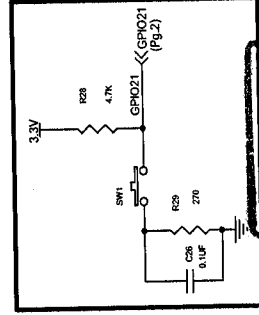
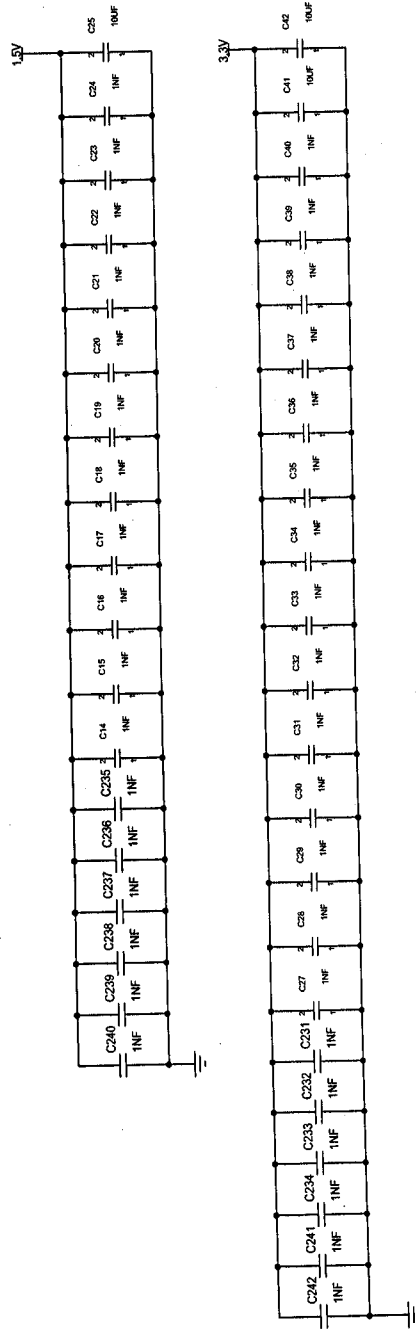
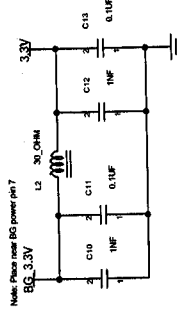
A5	N/A
A6	DEFAULT: 1-Internal default
A7	ETHCLKSEL: 1-ETH clock from XTAL_IN
A8	USBPSEL: 1-USB clock from XTAL_IN
A9	SYSPLSEL: 1-System clock from XTAL_IN
A10	N/A
A11	EMIF_TEST: 0-Normal
A12	EMIF_R: 1-Ext. memory speed as CPU
A13	Wdwp: 0-Watchdog timer disable bit is writable
A14	Wdhe: 0-Watchdog timer enable after reset
A15	PLBYPASS: 0-PLL mode
A16	
A17	
A18	BOOTS[8:16]: 001-Boot from external flash
A19	N/A
A20	VLYNQ_CLK (Default INPUT). 0 - VLYNQ_CLK is output
A21	ENDIAN: 1-Sig Endian
A22	
A23	FLASHW(23:22): 01-Flash Width 16 bit



Note: Place near PLL power pin 11



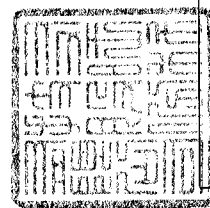
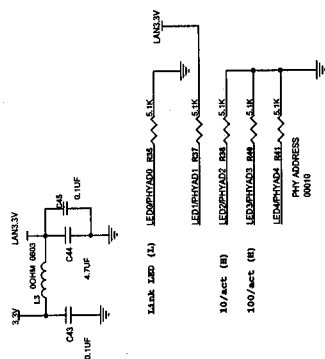
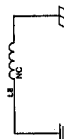
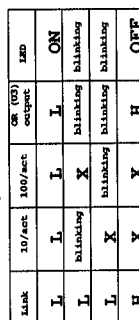
Note: Place near BG power pin 7



FACTORY TEST

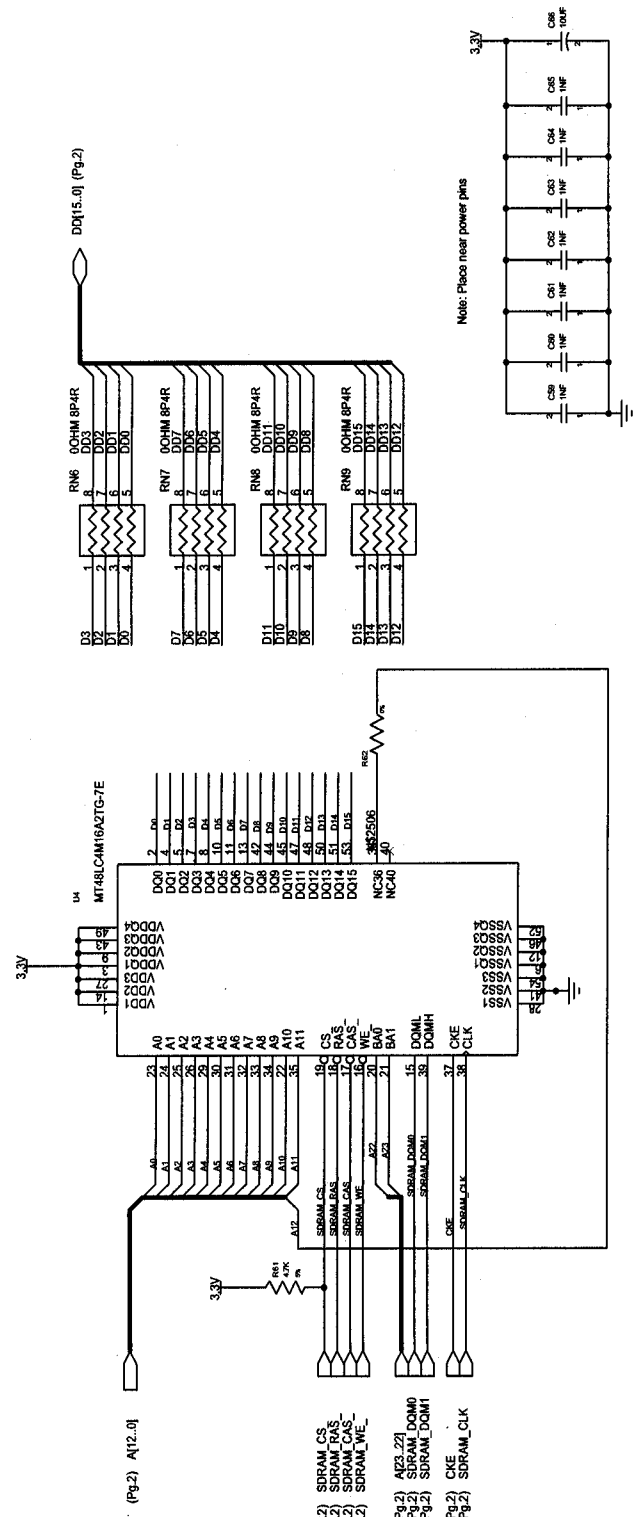


Title		WAP404 Wireless 802.11g AP	
Size	A3	Document Number	CPU TNE1W506_SHEET2
Rev	X1	Sheet	3 of 9
Date	2004.11.14	Sheet	3 of 9

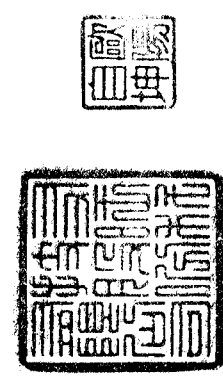
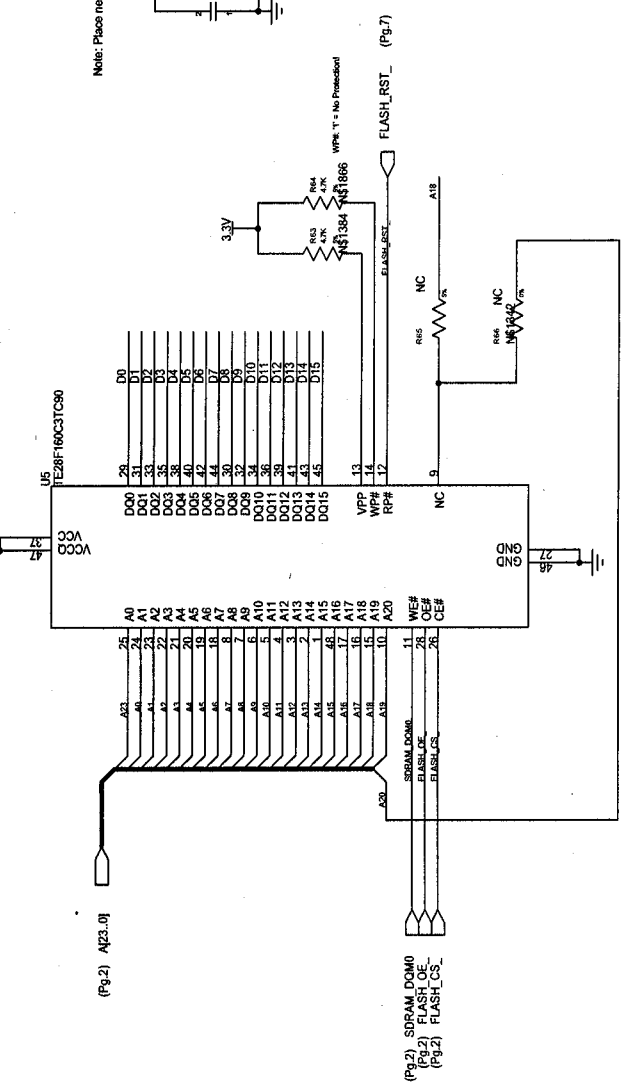


Model	WAP404 Wireless 802.11g AP	Rev	X1
Doc#	Document Number		
Custom	ETHERNET - RTL 8201CP		

SDRAM - 8Mbyte (4M x 16)
 For 16Mbyte SDRAM populate Micron MT48LC8M16A2TG-7E
 For 16Mbyte SDRAM populate R61



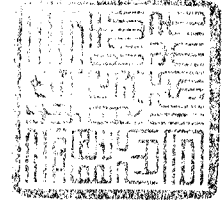
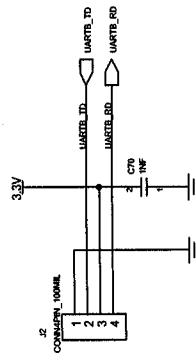
FLASH - 2Mbyte (1M x 16)
 For 4Mbyte FLASH populate Intel TE28F320C3TA110
 FLASH Change Note:
 1. INTEL TE28F320C3 - A19(15), NC(9)
 2. SST 39VF160 - A19(9), NC(15)



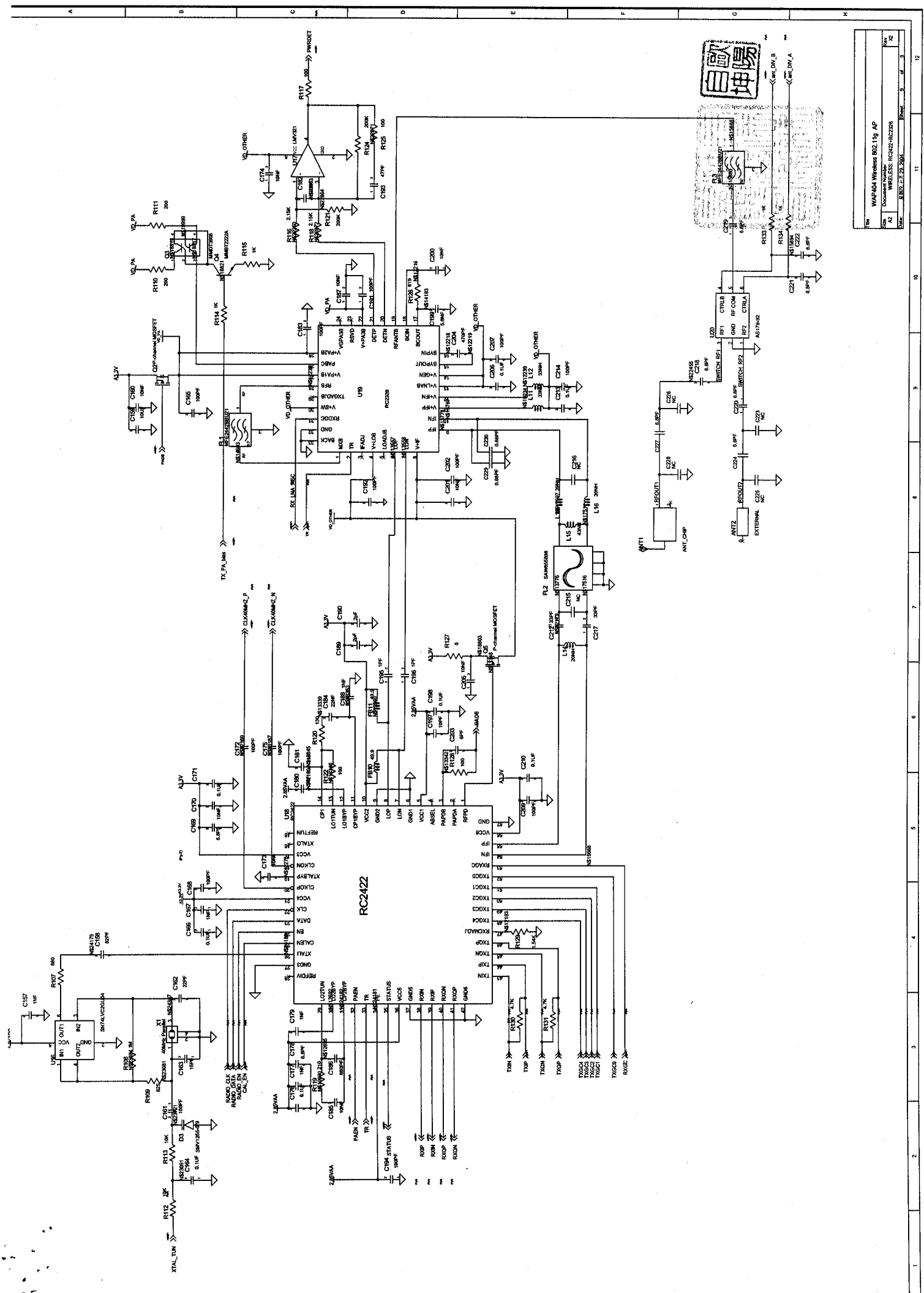
File	WAP404 Wireless 802.11g AP
Doc Number	MEMORY (SDRAM+FLASH)
Rev	XI
Date	2004.07.07
Sheet	5 of 9

UART#A

Connection for RS232 Adapter Card (PC31 Rev02 3.3V)
(FOR DEBUG ONLY)



Title		WAP404 Wireless 802.11g AP	
Size	Document Number	Rev	
A3	MISC_UART+E/JAG	XI	
Date:	日期	Sheet	6 of 9
	2004		



Rev	1.0	2.0	3.0	4.0	5.0	6.0	7.0	8.0	9.0	10.0	11.0	12.0
Doc	Doc00000000	Doc00000001	Doc00000002	Doc00000003	Doc00000004	Doc00000005	Doc00000006	Doc00000007	Doc00000008	Doc00000009	Doc00000010	Doc00000011
Rev	1.0	2.0	3.0	4.0	5.0	6.0	7.0	8.0	9.0	10.0	11.0	12.0