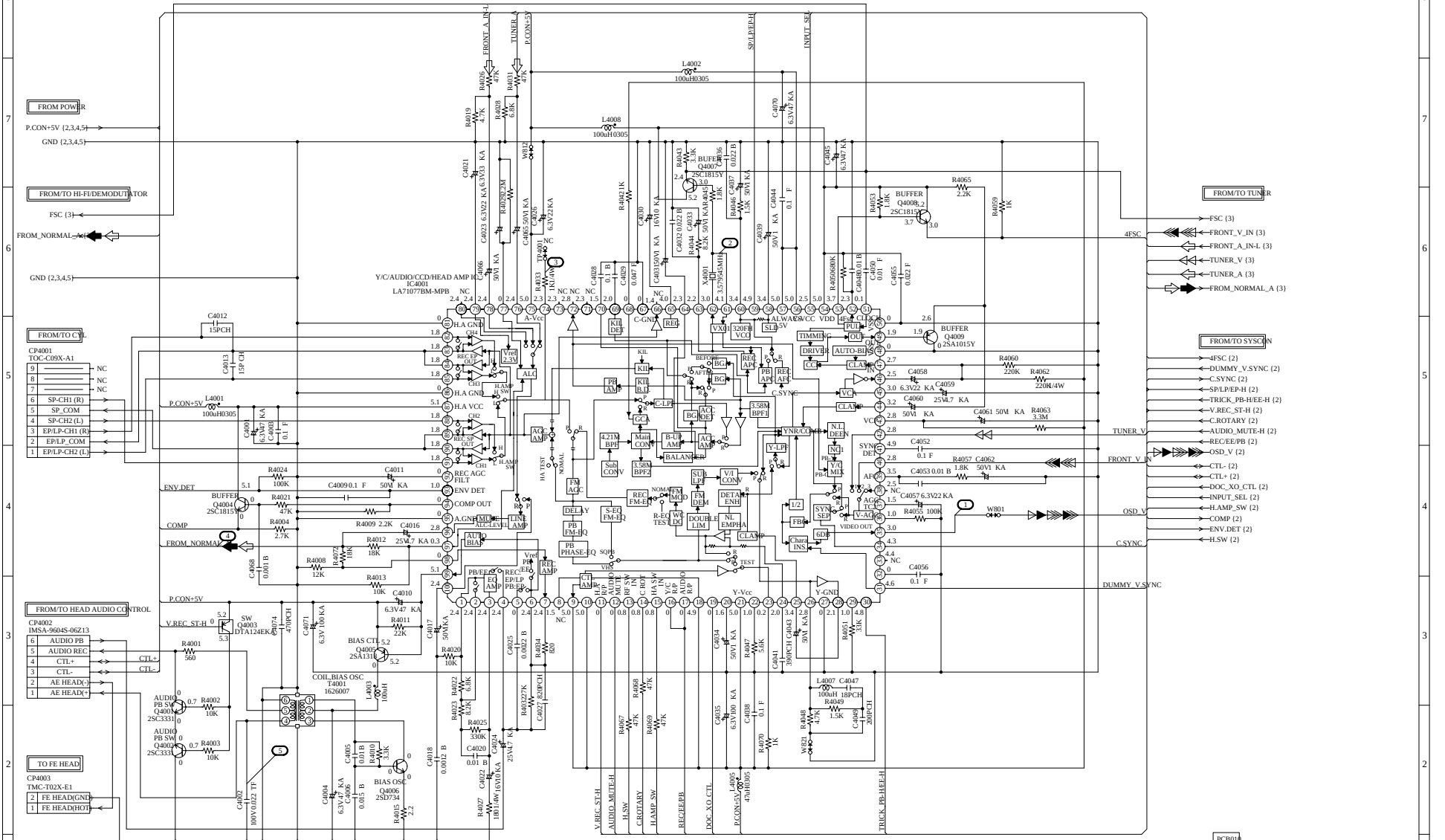


Y/C/AUDIO/CCD/HEAD AMP SCHEMATIC DIAGRAM (SYSCON PCB)



NOTES: THIS SCHEMATIC DIAGRAM IS THE LATEST AT THE TIME OF PRINTING AND SUBJECT TO CHANGE WITHOUT NOTICE.
THE DC VOLTAGE AT EACH PART WAS MEASURED WITH THE DIGITAL TESTER DURING PLAYBACK.

- ▶ RECORD COLOR SIGNAL
- ▶ RECORD LUMINANCE SIGNAL
- ▶ AUDIO SIGNAL (REC)
- ▶ AUDIO SIGNAL (PB)
- ▶ PLAYBACK COLOR SIGNAL
- ▶ PLAYBACK LUMINANCE SIGNAL
- ▶ TUNER VIDEO SIGNAL

PCB011 VMX111		DET. CHKD BY	DESCRIPTION	FUNCTION	PCB NAME
CHKD BY	STANDARD			Y/C/AUDIO/CCD/HEAD	VMX011A_B_BANK1
DESIGN BY	MODEL			DWG. NO.	TSU: A
DRAWN BY				SECTION: TECH-HQ3	DATE:
				DISTRIBUTE:	PUBLICATION: TECH-HQ3 SEC.

ATTENTION: LES PIÈCES RÉPARÉES PAR UN TECHNICIEN QUALIFIÉ. DANGER: NE PAS TOUCHER LES POINTS DE VUE SÉCURITÉ. N'UTILISER QUE DES CELLULES DÉCRITES DANS LA NOMÉNCLATURE DES PIÈCES.

CAUTION: THESE PARTS MARKED WITH A WARNING SYMBOL ARE CRITICAL FOR SAFETY. USE ONLY ONES DESCRIBED IN PARTS LIST ONLY.

SYSCON SCHEMATIC DIAGRAM (SYSCON PCB)

CAUTION: DIGITAL TRANSISTOR

RECORD COLOR SIGNAL
RECORD LUMINANCE SIGNAL
PLAYBACK COLOR SIGNAL
PLAYBACK LUMINANCE SIGNAL

FROM TO HI-FI/DEMODULATOR

FROM TO V/AUDIO/CCD HEAD AMP

FROM TO OPERATION DISPLAY

NOTE: THE DC VOLTAGE AT EACH PART WAS MEASURED WITH THE DIGITAL TESTER DURING PLAYBACK.

NOTE: THIS SCHEMATIC DIAGRAM IS THE LATEST AT THE TIME OF PRINTING AND SUBJECT TO CHANGE WITHOUT NOTICE.

DET. CHKD BY	STANDARD	FUNCTION	SYSCON	PCB NAME
CHKD BY	MODEL	DWG. NO.	1/2	VMX181A.B
DESIGN BY	MODEL	SECTION	TEC. HQ	DATE
DRAWN BY	MODEL	DISTRIBUTE	PUBLICATION	TEC. HQ 3 SEC.

CAUTION: DIGITAL TRANSISTOR

RECORD COLOR SIGNAL
RECORD LUMINANCE SIGNAL
PLAYBACK COLOR SIGNAL
PLAYBACK LUMINANCE SIGNAL

8

FROM/TO HI-FI/DEMODULATOR

→ V_REC_ST-H (1)
← HI-FI_ENV.DET
→ AUDIO_MUTE-H (1)
→ IIC_CLK (3)
← IIC_DATA (3)
← ST_SELECT

6

FROM/TO V/C(AUDIO/CCN/HEAD AND AMP)

7 → C.ROTARY (1)
 6 → AUDIO_MUTE-H (1)
 5 → REC_EE/PB (1)
 4 ← OSD_V (1)
 3
 2 ↔ CTL- (1)
 1 ↔ CTL+ (1)
 0 → DOC_XO_CTL (1)
 G → INPUT_SEL (1)
 G → HAMP_SW (1)
 G ← COMP (1)
 G → ENV_DET (1)
 0 → H.SW (1)

FROM/TO OPERATION/DISPLAY

← REM_IN (5)

← KEY-B (5)

← KEY-A (5)

→ TV/VCR_LED (5)

→ T-REC_LED (5)

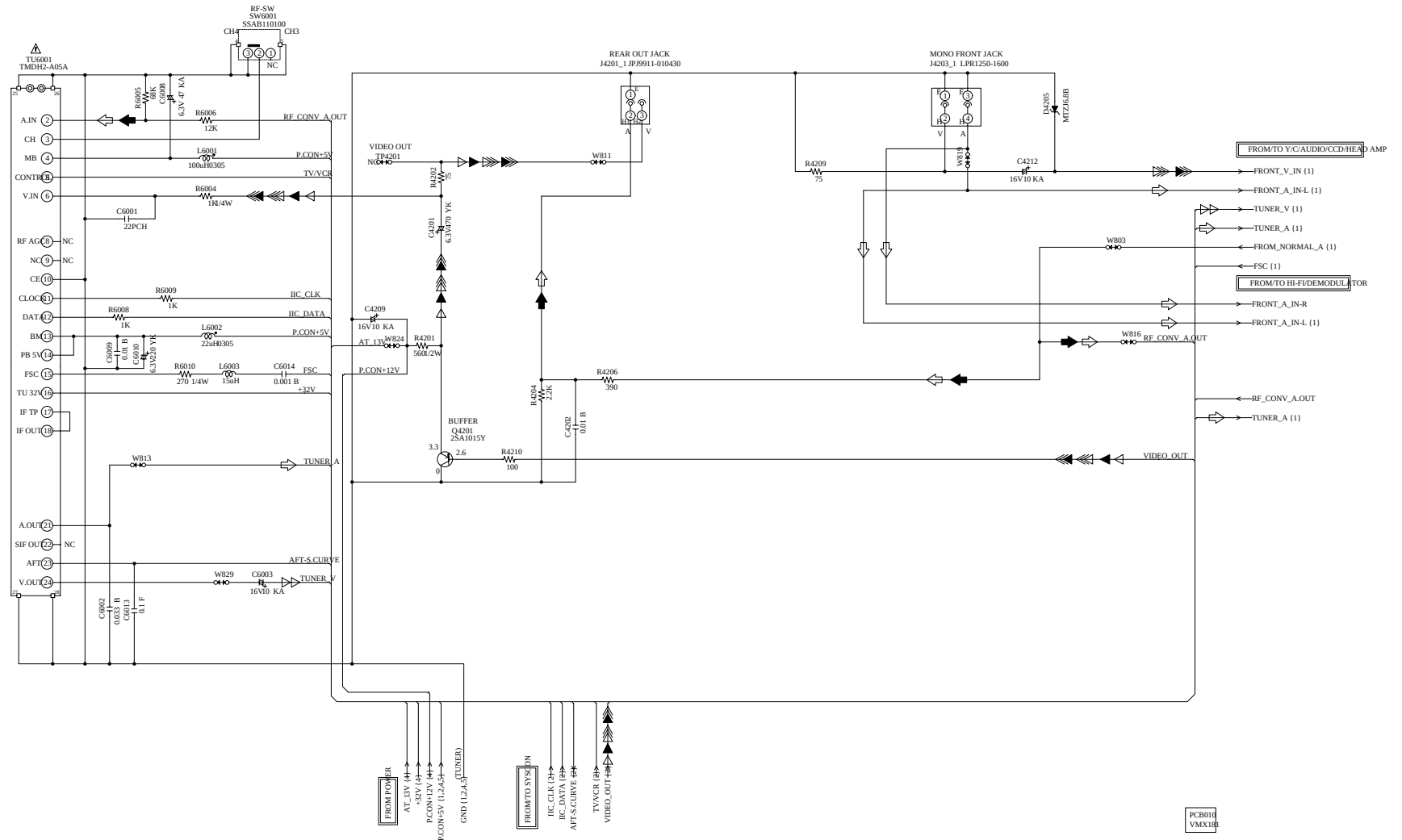
→ REC_LED (5)

NOTE: THE DC VOLTAGE AT EACH PART WAS MEASURED WITH THE DIGITAL TESTER DURING PLAYBACK

NOTE: THIS SCHEMATIC DIAGRAM IS THE LATEST AT THE TIME OF PRINTING AND SUBJECT TO CHANGE WITHOUT NOTICE

PCB010 VMX18B				
DET.CHKD BY	DESCRIPTION	FUNCTION	PCB NAME VMX18B_1A	ANKI
CHKD BY	STANDARD	DWG.NO.	1/5	1
DESIGN BY	MODEL	ORION ELECTRIC CO.LTD		
DRAWN BY		SECTION: TEC-HQ	DATE:	
		DISTRIBUTE:		
		PUBLICATION: TEC-HQ3 SEC.		

TUNER SCHEMATIC DIAGRAM (SYSCON PCB)



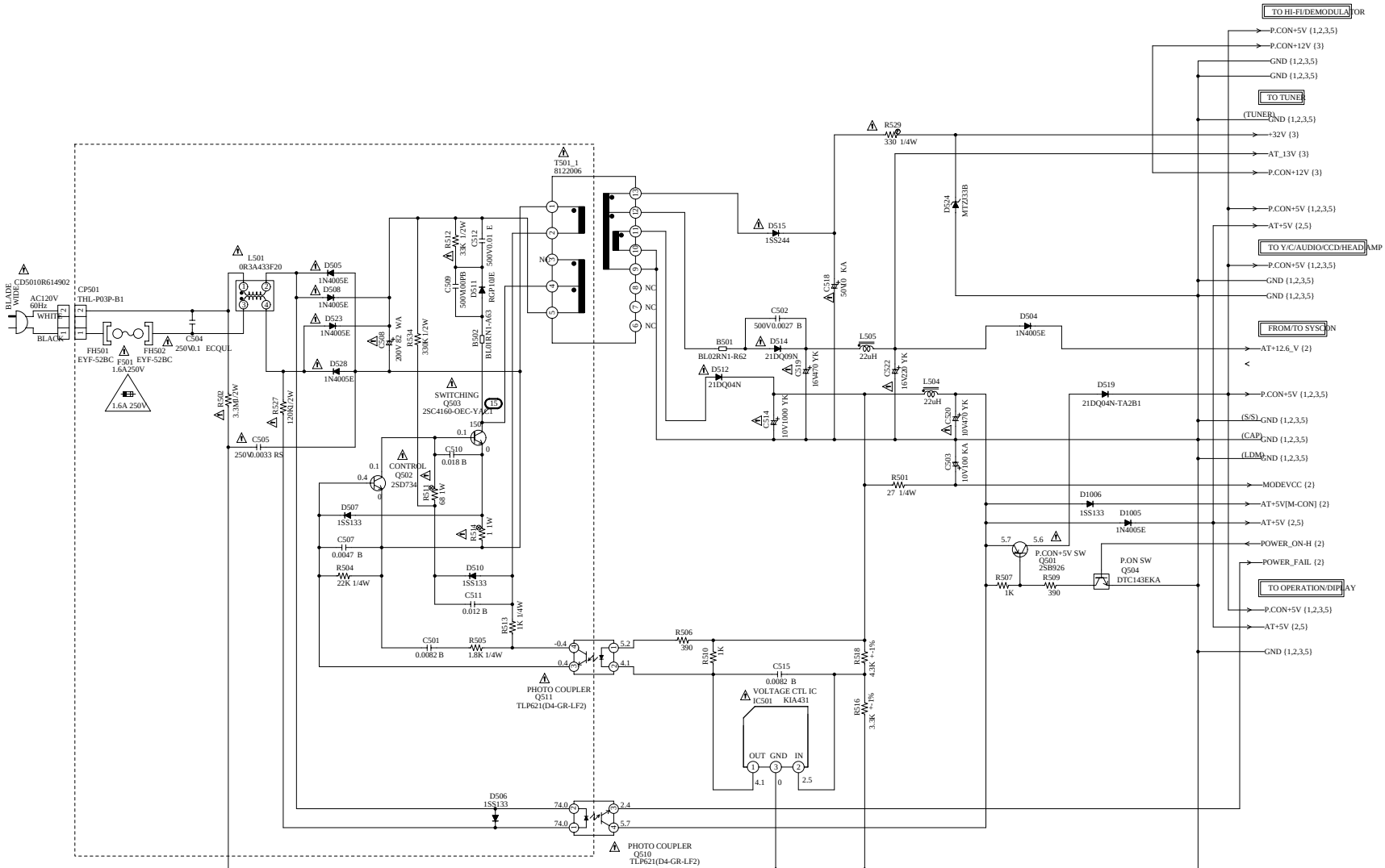
NOTE: THE DC VOLTAGE AT EACH PART OF THIS SCHEMATIC DIAGRAM IS THE LATEST AT THE TIME OF PRINTING AND SUBJECT TO CHANGE WITHOUT NOTICE. CRITICAL FOR SAFETY USE ONES DESCRIBED IN PARTS LIST ONLY.

ATTENTION: LES PIÈCES RÉPARÉES PAR UN MANUTENANT DANGEREUSES AN POINT DE VUE SECURITE. N'UTILISER QUE CELLES DÉCRITES DANS LA NOMENCLATURE DES PIÈCES.

◀ PLAYBACK LUMINANCE SIGNAL
◀ PLAYBACK COLOR SIGNAL
▶ RECORD LUMINANCE SIGNAL
▶ RECORD COLOR SIGNAL
◀ AUDIO SIGNAL (REC)
▶ AUDIO SIGNAL (PB)
◀ TUNER VIDEO SIGNAL

DET. CHKD BY	DESCRIPTION	FUNCTION	PCB NAME
CHKD BY	STANDARD	DWG. NO.	VMX181A_B
DESIGN BY	MODEL	Rev. No.	1
DRAWN BY		ISSU: A	
		ORION ELECTRIC CO. LTD	
		SECTION: TECH	
		DISTRIBUTE:	
		PUBLICATION: TEC-HQ	

POWER SCHEMATIC DIAGRAM (SYSCON PCB)



CAUTION FOR CONTINUED PROTECTION AGAINST FIRE HAZARD, REPLACE ONLY WITH THE SAME TYPE FUSE 1.6A 250V (F501).
ATTENTION POUR UNE PROTECTION CONTINUE LES RISQUES D'INCENDIE N'UTILISER QUE DES FUSIBLES DE MEME TYPE 1.6A 250V (F501).

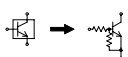
MEASURED WITH THE DIGITAL TESTER DURING PLAYBACK

THIS SCHEMATIC DIAGRAM IS THE LATEST AT THE TIME OF PRINTING AND SUBJECT TO CHANGE WITHOUT NOTICE

DES PIECES REPARÉES PAR UN
DANGEREUSES A POINT DE VUE SECURITE
N'UTILISER QUE CELLES DECRITES
DANS LA NOMENCLATURE DES PIECES

CAUTION: THESE PARTS MARKED ARE
CRITICAL FOR SAFETY, USE ONES
DESCRIBED IN PARTS LIST ONLY

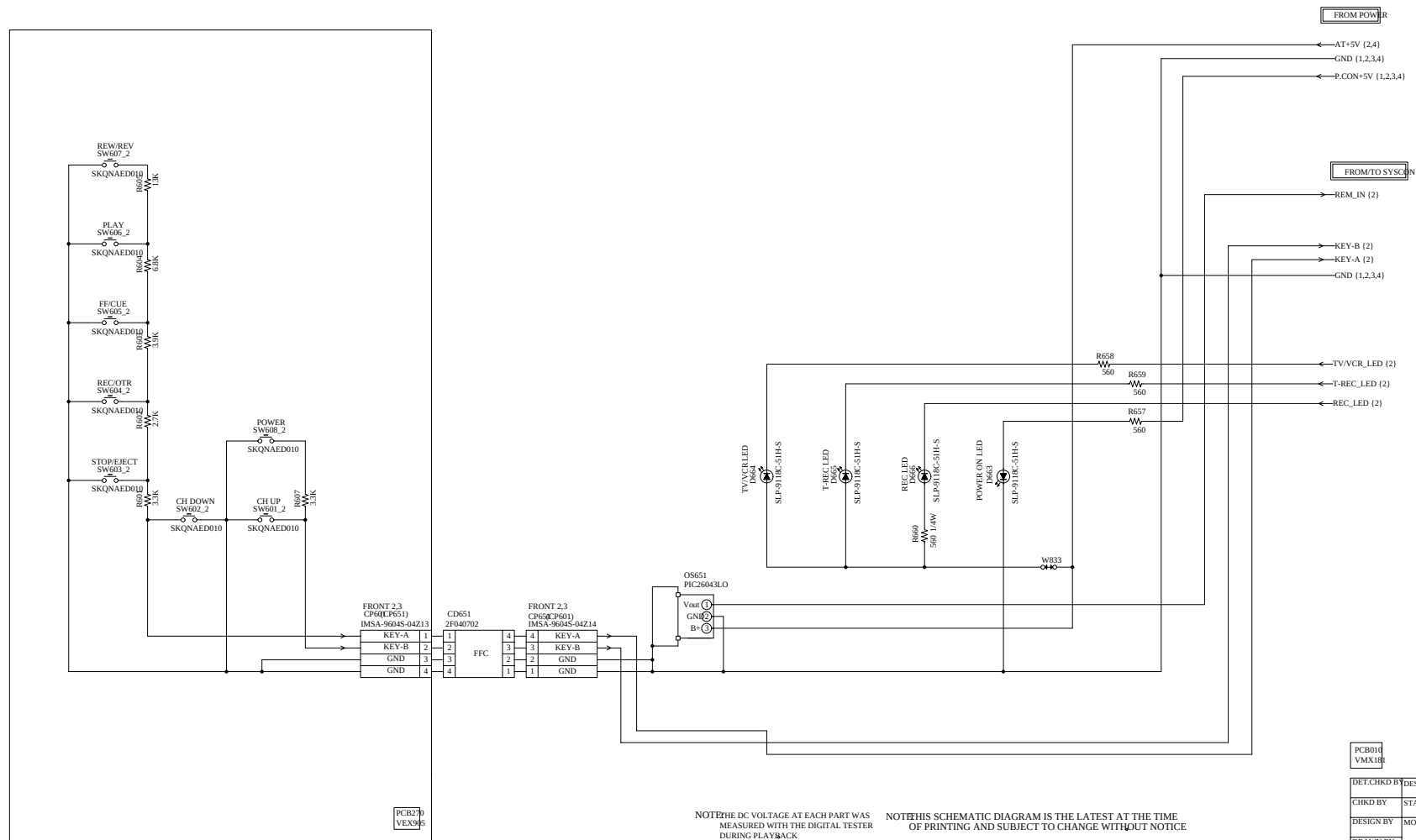
CAUTION: DIGITAL TRANSISTOR



PCB014
VMX118

DET. CHKD BY	DESCRIPTION	FUNCTION	POWER	PCB NAME
CHKD BY	STANDARD	DWG. NO.	Rev. No.	VMX181A_B
DESIGN BY	MODEL	ISSU	Rev. No.	1
DRAWN BY		SECTION: TECH	DATE: 09/90	
		DISTRIBUTE:	PUBLICATION: TEC-HQ 3 SEC.	

OPERATION/DISPLAY SCHEMATIC DIAGRAM (SYSCON PCB)



NOTE: THE DC VOLTAGE AT EACH PART WAS MEASURED WITH THE DIGITAL TESTER DURING PLAYBACK.

NOTE: THIS SCHEMATIC DIAGRAM IS THE LATEST AT THE TIME OF PRINTING AND SUBJECT TO CHANGE WITHOUT NOTICE.

DET. CHKD BY	DESCRIPTION	FUNCTION	PCB NAME
CHKD BY	STANDARD	OPERATION/DISPLAY	AX181A_B_ANKI
DESIGN BY	MODEL	ORION ELECTRIC CO., LTD.	Rev. No. 1
DRAWN BY		SECTION: TECH. DATA/09/90	ISSU: A
		PUBLICATION: TEC-HQ 3 SEC.	