KEYLESS ENTRY SYSTEM

A. BLOCK DIAGRAM DESCRIPTION FOR KEYLESS ENTRY SYSTEM(RECEIVER)

1.INPUT POWER

IT IS BATTERY INPUT(12V DC) AS THE SOURCE OF VEHICLE

2.DC POWER REGULATOR CIRCUIT

5V REGULATOR PROVIDES THE SETTED +5V FROM +12V BATTERY.
IT IS USED AS THE SOURCE OF ELECTRONIC POWER FOR THE COMPONENTS ON THE MAIN UNIT

3.RF POWER DUTY CONTROL SWITCHING CIRCUIT

IT IS USED AS THE SOURCE POWER SWITCHING OF RF SIGNAL RECEIVER OF INTEGEATED RECEIVER IC WHEN ARE RECEPTED THE RF SIGNAL

4.RF SIGNAL RECEPTION INTEGREATED RECEIVER CIRCUIT.

4.1 RF AMP

THE RF SIGNAL RECEIVED THROUGH ANTENNA COULD BE HIGH-SENSITIVITY

4.2 RF MIX

MIXING RF FREQUNCY AND LOCAL OSCILLATION FREQUENCY AND THEN IT IS MAKED IF FREQUNCY FROM THE RF MIX BLOCK CIRCUIT.

4.3 DETECTOR

DETECTING AF SIGNAL FROM IF ISGNAL

4.4 SLICER & DATA OUT

SLICING AN UNSTABLE ANALOG AF SIGNAL TO APPROPRIATE SQUARE WAVE SIGNAL AND MAKE CONTROLLER TO ACKNOWLEDGE THE FORMATTED SIGNAL.

5. EEPROM

IT IS STORE THE UNIQUE SECRET CODE FROM EACH TRANSMITTER.
THE STORED CODES IN EEPROM ARE USED FOR BEING COMPARED WITH THE DISPATCHED CODE WHEN TX ARS PRESSED.

6. CONTROLLER

8BIT MICRO PROCESSOR (PIC16C558) CONTROLS RF POWER DUTY CONTROL SWITCHING AND OUTPUT ACCORDING TO SOFTWARE ON CONTROLLER.

7.OUTPUT SIGNAL DRIVE CIRCUIT

IT IS DRIVE THE OUTPUT PATTERN SIGNAL TO CONNECTED OUTPUT TERMINAL.

FIGURE 1:KEYLESS ENTRY SYSTEM (RECEIVER) BLOCK DIAGRAM