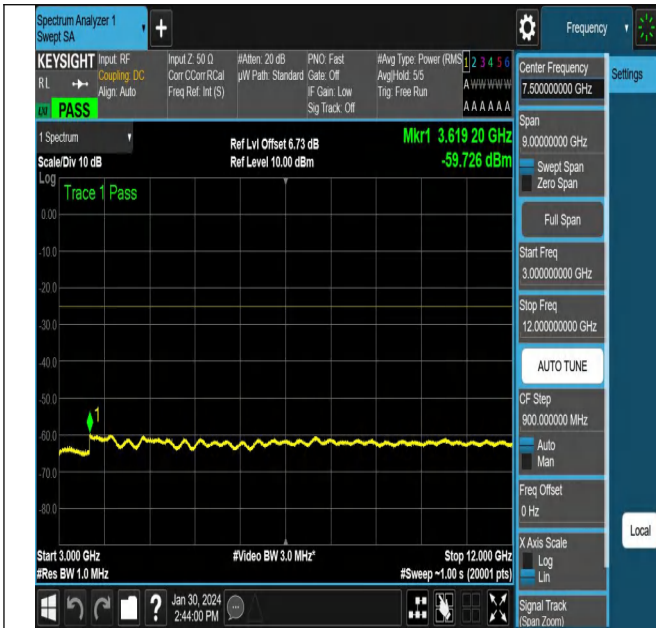
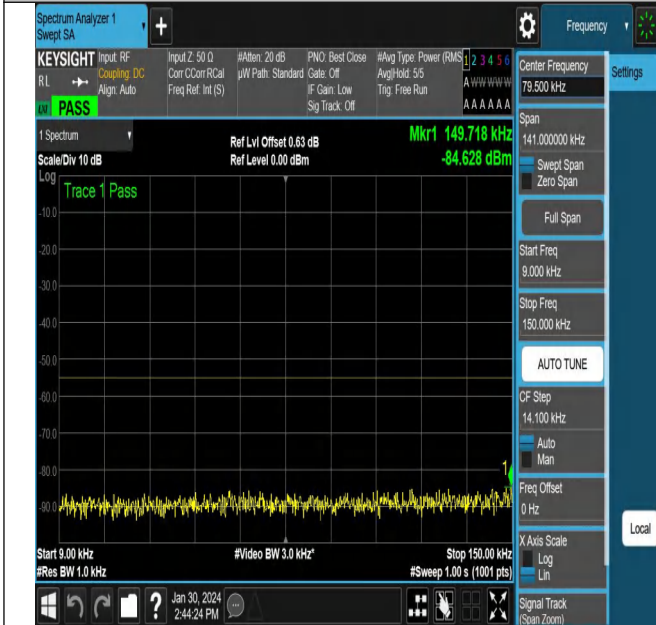




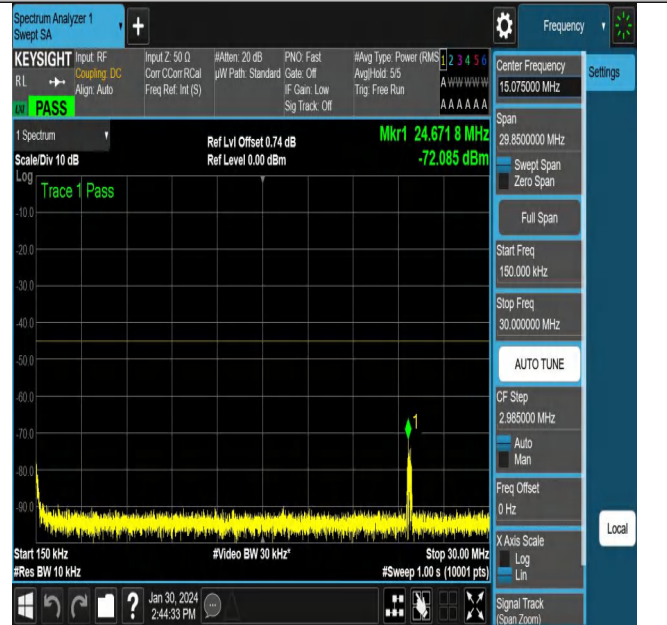
NTNV_N38_PC3_30_20_M_TID1_NS_01_3000_12000_#1



NTNV_N38_PC3_30_20_M_TID1_NS_01_12000_20000_#1



NTNV_N38_PC3_30_20_M_TID2_NS_01_0.009_0.15_#1



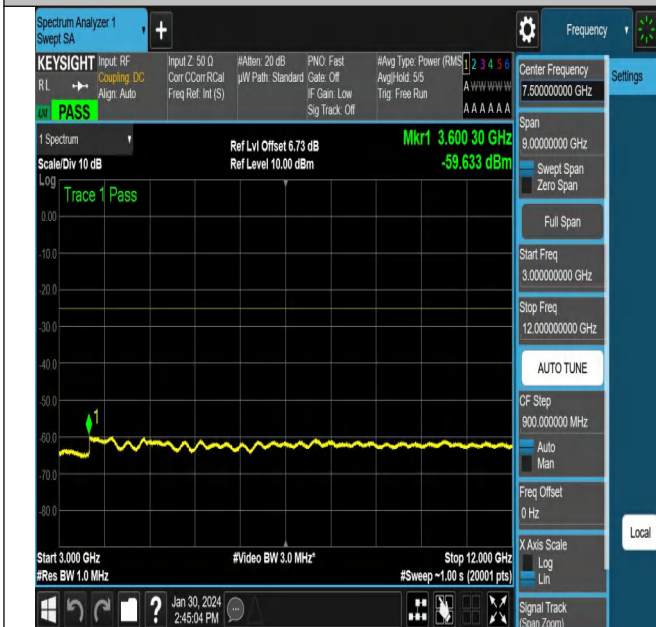
NTNV_N38_PC3_30_20_M_TID2_NS_01_0.15_30_#1



NTNV_N38_PC3_30_20_M_TID2_NS_01_30_1000_#1



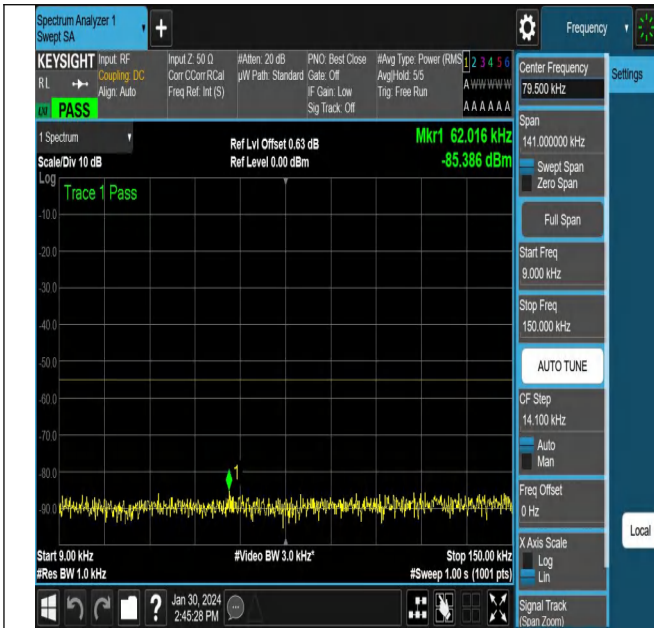
NTNV_N38_PC3_30_20_M_TID2_NS_01_1000_3000_#1



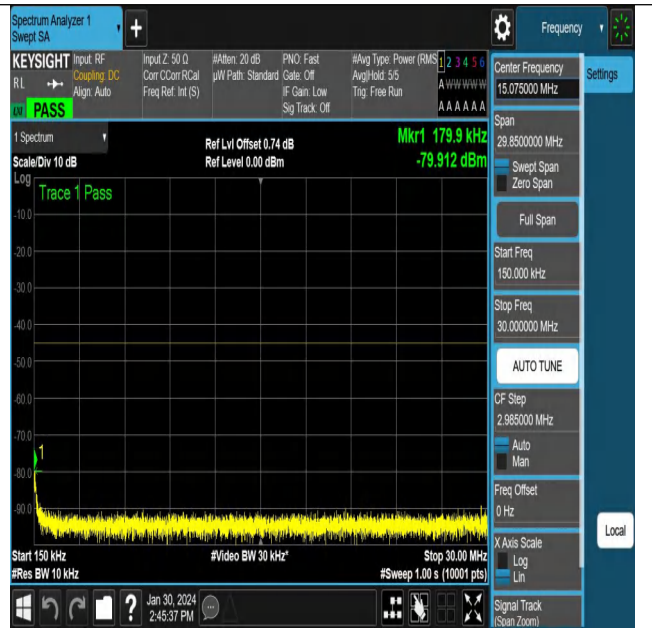
NTNV_N38_PC3_30_20_M_TID2_NS_01_3000_12000_#1



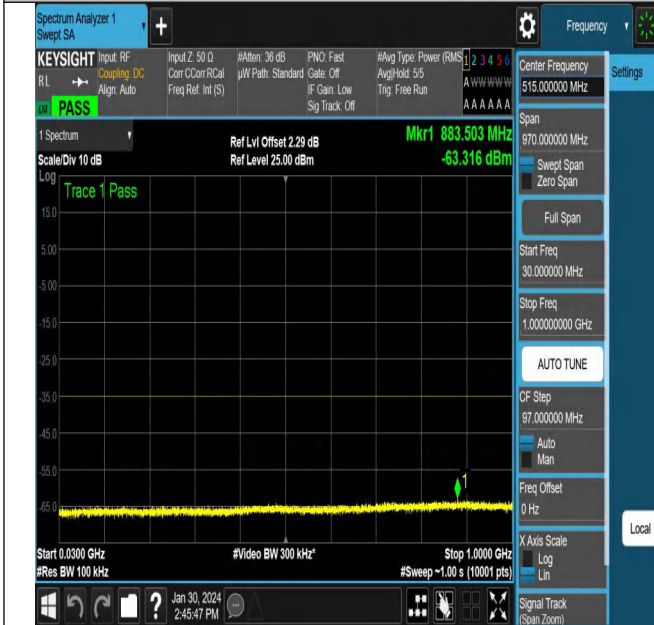
NTNV_N38_PC3_30_20_M_TID2_NS_01_12000_20000_#1



NTNV_N38_PC3_30_20_M_TID3_NS_01_0.009_0.15_#1



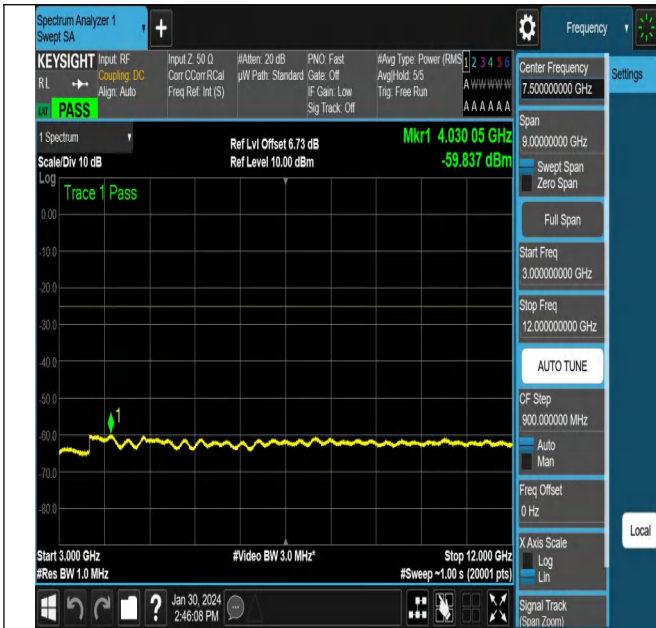
NTNV_N38_PC3_30_20_M_TID3_NS_01_0.15_30_#1



NTNV_N38_PC3_30_20_M_TID3_NS_01_30_1000_#1



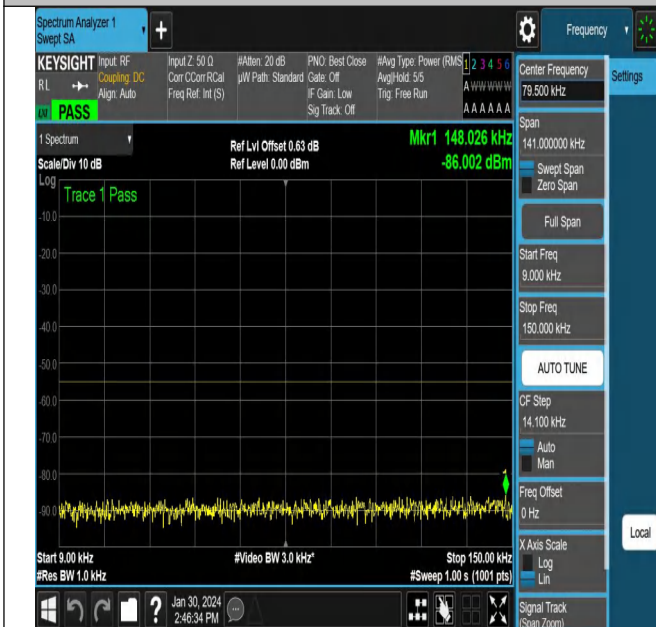
NTNV_N38_PC3_30_20_M_TID3_NS_01_1000_3000_#1



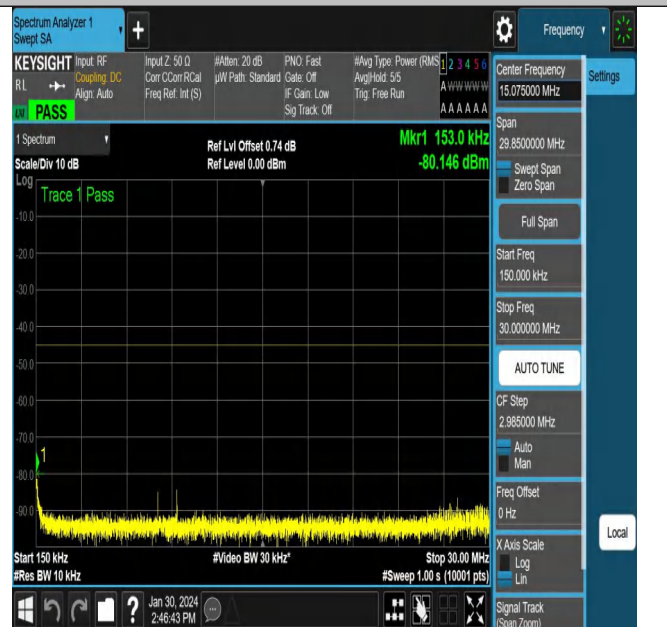
NTNV_N38_PC3_30_20_M_TID3_NS_01_3000_12000_#1



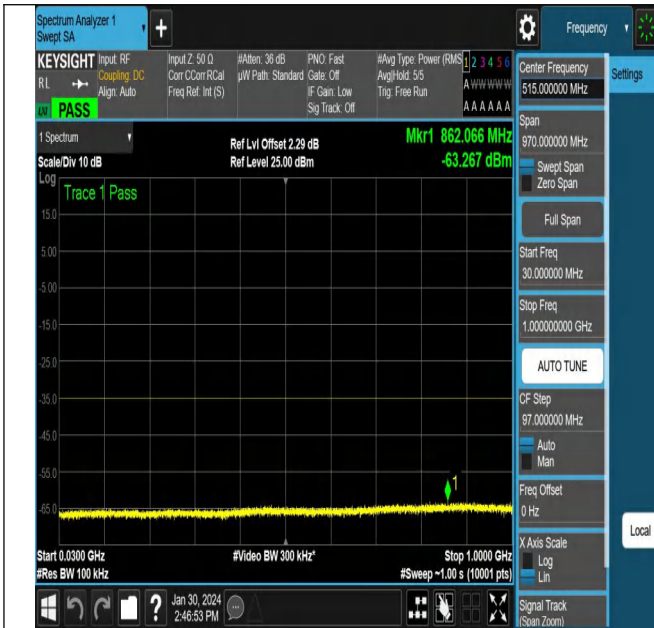
NTNV_N38_PC3_30_20_M_TID3_NS_01_12000_20000_#1



NTNV_N38_PC3_30_20_M_TID4_NS_01_0.009_0.15_#1



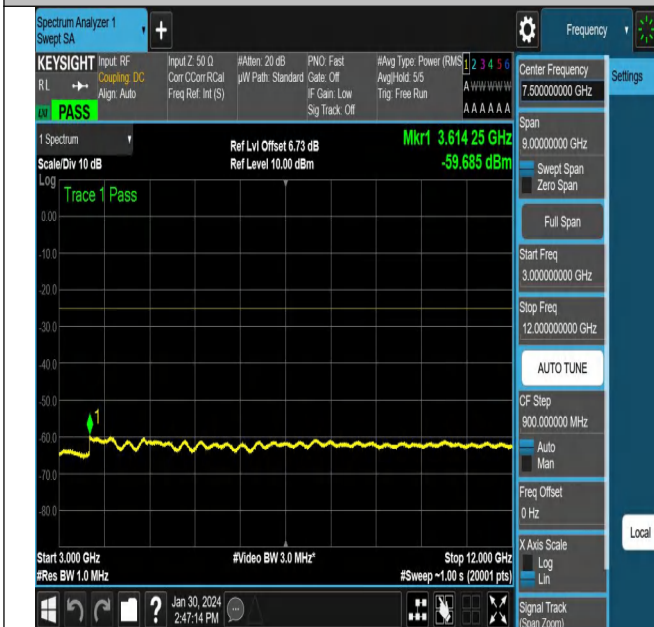
NTNV_N38_PC3_30_20_M_TID4_NS_01_0.15_30_#1



NTNV_N38_PC3_30_20_M_TID4_NS_01_30_1000_#1



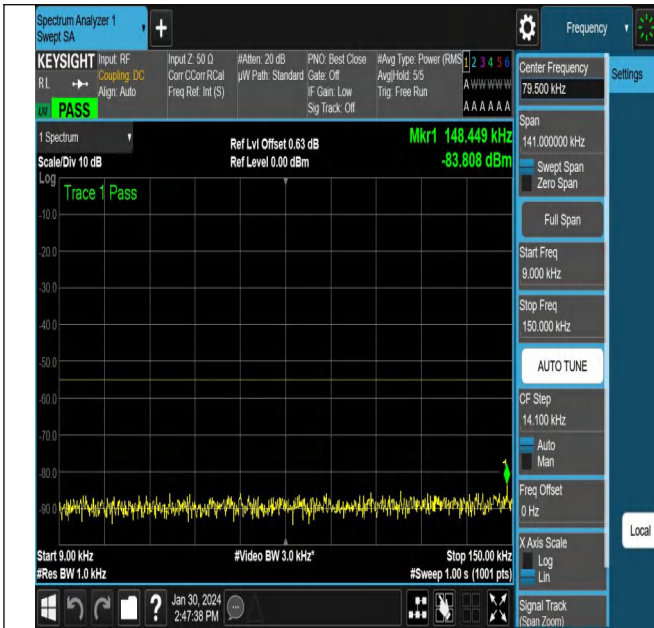
NTNV_N38_PC3_30_20_M_TID4_NS_01_1000_3000_#1



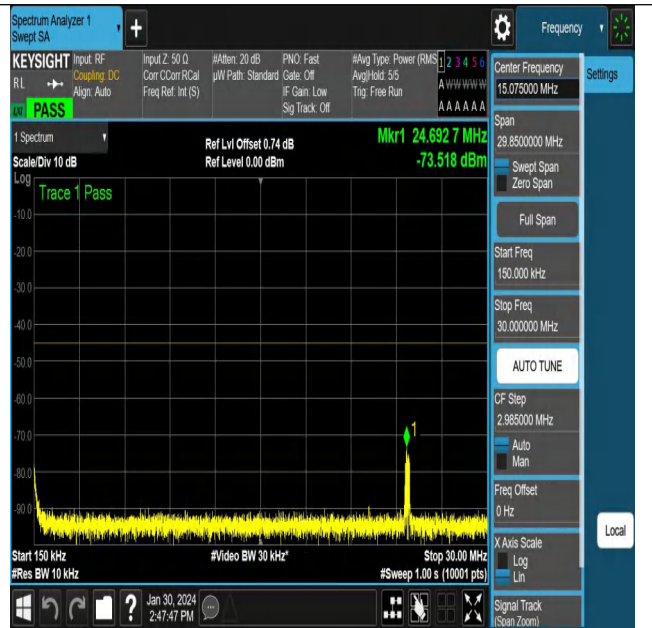
NTNV_N38_PC3_30_20_M_TID4_NS_01_3000_12000_#1



NTNV_N38_PC3_30_20_M_TID4_NS_01_12000_20000_#1



NTNV_N38_PC3_30_20_M_TID5_NS_01_0.009_0.15_#1



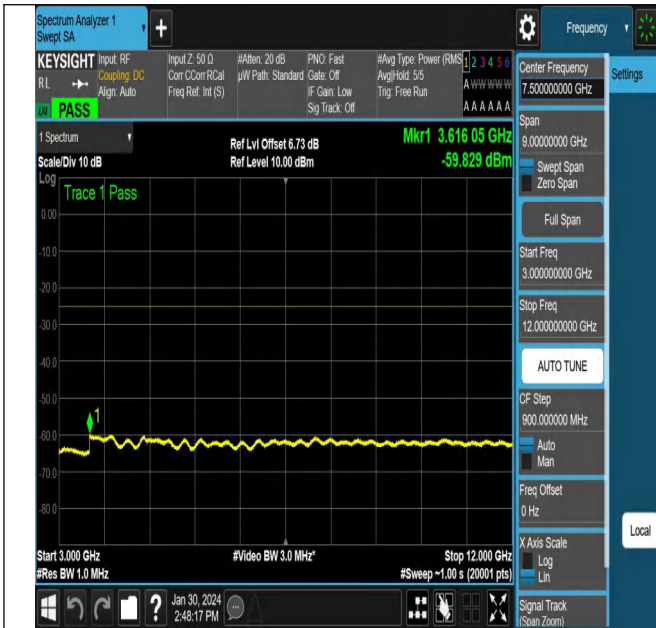
NTNV_N38_PC3_30_20_M_TID5_NS_01_0.15_30_#1



NTNV_N38_PC3_30_20_M_TID5_NS_01_30_1000_#1



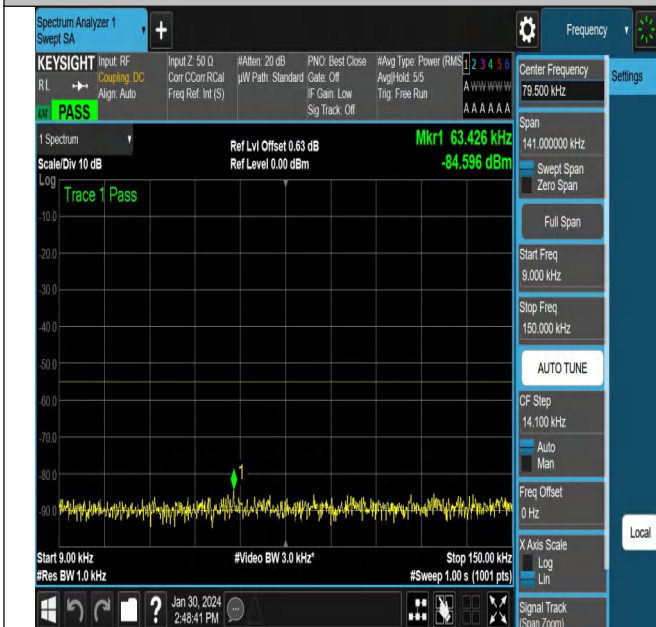
NTNV_N38_PC3_30_20_M_TID5_NS_01_1000_3000_#1



NTNV_N38_PC3_30_20_M_TID5_NS_01_3000_12000_#1



NTNV_N38_PC3_30_20_M_TID5_NS_01_12000_20000_#1



NTNV_N38_PC3_30_20_M_TID6_NS_01_0.009_0.15_#1



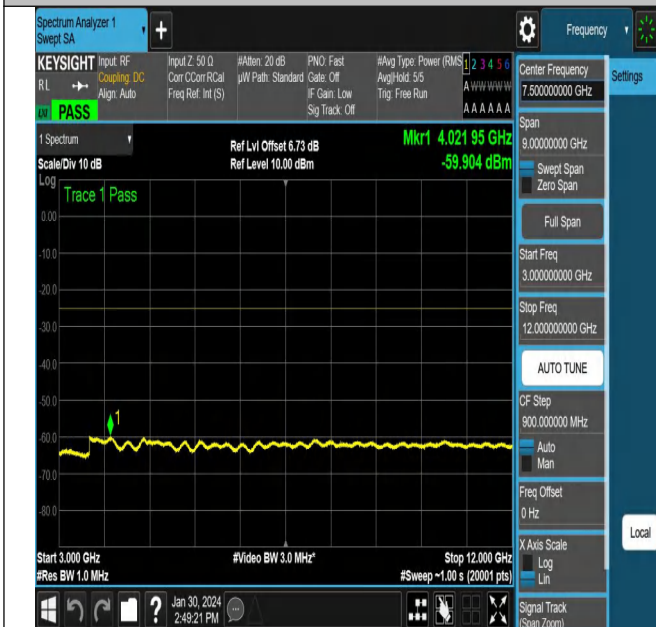
NTNV_N38_PC3_30_20_M_TID6_NS_01_0.15_30_#1



NTNV_N38_PC3_30_20_M_TID6_NS_01_30_1000_#1



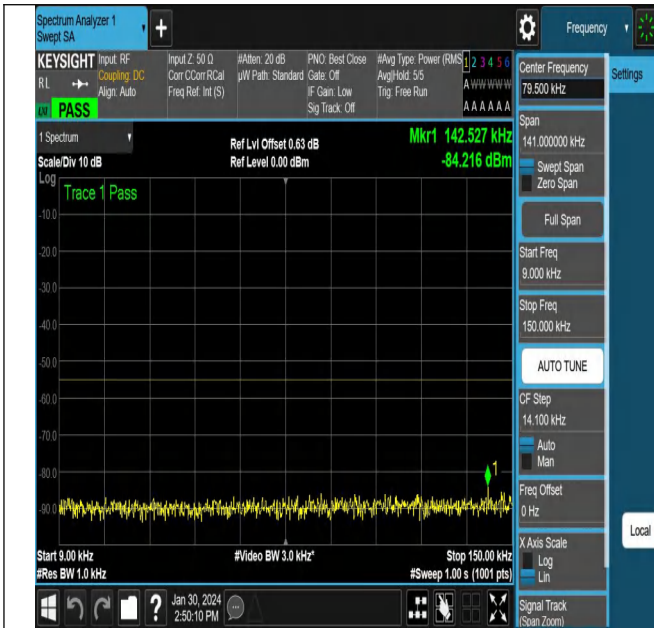
NTNV_N38_PC3_30_20_M_TID6_NS_01_1000_3000_#1



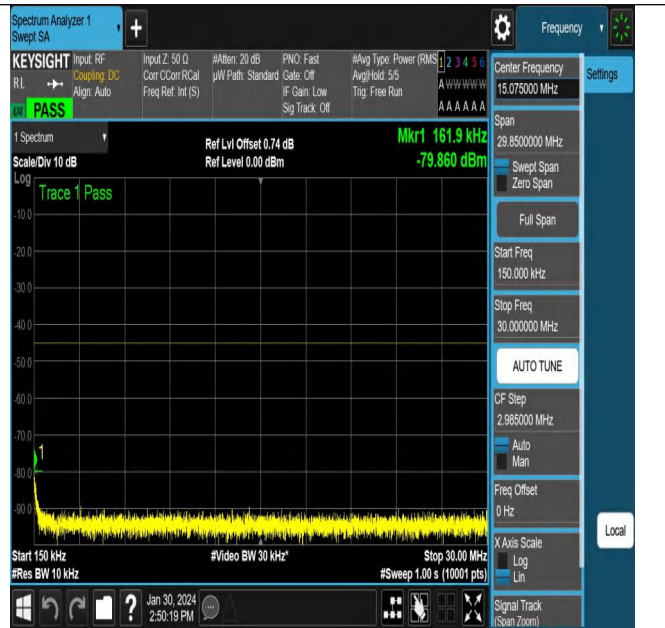
NTNV_N38_PC3_30_20_M_TID6_NS_01_3000_12000_#1



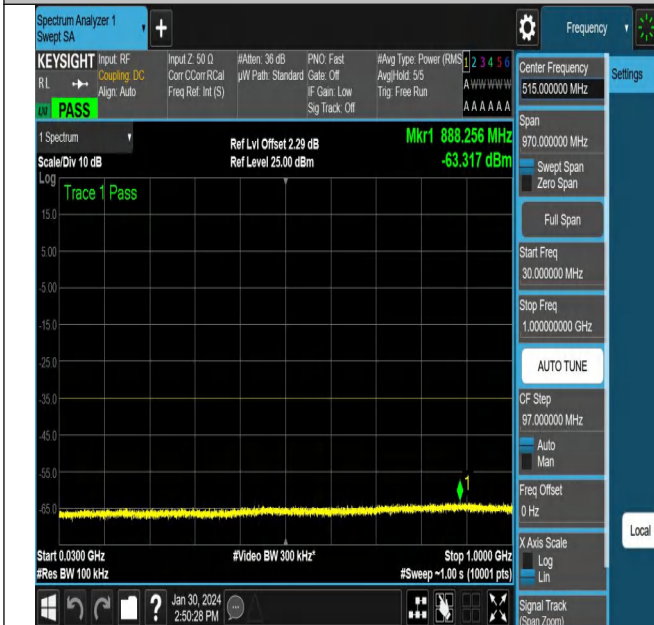
NTNV_N38_PC3_30_20_M_TID6_NS_01_12000_20000_#1



NTNV_N38_PC3_30_20_H_TID1_NS_01_0.009_0.15_#1



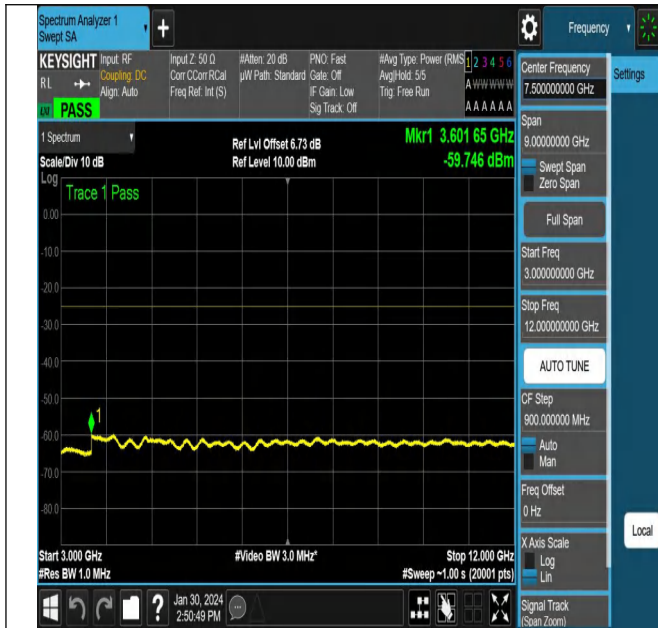
NTNV_N38_PC3_30_20_H_TID1_NS_01_0.15_30_#1



NTNV_N38_PC3_30_20_H_TID1_NS_01_30_1000_#1



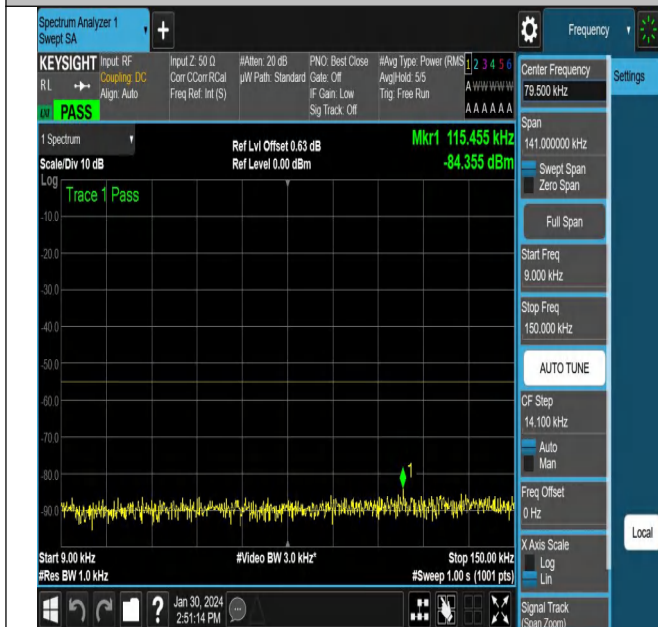
NTNV_N38_PC3_30_20_H_TID1_NS_01_1000_3000_#1



NTNV_N38_PC3_30_20_H_TID1_NS_01_3000_12000_#1



NTNV_N38_PC3_30_20_H_TID1_NS_01_12000_20000_#1



NTNV_N38_PC3_30_20_H_TID2_NS_01_0.009_0.15_#1



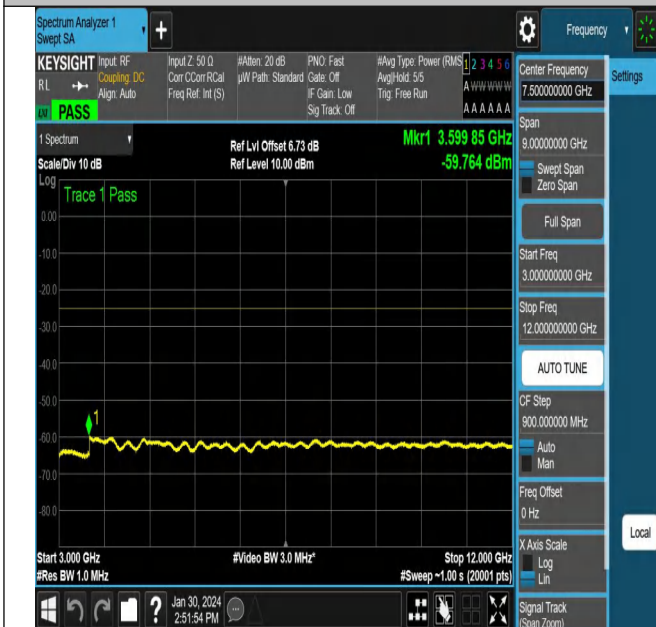
NTNV_N38_PC3_30_20_H_TID2_NS_01_0.15_30_#1



NTNV_N38_PC3_30_20_H_TID2_NS_01_30_1000_#1



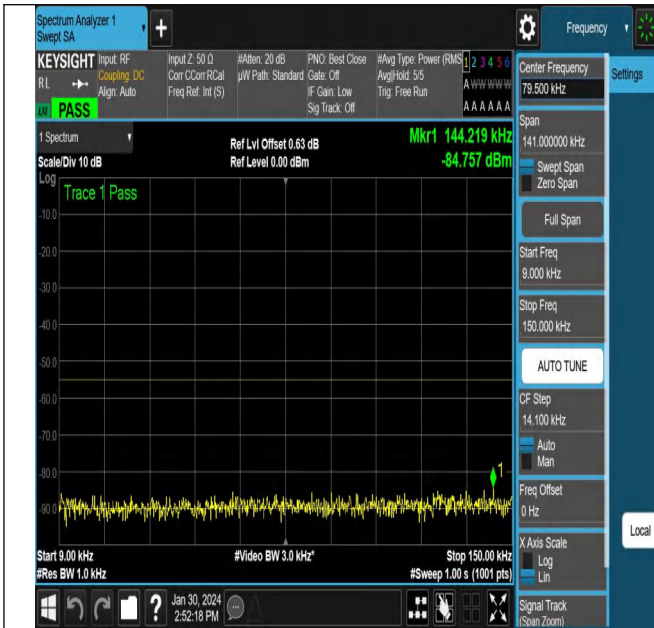
NTNV_N38_PC3_30_20_H_TID2_NS_01_1000_3000_#1



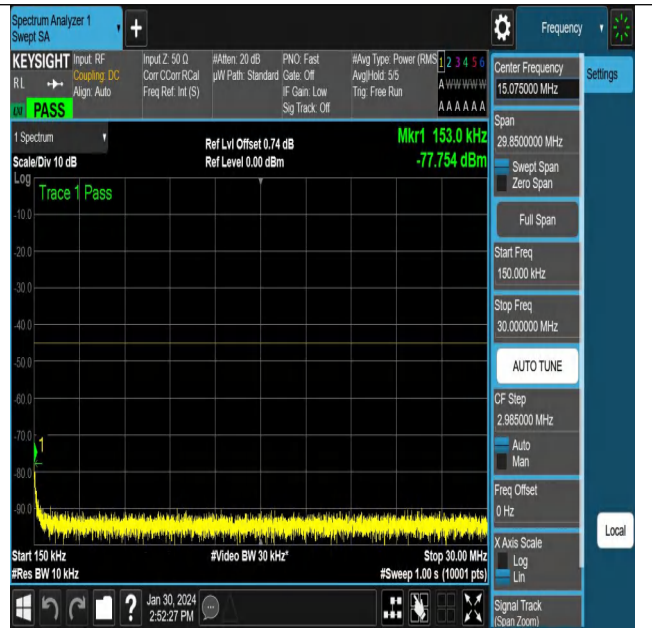
NTNV_N38_PC3_30_20_H_TID2_NS_01_3000_12000_#1



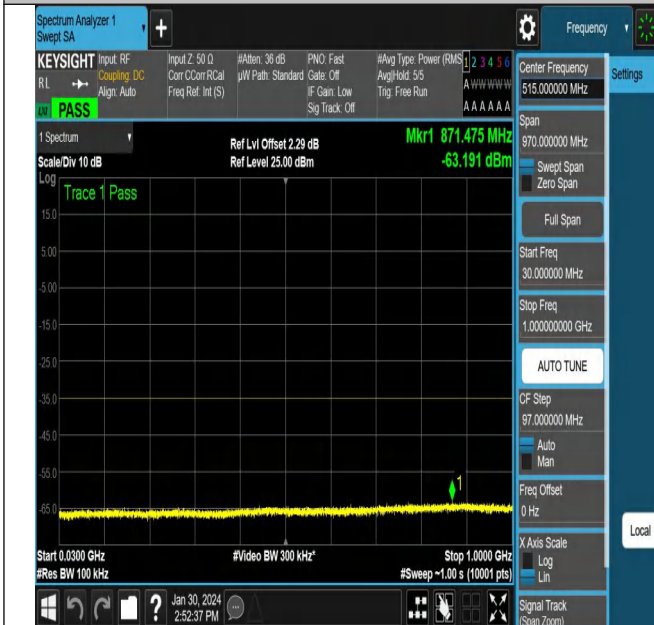
NTNV_N38_PC3_30_20_H_TID2_NS_01_12000_20000_#1



NTNV_N38_PC3_30_20_H_TID3_NS_01_0.009_0.15_#1



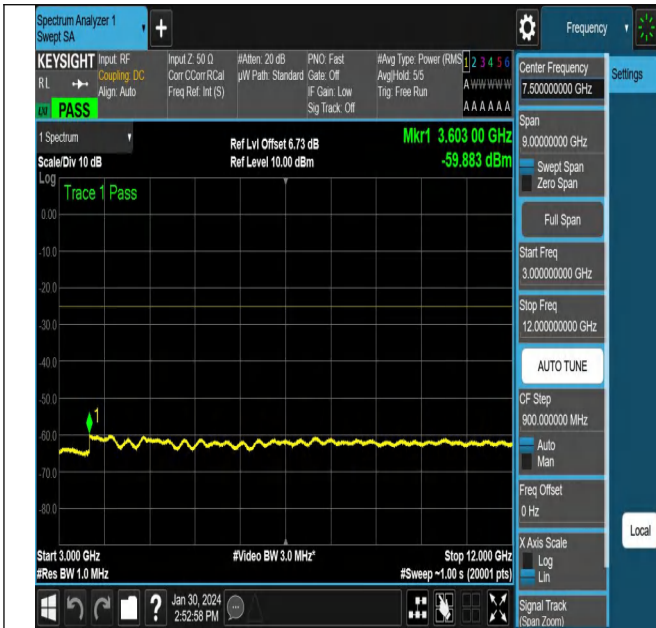
NTNV_N38_PC3_30_20_H_TID3_NS_01_0.15_30_#1



NTNV_N38_PC3_30_20_H_TID3_NS_01_30_1000_#1



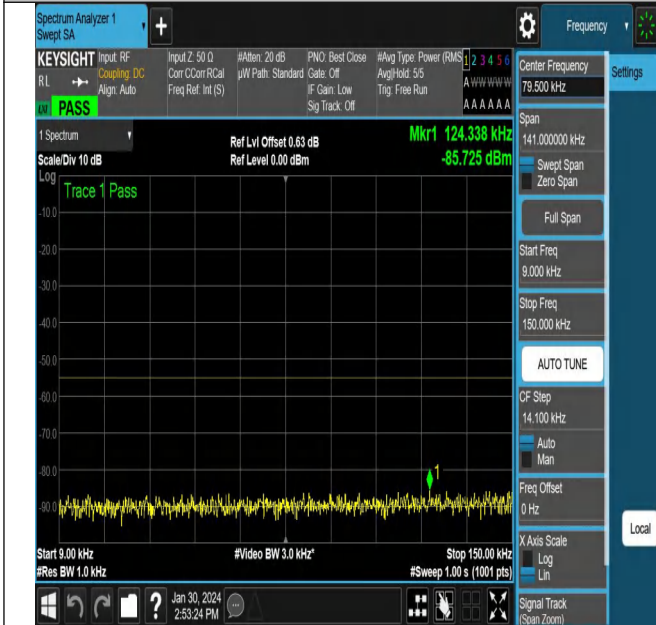
NTNV_N38_PC3_30_20_H_TID3_NS_01_1000_3000_#1



NTNV_N38_PC3_30_20_H_TID3_NS_01_3000_12000_#1



NTNV_N38_PC3_30_20_H_TID3_NS_01_12000_20000_#1



NTNV_N38_PC3_30_20_H_TID4_NS_01_0.009_0.15_#1



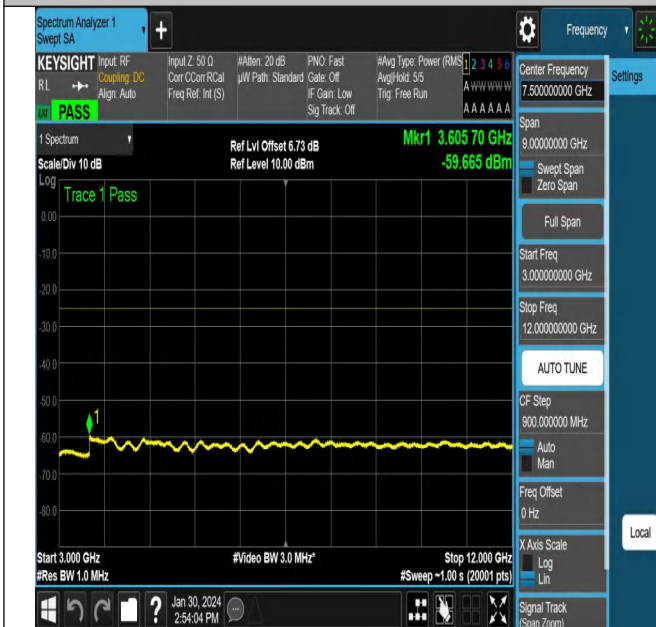
NTNV_N38_PC3_30_20_H_TID4_NS_01_0.15_30_#1



NTNV_N38_PC3_30_20_H_TID4_NS_01_30_1000_#1



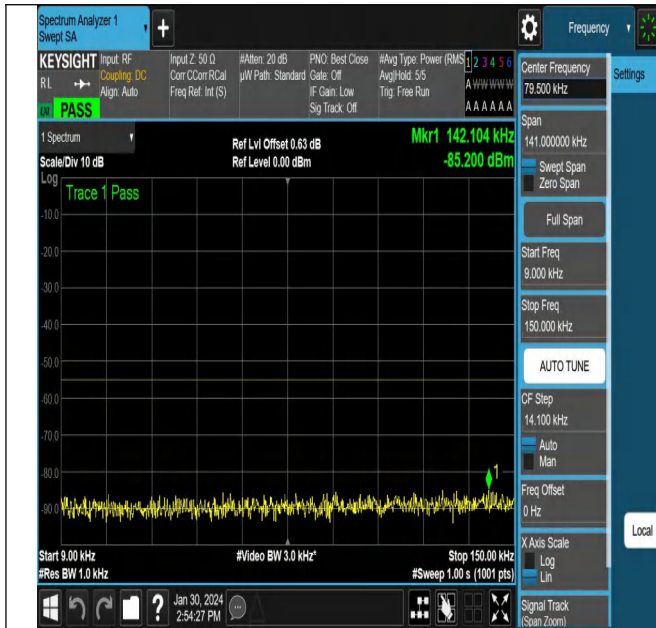
NTNV_N38_PC3_30_20_H_TID4_NS_01_1000_3000_#1



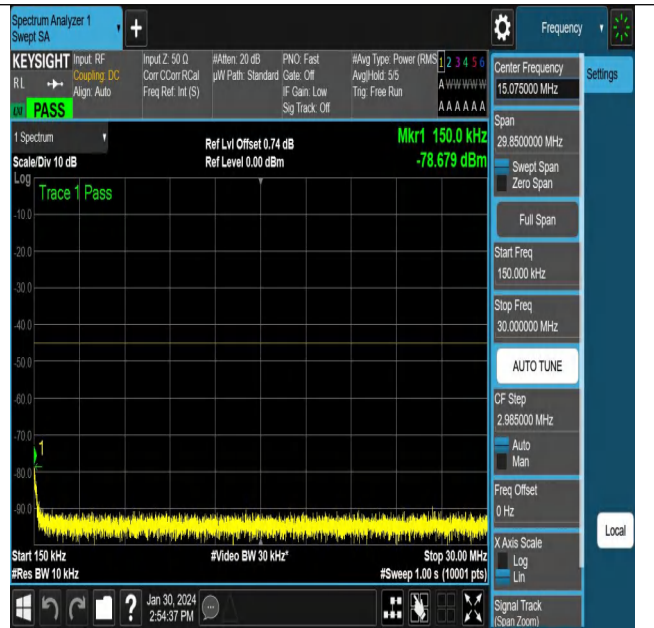
NTNV_N38_PC3_30_20_H_TID4_NS_01_3000_12000_#1



NTNV_N38_PC3_30_20_H_TID4_NS_01_12000_20000_#1



NTNV_N38_PC3_30_20_H_TID5_NS_01_0.009_0.15_#1



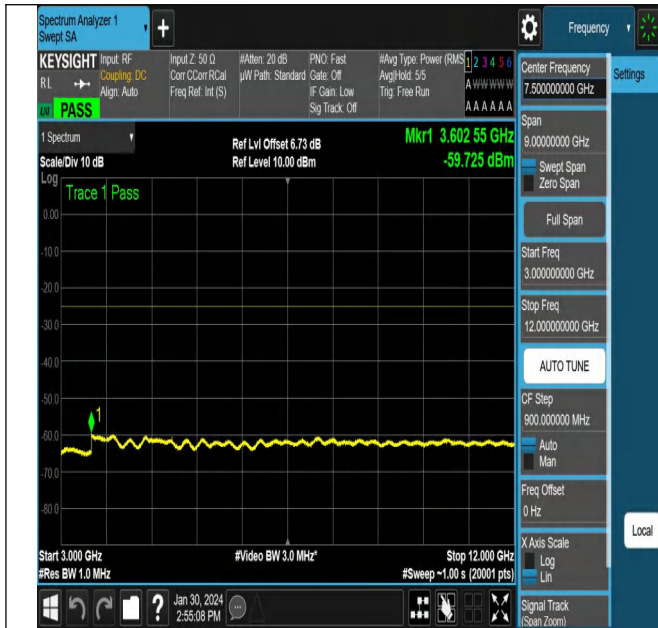
NTNV_N38_PC3_30_20_H_TID5_NS_01_0.15_30_#1



NTNV_N38_PC3_30_20_H_TID5_NS_01_30_1000_#1



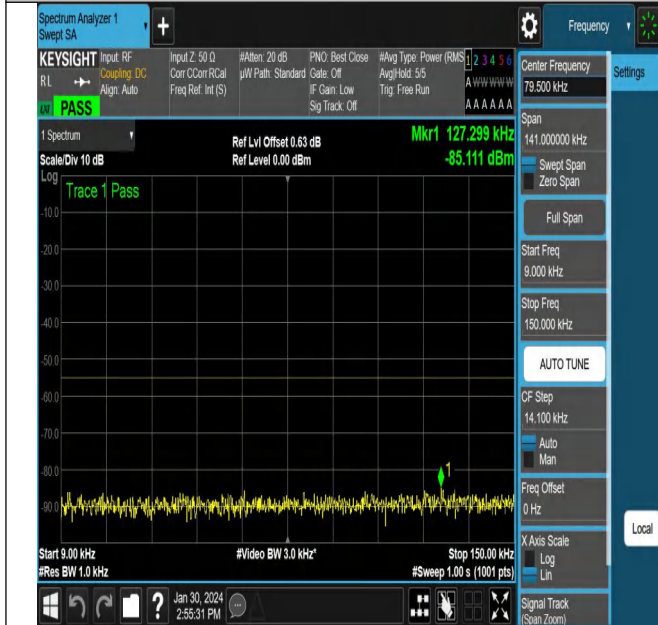
NTNV_N38_PC3_30_20_H_TID5_NS_01_1000_3000_#1



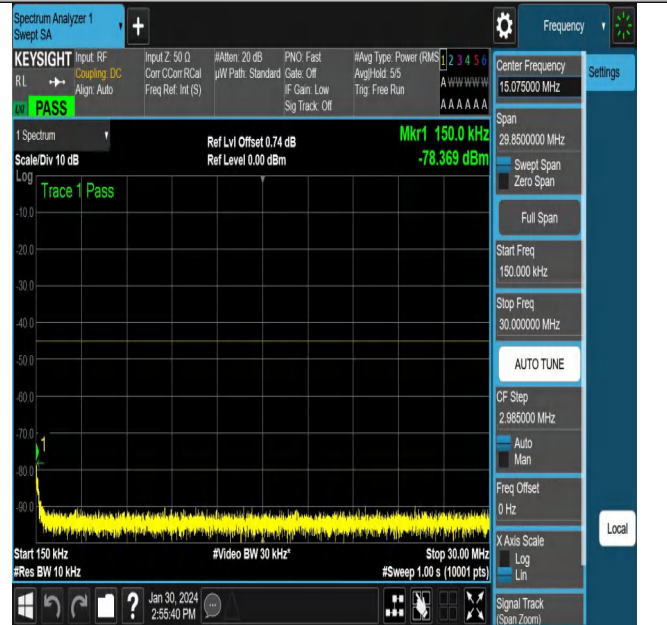
NTNV_N38_PC3_30_20_H_TID5_NS_01_3000_12000_#1



NTNV_N38_PC3_30_20_H_TID5_NS_01_12000_20000_#1



NTNV_N38_PC3_30_20_H_TID6_NS_01_0.009_0.15_#1



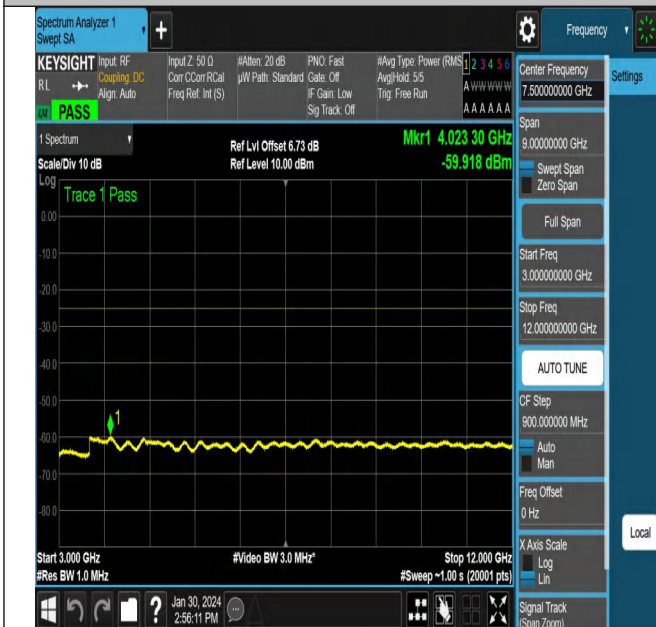
NTNV_N38_PC3_30_20_H_TID6_NS_01_0.15_30_#1



NTNV_N38_PC3_30_20_H_TID6_NS_01_30_1000_#1



NTNV_N38_PC3_30_20_H_TID6_NS_01_1000_3000_#1



NTNV_N38_PC3_30_20_H_TID6_NS_01_3000_12000_#1



NTNV_N38_PC3_30_20_H_TID6_NS_01_12000_20000_#1

Frequency Stability for SA

Test Result

Frequency Error VS. Voltage

Voltage										
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Voltage	Temperature	Deviation(Hz)	Deviation (ppm)	Verdict
N38	30	40	DFT-QPSK	M	Outer_Full	VL	NT	7.68	0.00	PASS
N38	30	40	DFT-QPSK	M	Outer_Full	VN	NT	4.83	0.00	PASS
N38	30	40	DFT-QPSK	M	Outer_Full	VH	NT	2.07	0.00	PASS

Frequency Error VS. Temperature

Voltage										
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Voltage	Temperature	Deviation(Hz)	Deviation (ppm)	Verdict
N38	30	40	DFT-QPSK	M	Outer_Full	NV	-30	0.730000	0.000281	PASS
N38	30	40	DFT-QPSK	M	Outer_Full	NV	-20	4.130000	0.001592	PASS
N38	30	40	DFT-QPSK	M	Outer_Full	NV	-10	-2.310000	-0.000890	PASS
N38	30	40	DFT-QPSK	M	Outer_Full	NV	0	5.600000	0.002158	PASS
N38	30	40	DFT-QPSK	M	Outer_Full	NV	10	7.930000	0.003056	PASS
N38	30	40	DFT-QPSK	M	Outer_Full	NV	20	5.090000	0.001961	PASS
N38	30	40	DFT-QPSK	M	Outer_Full	NV	30	0.980000	0.000378	PASS
N38	30	40	DFT-QPSK	M	Outer_Full	NV	40	-1.940000	-0.000748	PASS
N38	30	40	DFT-QPSK	M	Outer_Full	NV	50	3.980000	0.001534	PASS