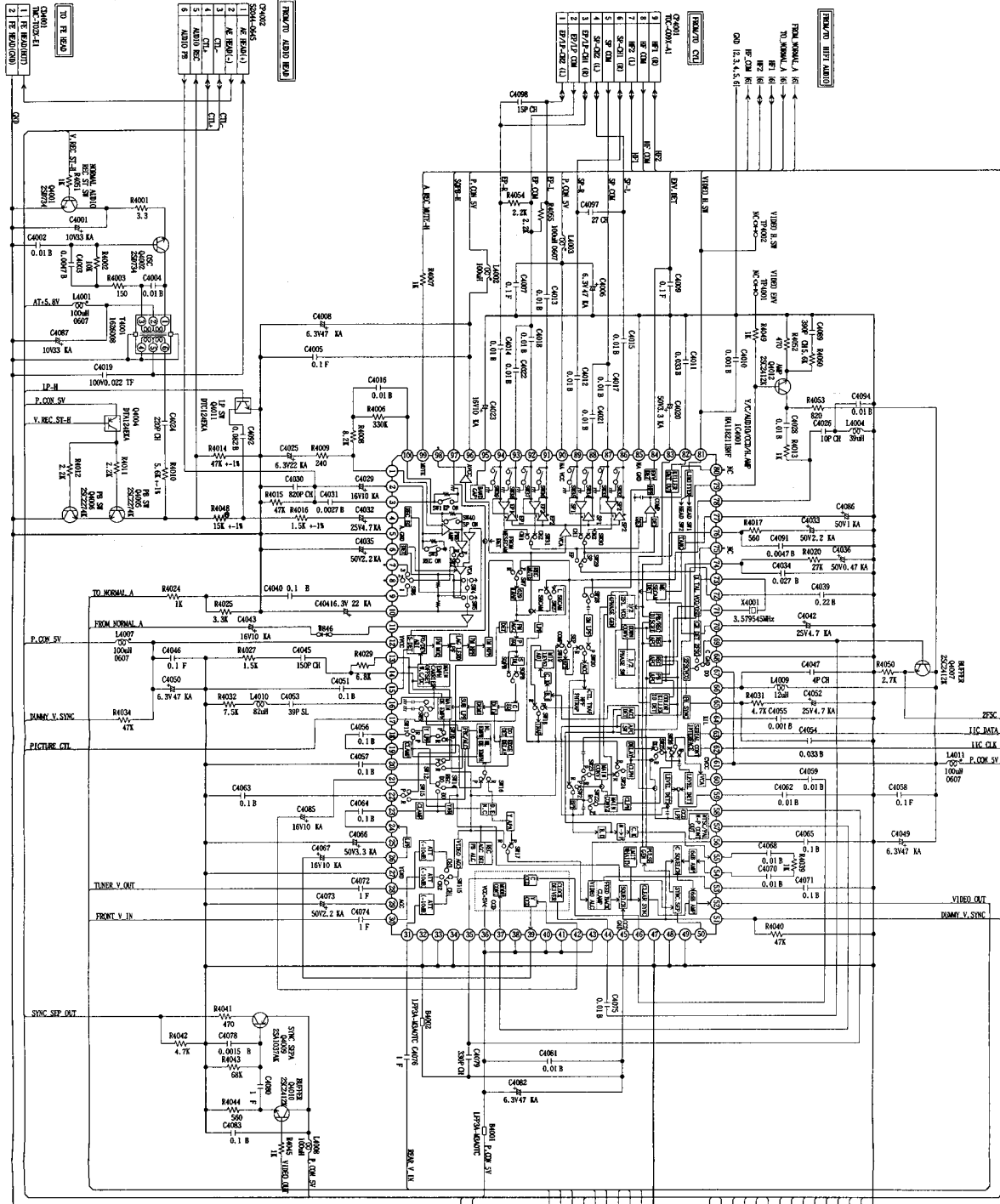
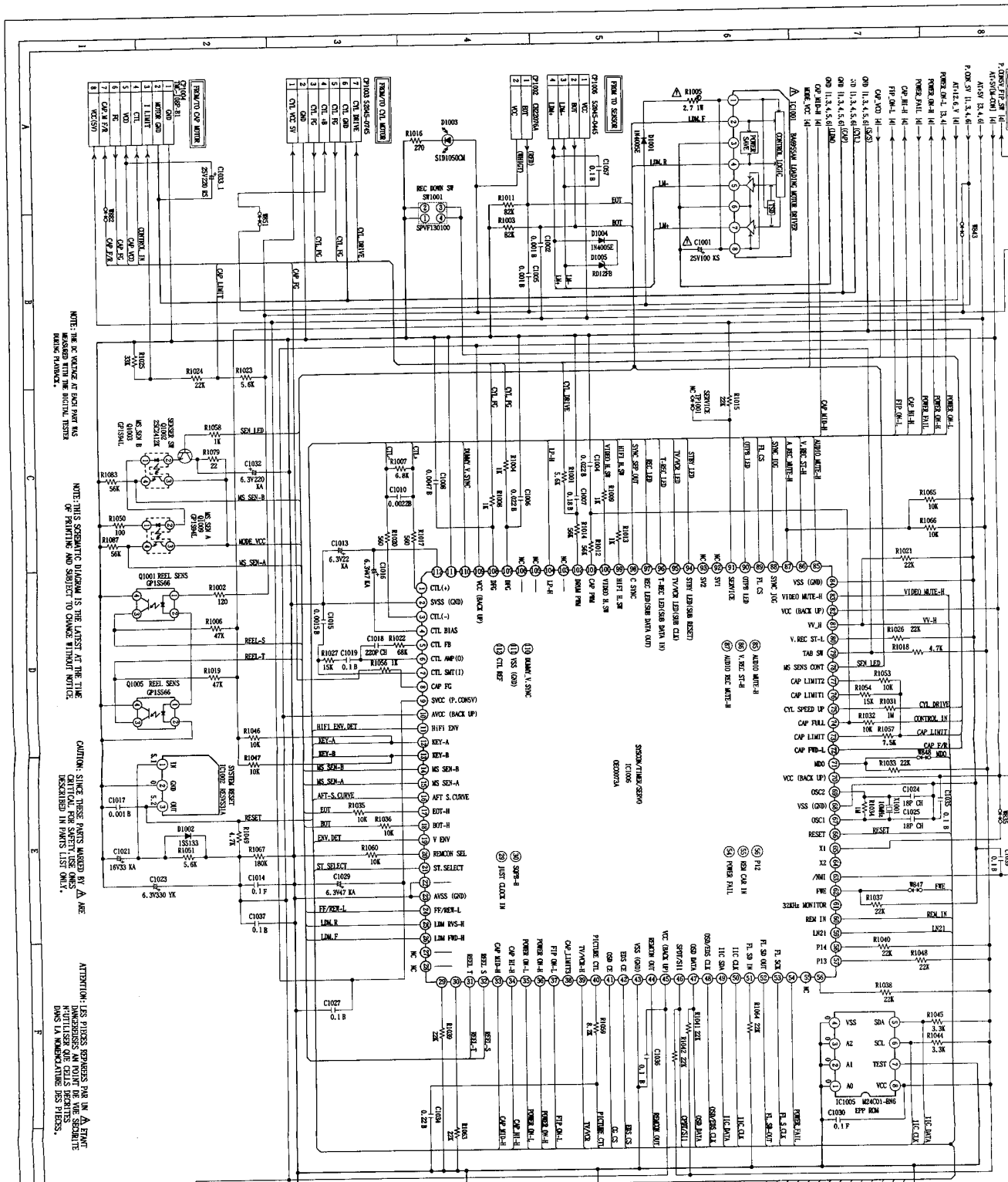


ATTACHMENT M Circuit Diagram



REF. NO.	DESCRIPTION	FUNCTION	UNIT
1	FROM NORMAL A	TO NORMAL A	1.0
2	FROM NORMAL B	TO NORMAL B	1.0
3	FROM NORMAL C	TO NORMAL C	1.0
4	FROM NORMAL D	TO NORMAL D	1.0
5	FROM NORMAL E	TO NORMAL E	1.0
6	FROM NORMAL F	TO NORMAL F	1.0
7	FROM NORMAL G	TO NORMAL G	1.0
8	FROM NORMAL H	TO NORMAL H	1.0
9	FROM NORMAL I	TO NORMAL I	1.0
10	FROM NORMAL J	TO NORMAL J	1.0
11	FROM NORMAL K	TO NORMAL K	1.0
12	FROM NORMAL L	TO NORMAL L	1.0
13	FROM NORMAL M	TO NORMAL M	1.0
14	FROM NORMAL N	TO NORMAL N	1.0
15	FROM NORMAL O	TO NORMAL O	1.0
16	FROM NORMAL P	TO NORMAL P	1.0
17	FROM NORMAL Q	TO NORMAL Q	1.0
18	FROM NORMAL R	TO NORMAL R	1.0
19	FROM NORMAL S	TO NORMAL S	1.0
20	FROM NORMAL T	TO NORMAL T	1.0
21	FROM NORMAL U	TO NORMAL U	1.0
22	FROM NORMAL V	TO NORMAL V	1.0
23	FROM NORMAL W	TO NORMAL W	1.0
24	FROM NORMAL X	TO NORMAL X	1.0
25	FROM NORMAL Y	TO NORMAL Y	1.0
26	FROM NORMAL Z	TO NORMAL Z	1.0

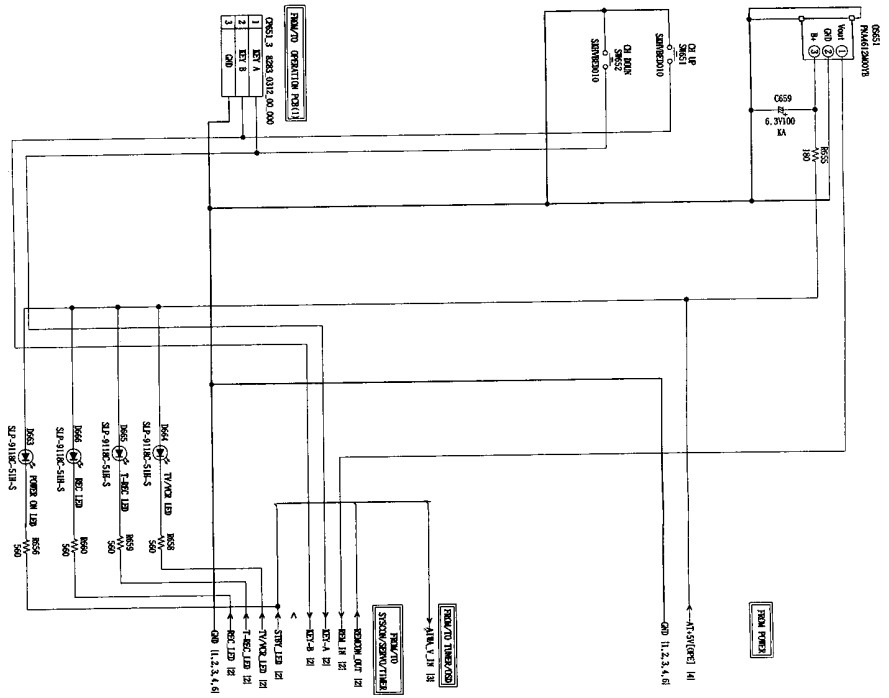
FCC ID: A7RM4C9B
Model No.: VR5005
Brand: ORION



SECTION	COMPONENT	VALUE	FUNCTION
1	7805	5.0V	Voltage Regulator
1	74LS00	NAND	Logic Gate
1	74LS161	COUNTER	Counter
1	74LS147	DECODER	Decoder
2	74LS14	INVERTER	Inverter
2	74LS161	COUNTER	Counter
2	74LS147	DECODER	Decoder
3	74LS14	INVERTER	Inverter
3	74LS161	COUNTER	Counter
3	74LS147	DECODER	Decoder
4	74LS14	INVERTER	Inverter
4	74LS161	COUNTER	Counter
4	74LS147	DECODER	Decoder
5	74LS14	INVERTER	Inverter
5	74LS161	COUNTER	Counter
5	74LS147	DECODER	Decoder
6	74LS14	INVERTER	Inverter
6	74LS161	COUNTER	Counter
6	74LS147	DECODER	Decoder
7	74LS14	INVERTER	Inverter
7	74LS161	COUNTER	Counter
7	74LS147	DECODER	Decoder
8	74LS14	INVERTER	Inverter
8	74LS161	COUNTER	Counter
8	74LS147	DECODER	Decoder

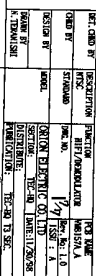
NOTE: THIS SCHEMATIC DIAGRAM IS THE LATEST AT THE TIME OF PRINTING AND SUBJECT TO CHANGE WITHOUT NOTICE.

NOTE: THE R. NUMBER OF EACH WIRE HAS BEING PLACED IN THE WIRE NUMBER.

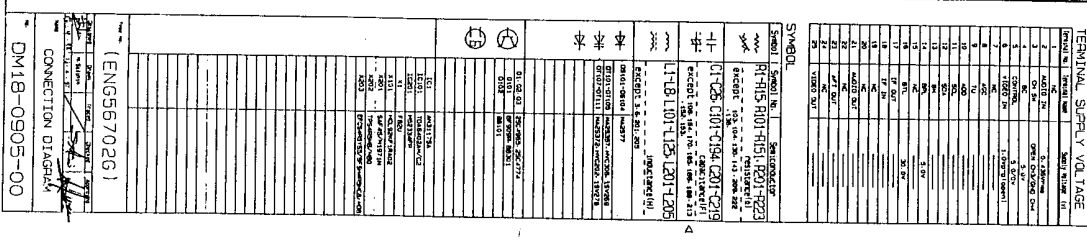


FCC ID : A7RM4C9B
Model No. : VR5005
Brand : ORION

DESIGN BY	DESIGN NO.	DESIGN DATE	DESIGN TIME
DESIGN BY	DESIGN NO.	DESIGN DATE	DESIGN TIME
DESIGN BY	DESIGN NO.	DESIGN DATE	DESIGN TIME
DESIGN BY	DESIGN NO.	DESIGN DATE	DESIGN TIME



FCC ID : A7RM4C9B
Model No. : VR5005
Brand : ORION



TUNER, DEMODULATOR AND RF CONVERTER CIRCUIT DIAGRAM