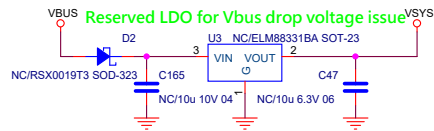
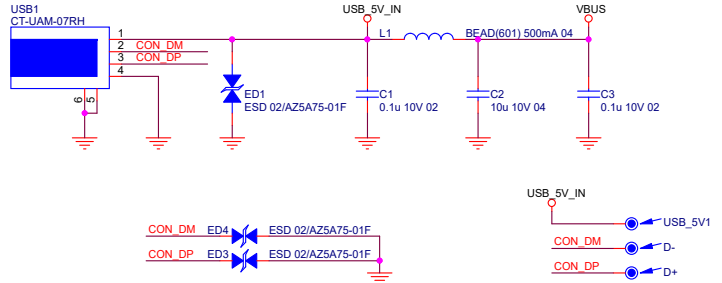
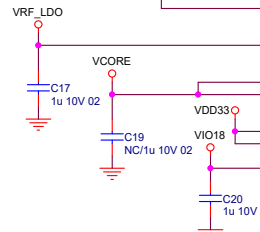


USB TYPE-A



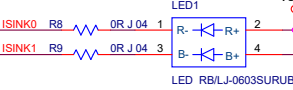
C11 C15 close to IC.



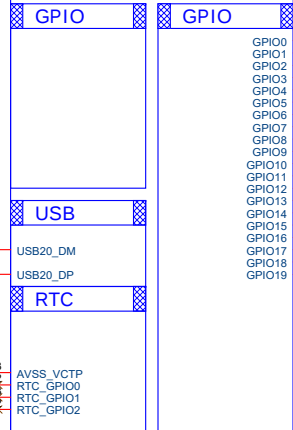
C17-C22 close to IC.

VBAT cap value should be smaller than 10uF

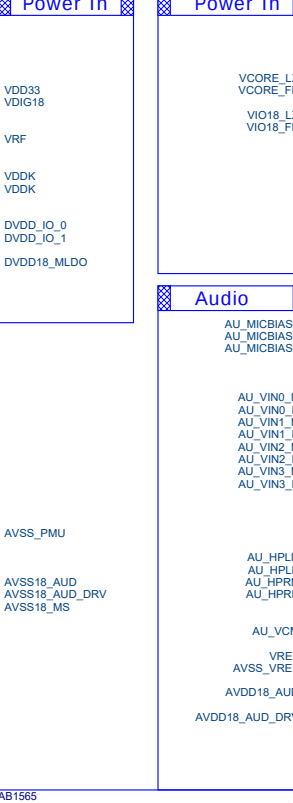
PAIRING LED



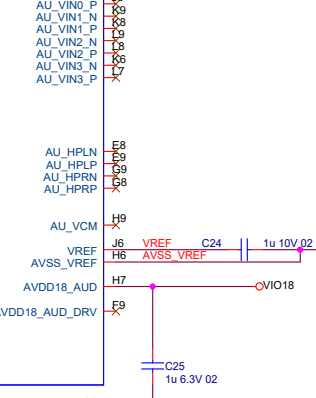
AB1565



AB1565



C16 Connect to AVSS18_MS First

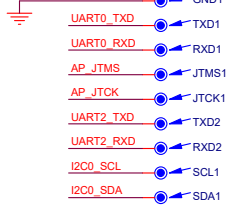
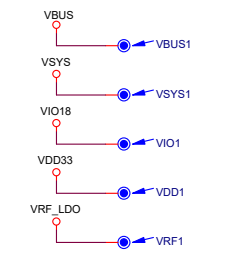
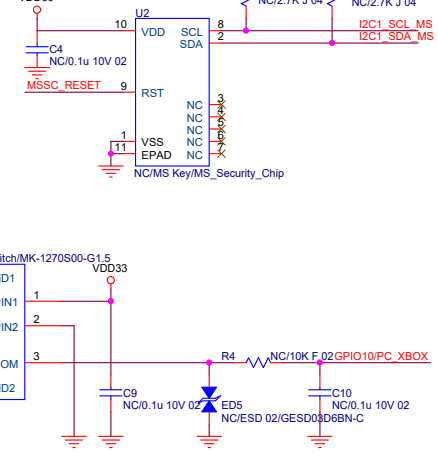


C25 Connect to AVSS18_AUD First



C25 close to IC.

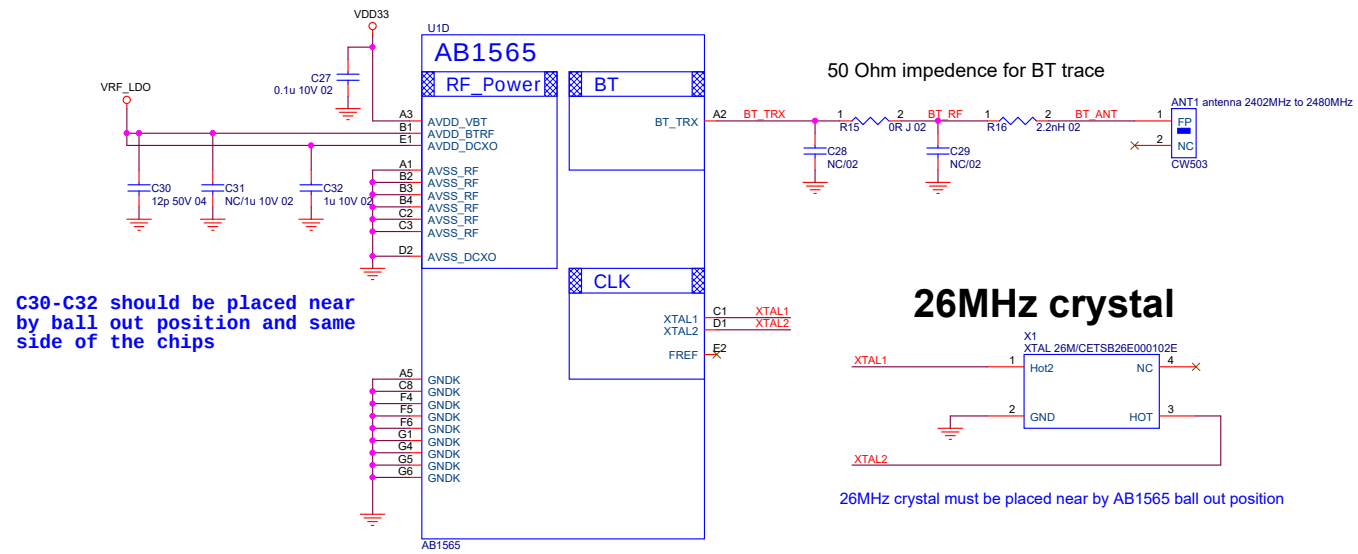
MS Security Chip



SLT Mode
{GPIO13} SLT mode
0: normal
1: SLT
The SLT mode is only used for debug in Airoha side .Please keep GPIOM3 be floating.

DL Mode
{GPIO2} DL mode
0: no USB download
1: USB download
Please reserve the test point if needs DL mode.
Please keep GPIOM2 be floating if not use DL mode.

Approved by	Checked by	Designer	AMPACS CORPORATION	
Eddie	Boyce		Project Number RIG800ProAB_HS_TX	AMPACS CORPORATION
			Size Custom	Rev A
			Title AB1565_Main	
			Date: Friday, November 26, 2021	Sheet 3 of 4



Approved by	Checked by	Designer	AMPACS CORPORATION		
Eddie		Boyce	Project Number RIG800rproAB_HS_TX		
			Size Custom	Title AB1565_RF	Rev A
			Date: Friday, November 26, 2021		