

(SYSCON PCB)

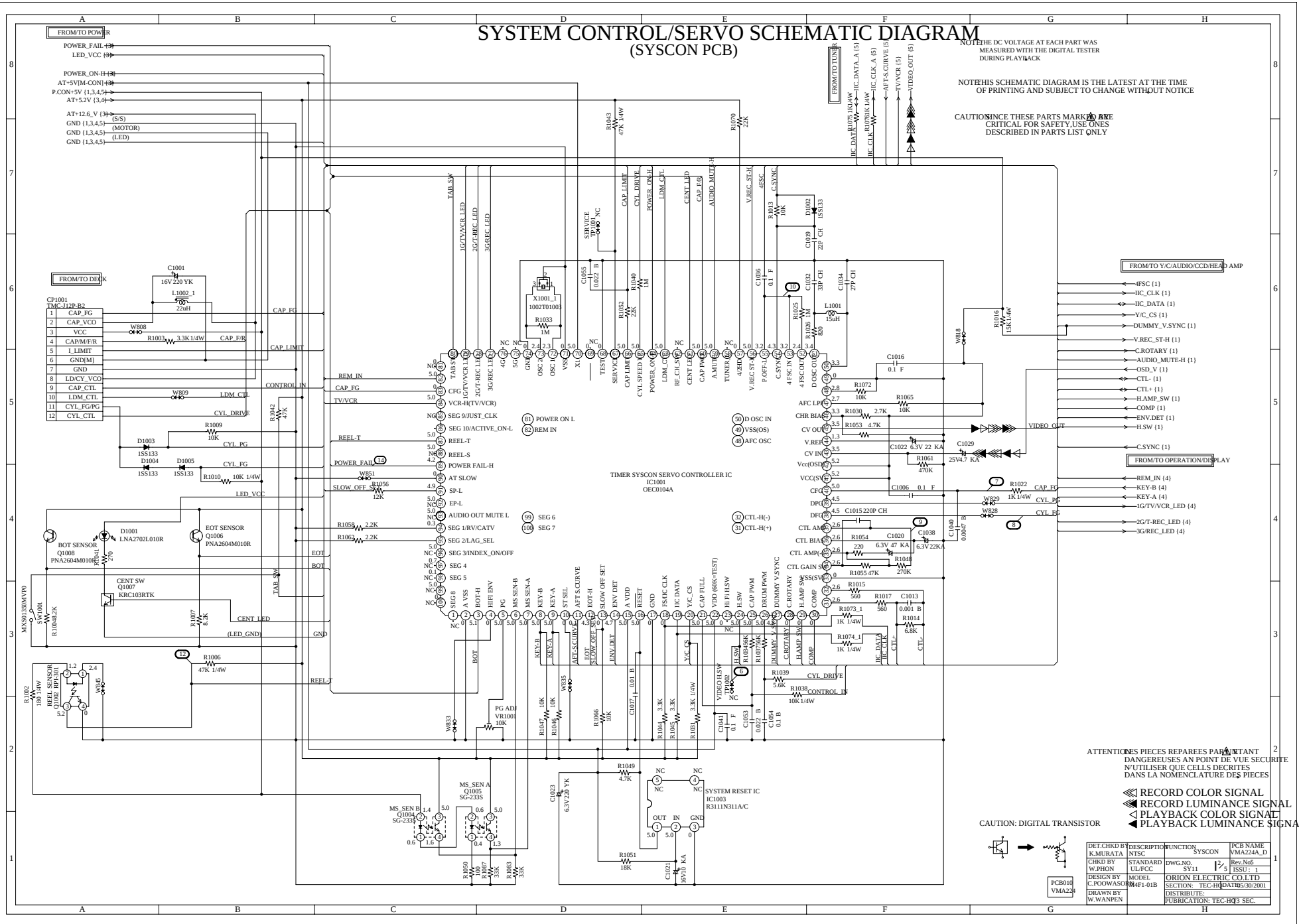


SYSTEM CONTROL/SERVO SCHEMATIC DIAGRAM (SYSCON PCB)

NOTE THE DC VOLTAGE AT EACH PART WAS
MEASURED WITH THE DIGITAL TESTER
DURING PLAYBACK

NOTE THIS SCHEMATIC DIAGRAM IS THE LATEST AT THE TIME
OF PRINTING AND SUBJECT TO CHANGE WITHOUT NOTICE

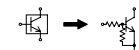
CAUTION SINCE THESE PARTS MARK ARE
CRITICAL FOR SAFETY, USE ONES
DESCRIBED IN PARTS LIST ONLY



ATTENTION: LES PIÈCES RÉPARÉES PAR L'USAGÉ
DANGEREUSES AN POINT DE VUE SÉCURITÉ
N'UTILISER QUE CELLES DÉCRITES
DANS LA NOMENCLATURE DES PIÈCES

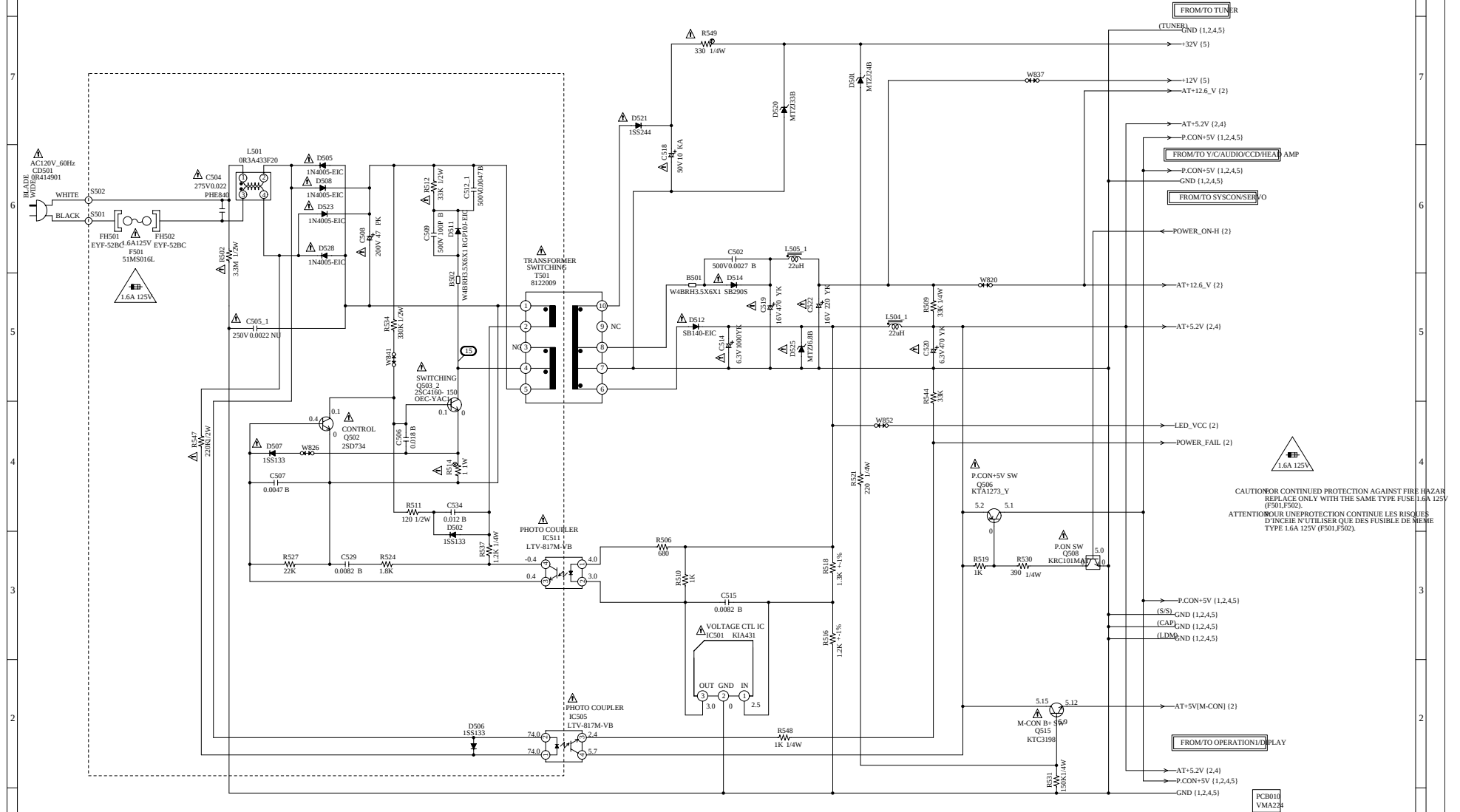
- RECORD COLOR SIGNAL
- RECORD LUMINANCE SIGNAL
- PLAYBACK COLOR SIGNAL
- PLAYBACK LUMINANCE SIGNAL

CAUTION: DIGITAL TRANSISTOR



DET. CHKD BY	DESCRIPTION	FUNCTION	PCB NAME
K. MURATA	NTSC	SYSCON	VMA224A_D
CHKD BY	STANDARD	DWG. NO.	Rev. No. 5
W. PHON	ULFCC	SV11	TSU: 1
DESIGN BY	C. POOWASORN	MODEL	ORION ELECTRIC CO. LTD.
		SECTION	TEC-HQ3/30/2001
		DISTRIBUTE	
		PUBLICATION	TEC-HQ3 SEC.

POWER SCHEMATIC DIAGRAM (SYSCON PCB)



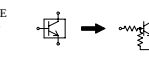
NOTE: THE DC VOLTAGE AT EACH PART WAS MEASURED WITH THE DIGITAL TESTER DURING PLAYBACK.

THIS SCHEMATIC DIAGRAM IS THE LATEST AT THE TIME OF PRINTING AND SUBJECT TO CHANGE WITHOUT NOTICE.

THIS SCHEMATIC DIAGRAM IS THE LATEST AT THE TIME OF PRINTING AND SUBJECT TO CHANGE WITHOUT NOTICE.

CAUTION: THESE PARTS MARKED WITH A CRITICAL FOR SAFETY. USE ONES DESCRIBED IN PARTS LIST ONLY.

CAUTION: DIGITAL TRANSISTOR



DET. CHKD BY	DESCRIPTION	FUNCTION	POWER	PCB NAME
K. MURATA	NTSC			VMA224A_D
CHKD BY	STANDARD	DWG. NO.	Rev. No.	
W. PHON	UL FCC	PPW11	TSU-1	
DESIGN BY	MODEL	ORION ELECTRIC CO. LTD.		
C. POOWASORN	M4F1-01B	SECTION: TEC-HQ1 SEC.		
DRAWN BY	DISTRIBUTE:			
W. WANPEN				

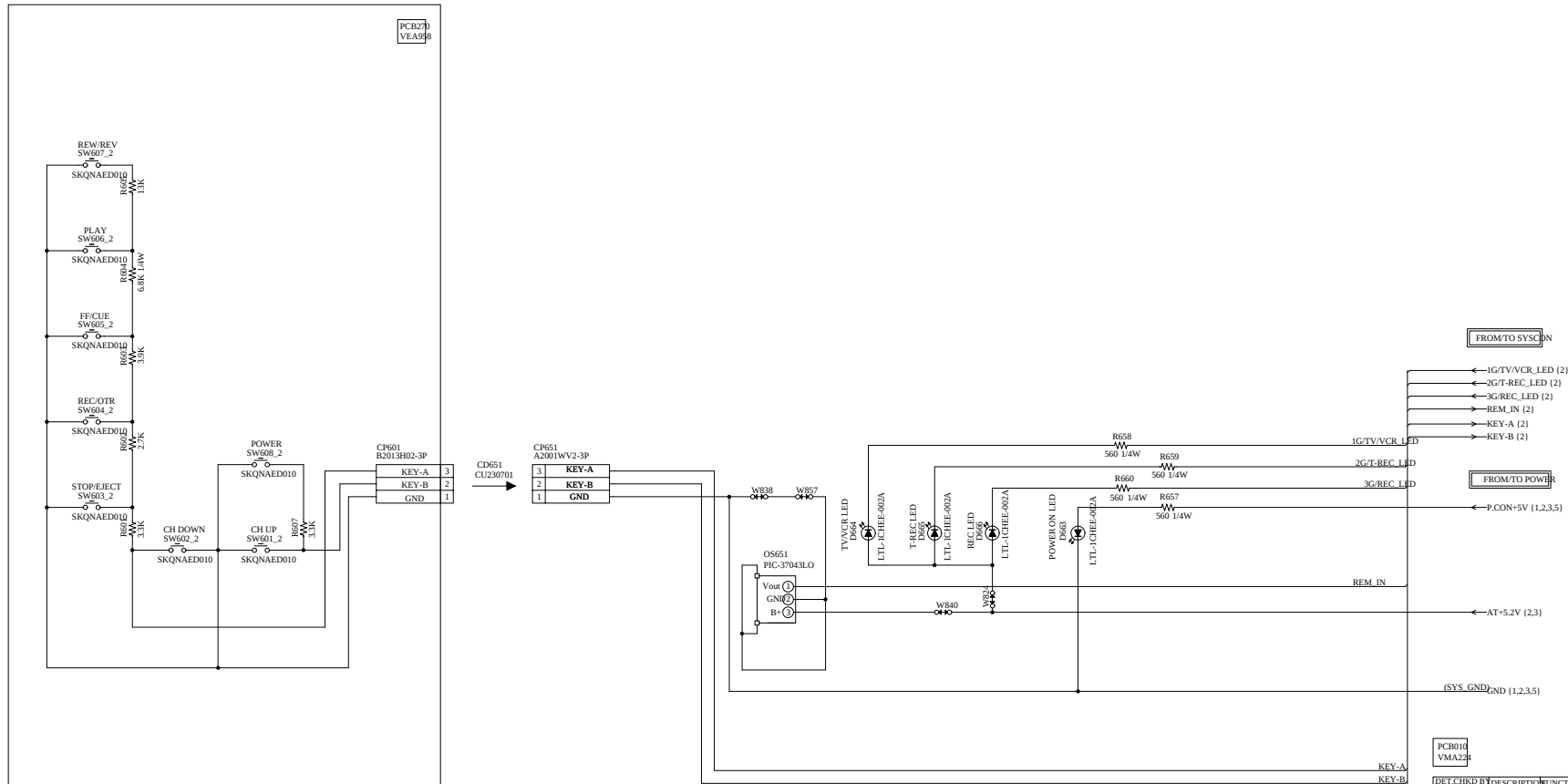
CAUTION: FOR CONTINUED PROTECTION AGAINST FIRE HAZARD, REPLACE ONLY WITH THE SAME TYPE FUSE (1.6A 125V) (F501, F502).
ATTENTION: POUR UNE PROTECTION CONTINUE LES RISQUES D'INCENDIE, N'UTILISER QUE DES FUSIBLES DE MEME TYPE 1.6A 125V (F501, F502).

PCB010
VMA224

OPERATION/DISPLAY SCHEMATIC DIAGRAM (SYSCON PCB)

8
7
6
5
4
3
2
1

8
7
6
5
4
3
2
1



NOTE: THE DC VOLTAGE AT EACH PART WAS
MEASURED WITH THE DIGITAL TESTER
DURING PLAYBACK

NOTE: THIS SCHEMATIC DIAGRAM IS THE LATEST AT THE TIME
OF PRINTING AND SUBJECT TO CHANGE WITHOUT NOTICE

DET. CHKD BY	DESCRIPTION	FUNCTION	PCB NAME
K. MURATA	NTSC	OPERATION/DISPLAY	VMA224A_D
GHKD BY	STANDARD	DWG. NO.	Rev. No. 6
W. PHON	ULPCC	OP11	ISSU. 1
DESIGN BY	MODEL	ORION ELECTRIC CO. LTD.	SECTION: TEC-HQ
C. POOWASORN	M4F1-01B	DATE: 85/30/2001	
DRAWN BY		DISTRIBUTE:	
W. WANPEN		PUBLICATION: TEC-HQ 1 SEC.	

TUNER SCHEMATIC DIAGRAM (SYSCON PCB)

◀ RECORD LUMINANCE SIGNAL (Y)
◀ RECORD COLOR SIGNAL (CB, CR)
◀ AUDIO SIGNAL (REC)
◀ AUDIO SIGNAL (PB)
◀ TUNER VIDEO SIGNAL
◀ PLAYBACK LUMINANCE SIGNAL (Y)
◀ PLAYBACK COLOR SIGNAL (CB, CR)

DET.CHKD BY K.MURATA	DESCRIPTION NTSC	FUNCTION TUNER	PCB NAME VMA224A_D
CHKD BY W.PHON	STANDARD UL/FCC	DWG.NO. TU11	Rev.No5 ISSU: 1
DESIGNED BY C.POOWASON	MODEL M4F1-01B	ORION ELECTRIC CO.,LTD	
DRAWN BY W.WANPEN		SECTION: TEC-HQ DATE: 5/30/2001	
		DISTRIBUTE:	
		PUBICATION: TEC-HQ 3 SEC.	