



FCC TEST REPORT

Application No.: ZR/2019/30015
Applicant: Qbic Technology Co., Ltd
Address of Applicant: 26F.-12, No.99, Sec. 1, Xintai 5th Rd., Xizhi Dist., New Taipei City 22175, Taiwan (R.O.C.)
Manufacturer: Qbic Technology Co., Ltd
Address of Manufacturer: 26F.-12, No.99, Sec. 1, Xintai 5th Rd., Xizhi Dist., New Taipei City 22175, Taiwan (R.O.C.)
Factory: TPV Electronics(Fujian) Co., Ltd
Address of Factory: Rongqiao Economic and Technological Development Zone, Fuqing City, Fujian Province, P.R.China
EUT Description: Panel PC
Model No.: TD-1050 PRO
Trade Mark: Qbic
FCC ID: 2AF82-TD1050HPRO
Standards: 47 CFR FCC Part 2, Subpart J
 47 CFR Part 15, Subpart C
Test Method: ANSI C63.10 (2013)
Date of Receipt: 2019/3/14
Date of Test: 2019/3/14to 2019/5/9
Date of Issue: 2019/5/9

Test Result:	PASS *
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* In the configuration tested, the EUT complied with the standards specified above.

Authorized Signature:

Derek Yang
Wireless Laboratory Manager



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1 Version

Revision Record				
Version	Chapter	Date	Modifier	Remark
00		2019/5/9		Original

Authorized for issue by:				
Tested By		 (Mike Hu) /Project Engineer		2019/5/9 Date
Checked By		 (David Chen) /Reviewer		2019/5/9 Date





2 Test Summary

Test Item	Test Requirement	Test method	Test Result	Result
AC Power Line Conducted Emission	15.207	ANSI C63.10 (2013)	Clause 4.3	PASS
Conducted Peak Output Power	15.247 (a)(1)	ANSI C63.10 (2013)	Clause 4.4	PASS
20dB Emission Bandwidth & 99% Occupied Bandwidth	15.247 (a)(1)	ANSI C63.10 (2013)	Clause 4.5	PASS
Carrier Frequencies Separation	15.247 (a)(1)	ANSI C63.10 (2013)	Clause 4.6	PASS
Hopping Channel Number	15.247 (a)(1)	ANSI C63.10 (2013)	Clause 4.7	PASS
Dwell Time	15.247 (a)(1)	ANSI C63.10 (2013)	Clause 4.8	PASS
Band-edge for RF Conducted Emissions	15.247(d)	ANSI C63.10 (2013)	Clause 4.9	PASS
RF Conducted Spurious Emissions	15.247(d)	ANSI C63.10 (2013)	Clause 4.10	PASS
Radiated Spurious emissions	15.247(d);15.205/15.209	ANSI C63.10 (2013)	Clause 4.11	PASS
Restricted bands around fundamental frequency (Radiated Emission)	15.247(d);15.205/15.209	ANSI C63.10 (2013)	Clause 4.12	PASS





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3 General Information

3.1 Client Information

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Manufacturer:	Qbic Technology Co., Ltd
Address of Manufacturer:	26F.-12, No.99, Sec. 1, Xintai 5th Rd., Xizhi Dist., New Taipei City 22175, Taiwan (R.O.C.)
Factory:	TPV Electronics(Fujian) Co., Ltd
Address of Factory:	Rongqiao Economic and Technological Development Zone, Fuqing City, Fujian Province, P.R.China

3.2 Test Location

Company:	SGS-CSTC Standards Technical Services Co., Ltd. Shenzhen Branch
Address:	No. 1 Workshop, M-10, Middle section, Science & Technology Park, Shenzhen, Guangdong, China
Post code:	518057
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SGS-CSTC Standards Technical Services Co., Ltd.
Shenzhen Branch Testing Center, EEC Laboratory.

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3.3 Test Facility

The test facility is recognized, certified, or accredited by the following organizations:

- **CNAS (No. CNAS L2929)**

CNAS has accredited SGS-CSTC Standards Technical Services Co., Ltd. Shenzhen Branch EMC Lab to ISO/IEC 17025:2005 General Requirements for the Competence of Testing and Calibration Laboratories (CNAS-CL01 Accreditation Criteria for the Competence of Testing and Calibration Laboratories) for the competence in the field of testing.

- **A2LA (Certificate No. 3816.01)**

SGS-CSTC Standards Technical Services Co., Ltd., Shenzhen EMC Laboratory is accredited by the American Association for Laboratory Accreditation (A2LA). Certificate No. 3816.01.

- **VCCI**

The 3m Fully-anechoic chamber for above 1GHz, 10m Semi-anechoic chamber for below 1GHz, Shielded Room for Mains Port Conducted Interference Measurement and Telecommunication Port Conducted Interference Measurement of SGS-CSTC Standards Technical Services Co., Ltd. have been registered in accordance with the Regulations for Voluntary Control Measures with Registration No.: G-20026, R-14188, C-12383 and T-11153 respectively.

- **FCC –Designation Number: CN1178**

SGS-CSTC Standards Technical Services Co., Ltd., Shenzhen EMC Laboratory has been recognized as an accredited testing laboratory.

Designation Number: CN1178. Test Firm Registration Number: 406779.

- **Industry Canada (IC)**

Two 3m Semi-anechoic chambers and the 10m Semi-anechoic chamber of SGS-CSTC Standards Technical Services Co., Ltd. Shenzhen Branch EMC Lab have been registered by Certification and Engineering Bureau of Industry Canada for radio equipment testing with Registration No.: 4620C-1, 4620C-2, 4620C-3.



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3.4 General Description of EUT

EUT Description::	Panel PC
Model No.:	TD-1050 PRO
Trade Mark:	Qbic
Hardware Version:	V0.010
Software Version:	715GA445-M0A-000-006K
Operation Frequency:	2400MHz~2483.5MHz $f_c = 2402 \text{ MHz} + N * 1 \text{ MHz}$, where: - f_c = "Operating Frequency" in MHz, - N = "Channel Number" with the range from 0 to 78.
Bluetooth Version:	Bluetooth V3.0 +EDR
Modulation Technique:	Frequency Hopping Spread Spectrum(FHSS)
Modulation Type:	GFSK, $\pi/4$ DQPSK, 8DPSK
Number of Channel:	79
Hopping Channel Type:	Adaptive Frequency Hopping systems
Sample Type:	<input type="checkbox"/> Portable Device, <input checked="" type="checkbox"/> Module
Antenna Type:	<input type="checkbox"/> External, <input checked="" type="checkbox"/> Integrated
Antenna Gain:	1.1dBi
Power Supply	<input checked="" type="checkbox"/> AC/DC Adapter; <input type="checkbox"/> Battery <input type="checkbox"/> PoE;; <input type="checkbox"/> Other:

Operation Frequency each of channel							
Channel	Frequency	Channel	Frequency	Channel	Frequency	Channel	Frequency
0	2402MHz	20	2422MHz	40	2442MHz	60	2462MHz
1	2403MHz	21	2423MHz	41	2443MHz	61	2463MHz
2	2404MHz	22	2424MHz	42	2444MHz	62	2464MHz
3	2405MHz	23	2425MHz	43	2445MHz	63	2465MHz
4	2406MHz	24	2426MHz	44	2446MHz	64	2466MHz
5	2407MHz	25	2427MHz	45	2447MHz	65	2467MHz
6	2408MHz	26	2428MHz	46	2448MHz	66	2468MHz
7	2409MHz	27	2429MHz	47	2449MHz	67	2469MHz
8	2410MHz	28	2430MHz	48	2450MHz	68	2470MHz
9	2411MHz	29	2431MHz	49	2451MHz	69	2471MHz
10	2412MHz	30	2432MHz	50	2452MHz	70	2472MHz
11	2413MHz	31	2433MHz	51	2453MHz	71	2473MHz
12	2414MHz	32	2434MHz	52	2454MHz	72	2474MHz
13	2415MHz	33	2435MHz	53	2455MHz	73	2475MHz
14	2416MHz	34	2436MHz	54	2456MHz	74	2476MHz
15	2417MHz	35	2437MHz	55	2457MHz	75	2477MHz
16	2418MHz	36	2438MHz	56	2458MHz	76	2478MHz



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17	2419MHz	37	2439MHz	57	2459MHz	77	2479MHz
18	2420MHz	38	2440MHz	58	2460MHz	78	2480MHz
19	2421MHz	39	2441MHz	59	2461MHz		

Remark:

In section 15.31(m), regards to the operating frequency range over 10 MHz, the Lowest frequency, the middle

frequency, and the highest frequency of channel were selected to perform the test, and the selected channel see below:

Channel	Frequency
The Lowest channel	2402MHz
The Middle channel	2441MHz
The Highest channel	2480MHz

3.5 Test Environment

Operating Environment	
Temperature:	24.0 °C
Humidity:	55 % RH
Atmospheric Pressure:	101.30 KPa

3.6 Description of Support Units

The EUT has been tested independent unit.



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4 Test results and Measurement Data

4.1 Antenna Requirement

Standard requirement:	47 CFR Part 15C Section 15.203 /247(c)
<p>15.203 requirement: An intentional radiator shall be designed to ensure that no antenna other than that furnished by the responsible party shall be used with the device. The use of a permanently attached antenna or of an antenna that uses a unique coupling to the intentional radiator, the manufacturer may design the unit so that a broken antenna can be replaced by the user, but the use of a standard antenna jack or electrical connector is prohibited.</p> <p>15.247(b) (4) requirement: The conducted output power limit specified in paragraph (b) of this section is based on the use of antennas with directional gains that do not exceed 6 dBi. Except as shown in paragraph (c) of this section, if transmitting antennas of directional gain greater than 6 dBi are used, the conducted output power from the intentional radiator shall be reduced below the stated values in paragraphs (b)(1), (b)(2), and (b)(3) of this section, as appropriate, by the amount in dB that the directional gain of the antenna exceeds 6 dBi.</p> <p>The antenna is integrated on the main PCB and no consideration of replacement. The best case gain of the antenna is 1.1dBi.</p>	

4.2 Other requirements Frequency Hopping Spread Spectrum System Hopping Sequence

4.2.1 Test Requirement:

47 CFR Part 15, Subpart C 15.247(a)(1),(g),(h)

4.2.2 Conclusion

Standard Requirement:

The system shall hop to channel frequencies that are selected at the system hopping rate from a Pseudorandom ordered list of hopping frequencies. Each frequency must be used equally on the average by each transmitter. The system receivers shall have input bandwidths that match the hopping channel bandwidths of their corresponding transmitters and shall shift frequencies in synchronization with the transmitted signals.

Frequency hopping spread spectrum systems are not required to employ all available hopping channels during each transmission. However, the system, consisting of both the transmitter and the receiver, must be designed to comply with all of the regulations in this section should the transmitter be presented with a continuous data (or information) stream. In addition, a system employing short transmission bursts must comply with the definition of a frequency hopping system and must distribute its transmissions over the minimum number of hopping channels specified in this section.

The incorporation of intelligence within a frequency hopping spread spectrum system that permits the system to recognize other users within the spectrum band so that it individually and independently chooses and adapts its hopsets to avoid hopping on occupied channels is permitted. The coordination of frequency hopping systems in any other manner for the express purpose of avoiding the simultaneous occupancy of individual hopping frequencies by multiple transmitters is not permitted.

Compliance for section 15.247(a)(1):

According to Technical Specification, the pseudorandom sequence may be generated in a nine-stage shift register whose 5th and 9th stage outputs are added in a modulo-two addition stage. And the result is fed back to the input of the first stage. The sequence begins with the first ONE of 9 consecutive ONES; i.e. the shift register is initialized with nine ones.



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> Number of shift register stages: 9

> Length of pseudo-random sequence: $2^9 - 1 = 511$ bits

> Longest sequence of zeros: 8 (non-inverted signal)

Linear Feedback Shift Register for Generation of the PRBS sequence

An example of Pseudorandom Frequency Hopping Sequence as follow:

Each frequency used equally on the average by each transmitter.

According to Technical Specification, the receivers are designed to have input and IF bandwidths that match the hopping channel bandwidths of any transmitters and shift frequencies in synchronization with the transmitted signals.

Compliance for section 15.247(g):

According to Technical Specification, the system transmits the packet with the pseudorandom hopping frequency with a continuous data and the short burst transmission from the RF system is also transmitted under the frequency hopping system with the pseudorandom hopping frequency system.

Compliance for section 15.247(h):

According to Technical specification, the system incorporates with an adaptive system to detect other user within the spectrum band so that it individually and independently to avoid hopping on the occupied channels. The system is designed not have the ability to coordinated with other FHSS System in an effort to avoid the simultaneous occupancy of individual hopping frequencies by multiple transmitter.



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4.3 AC Power Line Conducted Emissions

Test Requirement:	47 CFR Part 15C Section 15.207		
Test Method:	ANSI C63.10: 2013		
Test Frequency Range:	150kHz to 30MHz		
Limit:	Frequency range (MHz)	Limit (dBuV)	
		Quasi-peak	Average
	0.15-0.5	66 to 56*	56 to 46*
	0.5-5	56	46
	5-30	60	50
* Decreases with the logarithm of the frequency.			
Test Procedure:	<p>1) The mains terminal disturbance voltage test was conducted in a shielded room.</p> <p>2) The EUT was connected to AC power source through a LISN 1 (Line Impedance Stabilization Network) which provides a 50Ω/50μH + 5Ω linear impedance. The power cables of all other units of the EUT were connected to a second LISN 2, which was bonded to the ground reference plane in the same way as the LISN 1 for the unit being measured. A multiple socket outlet strip was used to connect multiple power cables to a single LISN provided the rating of the LISN was not exceeded.</p> <p>3) The tabletop EUT was placed upon a non-metallic table 0.8m above the ground reference plane. And for floor-standing arrangement, the EUT was placed on the horizontal ground reference plane,</p> <p>4) The test was performed with a vertical ground reference plane. The rear of the EUT shall be 0.4 m from the vertical ground reference plane. The vertical ground reference plane was bonded to the horizontal ground reference plane. The LISN 1 was placed 0.8 m from the boundary of the unit under test and bonded to a ground reference plane for LISNs mounted on top of the ground reference plane. This distance was between the closest points of the LISN 1 and the EUT. All other units of the EUT and associated equipment was at least 0.8 m from the LISN 2.</p> <p>5) In order to find the maximum emission, the relative positions of equipment and all of the interface cables must be changed according to ANSI C63.10: 2013 on conducted measurement.</p>		



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Test Setup:	
Exploratory Test Mode:	Non-hopping transmitting mode with all kind of modulation and all kind of data type at the lowest, middle, high channel. Charge + Transmitting mode.
Final Test Mode:	Through Pre-scan, find the DH5 of data type and GFSK modulation at the lowest channel is the worst case. Charge + Transmitting mode Only the worst case is recorded in the report.
Instruments Used:	Refer to section 5.10 for details
Test Results:	Pass



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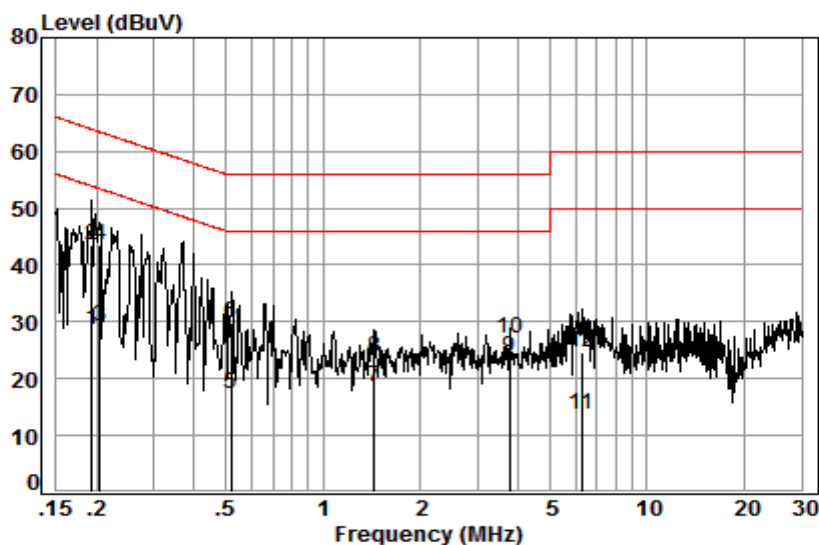
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Measurement Data

An initial pre-scan was performed on the live and neutral lines with peak detector.

Quasi-Peak and Average measurement were performed at the frequencies with maximized peak emission were detected.

Live line:



Site : Shielding Room

Condition: Line

Job No. : 11707CR

Test mode: b

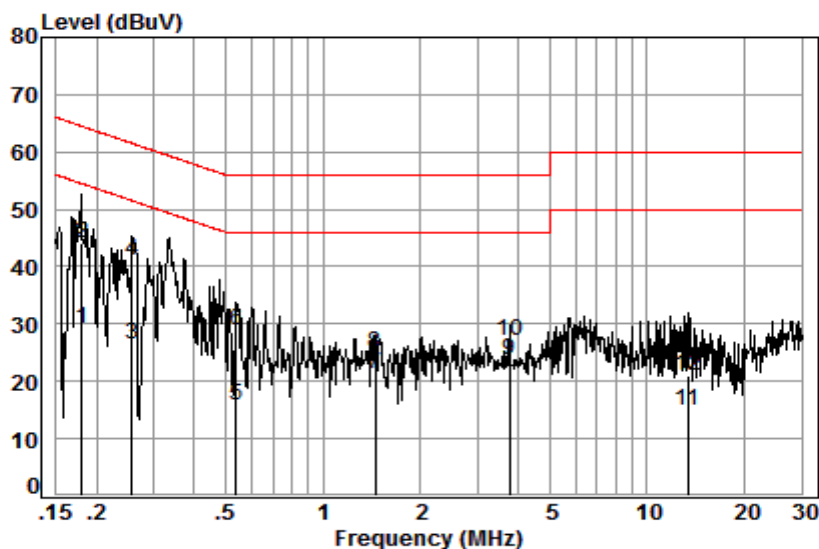
	Freq	Cable Loss	LISN Factor	Read Level	Level	Limit Line	Over Limit	Remark
	MHz	dB	dB	dBuV	dBuV	dBuV	dB	
1	0.19	0.02	9.66	18.94	28.62	53.89	-25.27	Average
2	0.19	0.02	9.66	33.94	43.62	63.89	-20.27	QP
3	0.20	0.02	9.66	19.46	29.14	53.45	-24.31	Average
4	0.20	0.02	9.66	33.89	43.57	63.45	-19.88	QP
5	0.52	0.06	9.67	7.46	17.19	46.00	-28.81	Average
6	0.52	0.06	9.67	20.08	29.81	56.00	-26.19	QP
7	1.44	0.13	9.73	8.77	18.63	46.00	-27.37	Average
8	1.44	0.13	9.73	13.72	23.58	56.00	-32.42	QP
9	3.76	0.16	9.72	13.73	23.61	46.00	-22.39	Average
10	3.76	0.16	9.72	17.11	26.99	56.00	-29.01	QP
11	6.29	0.17	9.77	3.78	13.72	50.00	-36.28	Average
12	6.29	0.17	9.77	14.78	24.72	60.00	-35.28	QP



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Neutral line:



Site : Shielding Room

Condition: Neutral

Job No. : 11707CR

Test mode: b

	Freq	Cable Loss	LISN Factor	Read Level	Level	Limit Line	Over Limit	Remark
	MHz	dB	dB	dBuV	dBuV	dBuV	dB	
1	0.18	0.02	9.64	19.60	29.26	54.50	-25.24	Average
2	0.18	0.02	9.64	34.33	43.99	64.50	-20.51	QP
3	0.26	0.03	9.64	16.87	26.54	51.51	-24.97	Average
4	0.26	0.03	9.64	31.32	40.99	61.51	-20.52	QP
5	0.54	0.06	9.64	6.13	15.83	46.00	-30.17	Average
6	0.54	0.06	9.64	19.22	28.92	56.00	-27.08	QP
7	1.45	0.13	9.70	11.43	21.26	46.00	-24.74	Average
8	1.45	0.13	9.70	15.09	24.92	56.00	-31.08	QP
9	3.76	0.16	9.69	14.01	23.86	46.00	-22.14	Average
10	3.76	0.16	9.69	17.17	27.02	56.00	-28.98	QP
11	13.34	0.20	10.22	4.35	14.77	50.00	-35.23	Average
12	13.34	0.20	10.22	10.52	20.94	60.00	-39.06	QP

Remarks:

1. The following Quasi-Peak and Average measurements were performed on the EUT:
2. Final Test Level = Receiver Reading + LISN Factor + Cable Loss.

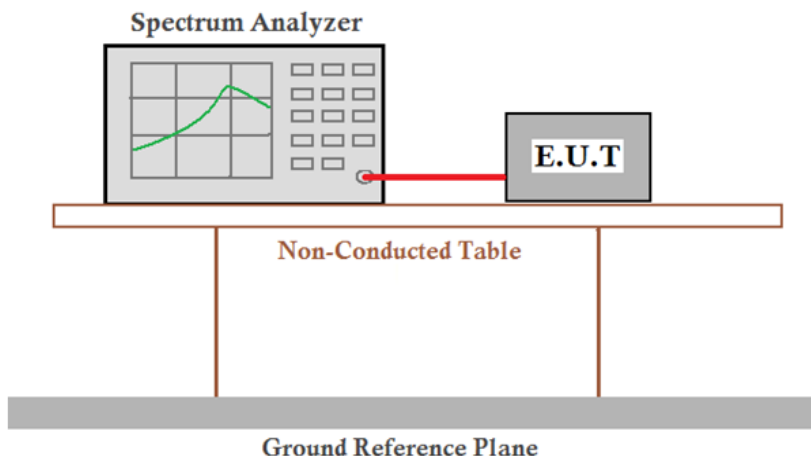


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4.4 Conducted Peak Output Power

Test Requirement:	47 CFR Part 15C Section 15.247 (a)(1)
Test Method:	ANSI C63.10:2013 Section 7.8.5
Test Setup:	
Limit:	(20.97dBm) 125mW
Exploratory Test Mode:	Non-hopping transmitting with all kind of modulation and all kind of data type.
Final Test Mode:	Through Pre-scan, find the DH5 of data type is the worst case of GFSK modulation type, 2-DH5 of data type is the worst case of $\pi/4$ QPSK modulation type, 3-DH5 of data type is the worst case of 8DPSK modulation type.
Instruments Used:	Refer to section 5.10 for details
Test Results:	Pass



4.4.1 Test Results

Measurement Data of Average power:

GFSK mode		
Test channel	Average Output Power (dBm)	Result
Lowest	9.06	Report purpose only
Middle	8.78	Report purpose only
Highest	8.21	Report purpose only
$\pi/4$ DQPSK mode		
Test channel	Average Output Power (dBm)	Result
Lowest	8.39	Report purpose only
Middle	8.02	Report purpose only
Highest	7.39	Report purpose only
8DPSK mode		
Test channel	Average Output Power (dBm)	Result
Lowest	8.36	Report purpose only
Middle	8.03	Report purpose only
Highest	7.40	Report purpose only

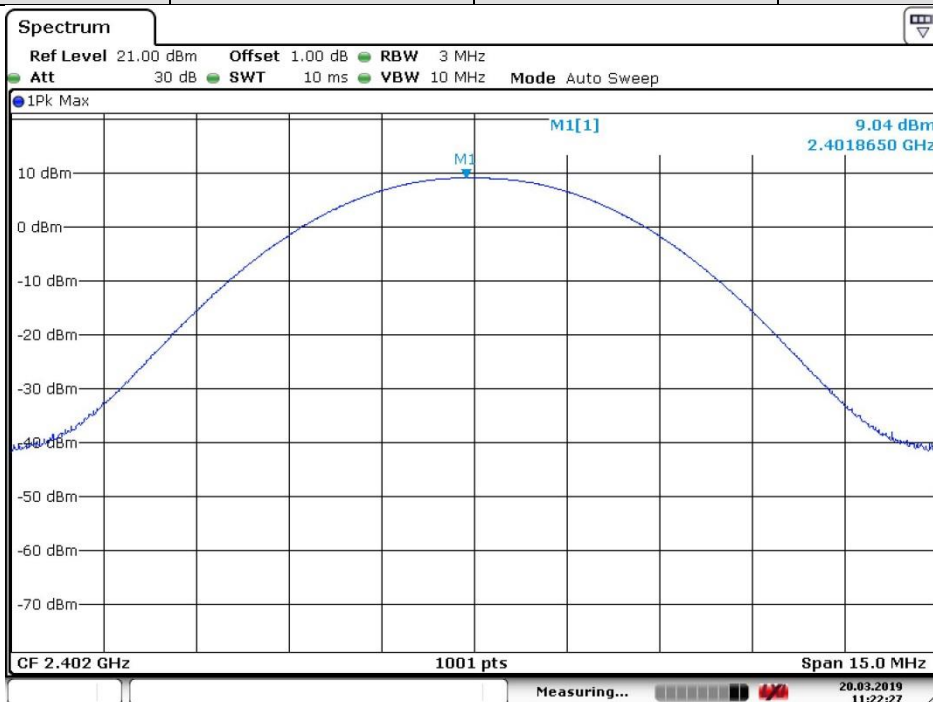
Measurement Data of Peak power:

GFSK mode			
Test channel	Peak Output Power (dBm)	Limit (dBm)	Result
Lowest	9.04	20.97	Pass
Middle	8.79	20.97	Pass
Highest	8.36	20.97	Pass
$\pi/4$ DQPSK mode			
Test channel	Peak Output Power (dBm)	Limit (dBm)	Result
Lowest	9.94	20.97	Pass
Middle	9.67	20.97	Pass
Highest	9.32	20.97	Pass
8DPSK mode			
Test channel	Peak Output Power (dBm)	Limit (dBm)	Result
Lowest	10.05	20.97	Pass
Middle	9.80	20.97	Pass
Highest	9.47	20.97	Pass



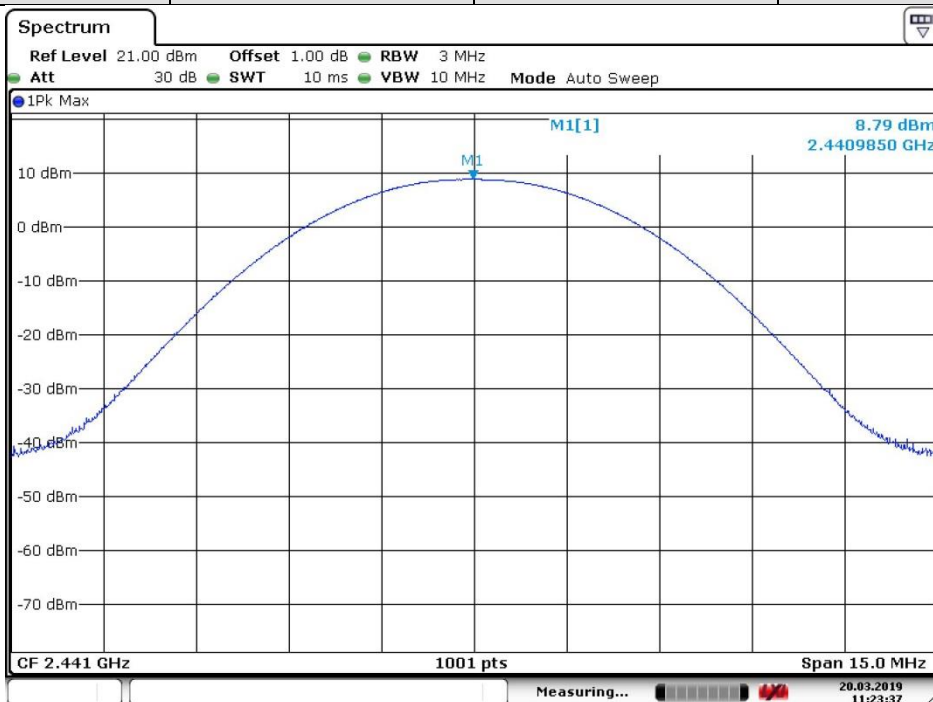
Test plots

Test mode:	GFSK	Test channel:	Lowest
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Date: 20.MAR.2019 11:22:28

Test mode:	GFSK	Test channel:	Middle
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Date: 20.MAR.2019 11:23:38



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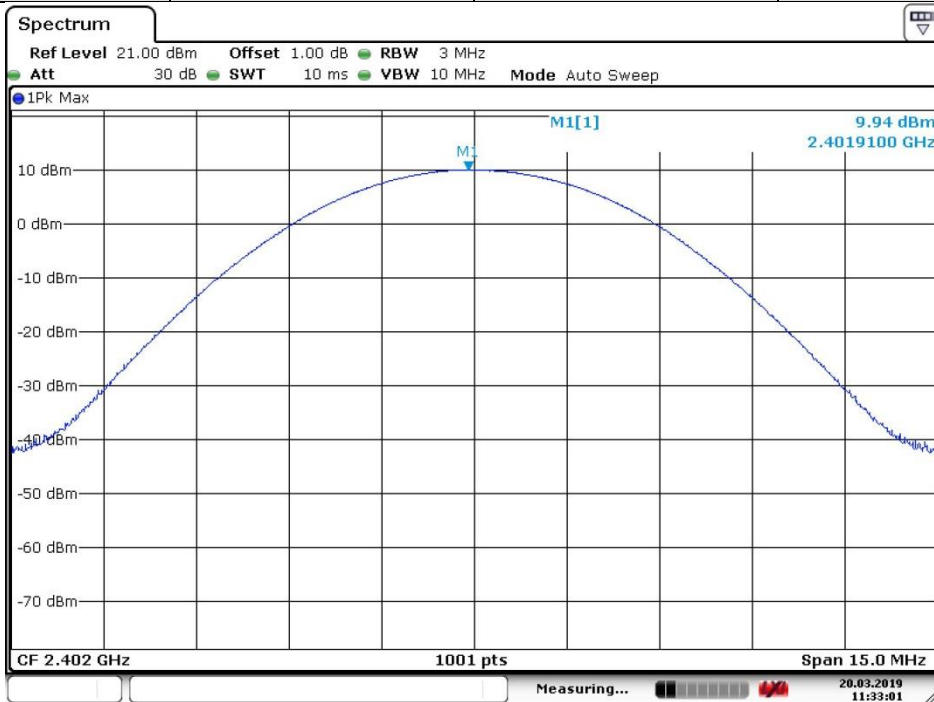
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Test mode:	GFSK	Test channel:	Highest
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Date: 20.MAR.2019 11:24:15

Test mode:	π /4DQPSK	Test channel:	Lowest
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Date: 20.MAR.2019 11:33:02



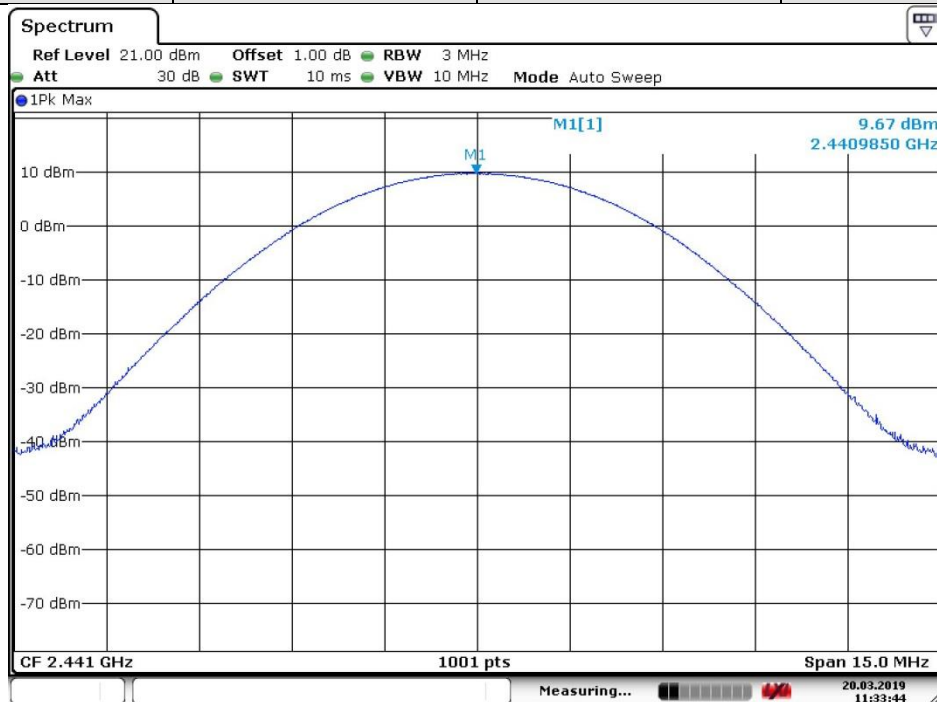
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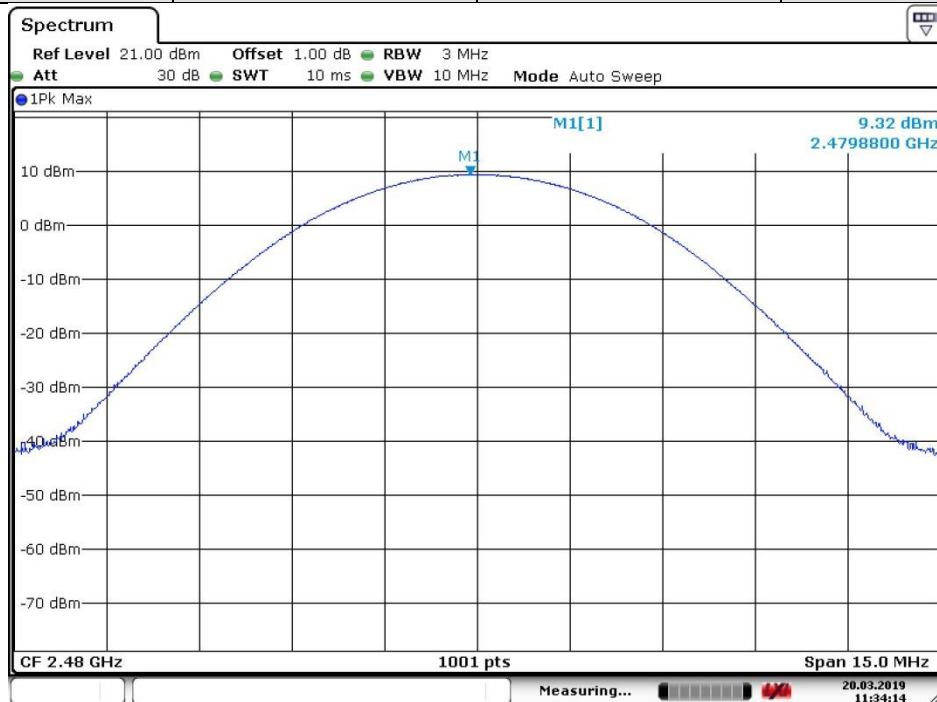
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Test mode:	$\pi/4$ DQPSK	Test channel:	Middle
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Date: 20.MAR.2019 11:33:45

Test mode:	$\pi/4$ DQPSK	Test channel:	Highest
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Date: 20.MAR.2019 11:34:14



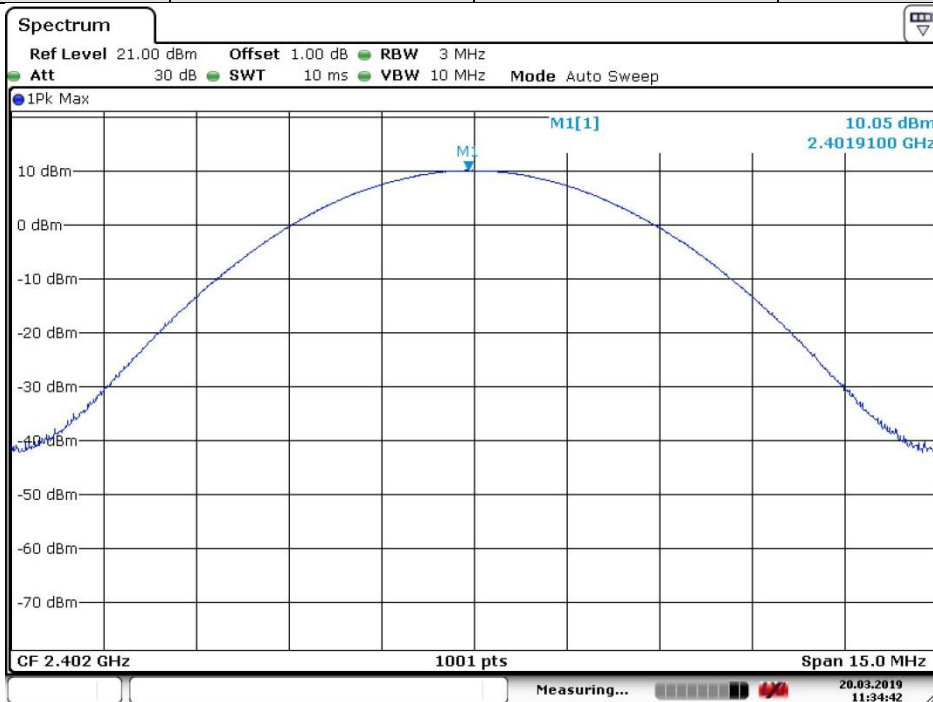
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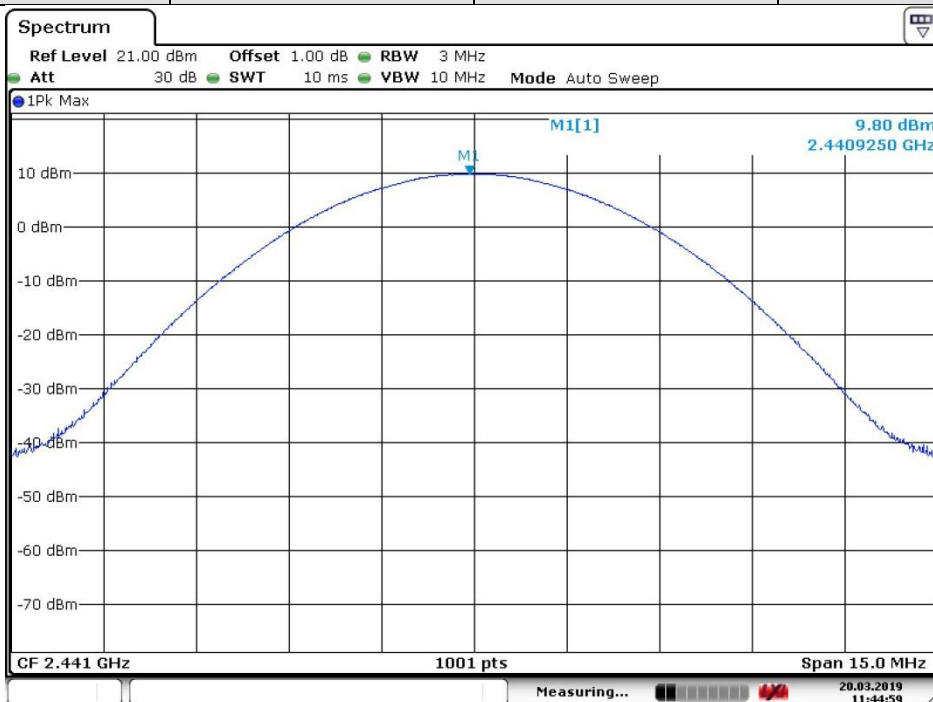
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Test mode:	8DPSK	Test channel:	Lowest
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Date: 20.MAR.2019 11:34:43

Test mode:	8DPSK	Test channel:	Middle
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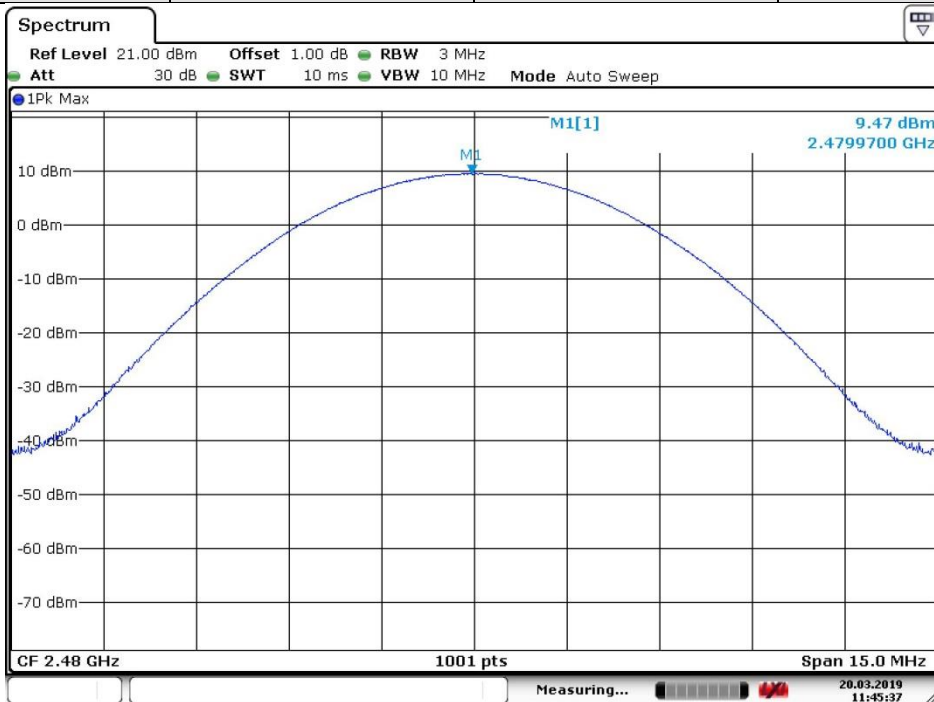


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Test mode:	8DPSK	Test channel:	Highest
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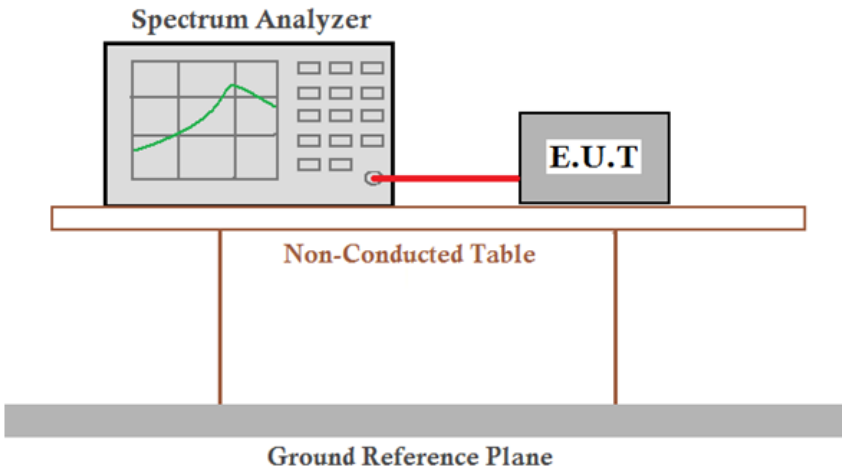
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4.5 20dB Emission Bandwidth & 99% Occupied Bandwidth

Test Requirement:	47 CFR Part 15C Section 15.247 (a)(1)
Test Method:	ANSI C63.10:2013 Section 7.8.7
Test Setup:	 <p>The diagram illustrates the test setup. A Spectrum Analyzer is connected via a red cable to an E.U.T. (Equipment Under Test). Both are placed on a Non-Conducted Table. Below the table is a Ground Reference Plane.</p>
Limit:	NA
Exploratory Test Mode:	Non-hopping transmitting with all kind of modulation and all kind of data type.
Final Test Mode:	Through Pre-scan, find the DH5 of data type is the worst case of GFSK modulation type, 2-DH5 of data type is the worst case of $\pi/4$ DQPSK modulation type, 3-DH5 of data type is the worst case of 8DPSK modulation type.
Instruments Used:	Refer to section 5.10 for details
Test Results:	Pass

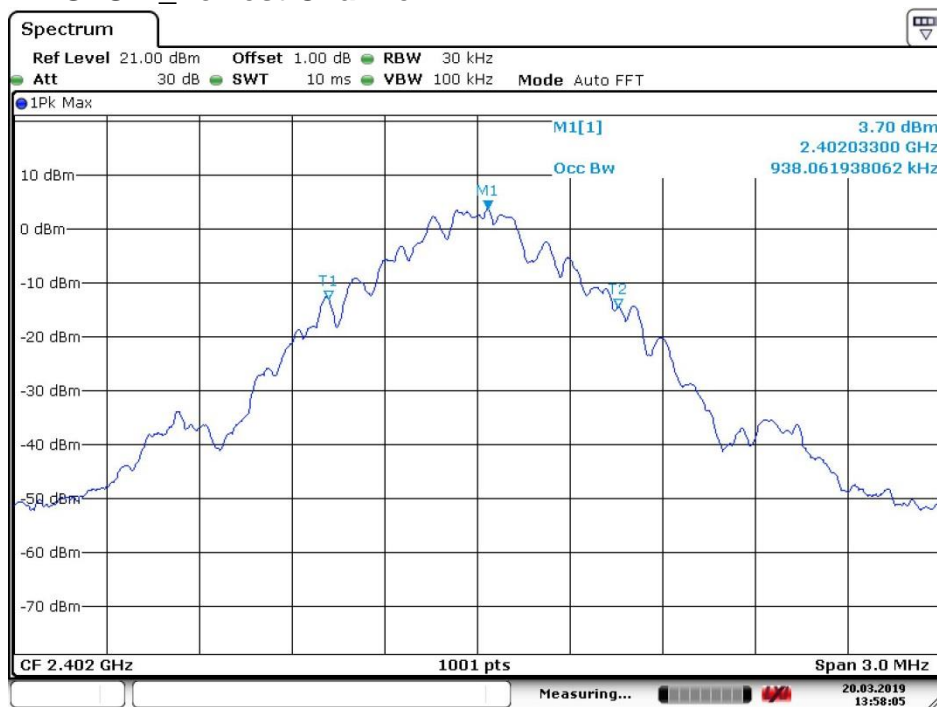
4.5.1 Test Results

Mode	Test Channel	99% Occupied Bandwidth (KHz)	20dB Emission Bandwidth (KHz)	Result
GFSK	Lowest	938.06	917.1	Pass
	Middle	938.06	920.1	Pass
	Highest	941.06	920.1	Pass
$\pi/4$ DQPSK	Lowest	1219.78	1366.6	Pass
	Middle	1219.78	1366.6	Pass
	Highest	1216.78	1366.6	Pass
8DPSK	Lowest	1207.79	1306.7	Pass
	Middle	1204.80	1306.7	Pass
	Highest	1204.80	1306.7	Pass

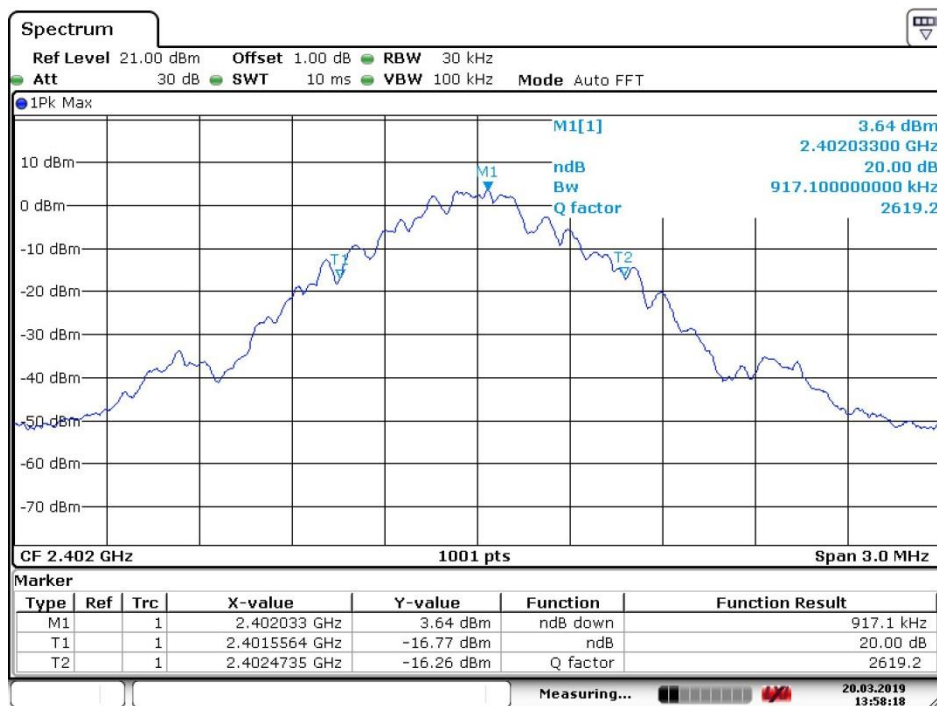


4.4.1 Test plots

4.4.1.1 GFSK _Lowest Channel



Date: 20. MAR 2019 13:58:05

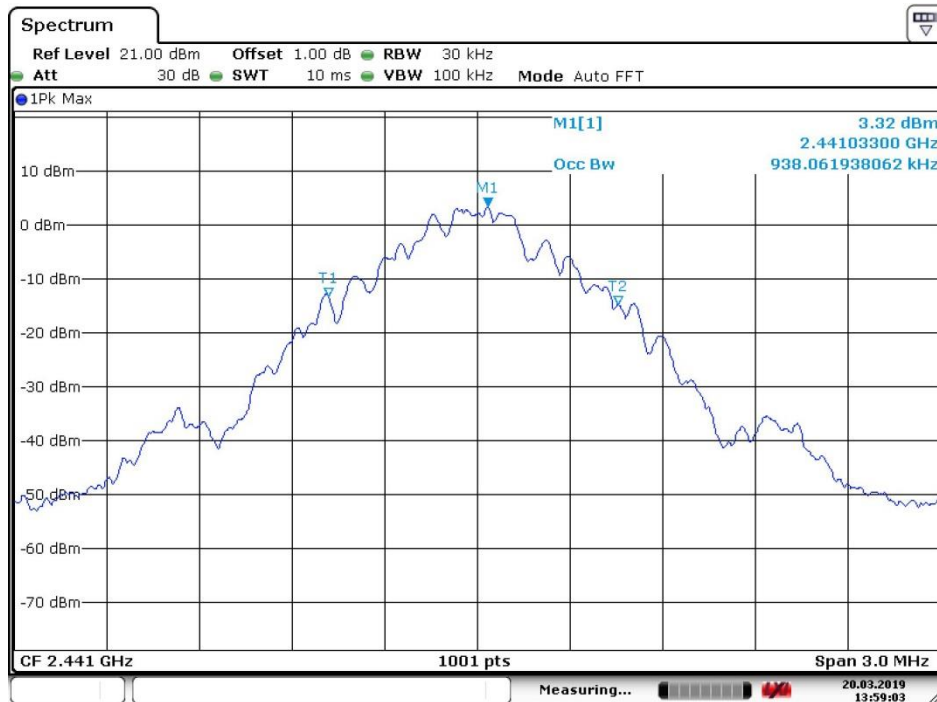


Date: 20. MAR 2019 13:58:19

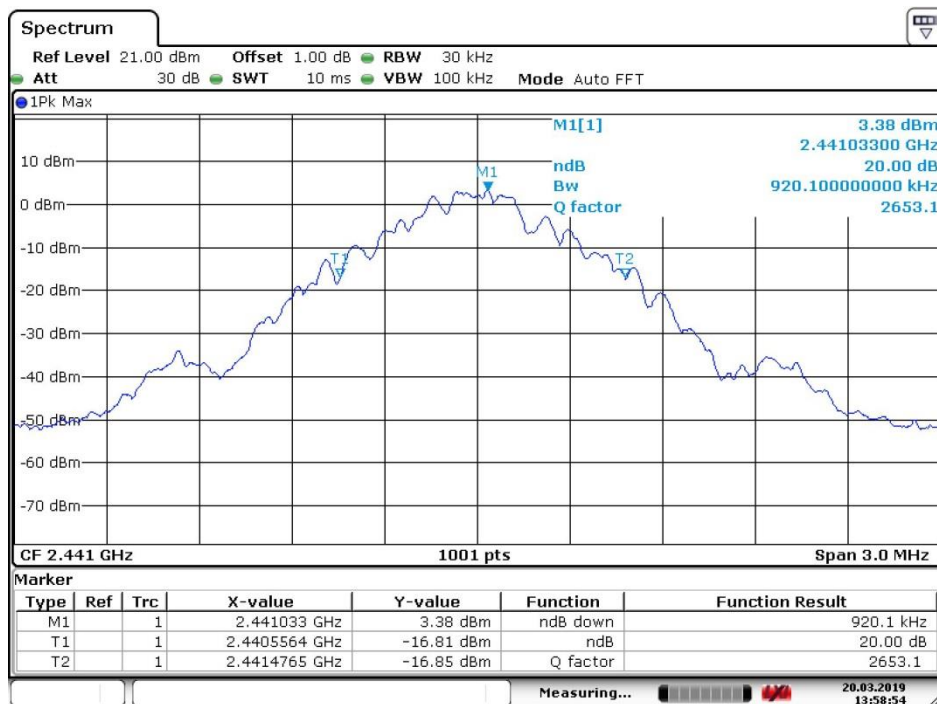


4.4.1.2

GFSK_Middle Channel



Date: 20.MAR.2019 13:59:04



Date: 20.MAR.2019 13:58:54

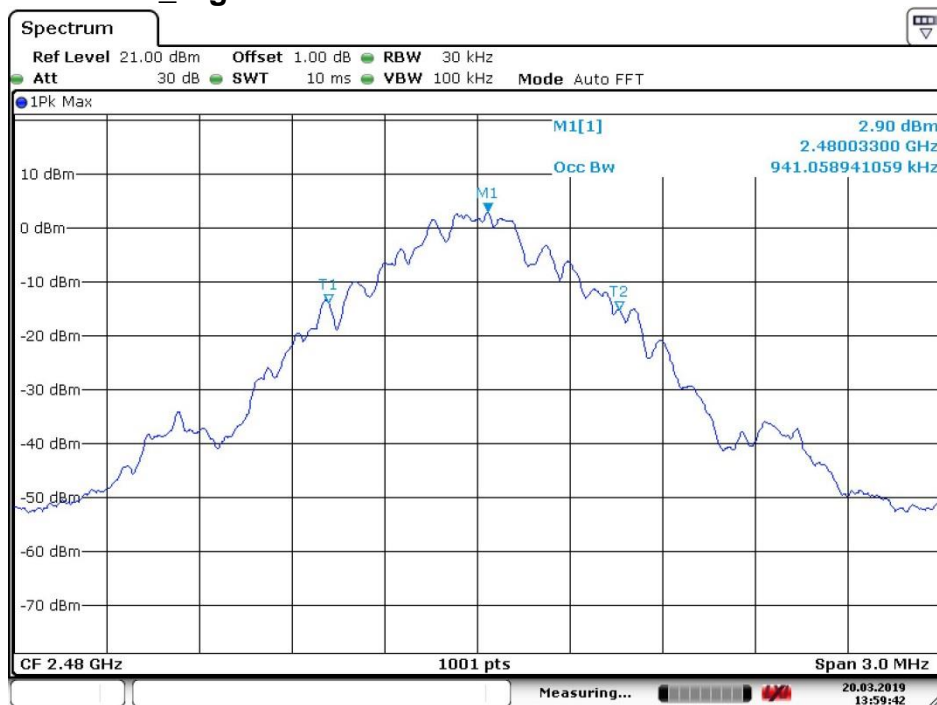


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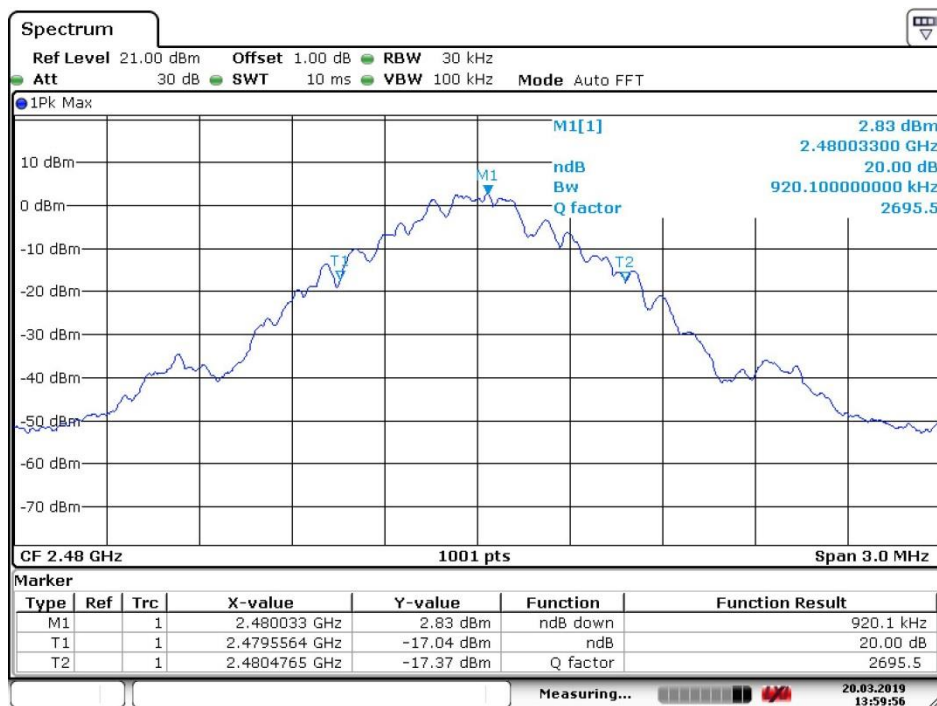
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4.4.1.3 GFSK_Highest Channel



Date: 20.MAR.2019 13:59:42



Date: 20.MAR.2019 13:59:56

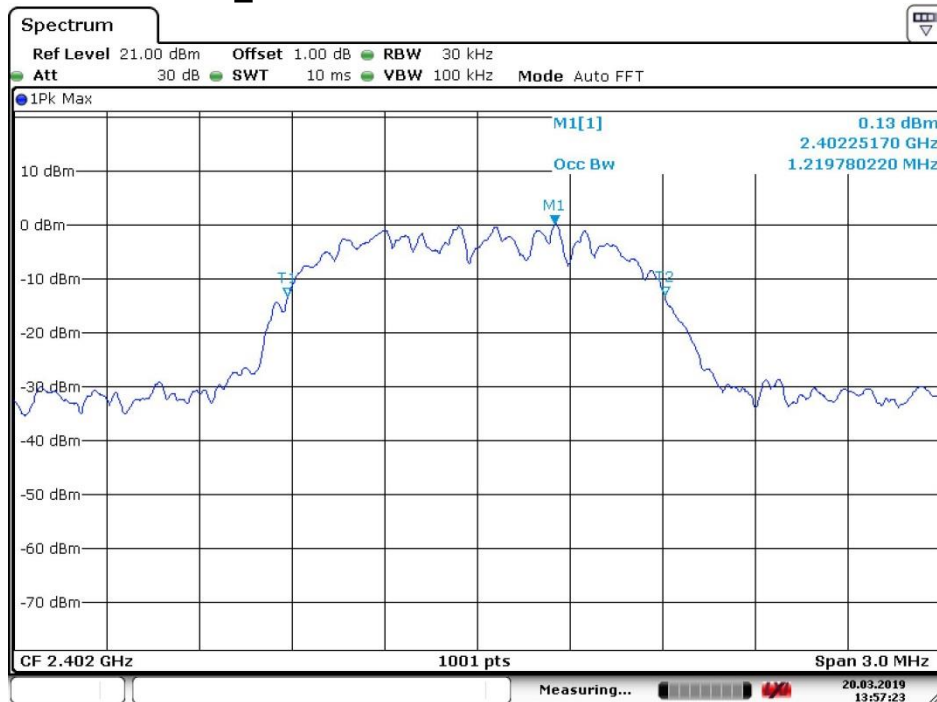


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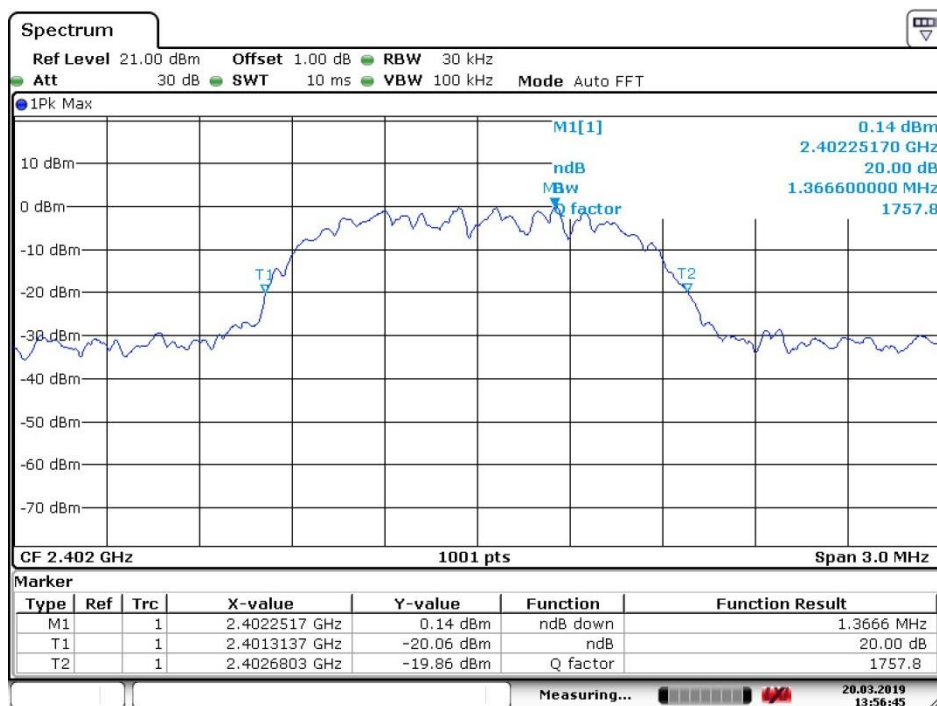
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4.4.1.4

 $\pi/4$ DQPSK _Lowest Channel

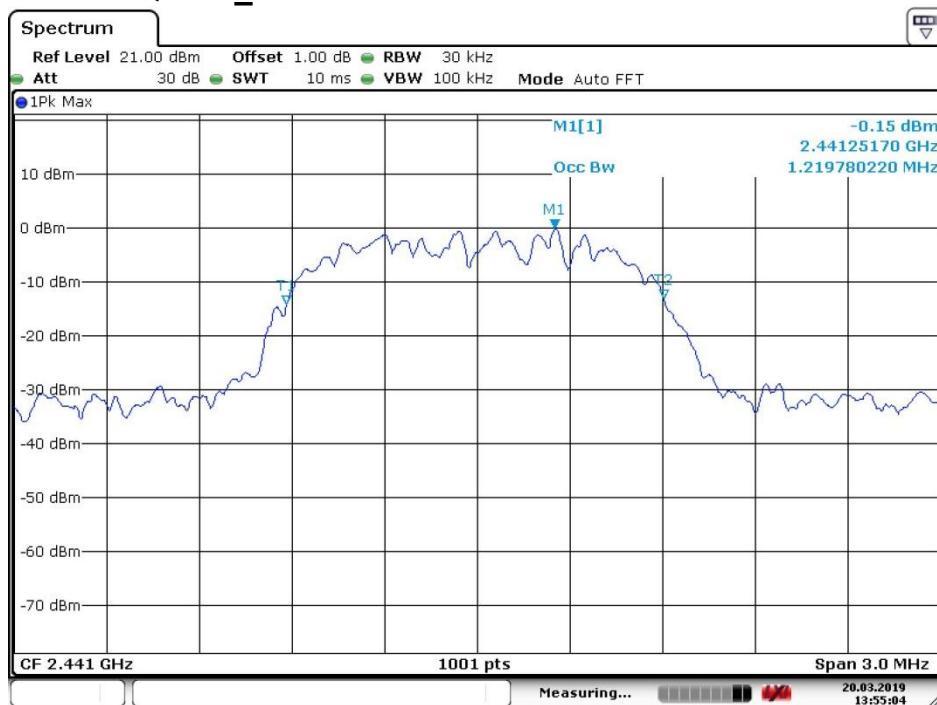
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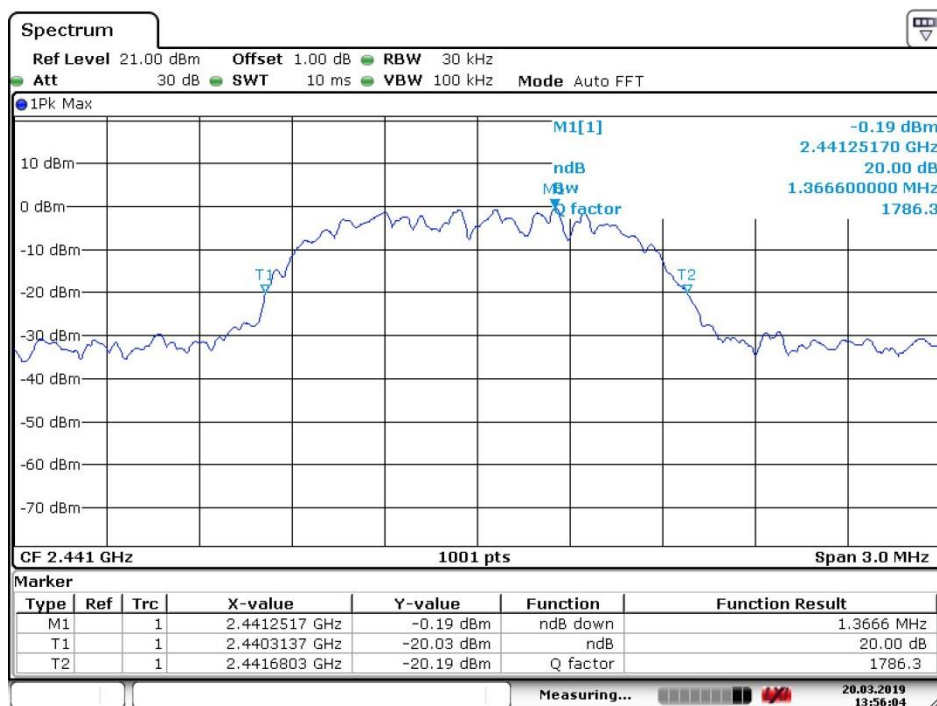
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4.4.1.5 $\pi/4$ DQPSK_Middle Channel



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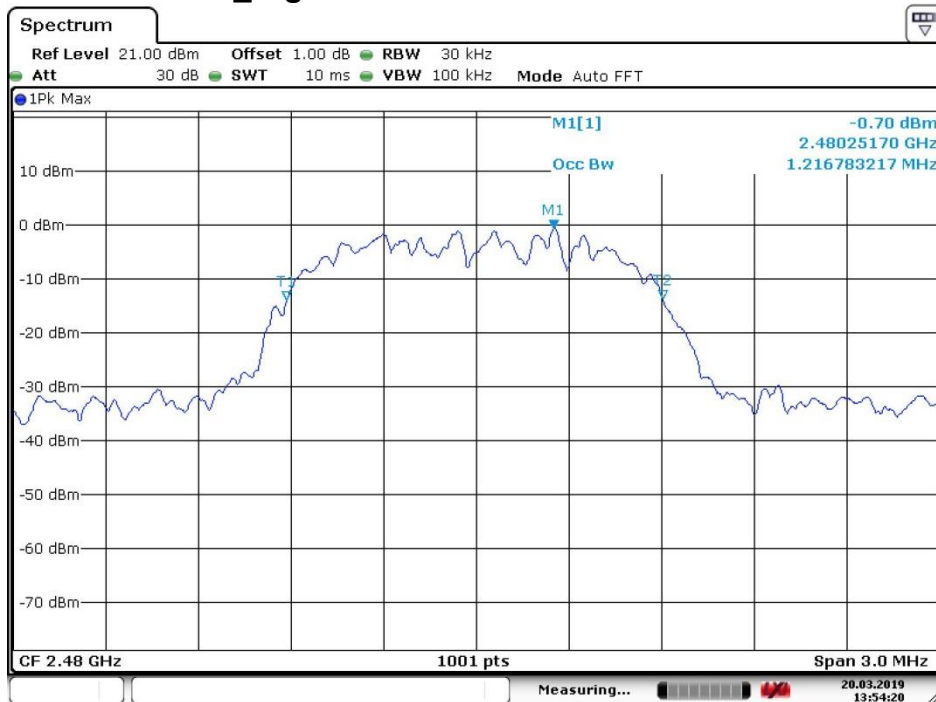


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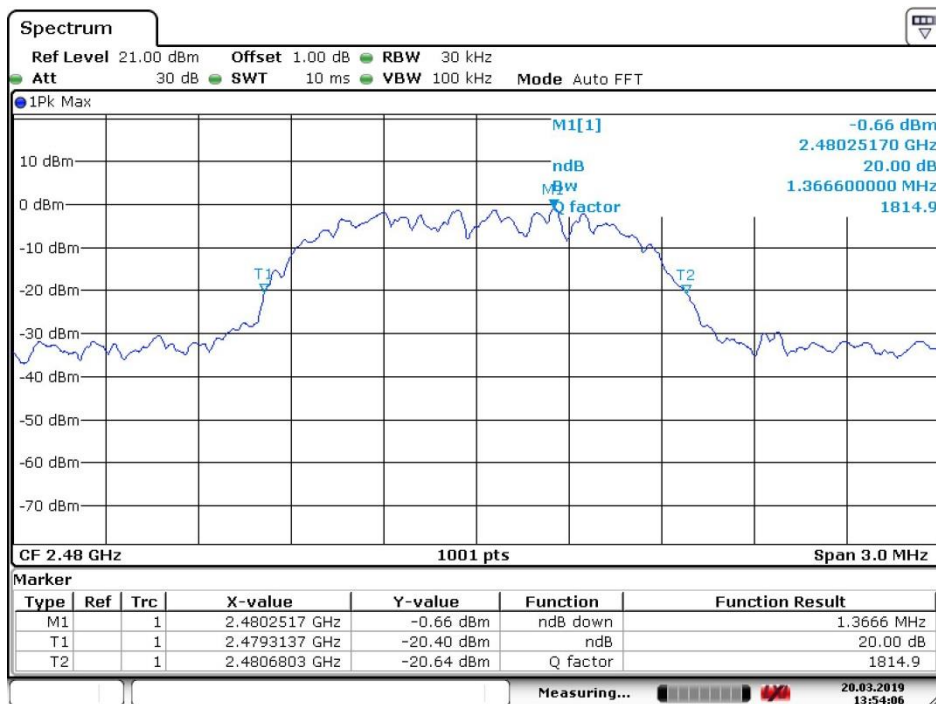
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4.4.1.6 $\pi/4$ DQPSK_Highest Channel



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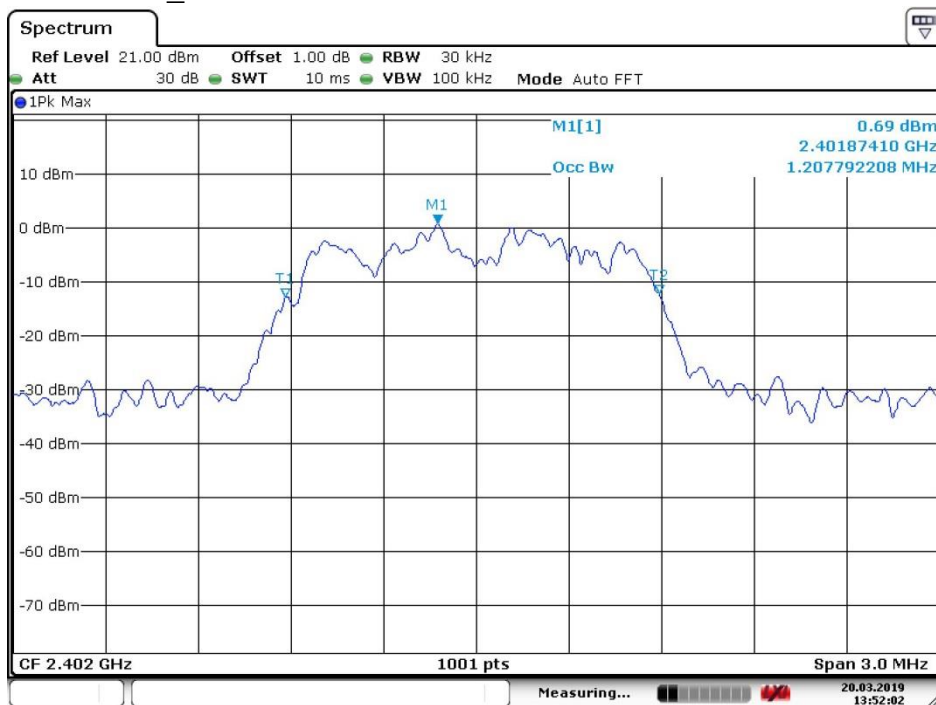


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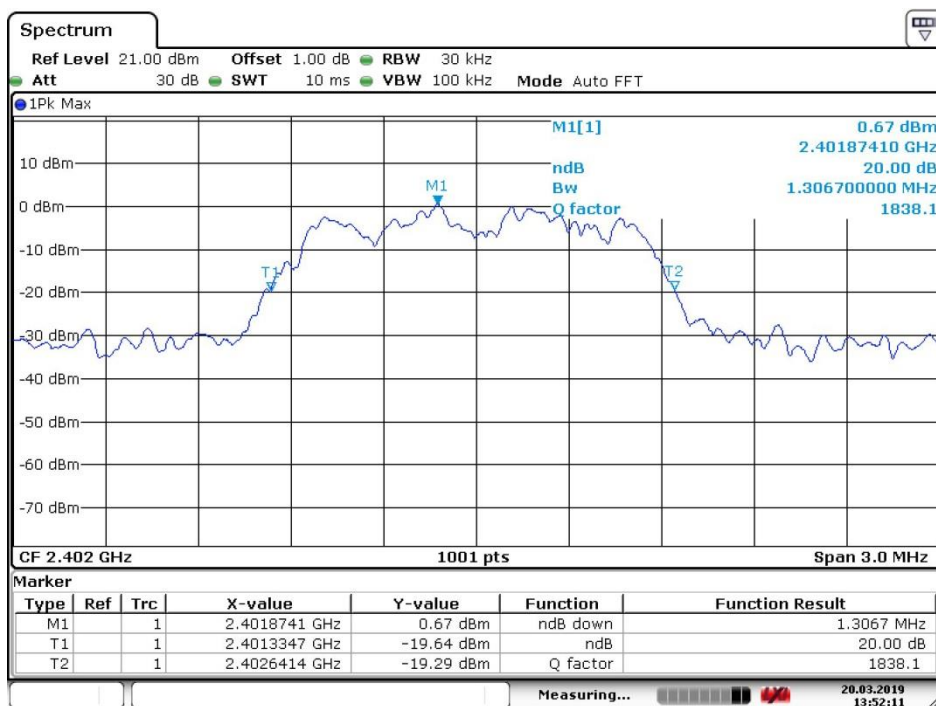
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4.4.1.7 8DPSK_Lowest Channel



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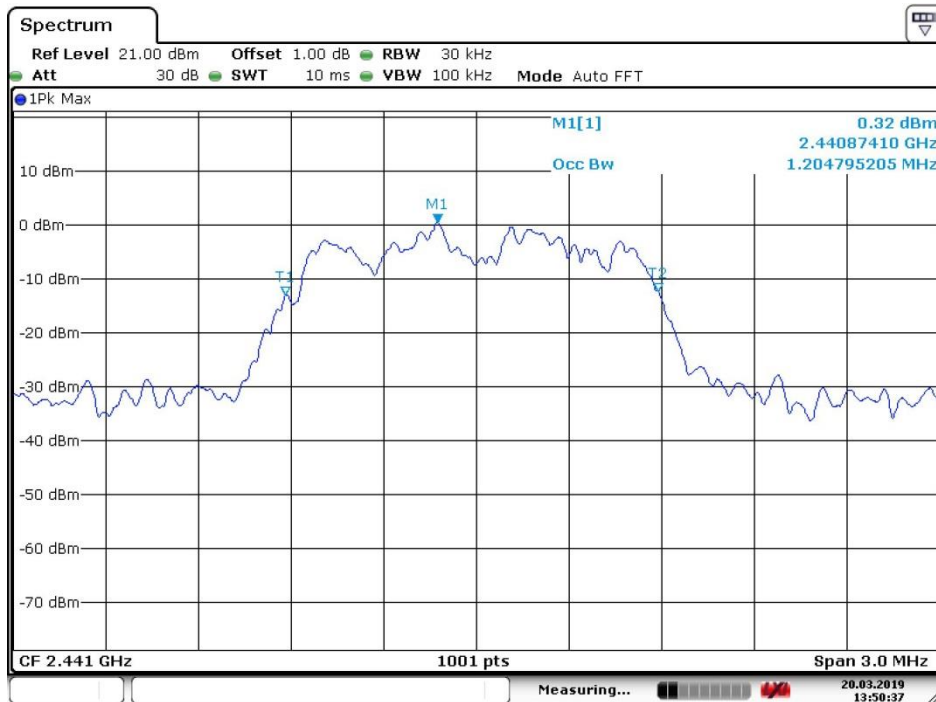
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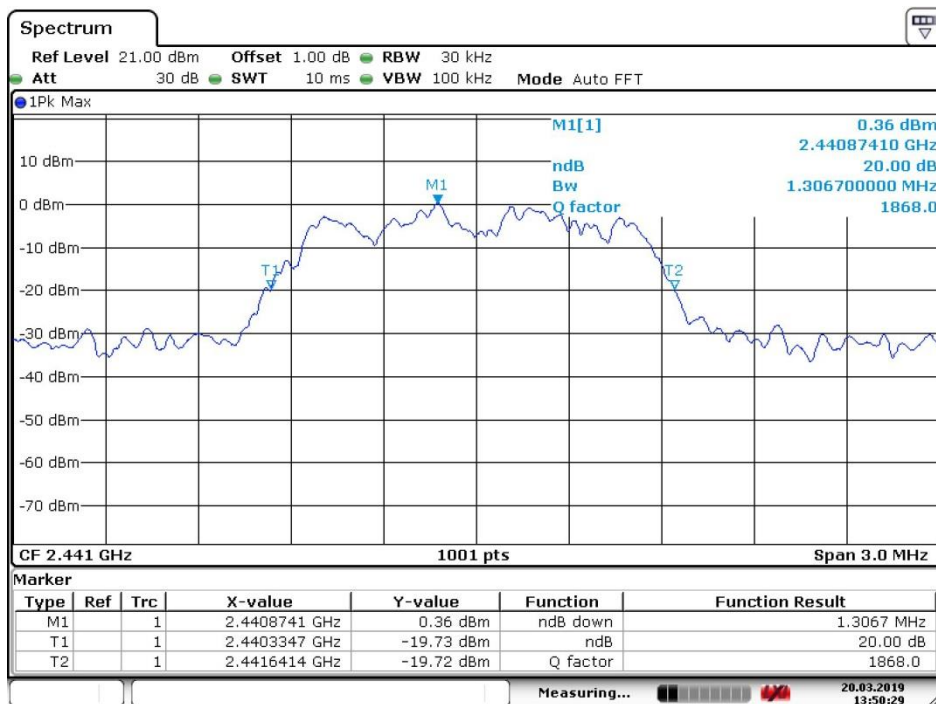
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4.4.1.8 8DPSK_Middle Channel



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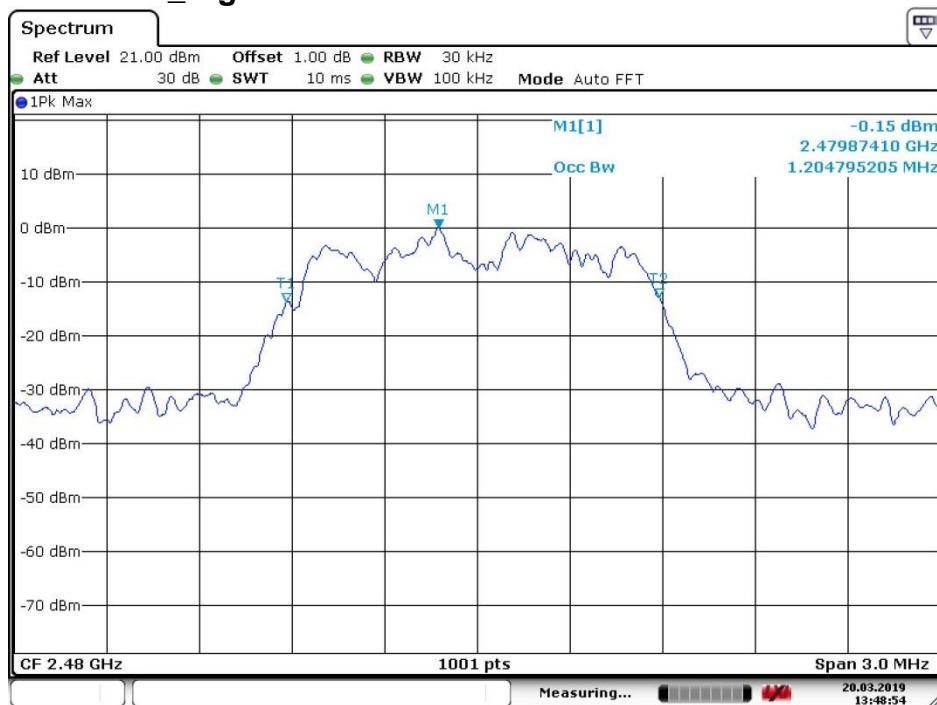


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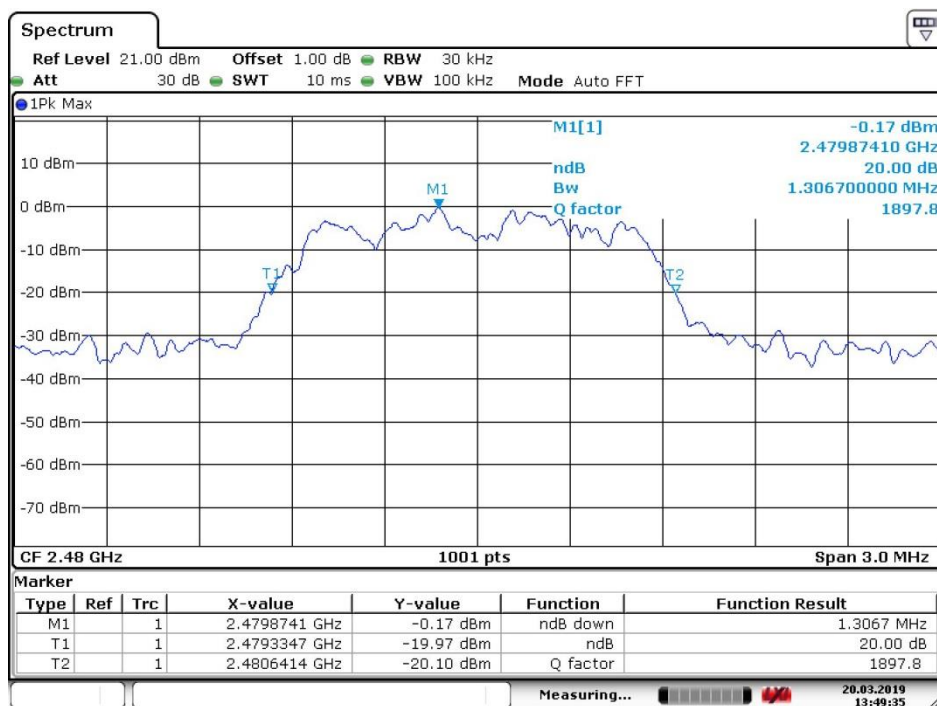
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4.4.1.9 8DPSK_Highest Channel



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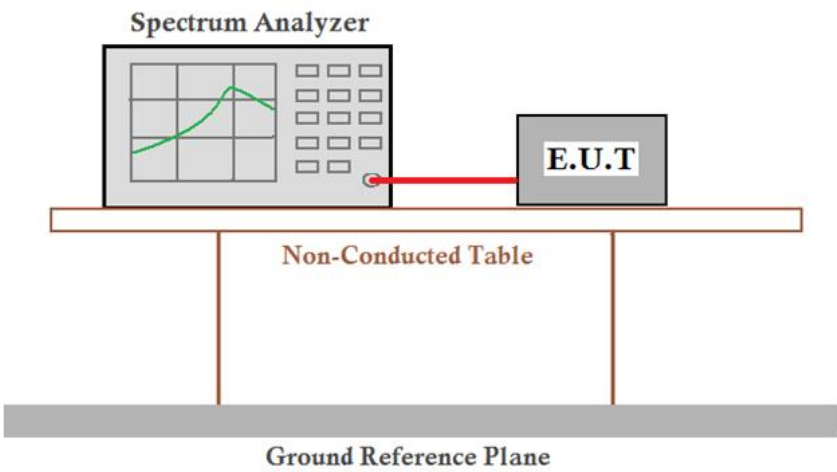


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4.6 Carrier Frequencies Separation

Test Requirement:	47 CFR Part 15C Section 15.247 (a)(1)
Test Method:	ANSI C63.10:2013 Section 7.8.2
Test Setup:	
Limit:	2/3 of the 20dB bandwidth
	Remark: the transmission power is less than 0.125W.
Exploratory Test Mode:	Hopping transmitting with all kind of modulation and all kind of data type.
Final Test Mode:	Through Pre-scan, find the DH5 of data type is the worst case of GFSK modulation type, 2-DH5 of data type is the worst case of $\pi/4$ DQPSK modulation type, 3-DH5 of data type is the worst case of 8DPSK modulation type.
Instruments Used:	Refer to section 5.10 for details
Test Results:	Pass



4.6.1 Test Results

GFSK mode			
Test channel	Carrier Frequencies Separation (kHz)	Limit (kHz)	Result
Middle	1001	613.4	Pass
$\pi/4$ DQPSK mode			
Test channel	Carrier Frequencies Separation (kHz)	Limit (kHz)	Result
Middle	1001	911.1	Pass
8DPSK mode			
Test channel	Carrier Frequencies Separation (kHz)	Limit (kHz)	Result
Middle	998	871.1	Pass

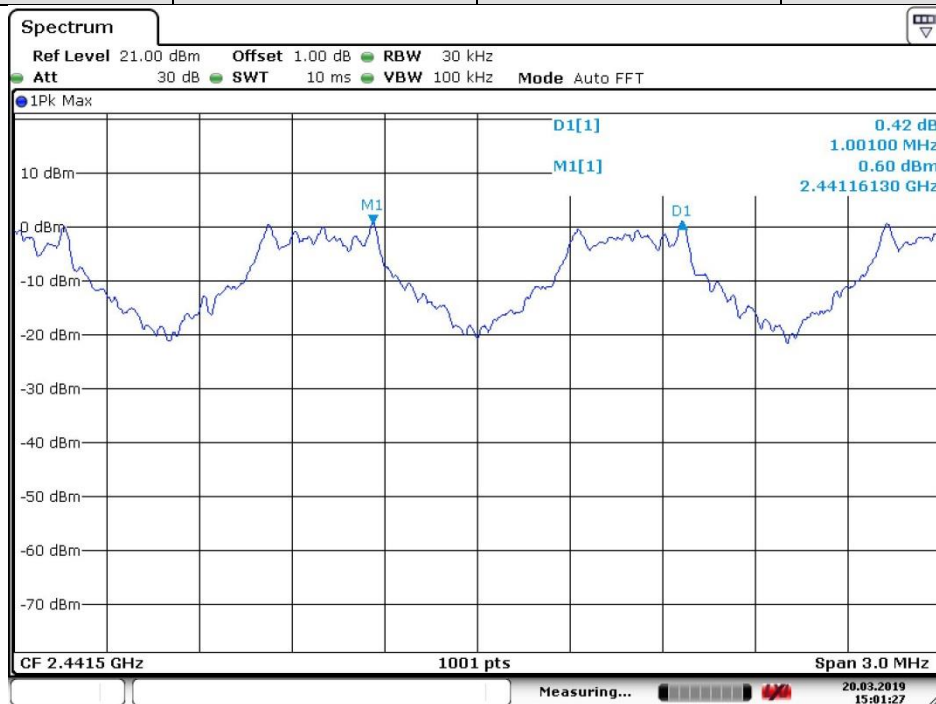
Remark: According to section 6.4,

Mode	20dB bandwidth (kHz) (worse case)	Limit (kHz) (Carrier Frequencies Separation)
GFSK	920.1	613.4
$\pi/4$ DQPSK	1366.6	911.1
8DPSK	1306.7	871.1



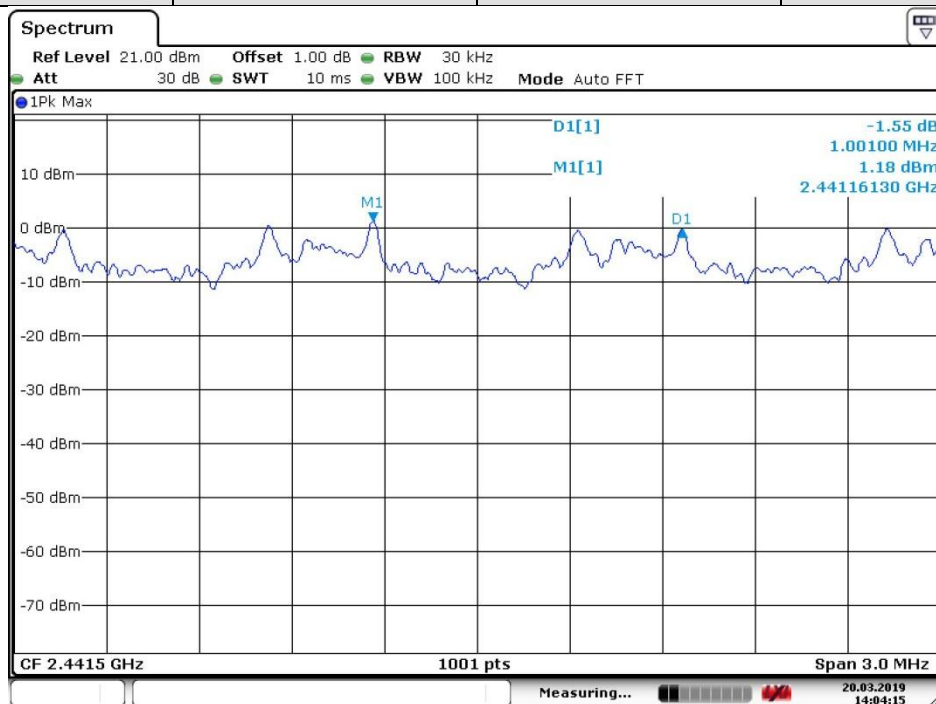
4.6.2 Test plots:

Test mode:	GFSK	Test channel:	Middle
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Test mode:	π /4DQPSK	Test channel:	Middle
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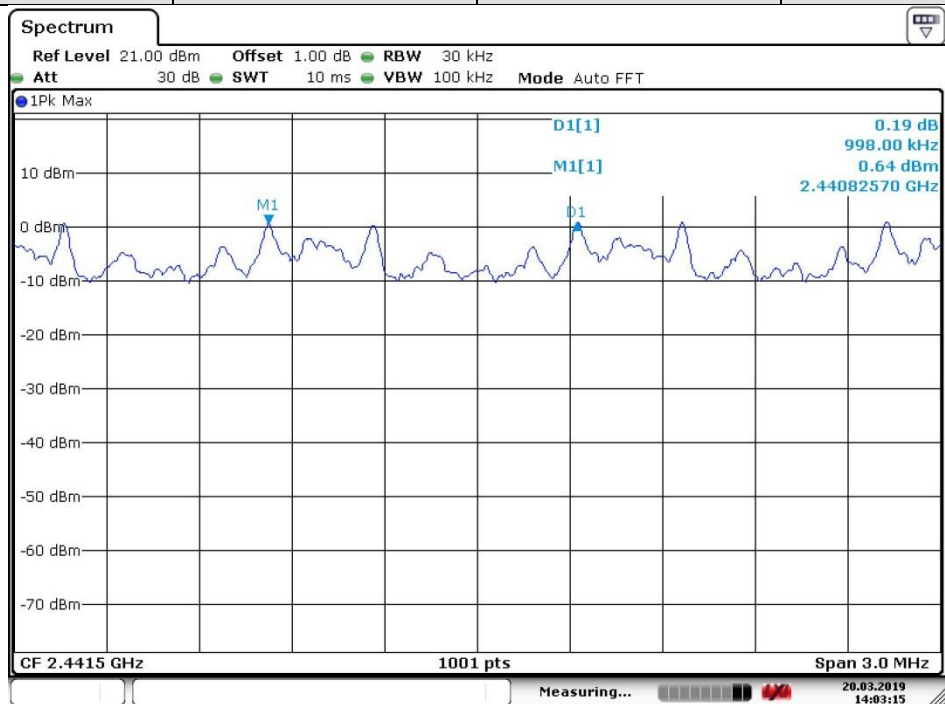
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Test mode:	8DPSK	Test channel:	Middle
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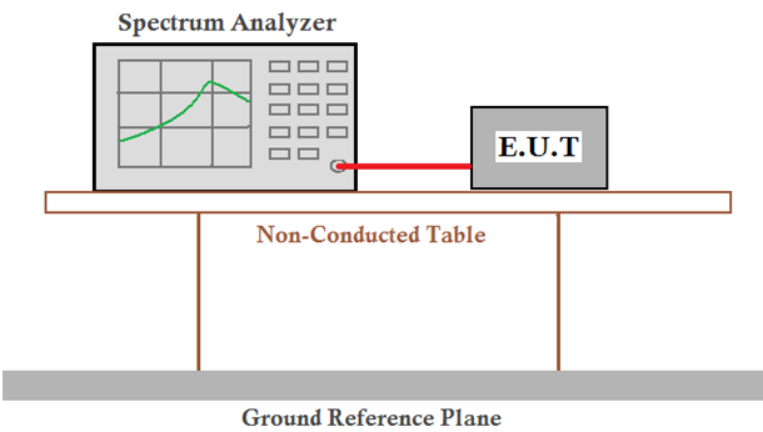
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4.7 Hopping Channel Number

Test Requirement:	47 CFR Part 15C Section 15.247 (a)(1)
Test Method:	ANSI C63.10:2013 Section 7.8.3
Test Setup:	
Limit:	At least 15 channels
Test Mode:	Hopping transmitting with all kind of modulation
Instruments Used:	Refer to section 5.10 for details
Test Results:	Pass

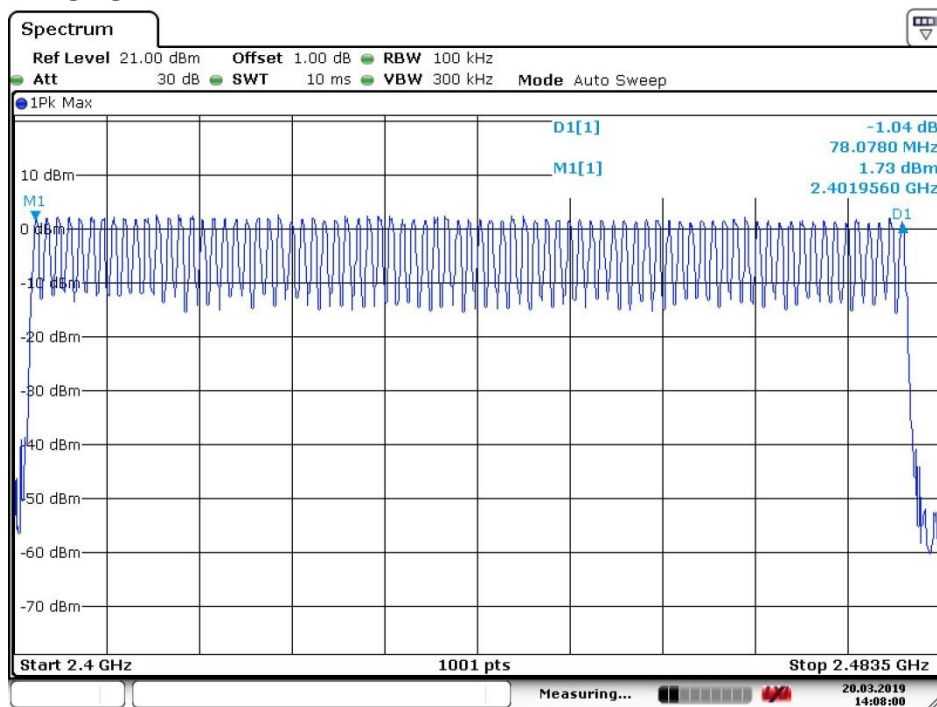
4.7.1 Test Results

Mode	Hopping channel numbers	Limit
GFSK	79	≥15
$\pi/4$ DQPSK	79	≥15
8DPSK	79	≥15

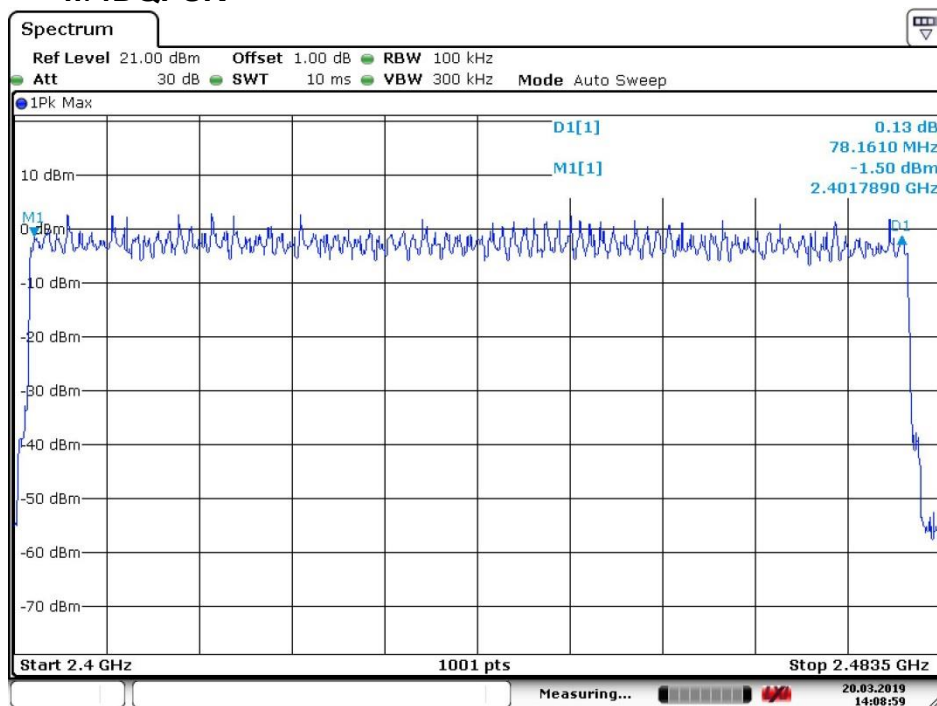


4.7.2 Test plots

4.7.2.1 GFSK



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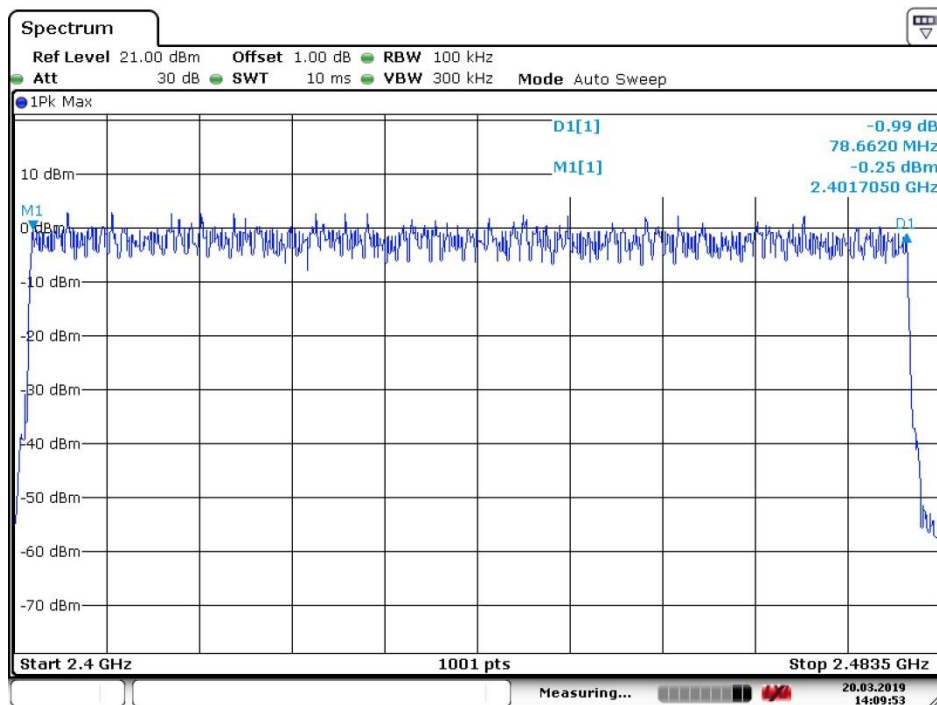
4.7.2.2 $\pi/4$ DQPSK

Date: 20. MAR 2019 14:09:00



4.7.2.3

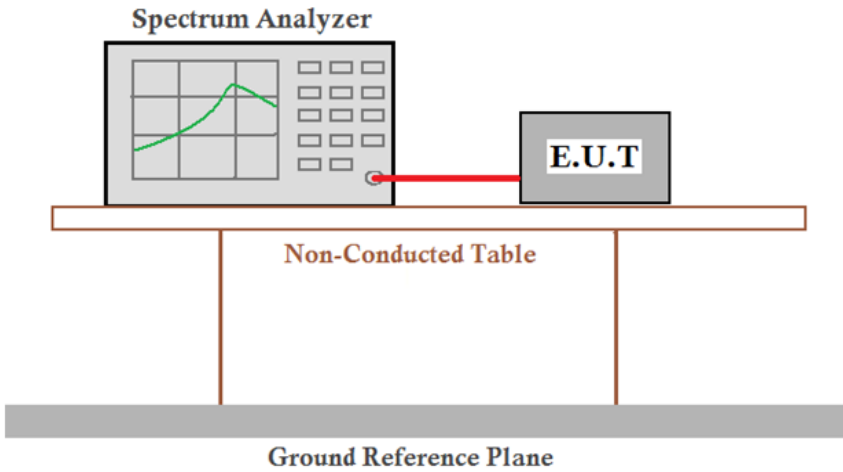
8DPSK



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4.8 Dwell Time

Test Requirement:	47 CFR Part 15C Section 15.247 (a)(1)
Test Method:	ANSI C63.10:2013 Section 7.8.4
Test Setup:	 <p>The diagram illustrates the test setup. A Spectrum Analyzer is connected to an E.U.T. (Equipment Under Test) via a red cable. Both the Spectrum Analyzer and the E.U.T. are placed on a Non-Conducted Table. Below the table is a Ground Reference Plane.</p>
Instruments Used:	Refer to section 5.10 for details
Test Mode:	Hopping transmitting with all kind of modulation and all kind of data type.
Limit:	0.4 Second
Test Results:	Pass



4.8.1 Test Results

Operation Modes	On time (ms) on one channel
DH1	0.411
DH3	1.671
DH5	2.925
2-DH1	0.420
2-DH3	1.683
2-DH5	2.925
3-DH1	0.421
3-DH3	1.677
3-DH5	2.925

Bluetooth Time of Occupancy Calculation

Typically, Bluetooth 1x/EDR mode has a channel hopping rate of 1600 hops/s, since 1x/EDR modes use 5 transmit and 1 receive slot, for a total of 6 slots, the Bluetooth transmitter is actually hopping at a rate of $1600/6=266.67$ hops/slot

$400\text{ms} \times 79 \text{ Channel} = 31.6 \text{ s}$ (Time of Occupancy Limit)

Worst case BT has 266.67 hops/second (for 1x/EDR modes with 1-DH5 operation)

$266.67 \text{ hops/second} / 79 \text{ channels} = 3.38 \text{ hops/second}$ (# of hops/second on one channel)

$3.38 \text{ hops/second} / \text{channel} \times 31.6 \text{ seconds} = 106.67 \text{ hops}$ (#hops over a 31.6 second period)

$106.67 \text{ hops} \times 2.925 \text{ ms/channel} = 312.01 \text{ ms}$ (worst case dwell time for one channel in 1x/EDR modes)

With AFH, the number of channels is reduced to a minimum of 20 channels and the channel hopping rate is reduced by 50% to 800hops/s, AFH mode also uses 6 slots so the Bluetooth transmitter hops at a rate of $800/6=133.3$ hops/s/slot

$400\text{ms} \times 20 \text{ Channel} = 8 \text{ s}$ (Time of Occupancy Limit)

Worst case BT has 133.3 hops/second/slot (for AFH mode with 1-DH5 operation)

$133.3 \text{ hops/second} / 20 \text{ channels} = 6.67 \text{ hops/second}$ (#hops/second on one channel)

$6.67 \text{ hops/second} \times 8 \text{ seconds} = 53.34 \text{ hops}$ (#hops over a 8 seconds period)

$53.34 \text{ hops} \times 2.925 \text{ ms/channel} = 156.02 \text{ ms}$ (worst case dwell time for one channel in AFH mode)



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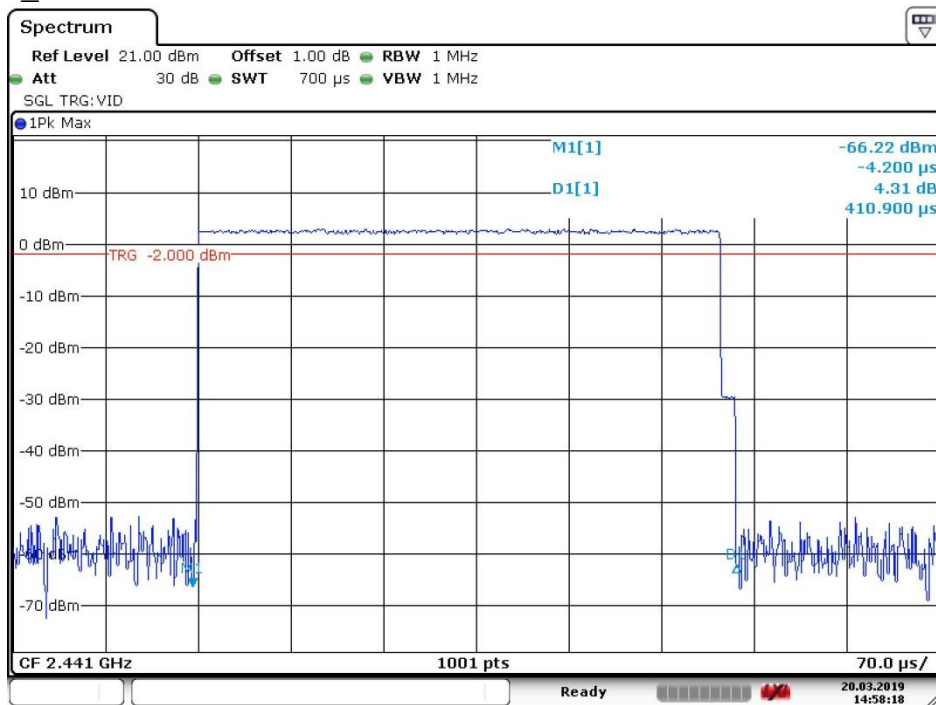
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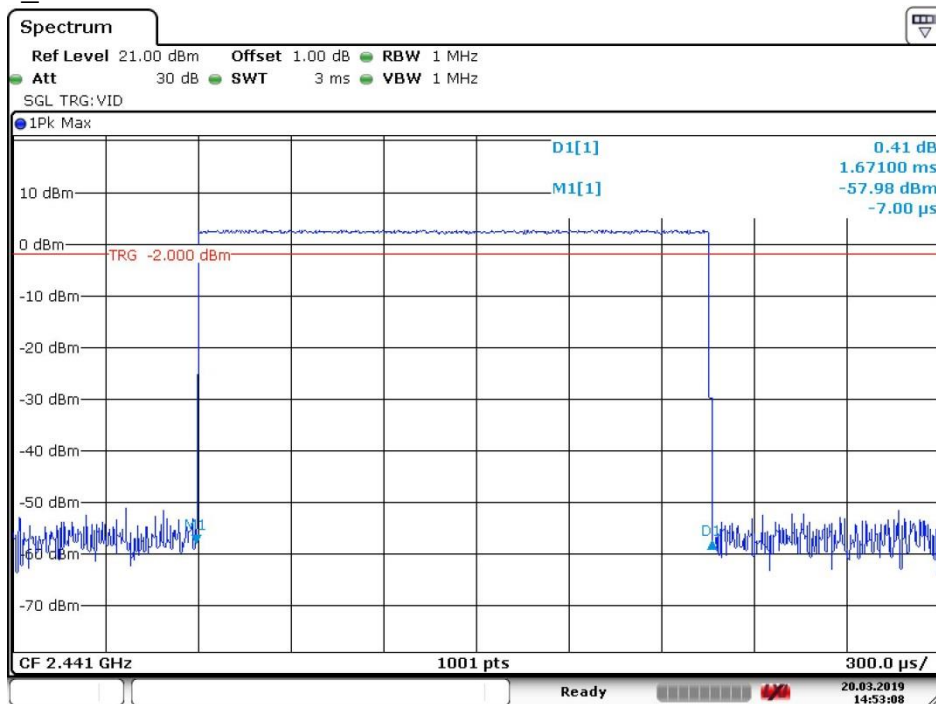
4.8.2 Test plots

4.8.2.1 DH1 _Middle Channel



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4.8.2.2 DH3 _Middle Channel



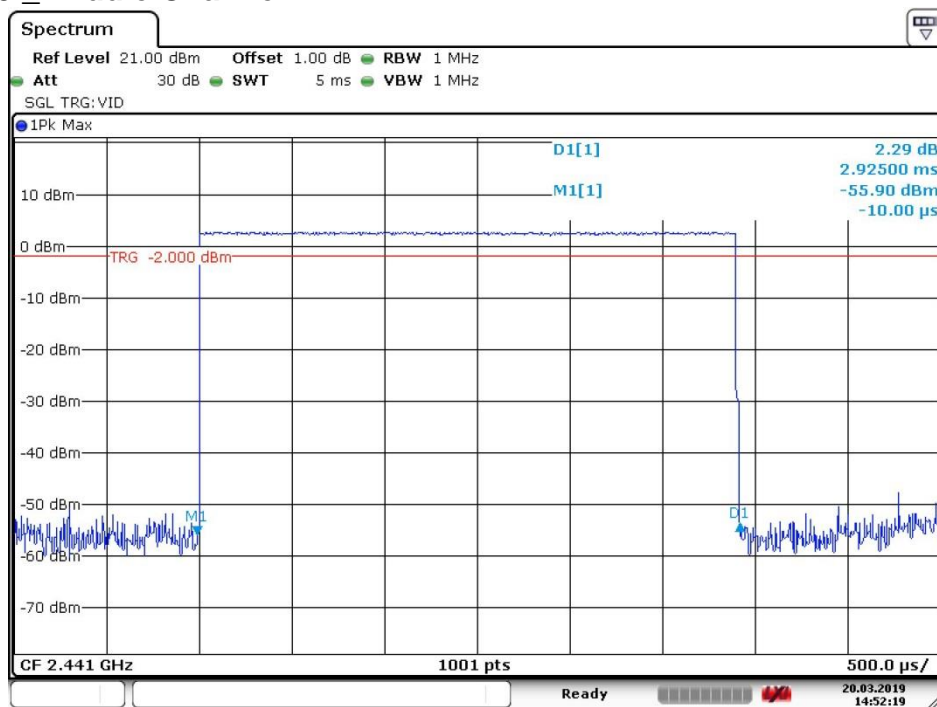
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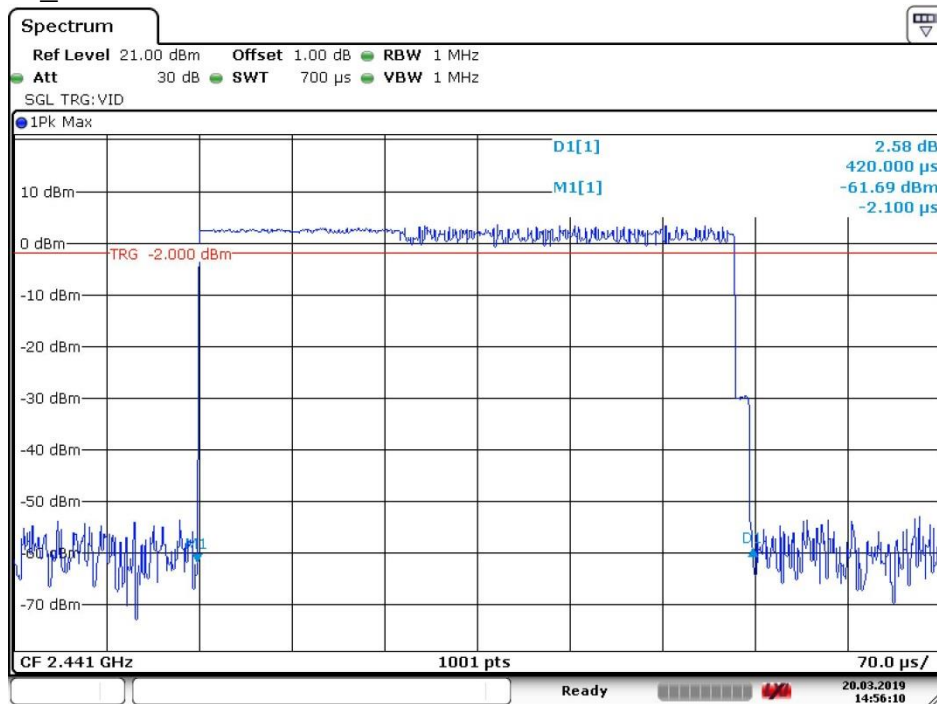
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4.8.2.3 DH5 _ Middle Channel



Date: 20.MAR.2019 14:52:20

4.8.2.4 2DH1 _Middle Channel



Date: 20.MAR.2019 14:56:10

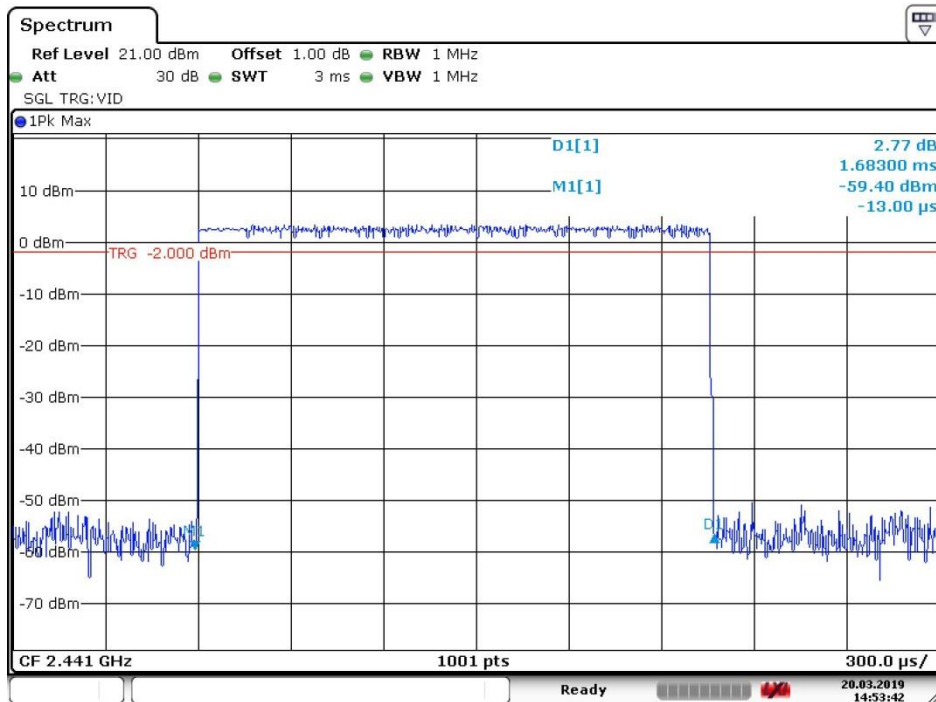


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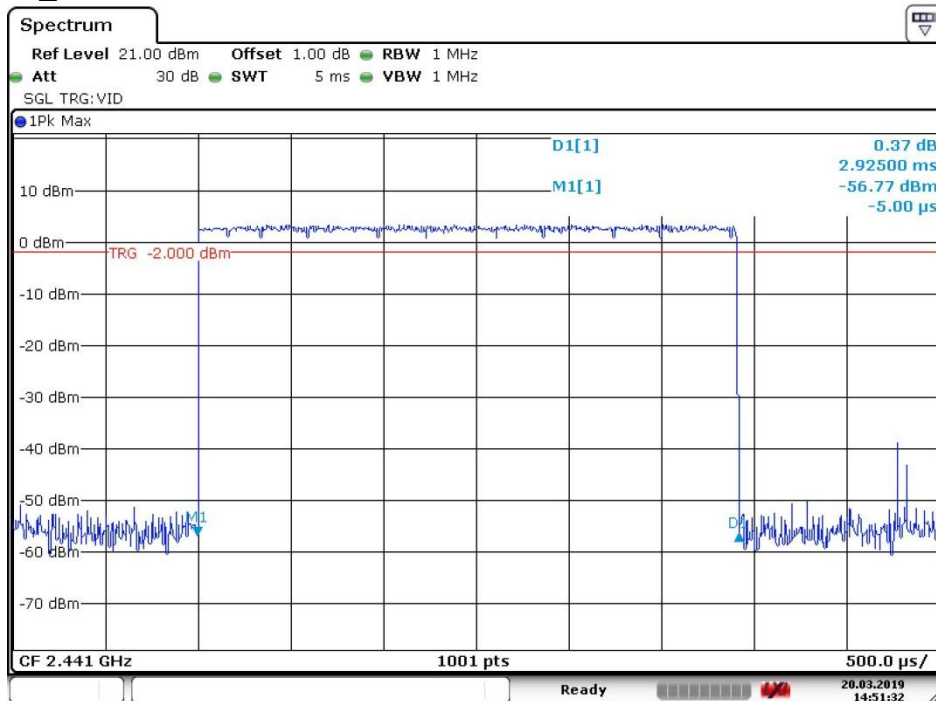
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4.8.2.5 2DH3 _ Middle Channel



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4.8.2.6 2DH5 _ Middle Channel



Date: 20.MAR.2019 14:51:32



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