Calculation of power density

The following calculation is based on the ANSI IEEE C95.1-1991 method, as that provides a worst case analysis. Details of the assessment to EN50383:2002 can be provided, if required.

Peak power density in the far field of a radio frequency point source is calculated as follows:

$$S = \frac{P.G}{4\pi d^2}$$
Where:
$$S$$

$$S$$

$$P$$

$$maximum average transmit power capability of the radio, in W
$$G$$

$$total Tx gain as a factor, converted from dB
$$d$$

$$d$$

$$d$$

$$distance from point source, in m$$$$$$

Rearranging terms to solve for distance yields:

$$d = \sqrt{\frac{P.G}{4\pi . S}}$$

Calculated distances and power compliance margins

Table 92 and Table 93 shows calculated minimum separation distances, recommended distances and resulting margins for each frequency band and antenna combination for the USA and Canada. These are conservative distances that include compliance margins. At these and greater separation distances, the power density from the RF field is below generally accepted limits for the general population.

450 Platform Family ODU adheres to all applicable EIRP limits for transmit power when operating in MIMO mode. Separation distances and compliance margins include compensation for both transmitters.

Explanation of terms used in Table 92 and Table 93:

P burst - maximum average transmit power during transmit burst (Watt)

P – maximum average transmit power of the radio (Watt)

G - total transmit gain as a factor, converted from dB

S – power density (Watt/m2)

d – minimum safe separation distance from point source (meters)

Table 89 FCC minimum safe distances – PMP 450m 5.1 GHz, 5.2 GHz, 5.4 GHz and 5.8 GHz

Band (GHz)	Antenna	PG (W)	S (W/ m ₂)	d (m)
5.1	90° sector	3.38	10	0.16
5.2	90° sector	0.85	10	0.08
5.4	90° sector	0.85	10	0.08
5.8	90° sector	3.38	10	0.16

Table 90 FCC minimum safe distances – PMP/PTP 450b 4.9 GHz, 5.1 GHz, 5.2 GHz, 5.4 GHz and 5.8 GHz

Band (GHz)	Channel Size (MHz)	Antenna	P (W)	G (dBi)	S (W/ m ₂)	d (m)
4.9		Dish	0.110	24	10	0.47
		Patch Array	0.501	17	10	0.45
		On-board	0.548	2	10	0.08
5.1		Dish	0.110	24	10	0.47
		Patch Array	0.501	17	10	0.45
		On-board	0.548	2	10	0.08
5.2		Dish	0.040	24	10	0.28
	5	Patch Array	0.020	17	10	0.09
		On-board	0.548	2	10	0.08
5.4		Dish	0.040	24	10	0.28
		Patch Array	0.020	17	10	0.09
		On-board	0.548	2	10	0.08
5.8		Dish	0.110	24	10	0.47
		Patch Array	0.501	17	10	0.45
		On-board	0.548	2	10	0.08
4.9	40 -	Dish	0.110	24	10	0.47
		Patch Array	0.501	17	10	0.45
		On-board	0.550	2	10	0.08
5.1		Dish	0.110	24	10	0.47
		Patch Array	0.501	17	10	0.45
		On-board	0.550	2	10	0.08
5.2		Dish	0.040	24	10	0.28
		Patch Array	0.020	17	10	0.09
		On-board	0.550	2	10	0.08
5.4		Dish	0.040	24	10	0.28
		Patch Array	0.020	17	10	0.09
		On-board	0.550	2	10	0.08
5.8		Dish	0.110	24	10	0.47
		Patch Array	0.501	17	10	0.45