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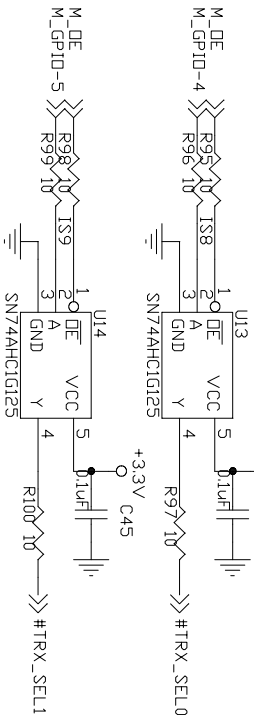
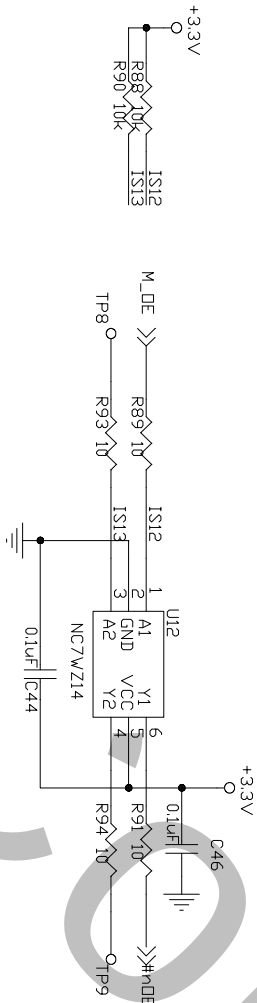
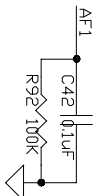
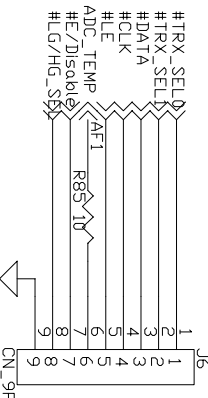
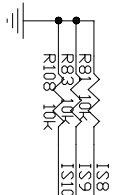
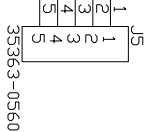
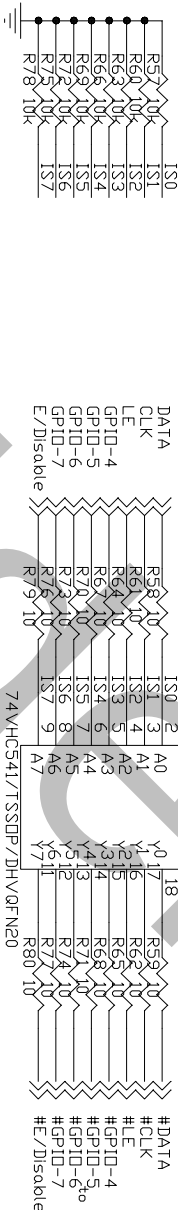
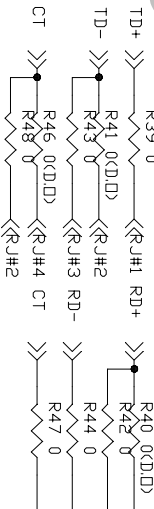
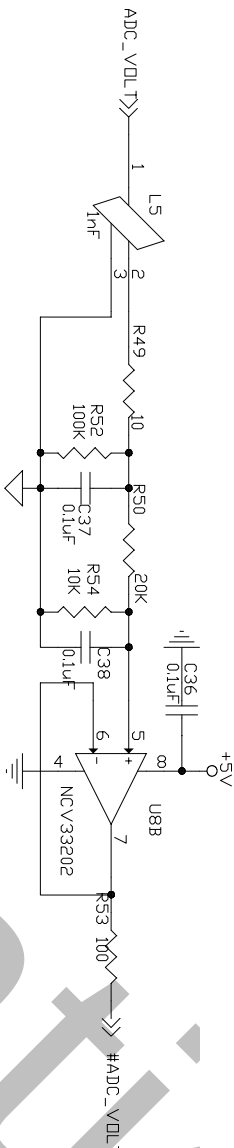
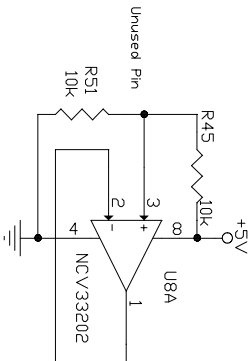
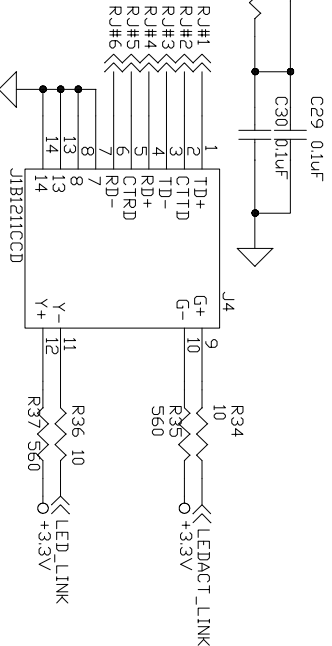
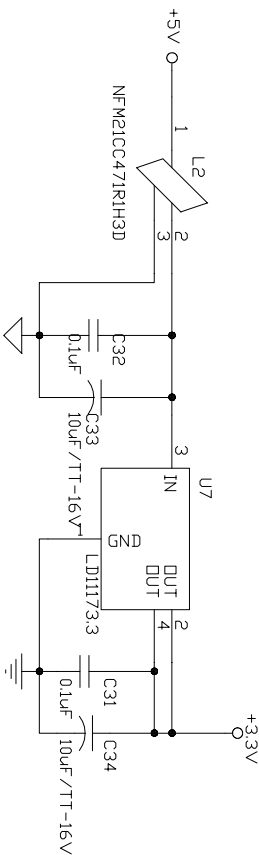
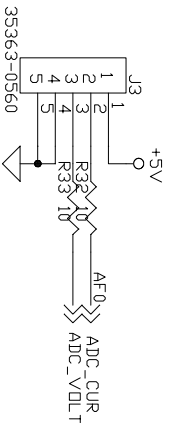
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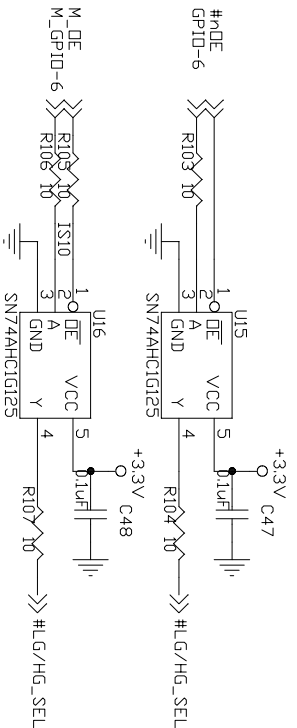
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to Connector



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A1 (841x594)

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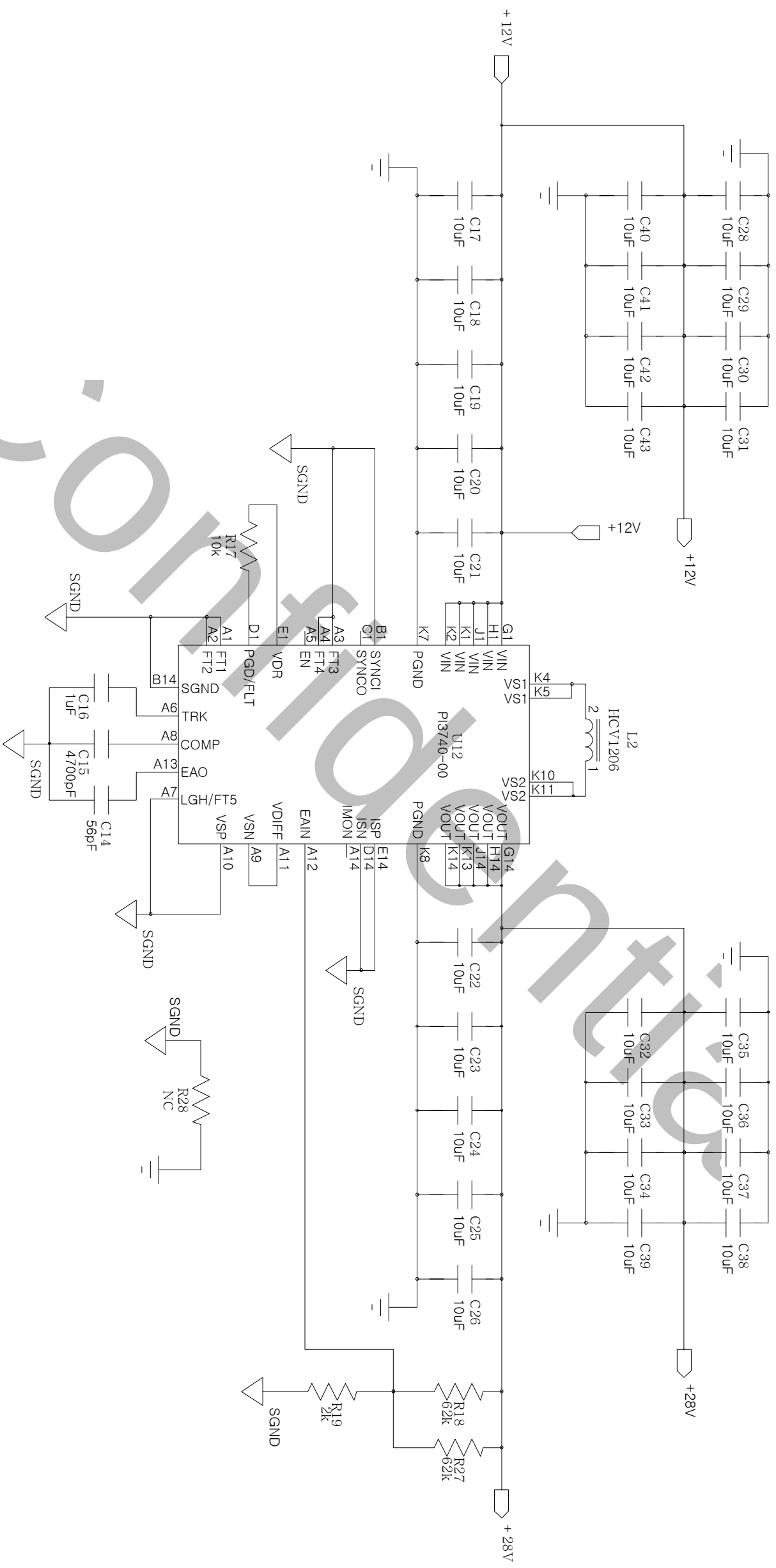
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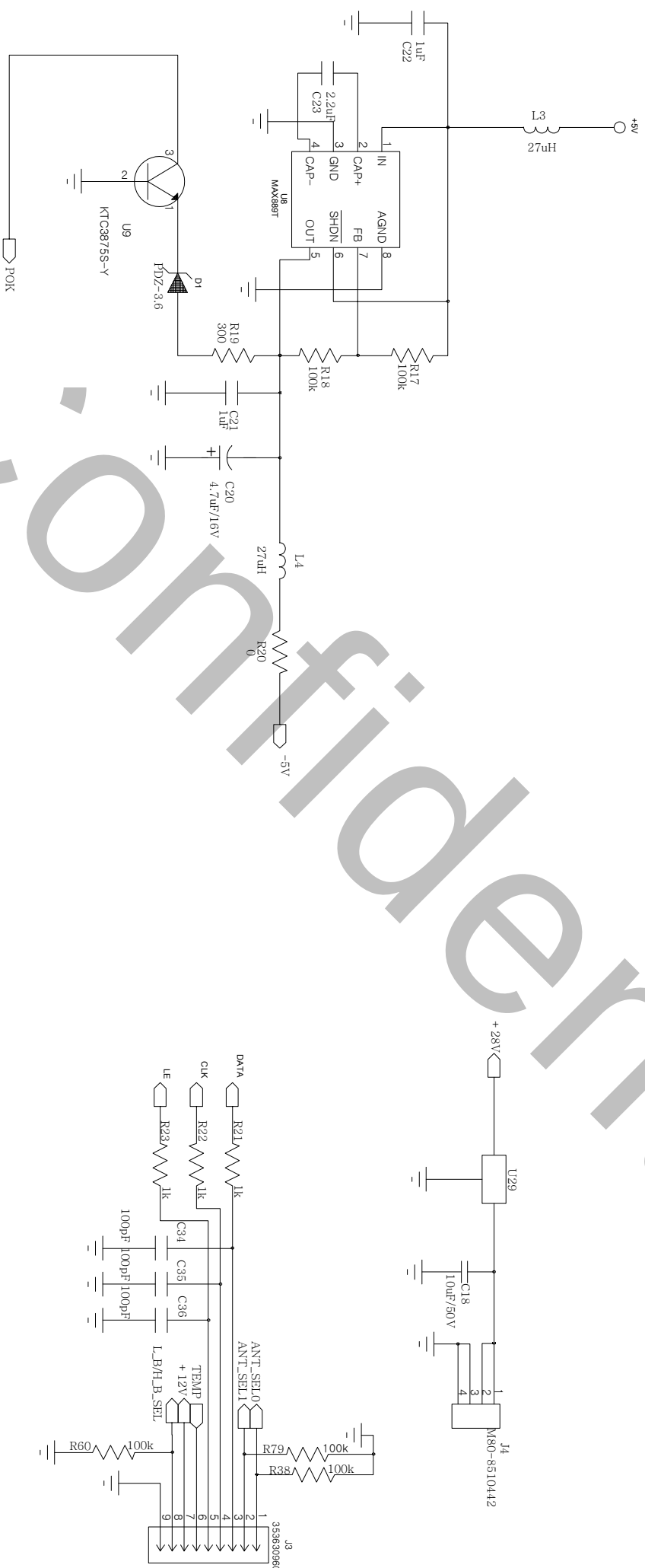
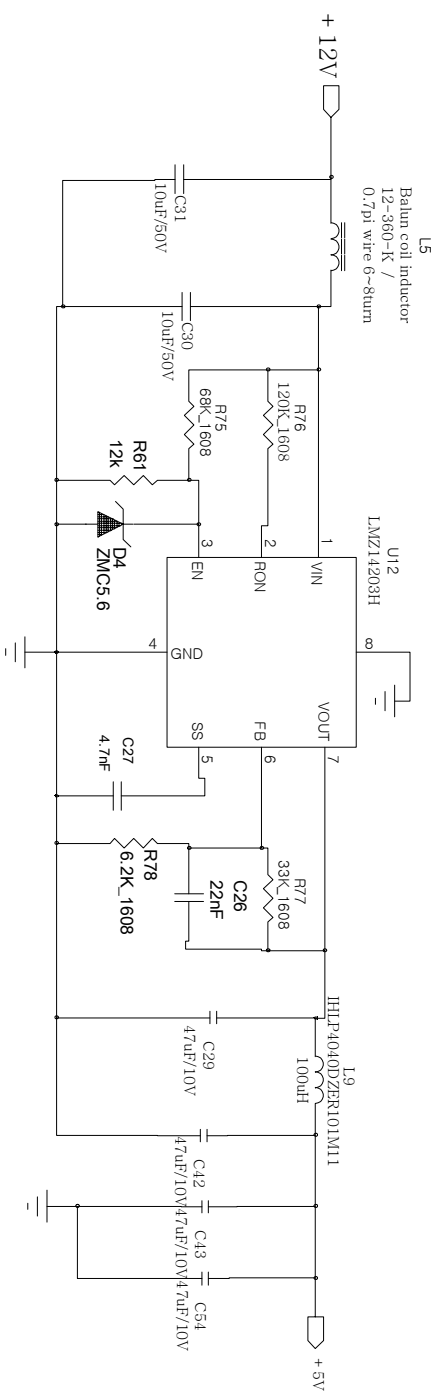
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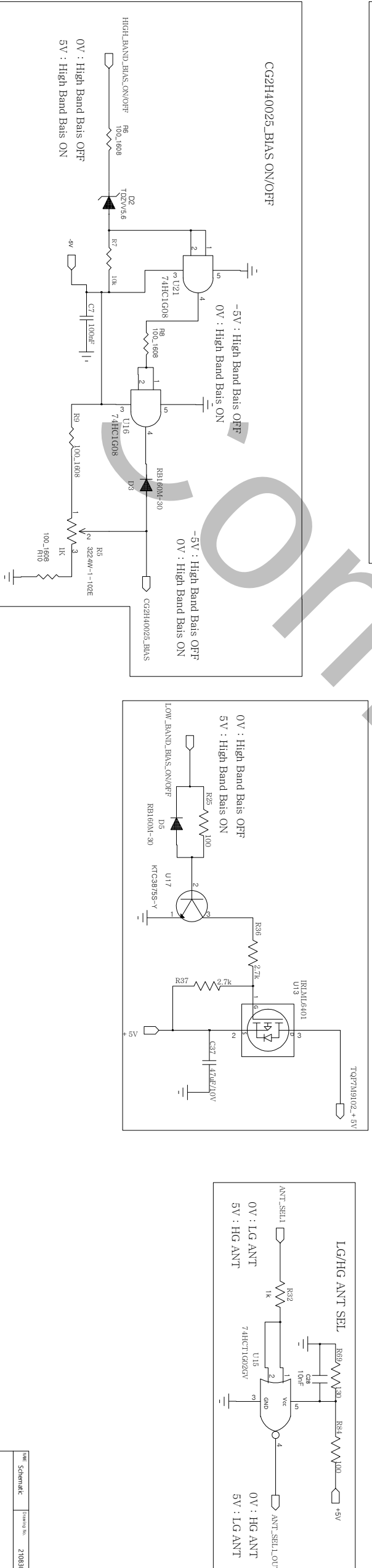
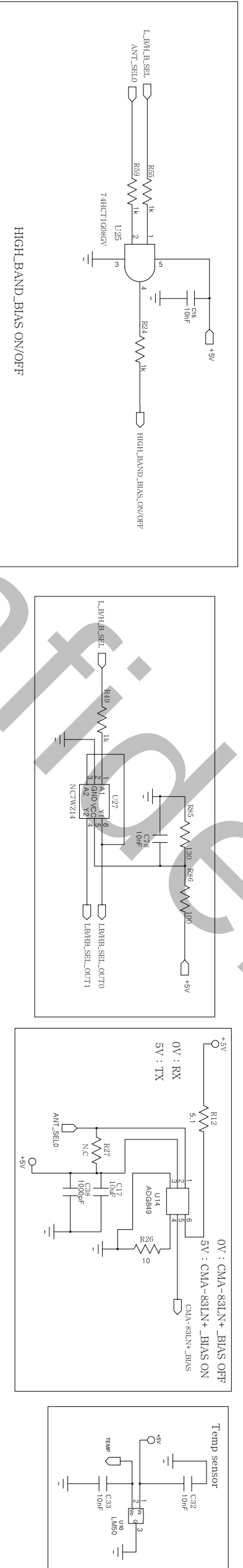
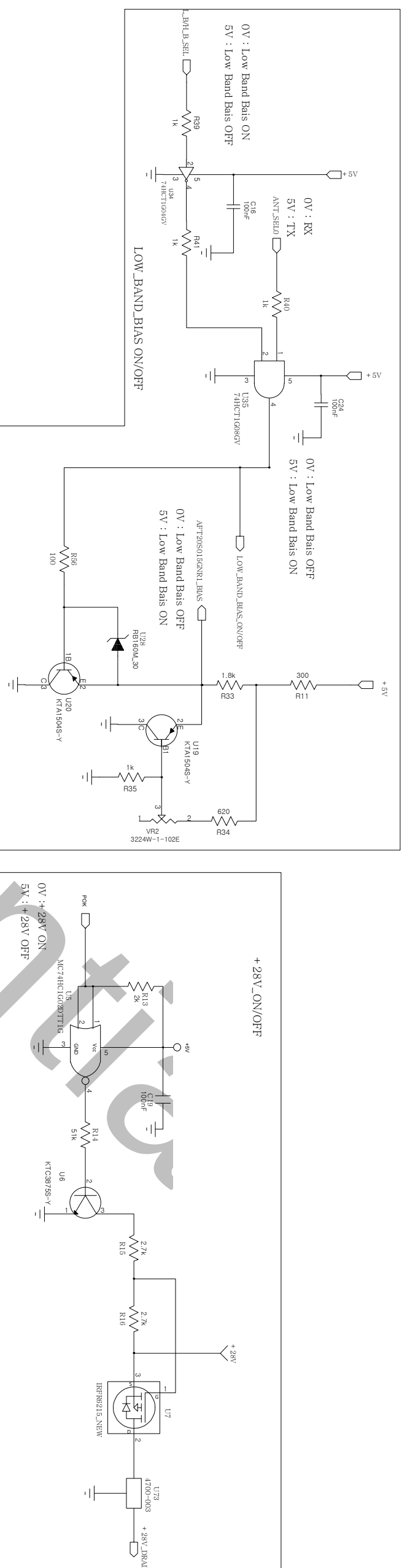
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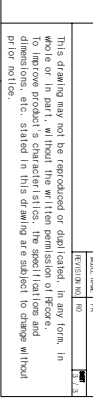
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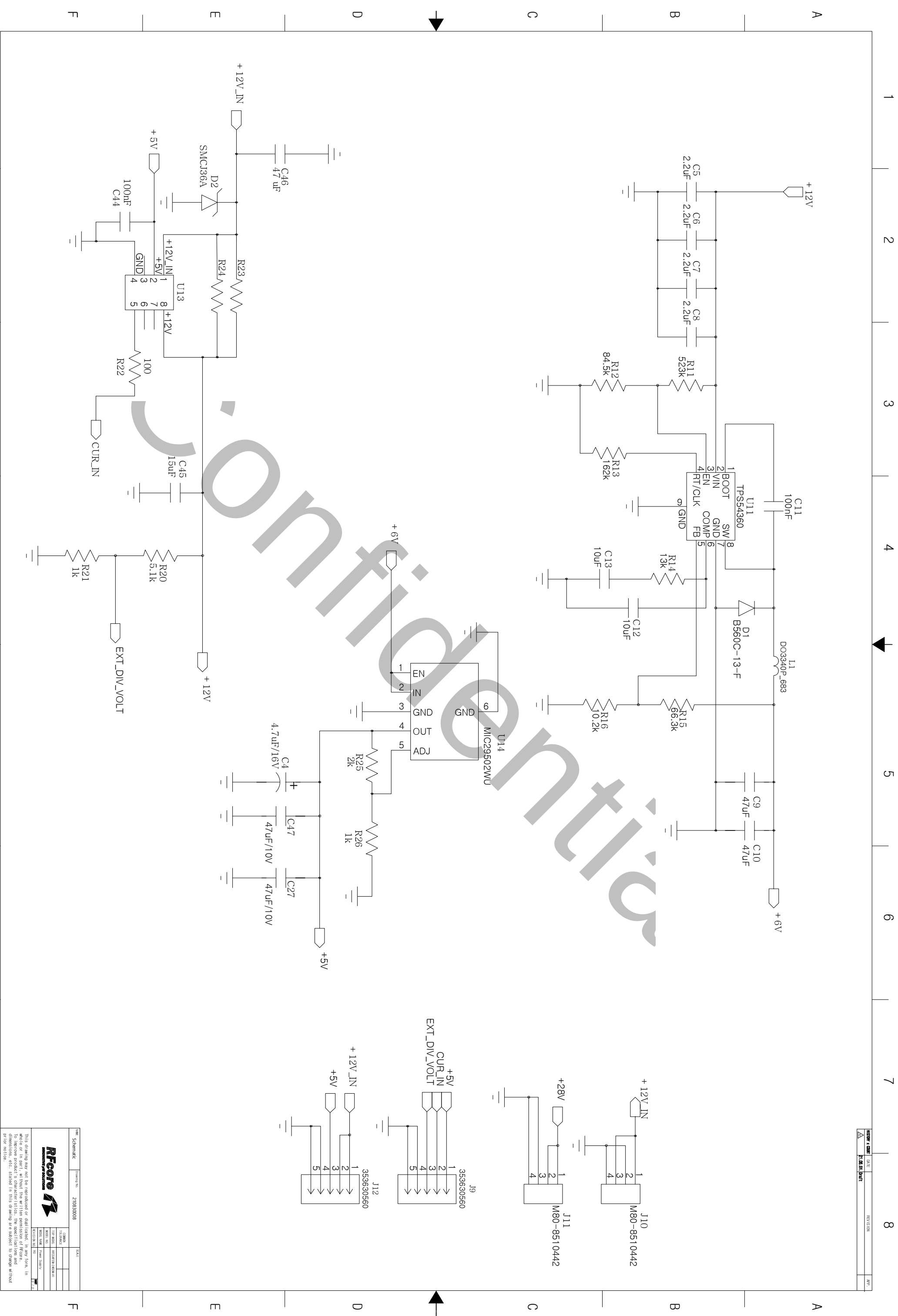


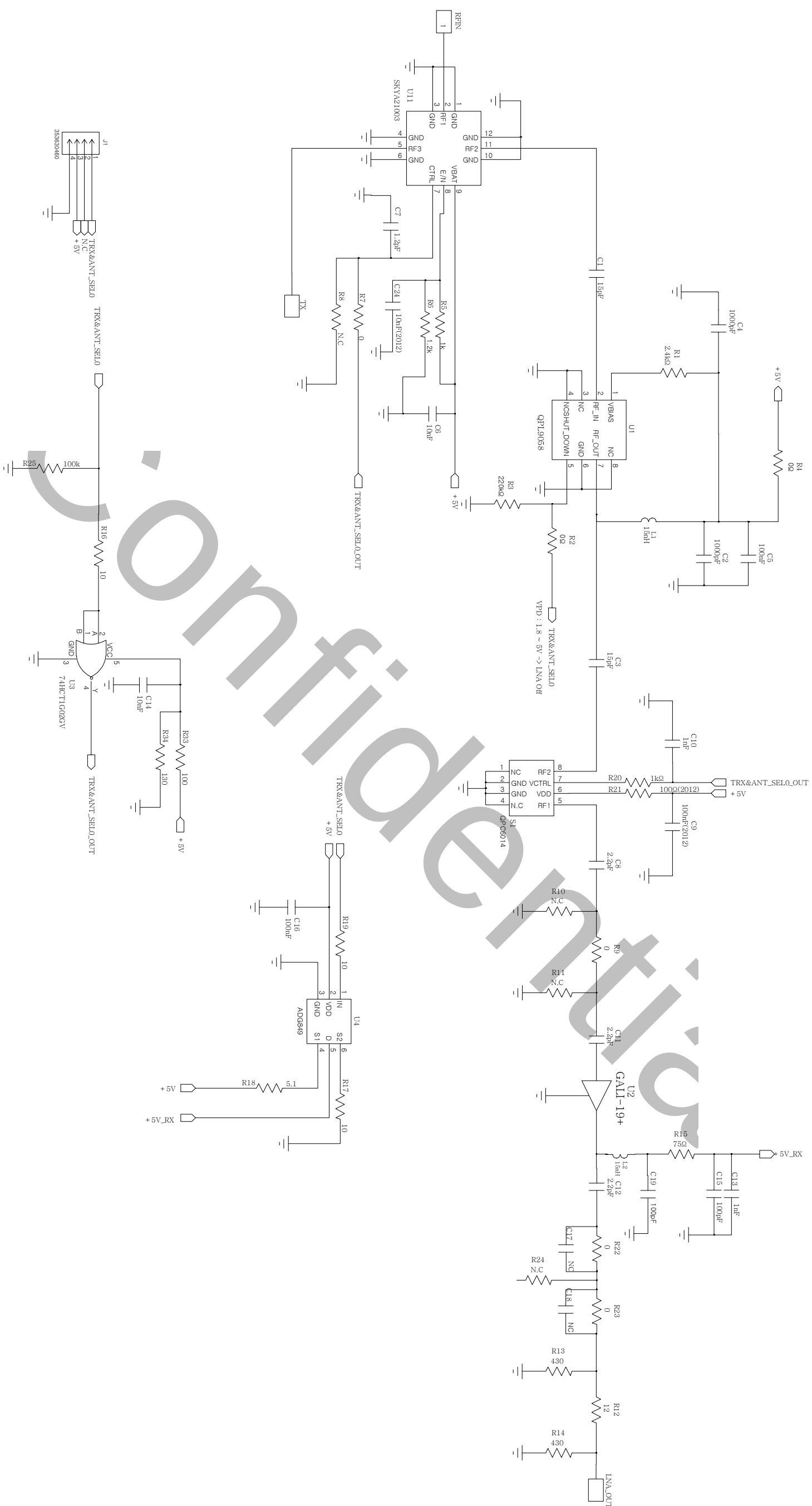




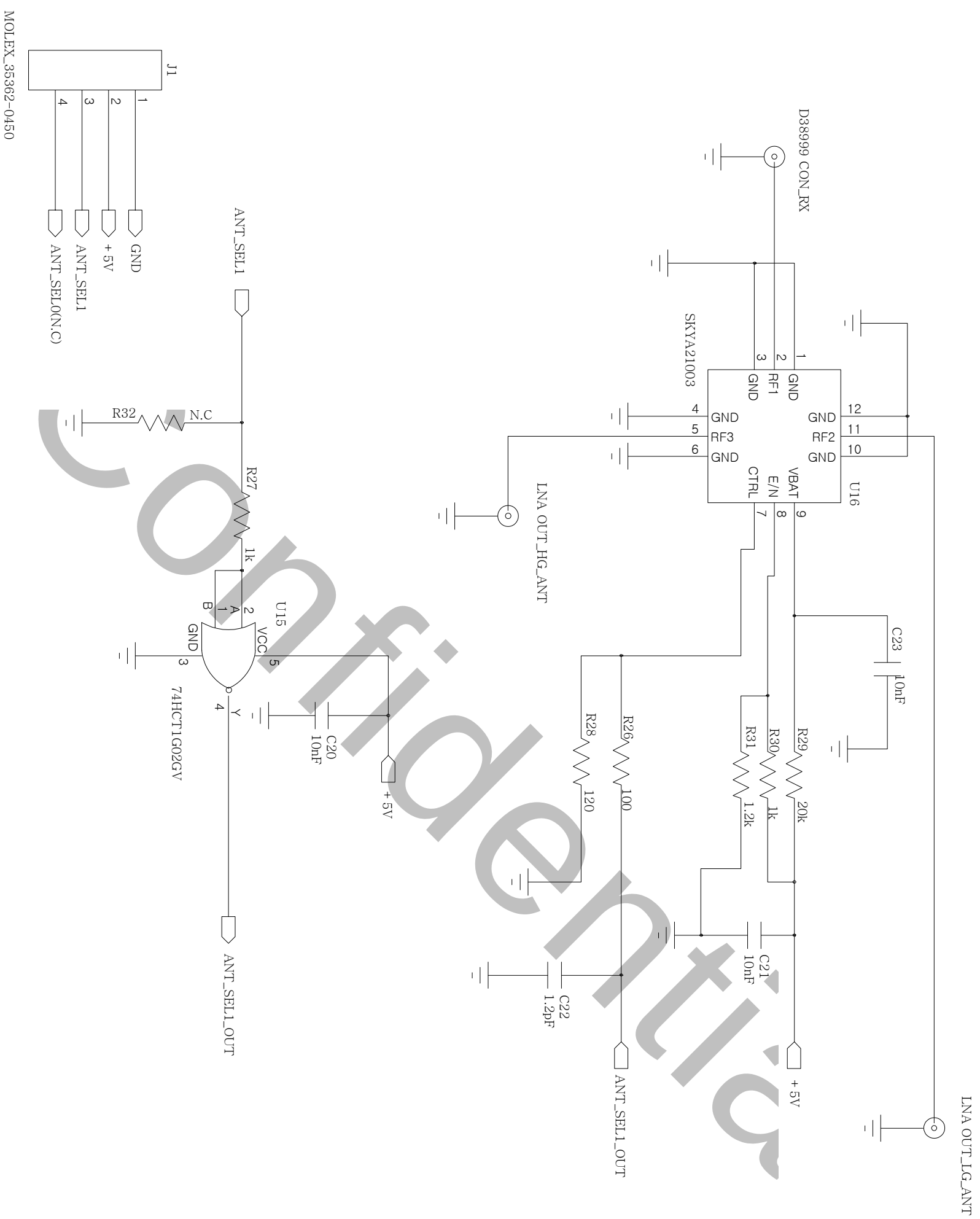








HISTORY x COUNT	DATE	REVISION	APP.
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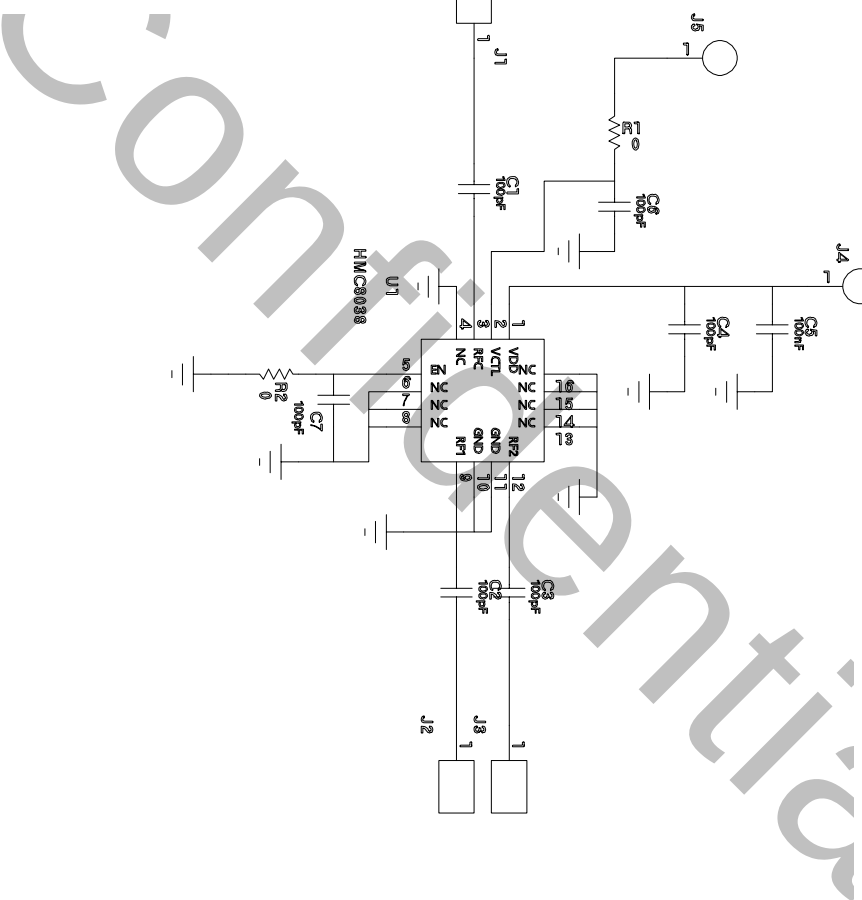


HISTORY x COUNT	DATE	REVISION	APP.
 21.06.01. Draft			

NAME	Schematic		Drawing No.	210830031	DATE	
COMMA						
TOL PENCE						
TYPE MODEL	PFC0410-24000-A1					
BOOK NO.						
MARK NAME	SPOT					
REVISION NO.	NO					
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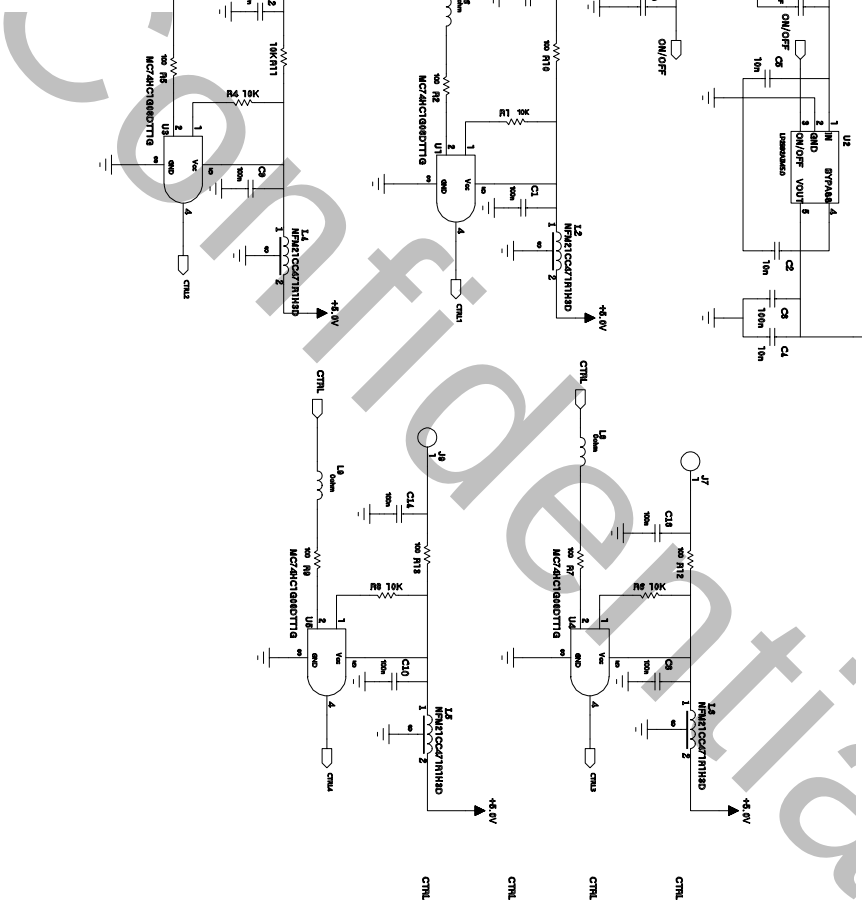
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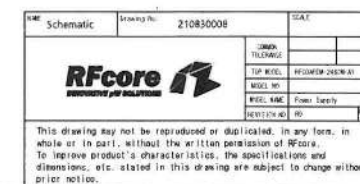


 RFcore <i>accelerating your solutions</i>	WAL Schenectady Created On: 2/10/2003	TOTAL
CHANGE TOTALS TYPE: 0001 REASON: 00000004 EFFECT: 00 REASON: 0000 TYPE: 00		

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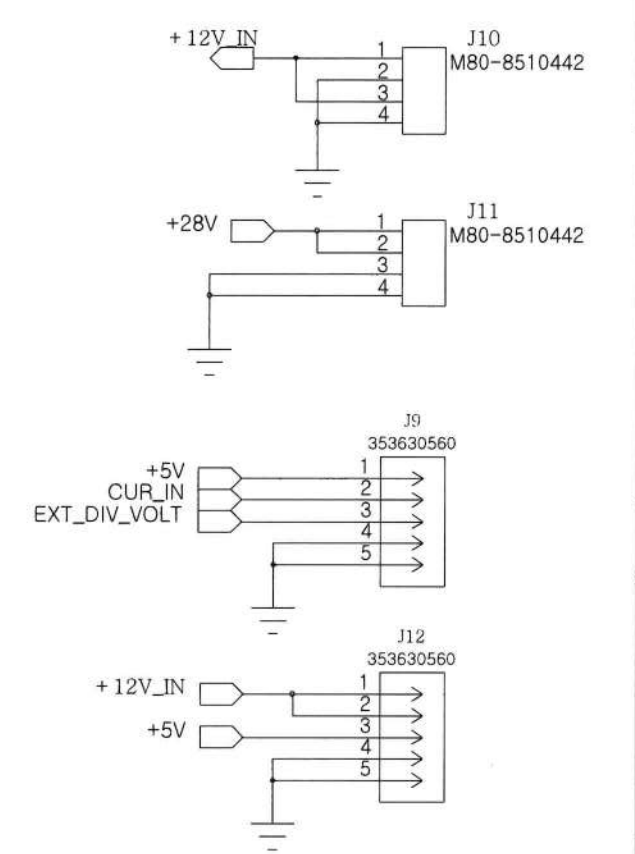
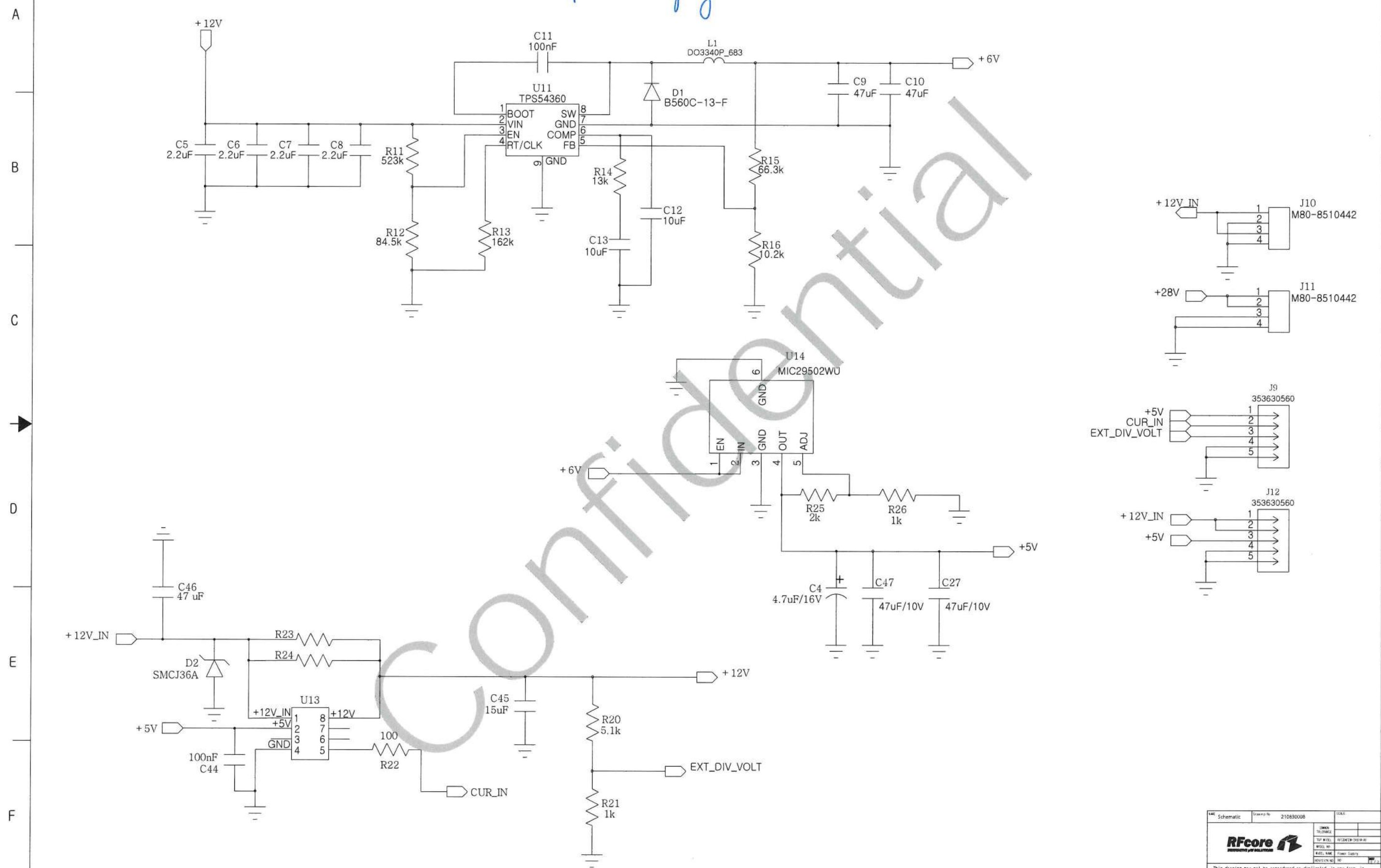
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Power Supply

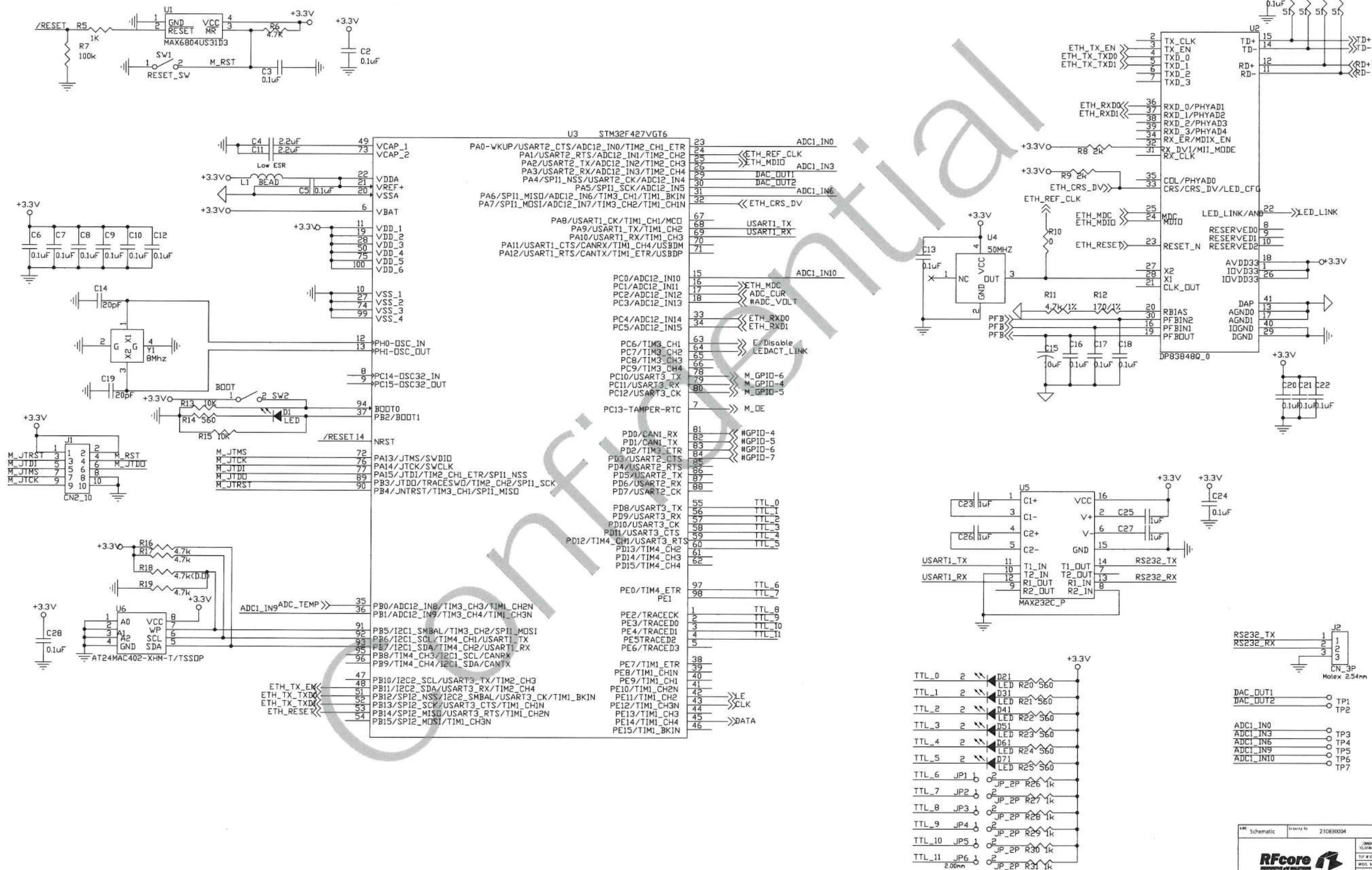
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DESIGNER		APP.	



NAME	Schematic	Drawing No	21080008	SCALE	
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LEVEL NAME	Power Supply	REVISION NO	01		

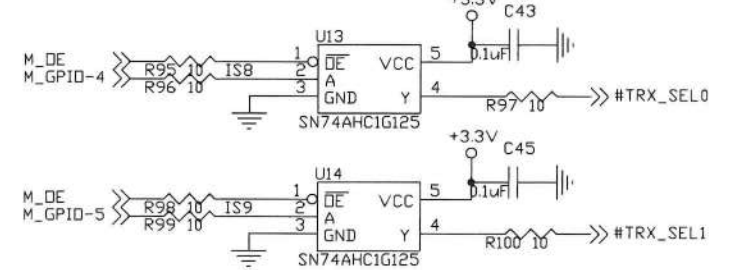
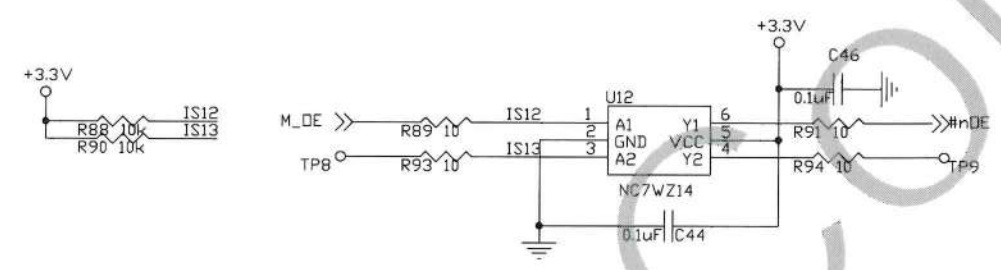
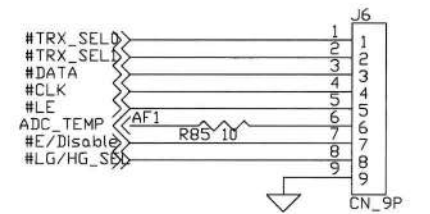
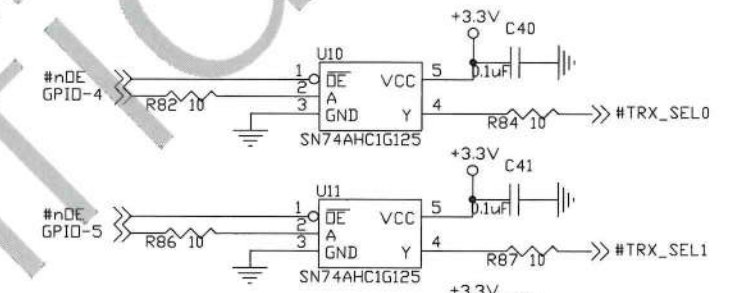
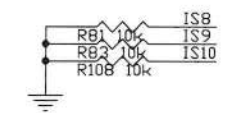
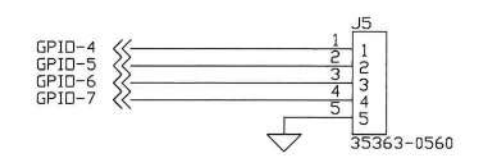
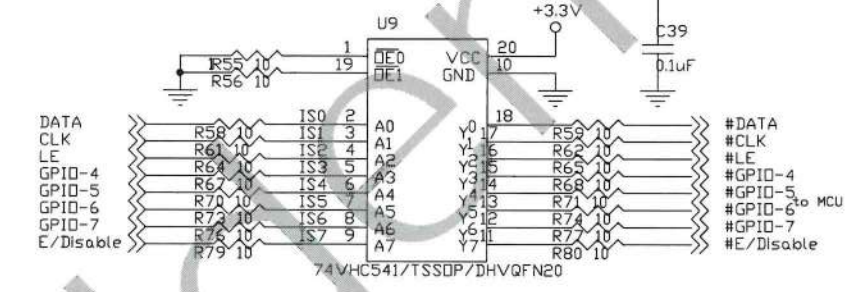
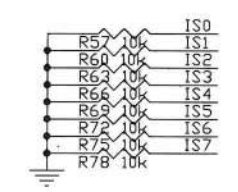
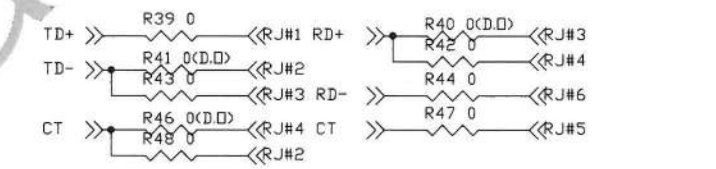
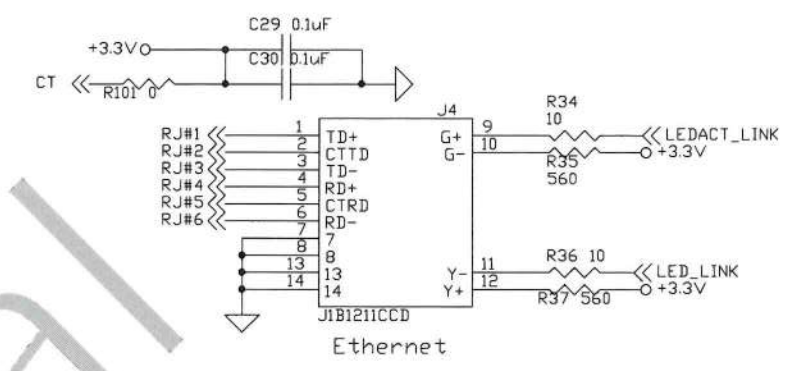
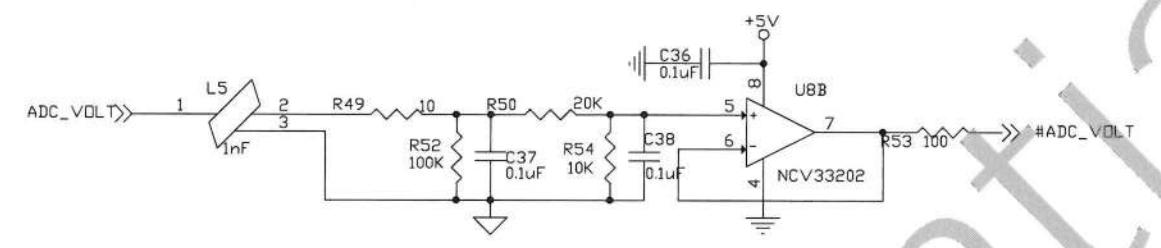
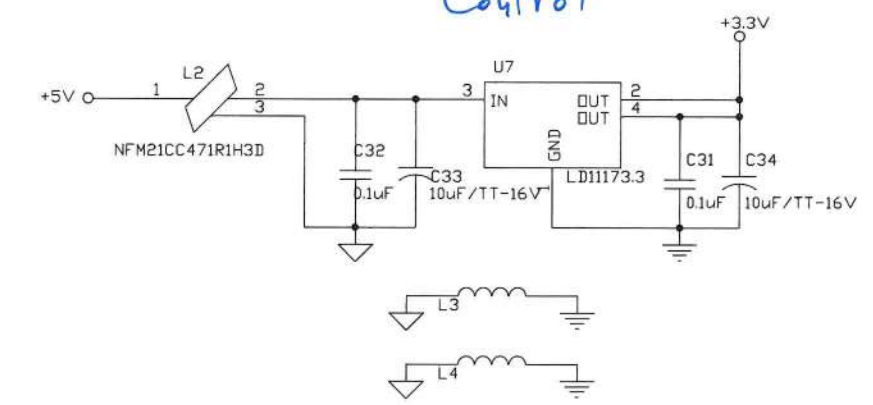
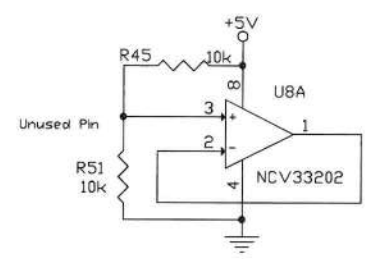
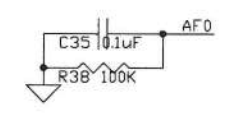
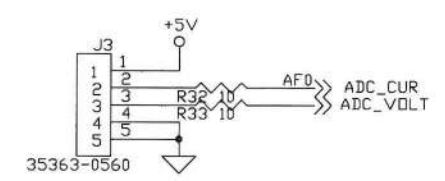
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Control Board

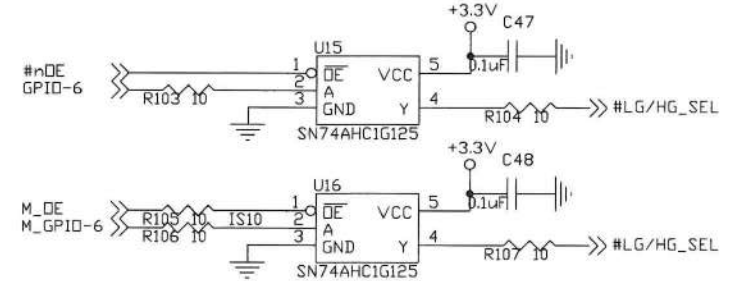


Control

REV	DATE	DESCRIPTION	APP.
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to Connector



Rev	1	210830004	SCALE
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TYP. # 002			
TYP. # 003			
TYP. # 004			
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TYP. # 099			
TYP. # 100			

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Switch Distribution Board

REV	DATE	BY	CHK
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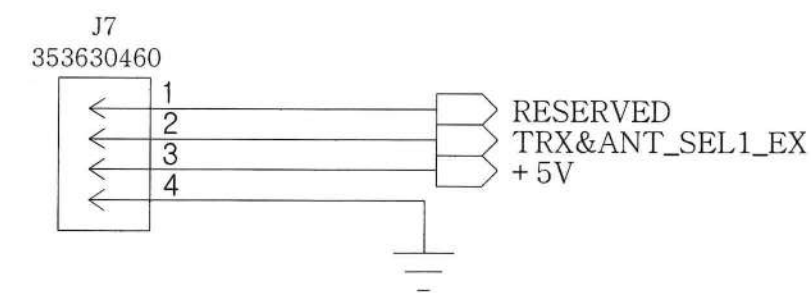
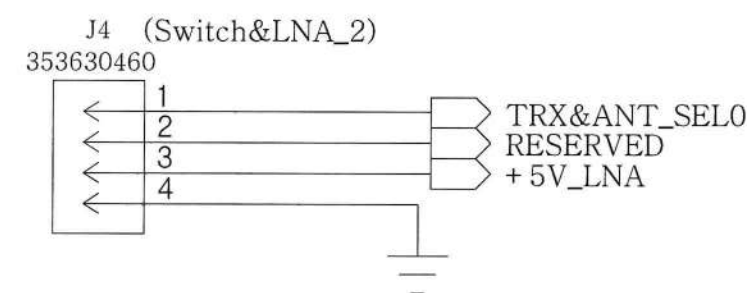
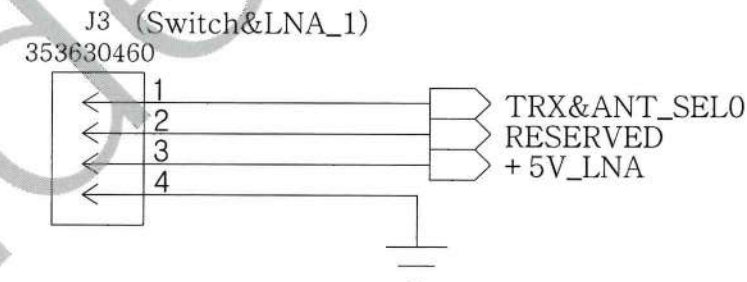
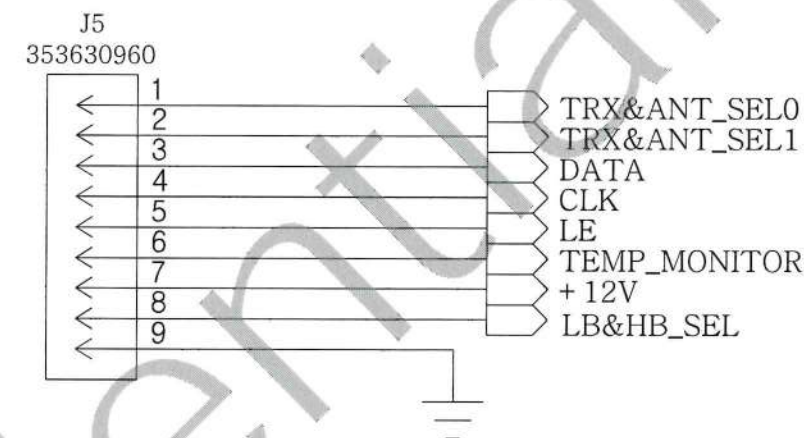
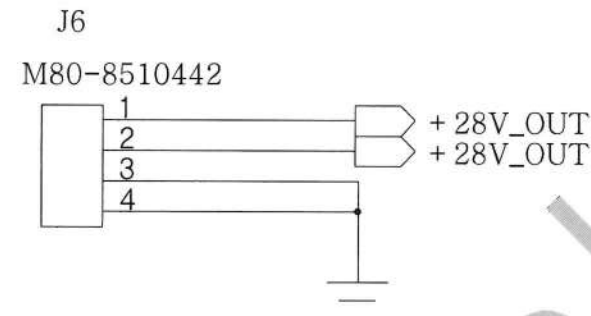
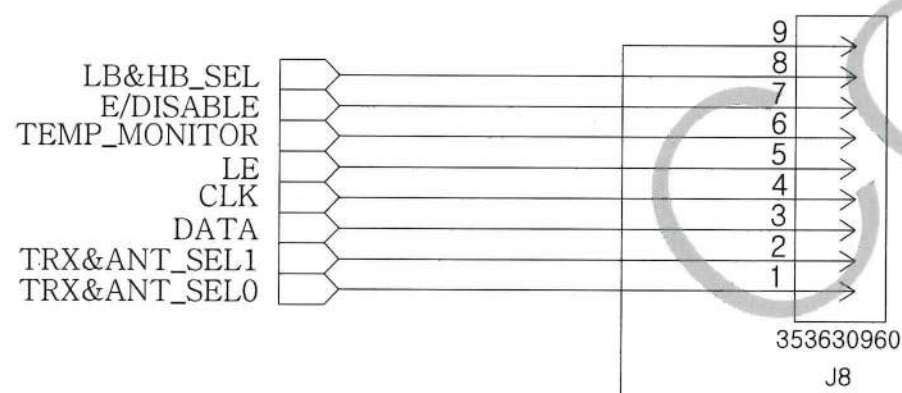
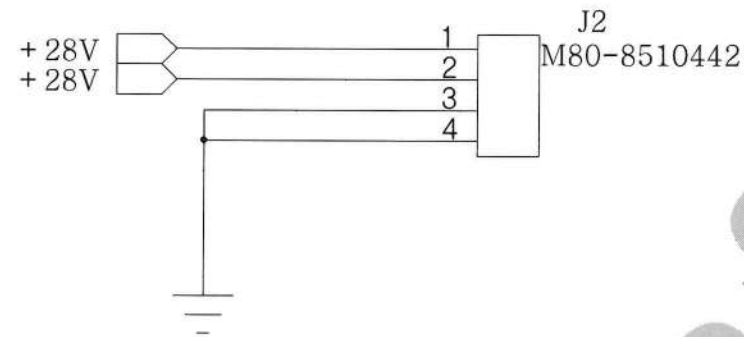
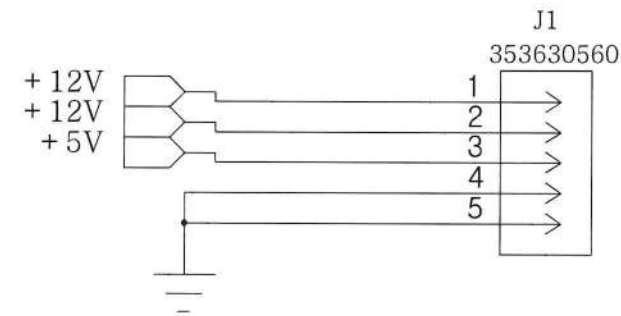
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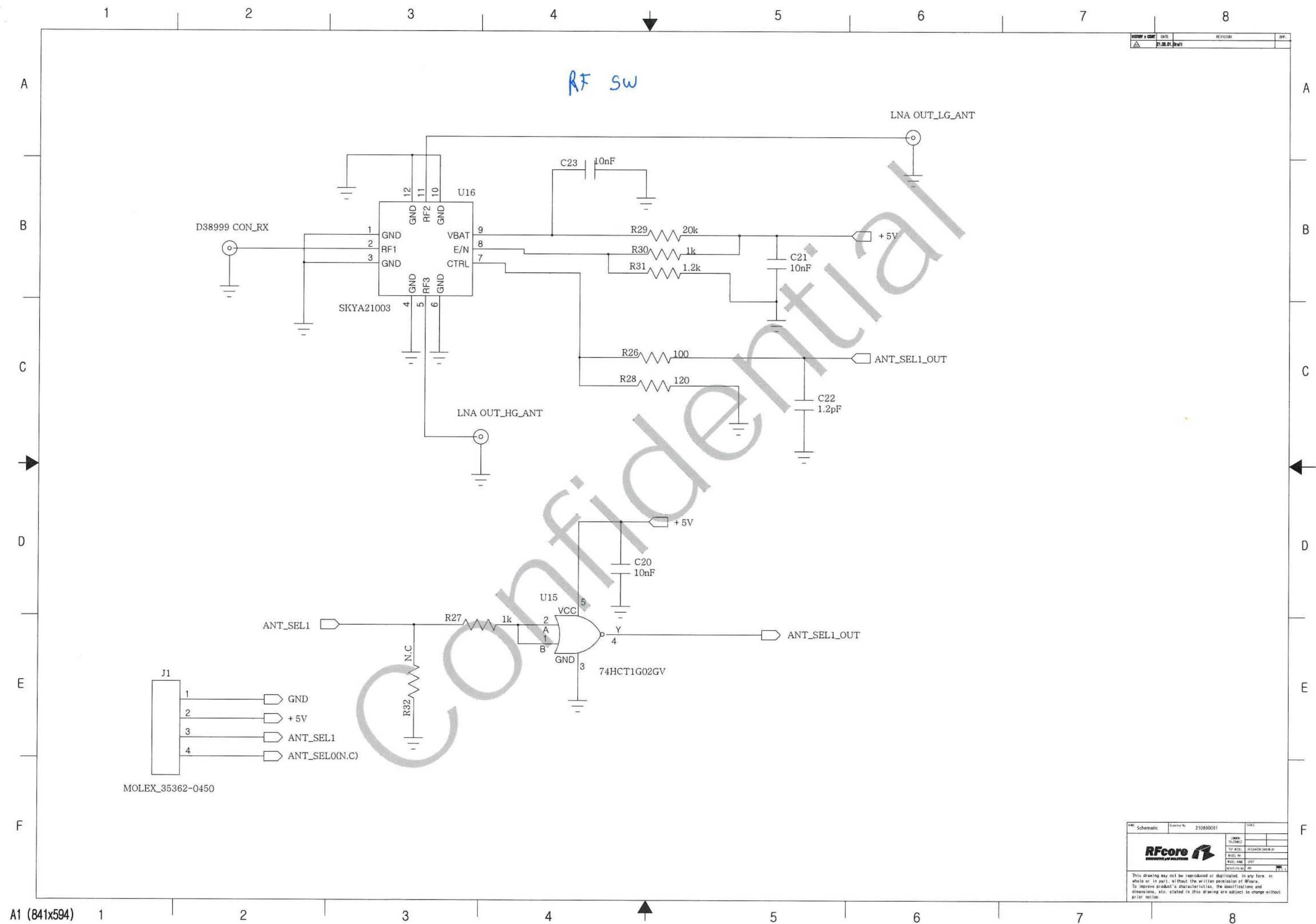
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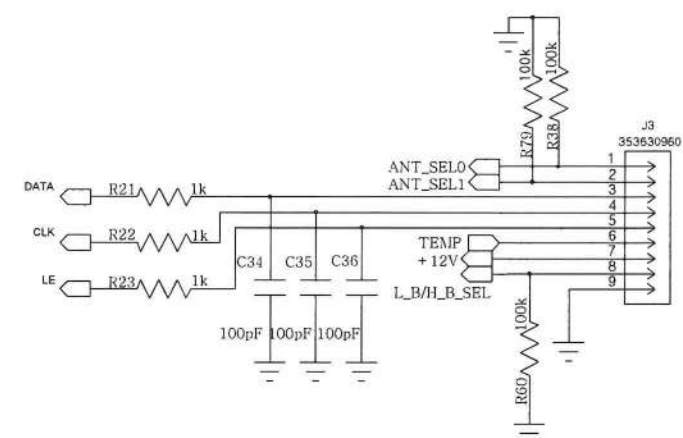
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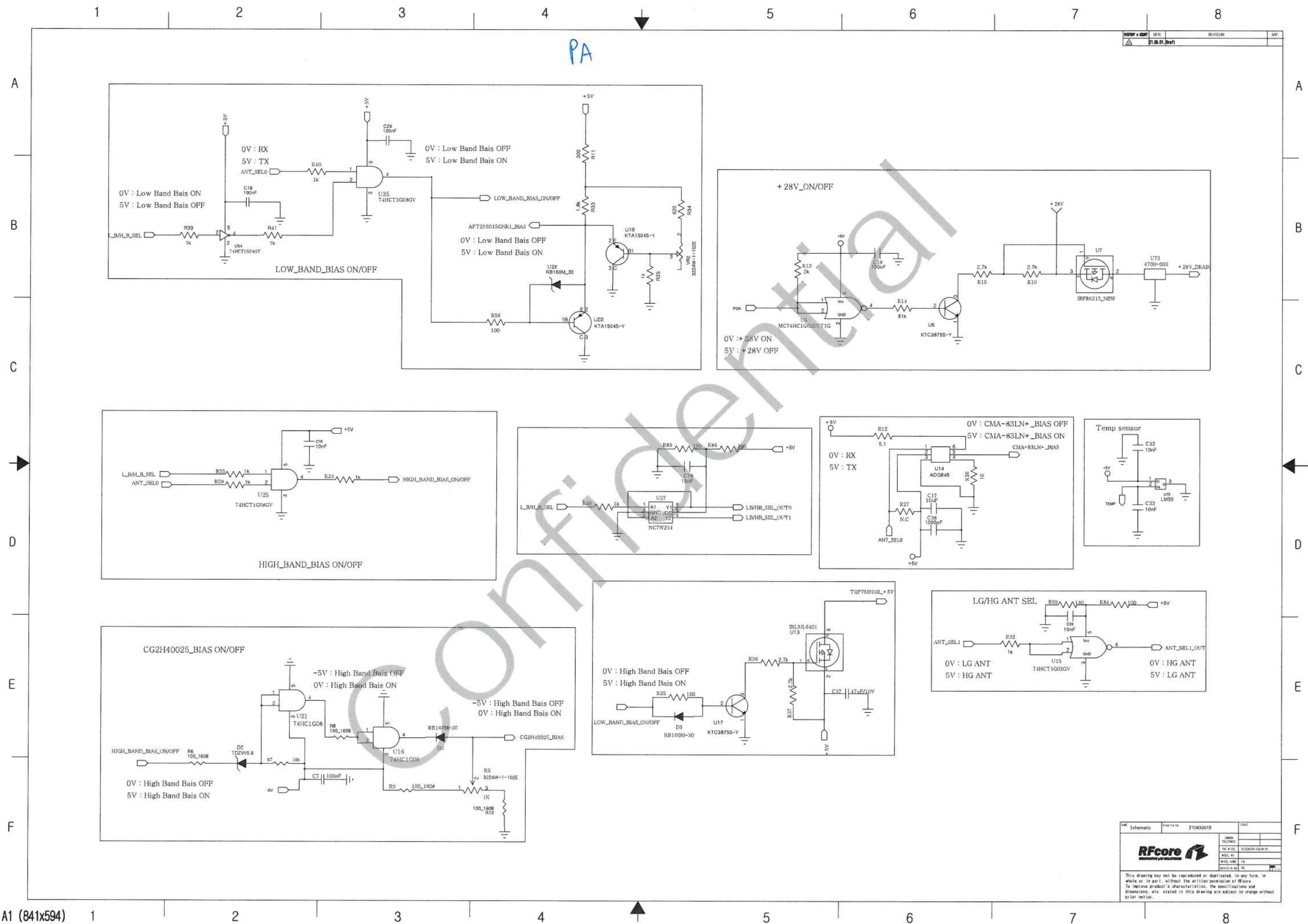
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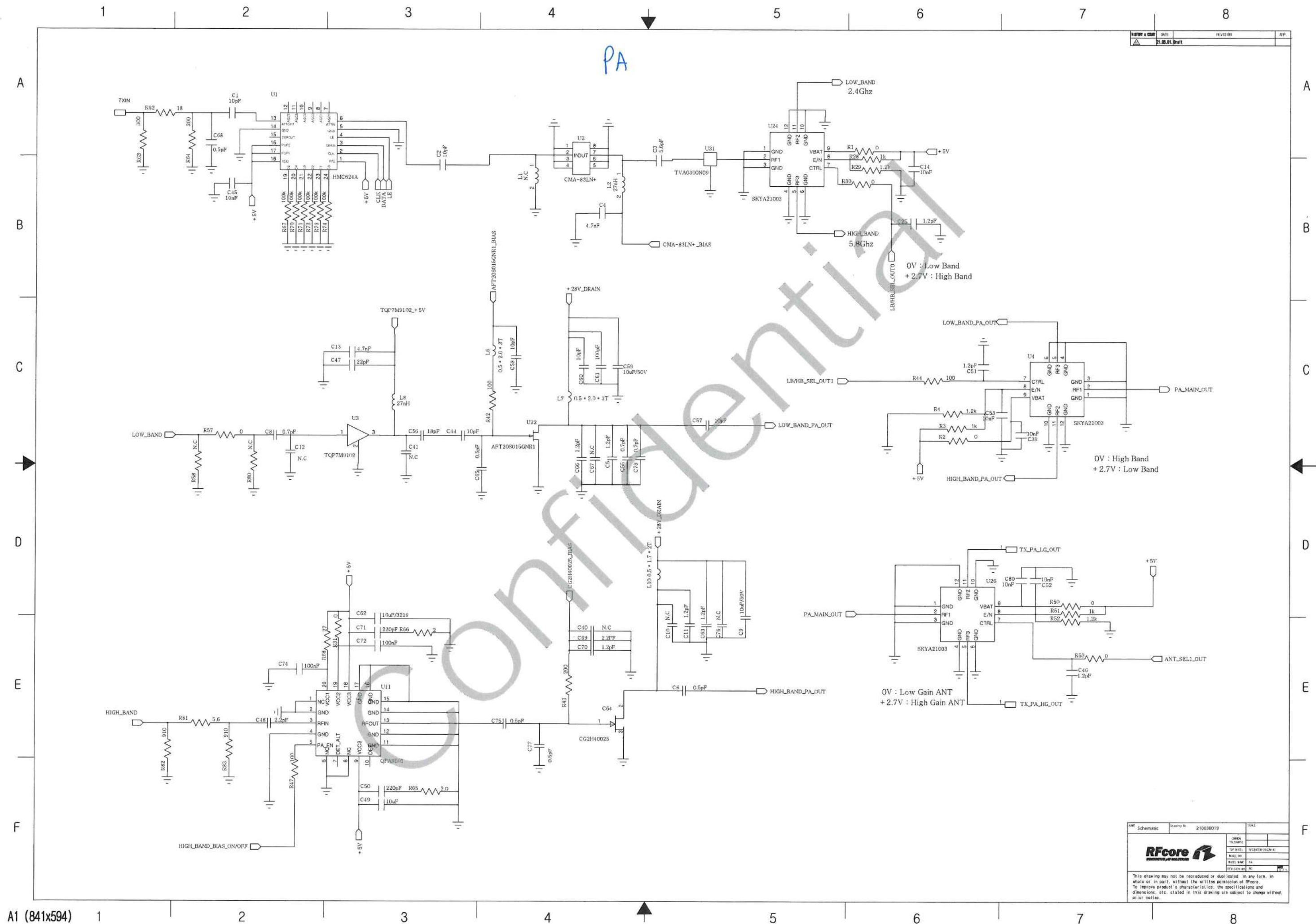


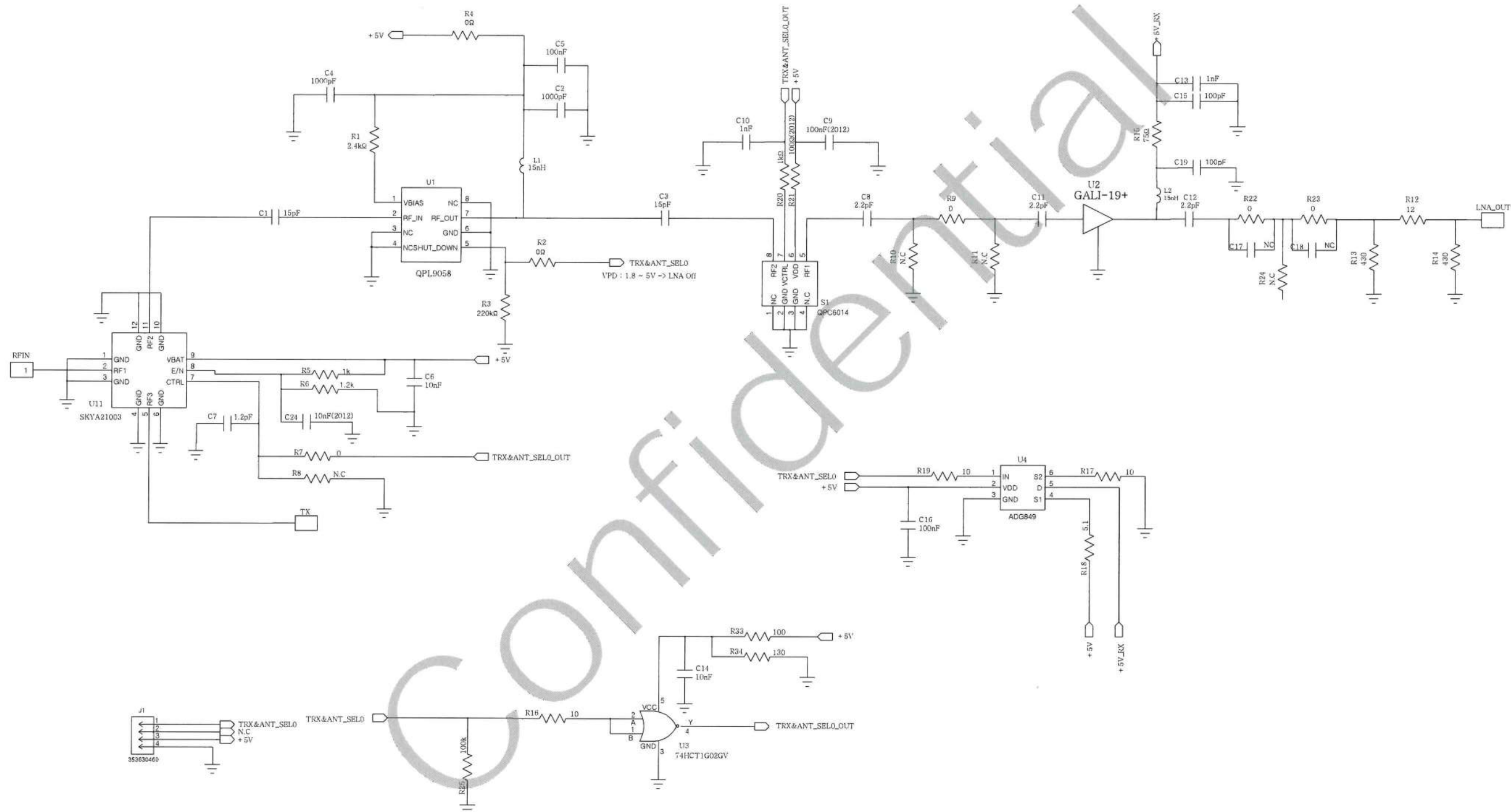
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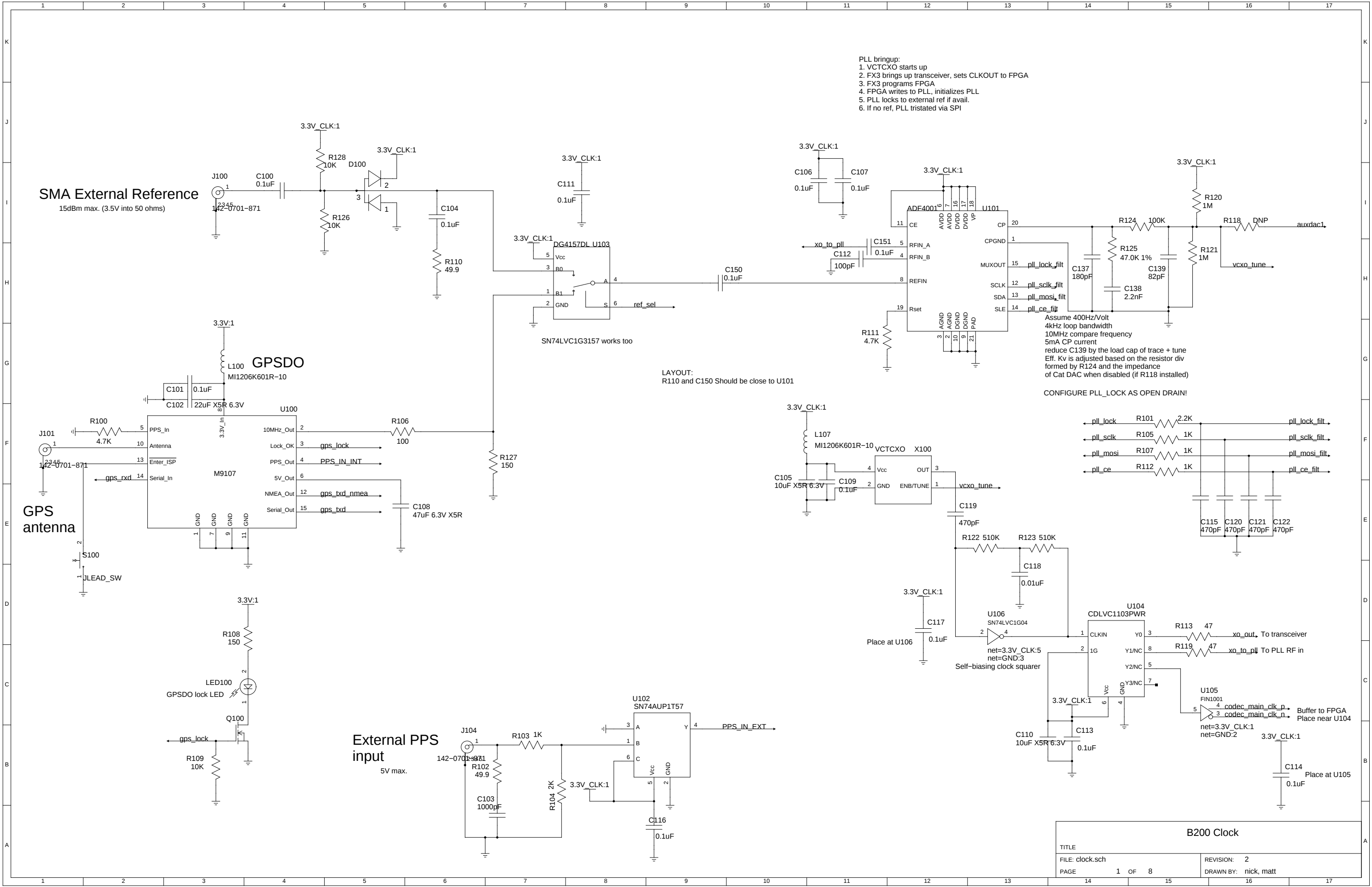


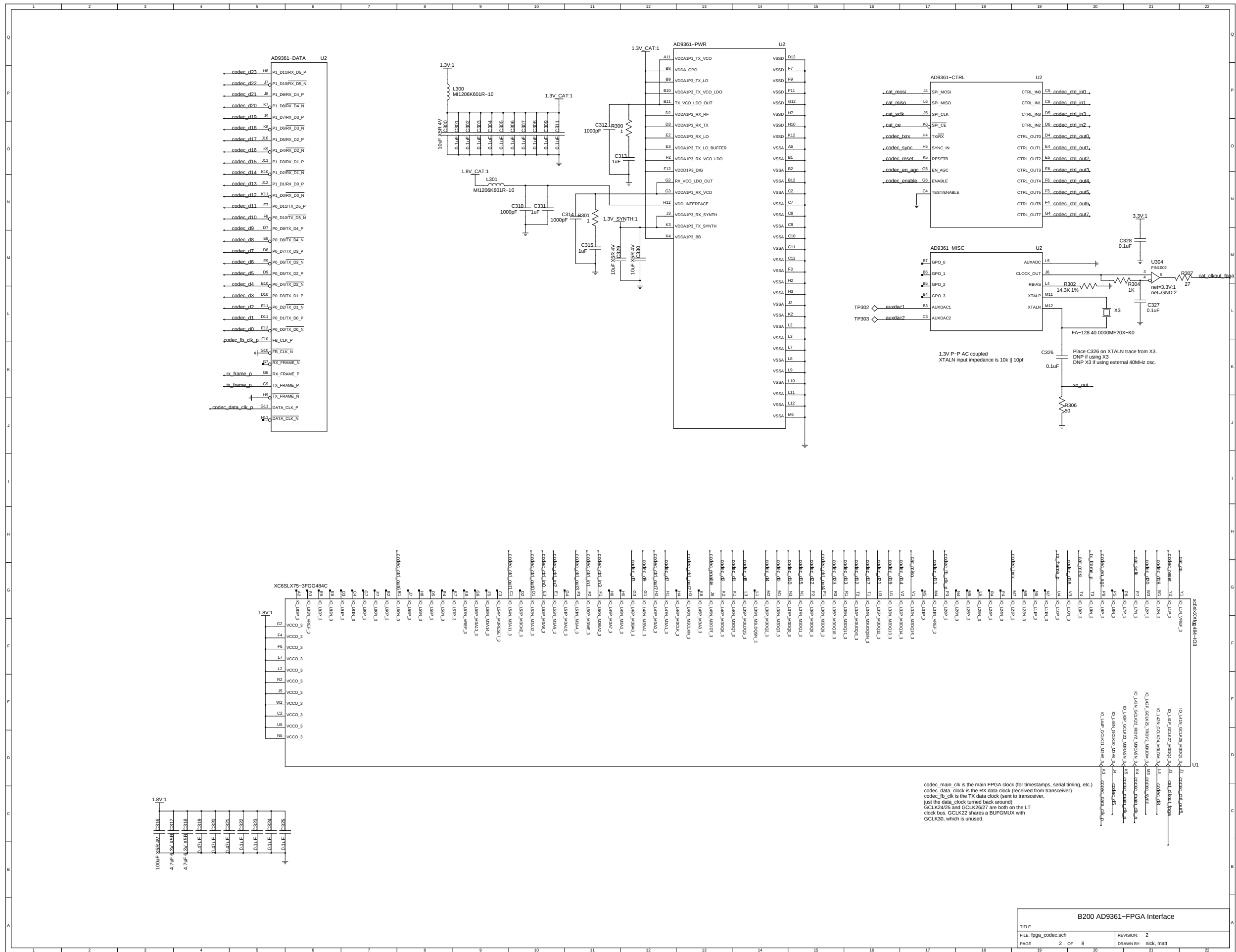




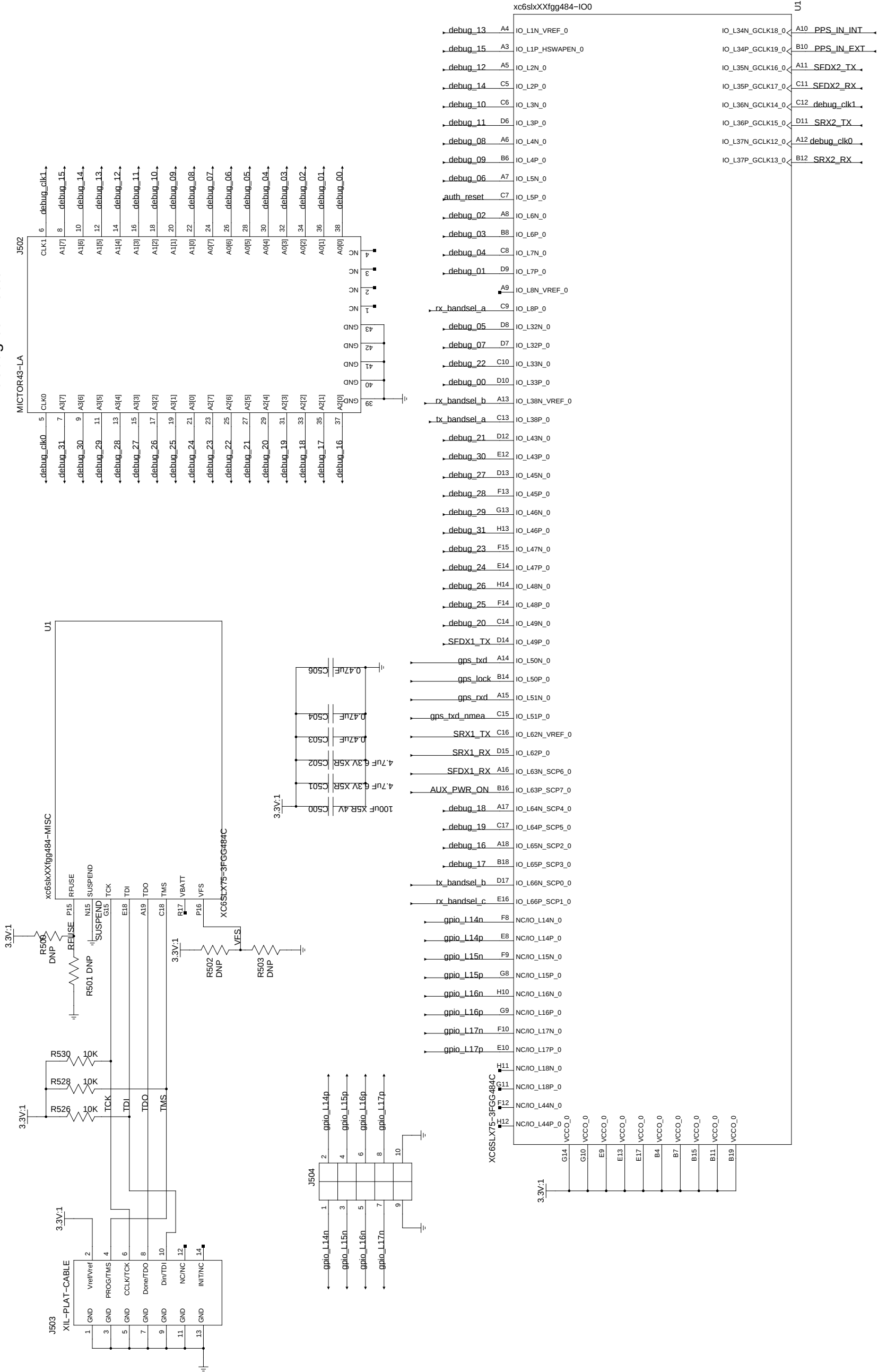






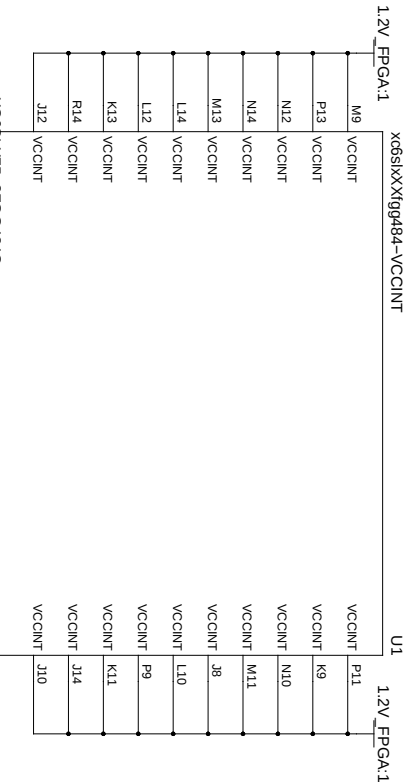
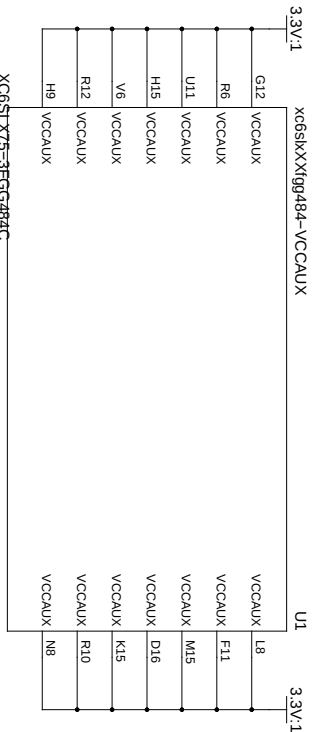


debug connector

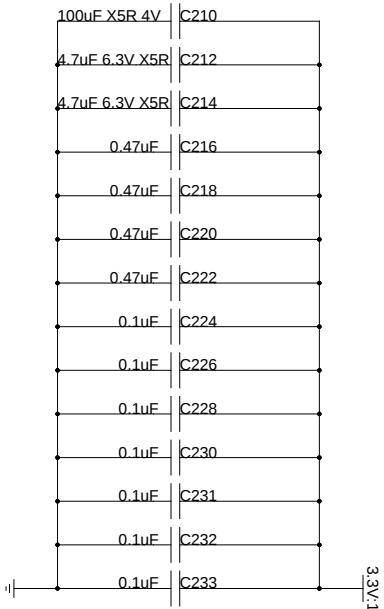
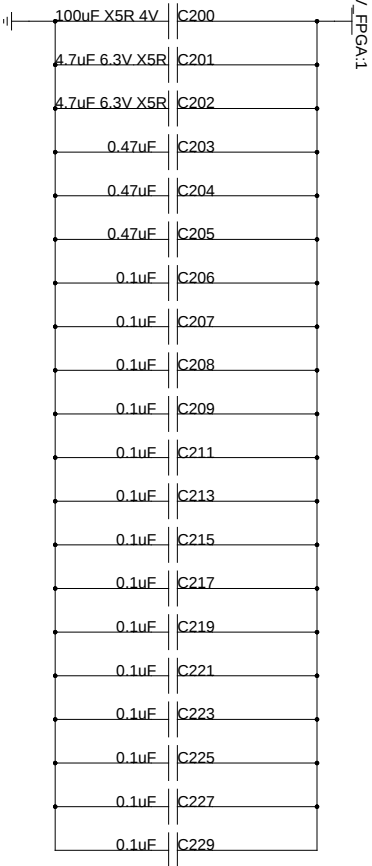
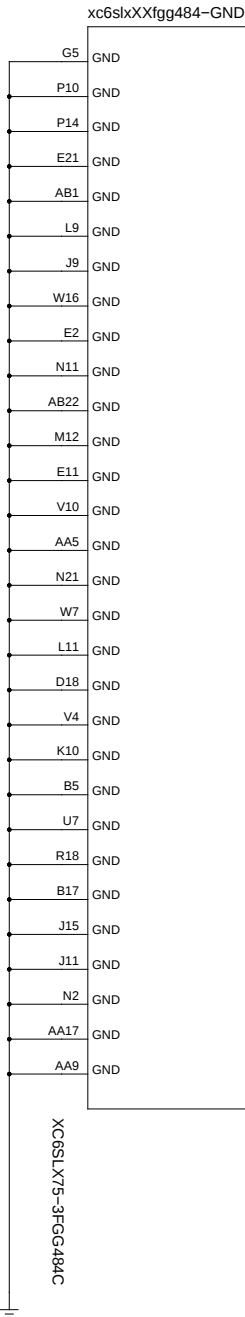
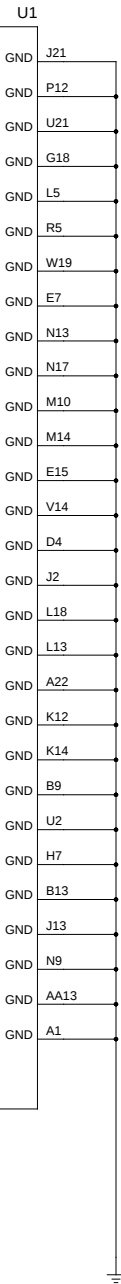


B200 debug and misc

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FILE: fpga_debug.sch
PAGE 3 OF 8
REVISION: 2
DRAWN BY: nick matt



XC6SLX75-3FG484C



B200 FPGA Power

TITLE				REVISION: 2	
FILE: fpga_power.sch				DRAWN BY: nick.matt	
PAGE 4 OF 8					

