CY8CKIT-062-BLE PSoC 6 BLE Pioneer Board

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Drawing Numbers

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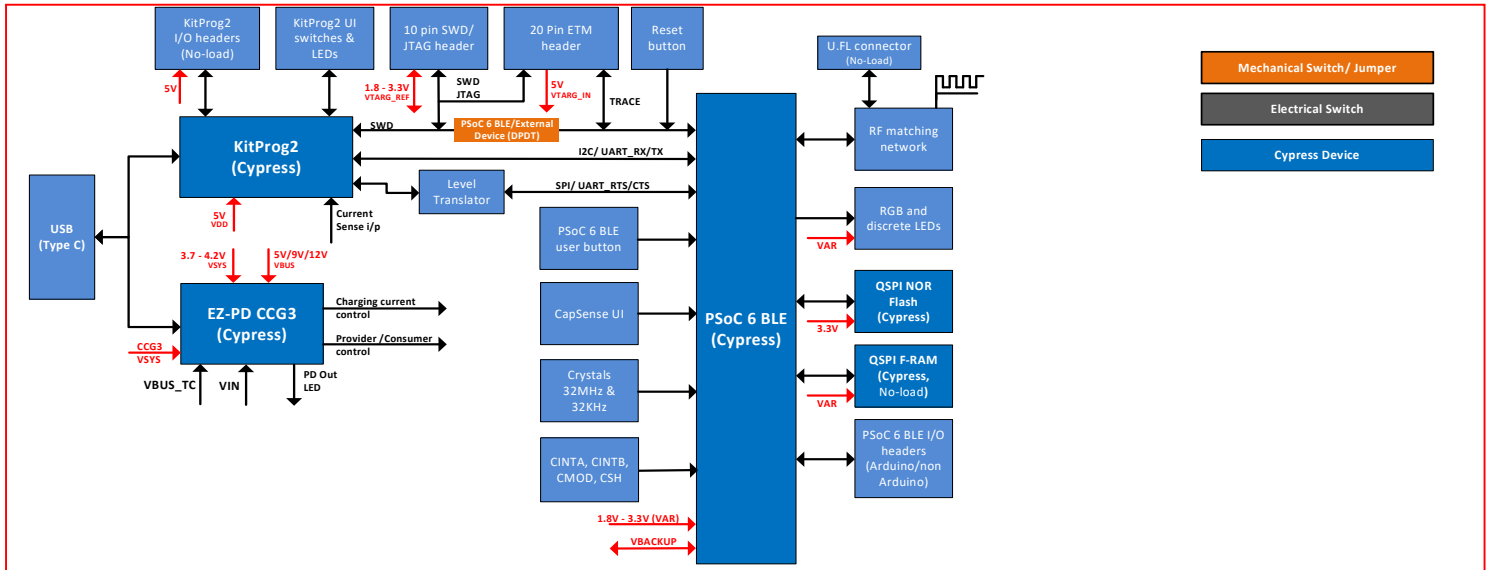
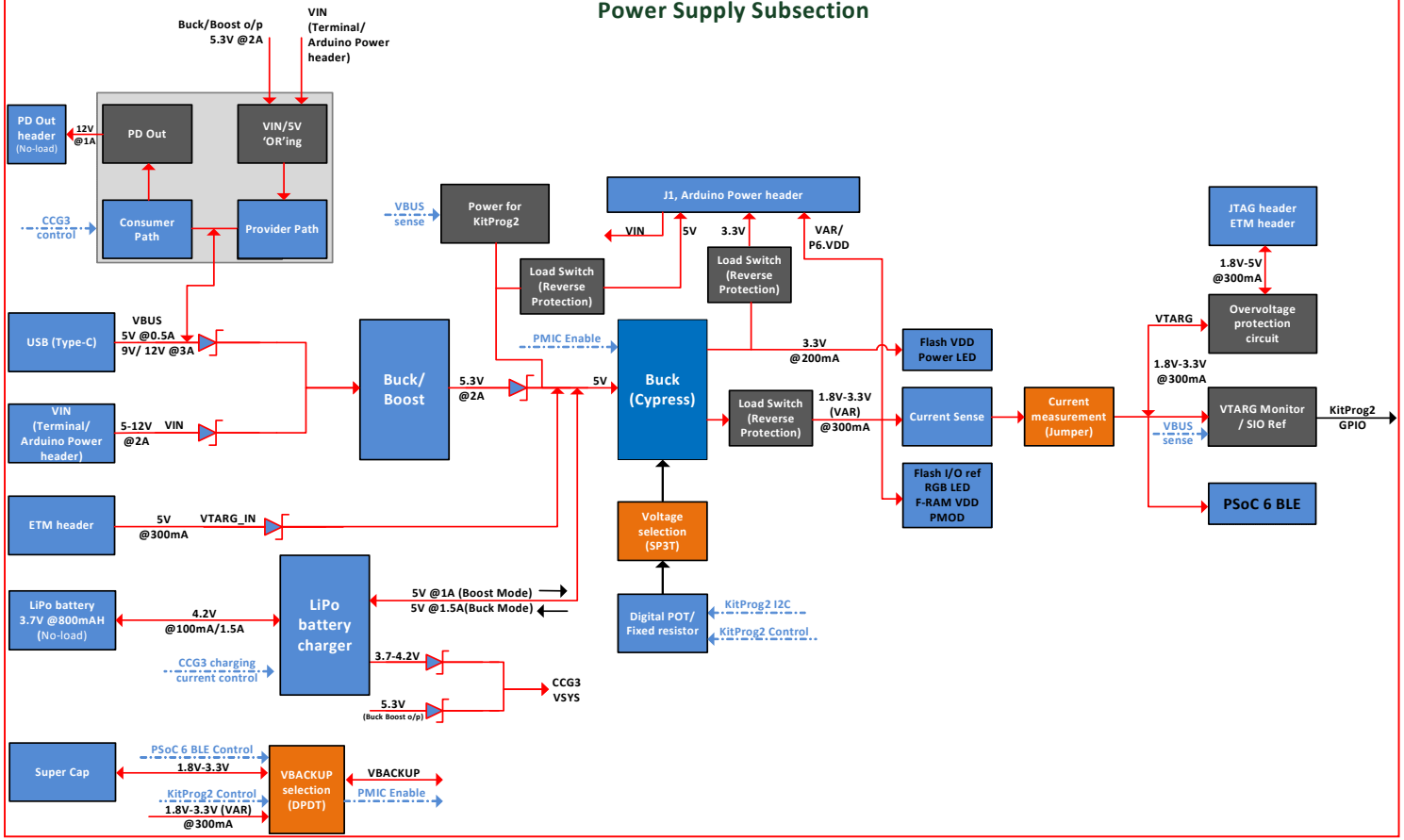


Power Supply Subsection



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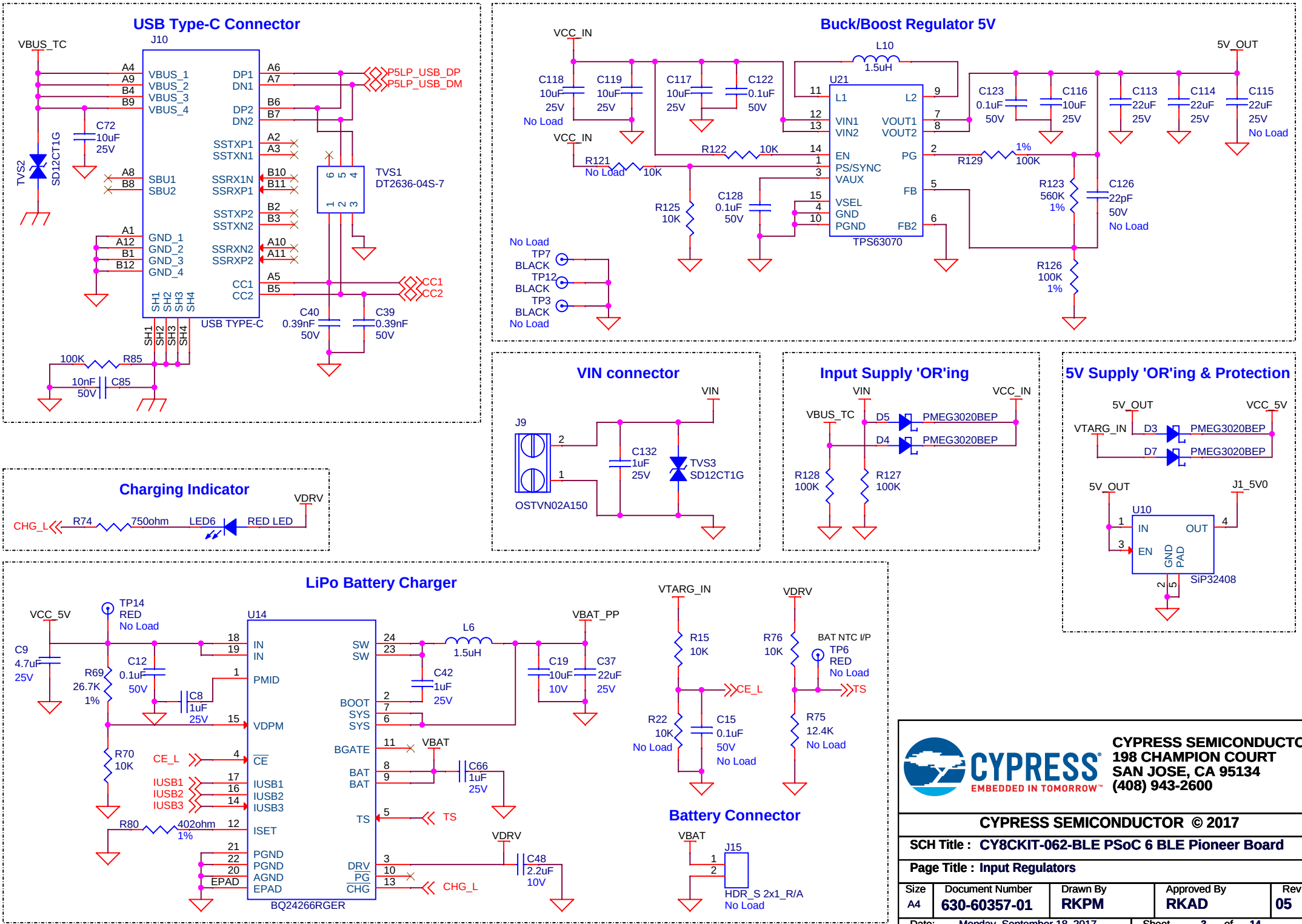

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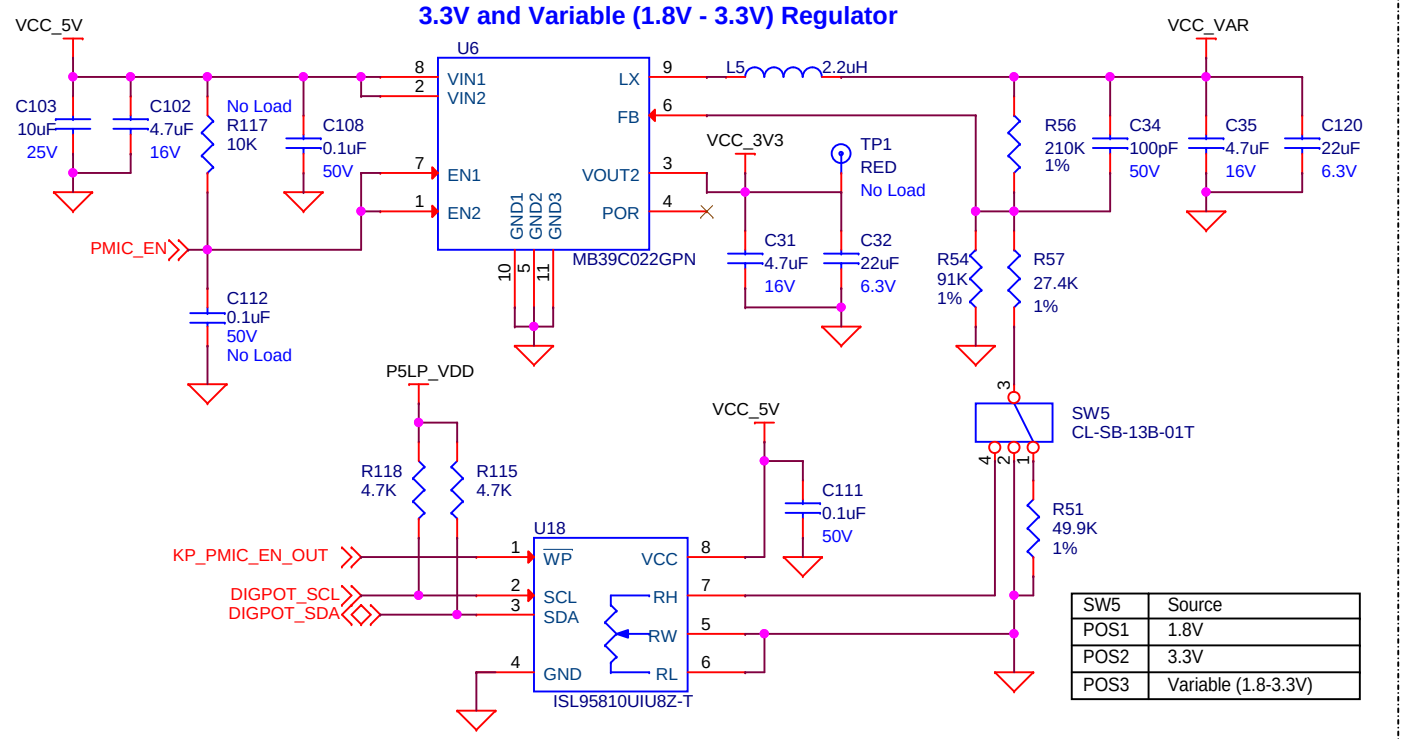
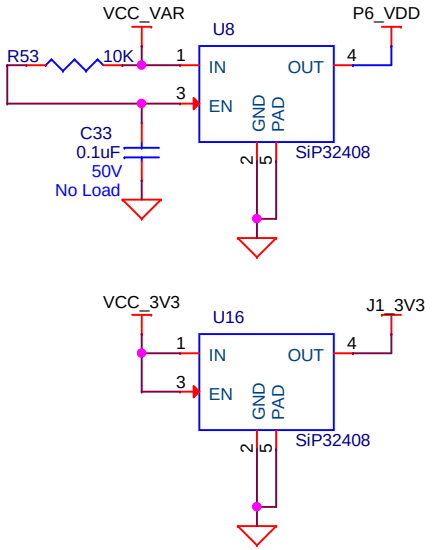
3.3V and Variable (1.8V - 3.3V) Regulator



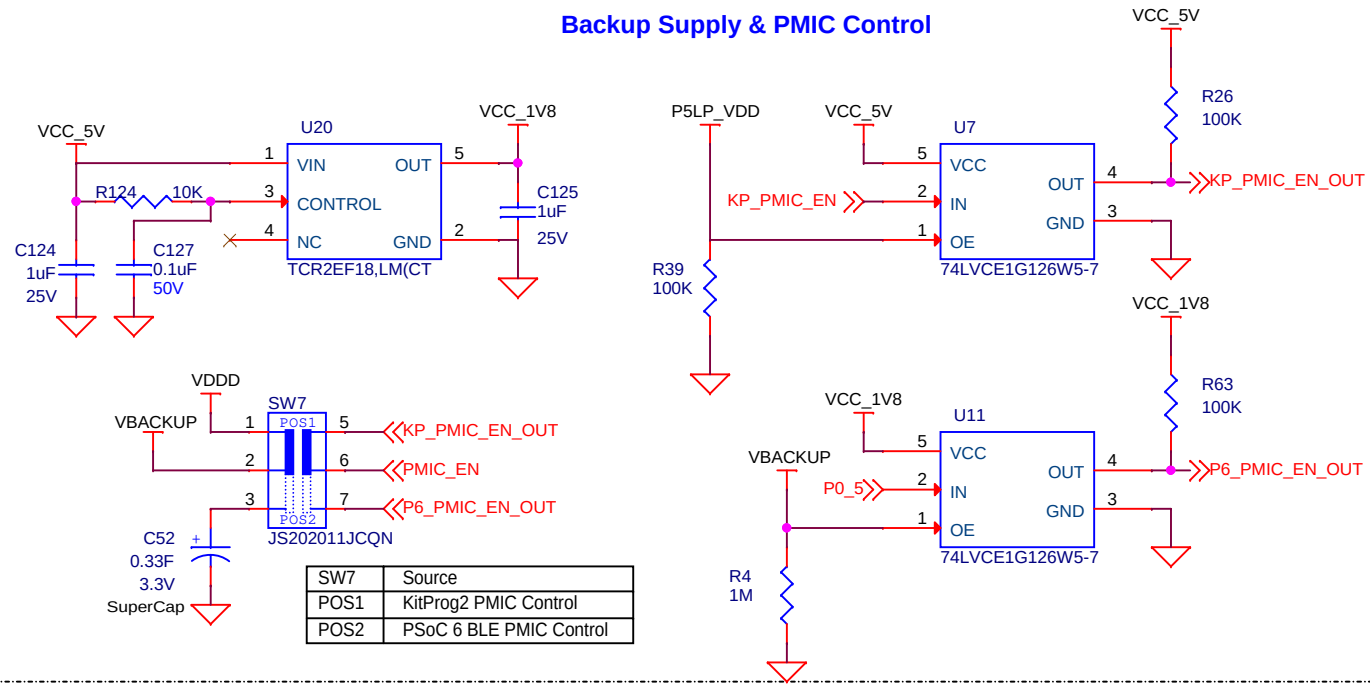
SW5	Source
POS1	1.8V
POS2	3.3V
POS3	Variable (1.8-3.3V)

Power & Charging LED

Reverse protection



Backup Supply & PMIC Control



SW7	Source
POS1	KitProg2 PMIC Control
POS2	PSoC 6 BLE PMIC Control

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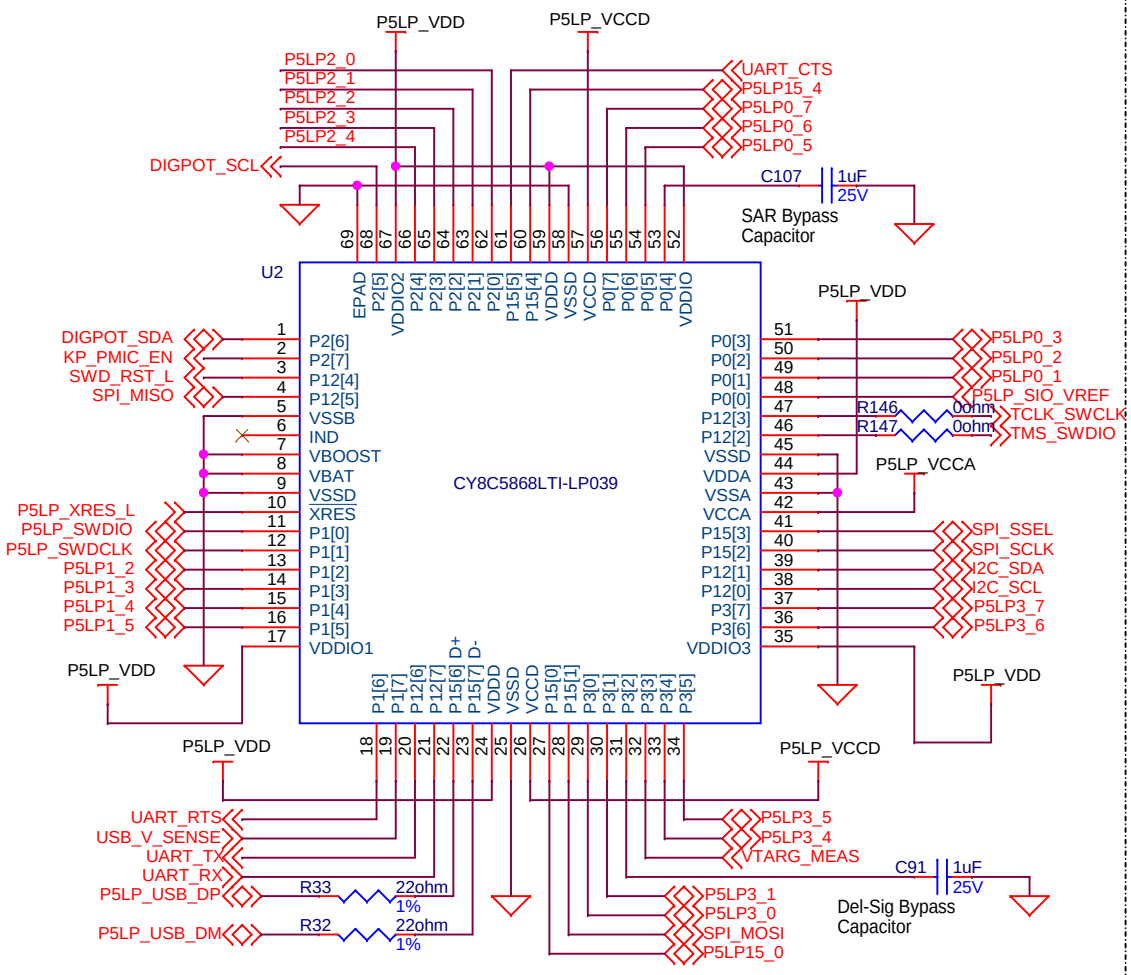
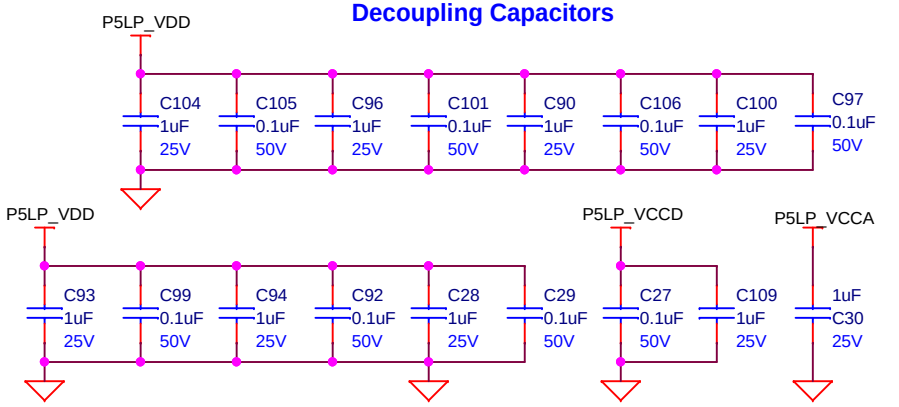
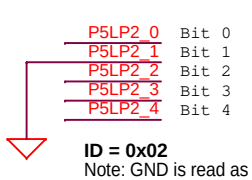
PSoC 5LP based KitProg2



Decoupling Capacitors



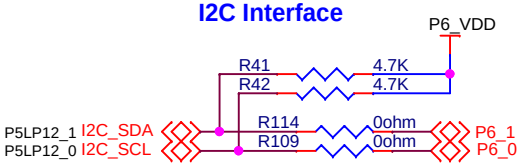
KitProg2 HW Revision



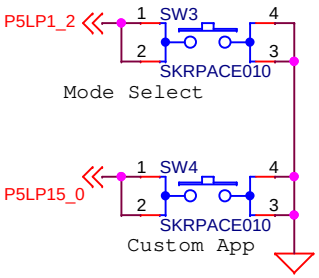
ID = 0x02
Note: GND is read as binary "1" and floating as "0"

SWD Interface

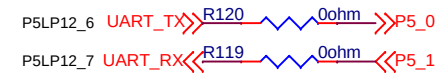
I2C Interface



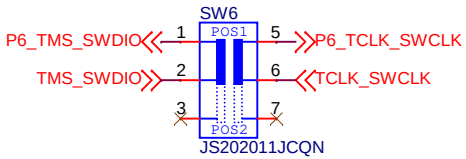
PSoC 5LP User Switches



UART Interface

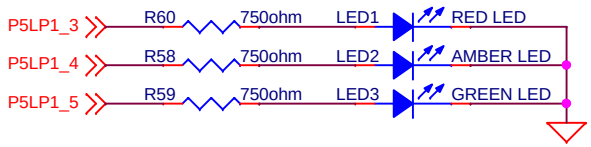


SWD Target Select



SW6	Target
POS1	PSoC 6 BLE
POS2	External

PSoC 5LP Status LEDs



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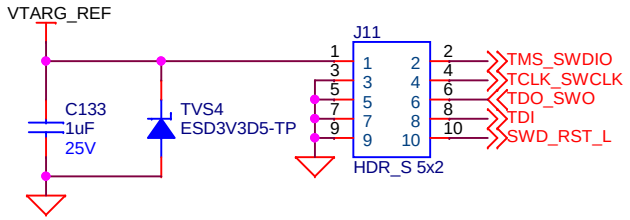
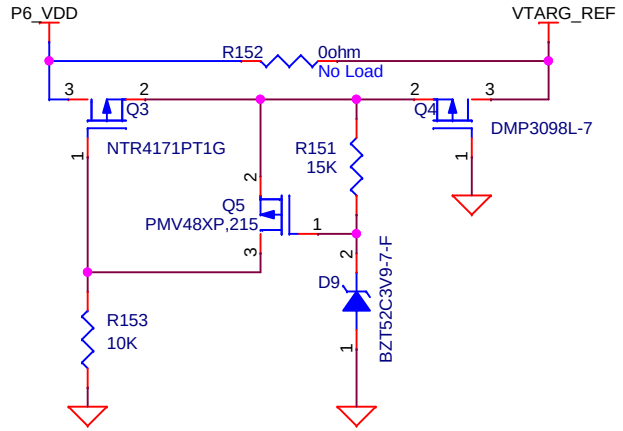
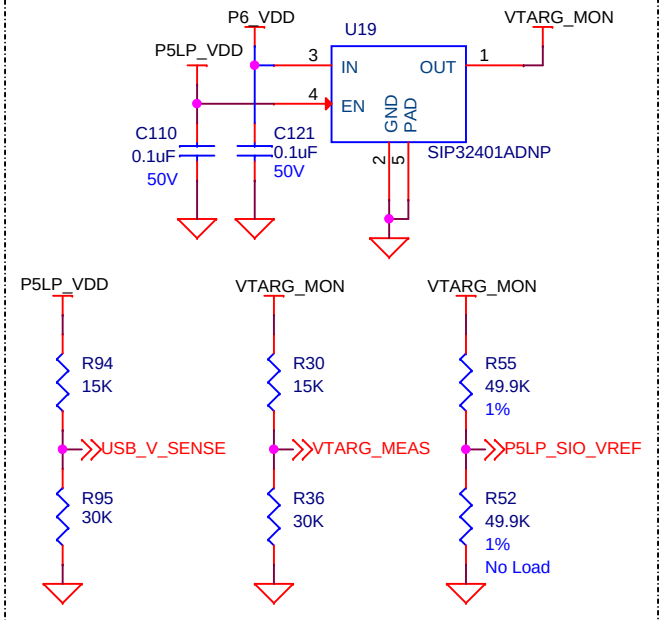
Target PSoC Program/Debug Header



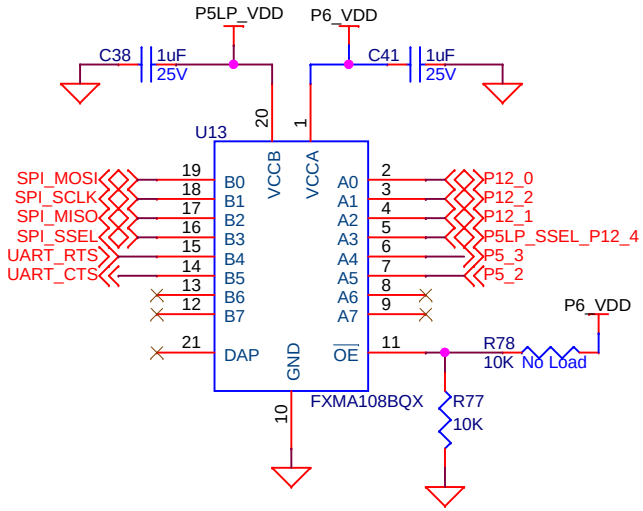
Program/Debug Overvoltage Protection



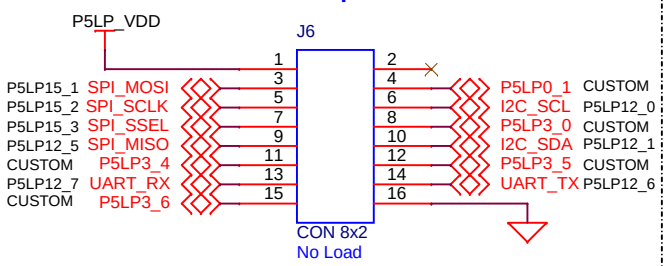
Voltage Monitoring



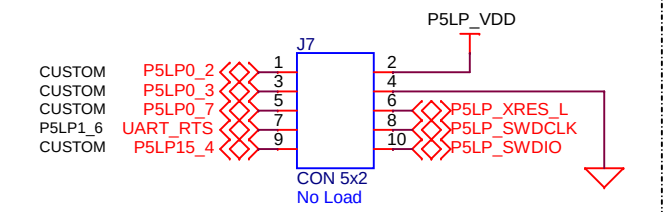
Level Translator for SPI and UART flow control



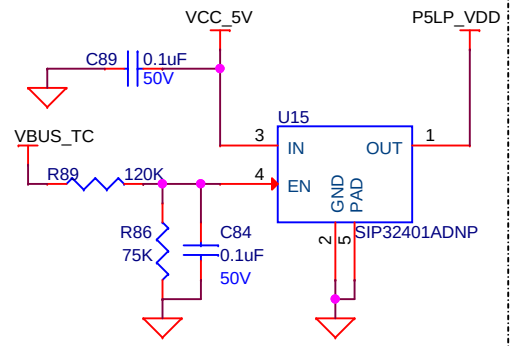
PSoC 5LP GPIO Expansion Header



PSoC 5LP Custom Application Header



Supply for PSoC 5LP



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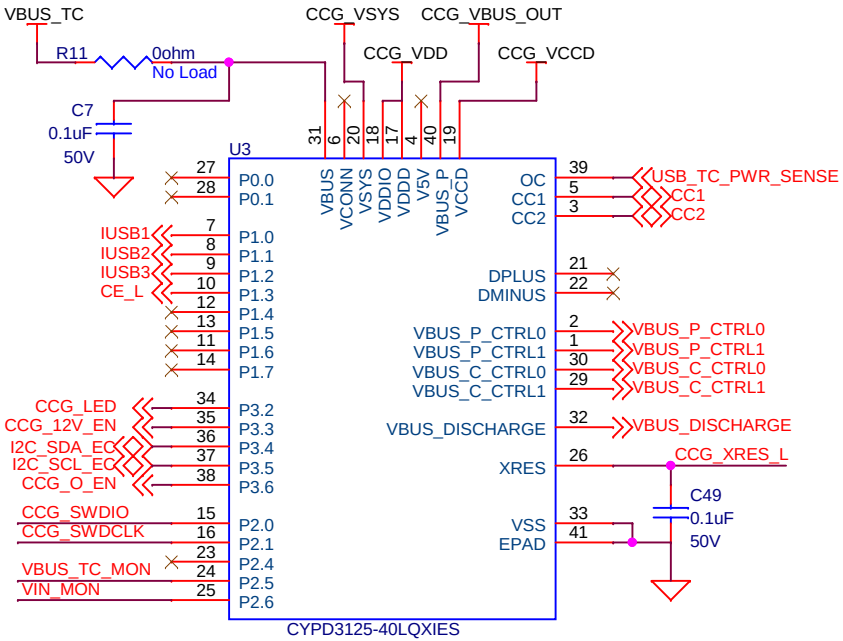
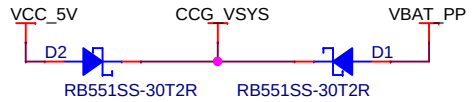
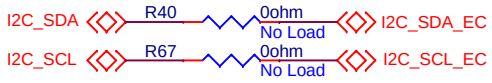
CCG3, USB Type-C and Power Delivery



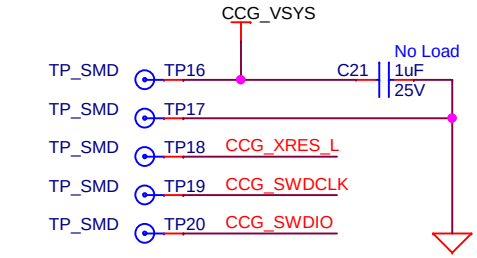
CCG3 supply selection



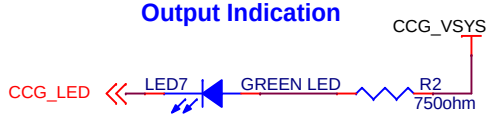
I2C Bootloader



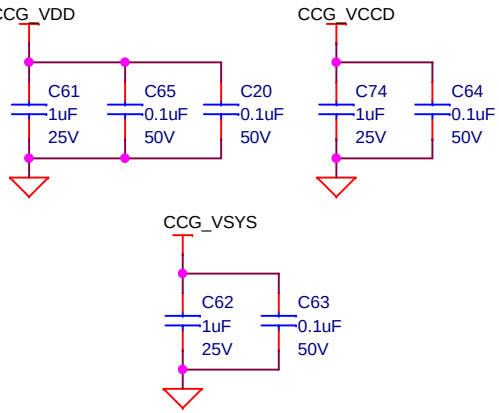
CCG3 Programming



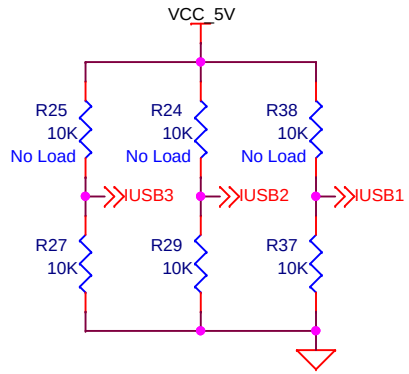
Output Indication



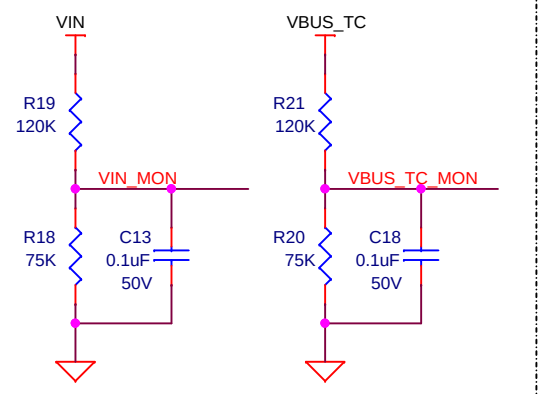
Decoupling Capacitors



LiPo battery charger pull-up



VBUS and VIN voltage monitoring



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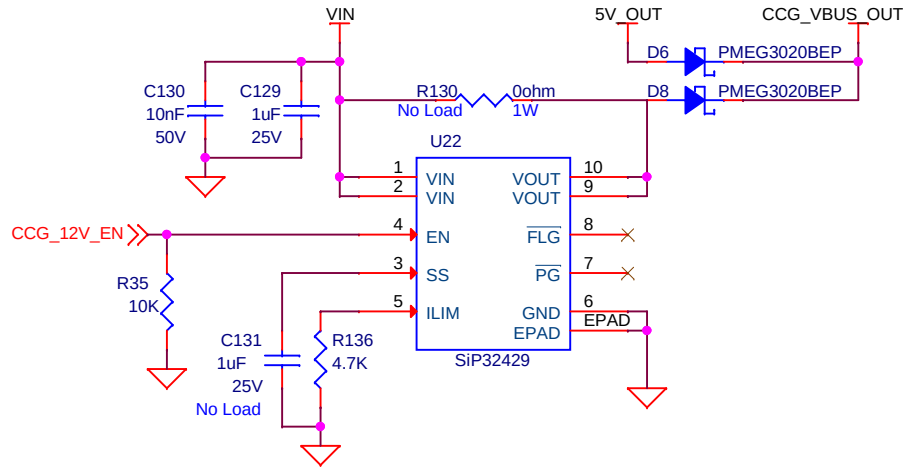
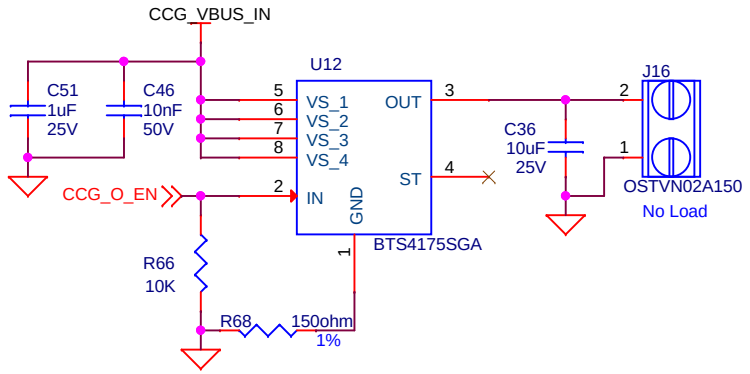
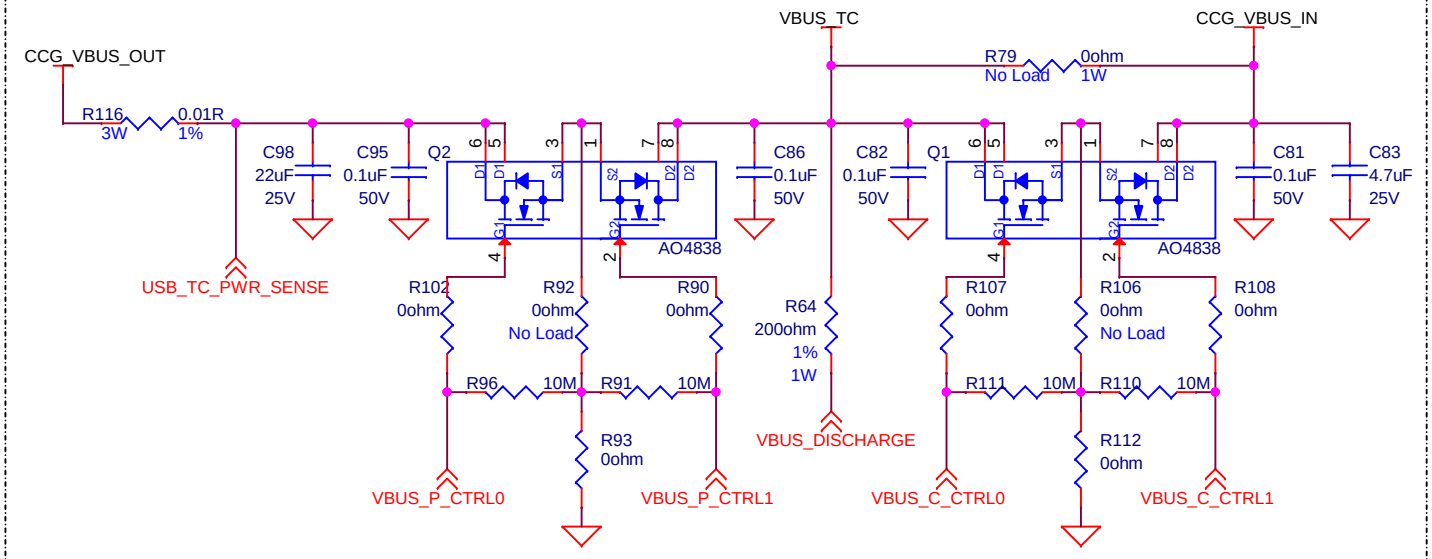
CCG3 Provider path 'OR'ing



CCG3 12V @1A Output



VBUS Provider/Consumer Path



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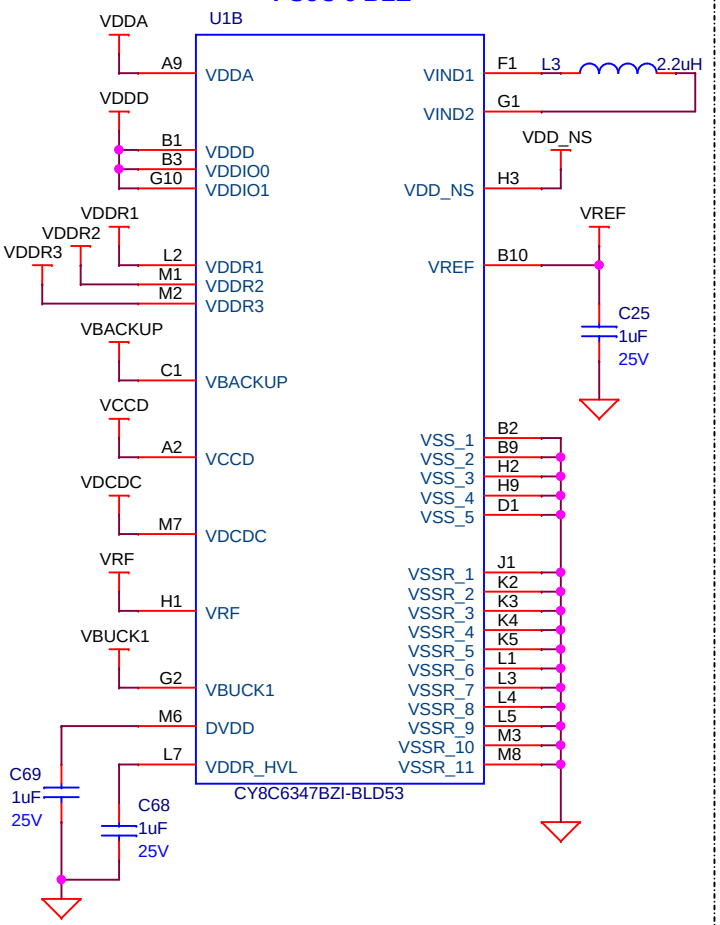
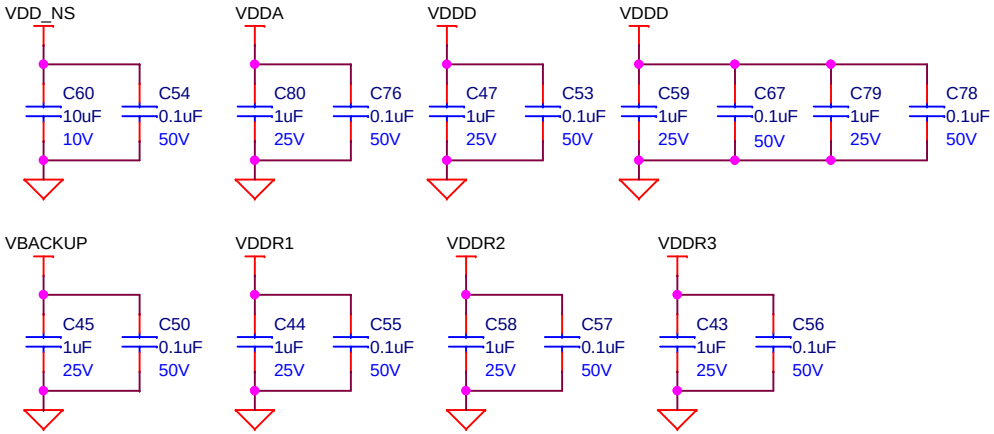
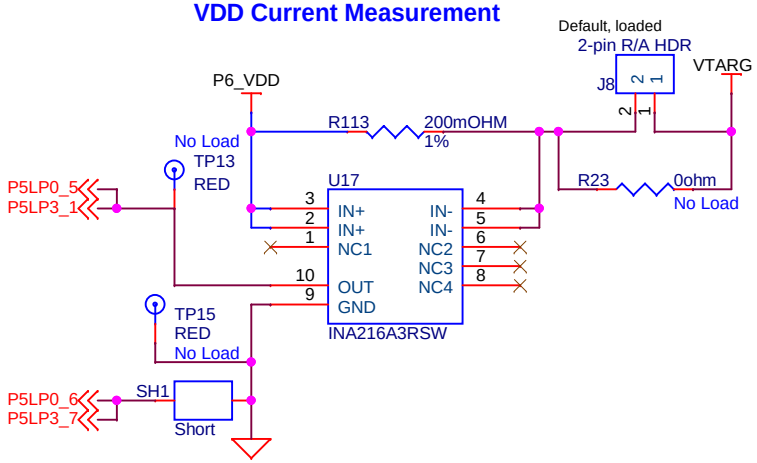


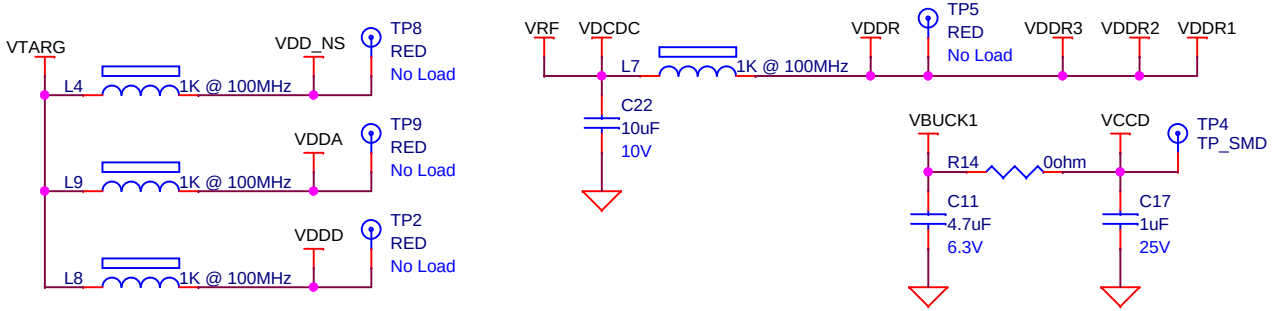
Decoupling Capacitors



VDD Current Measurement



Decoupling Capacitors



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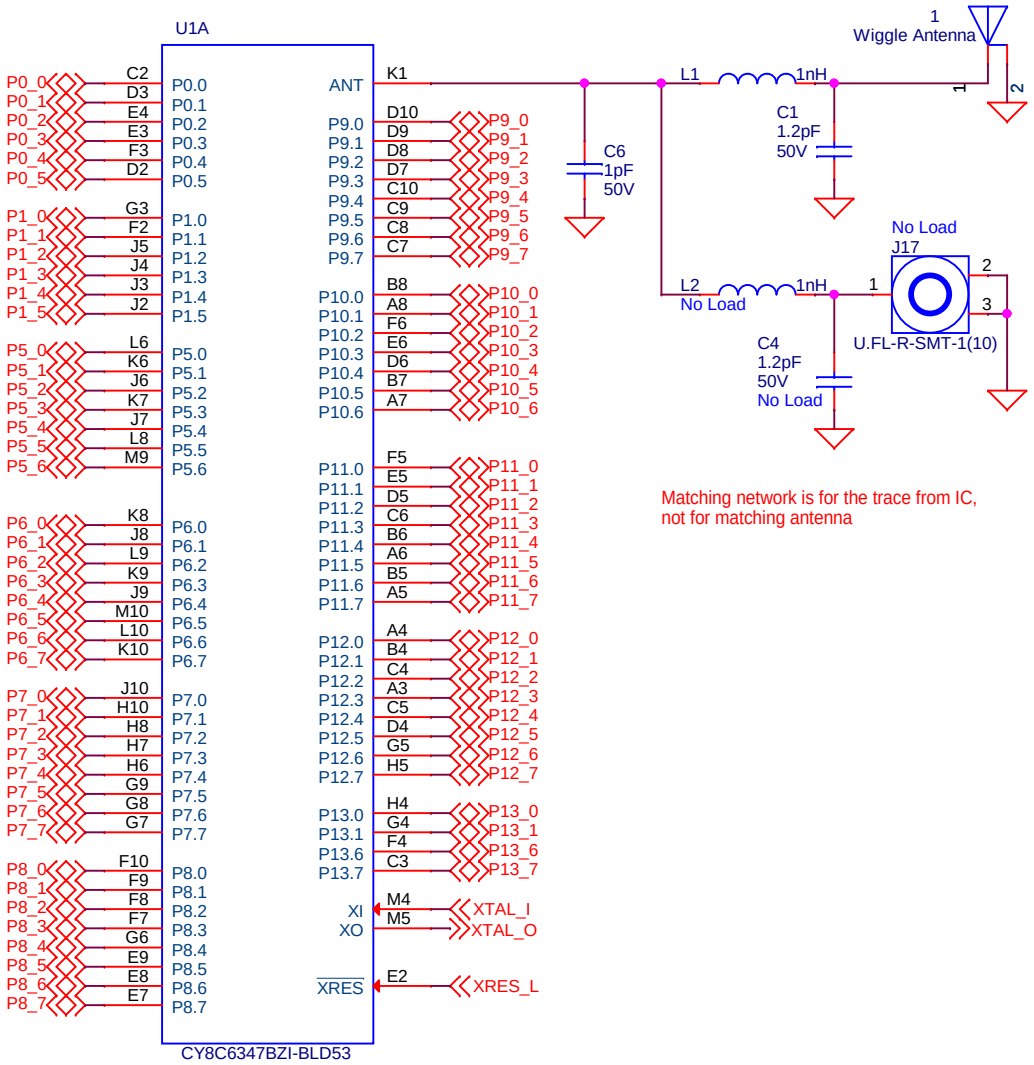
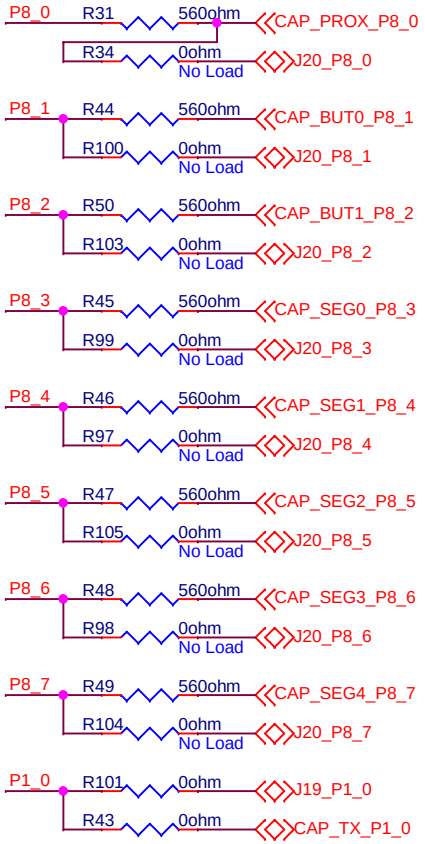
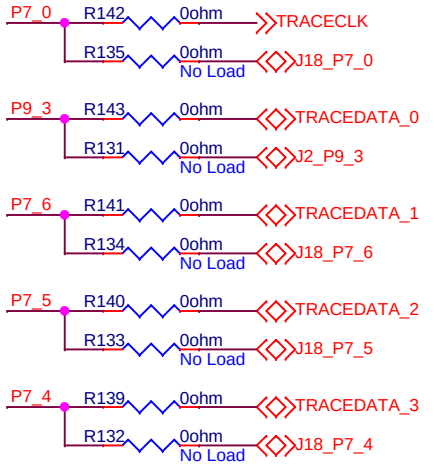


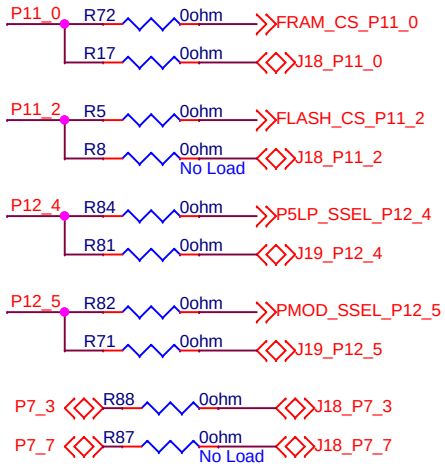
CapSense Multiplexed pins



TRACE Multiplexed pins



Additional Multiplexed pins



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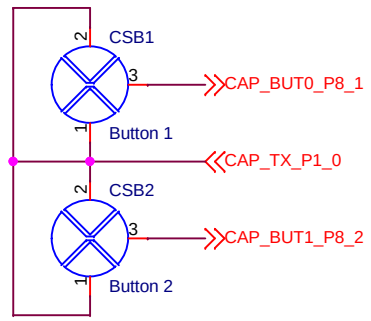
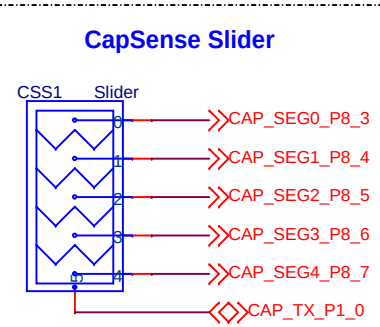
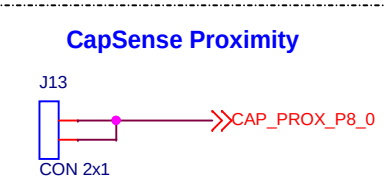
CapSense Button



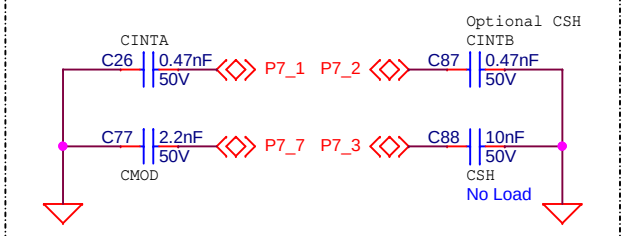
CapSense Slider



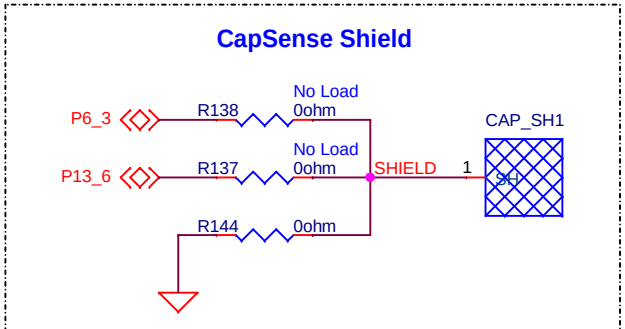
CapSense Proximity



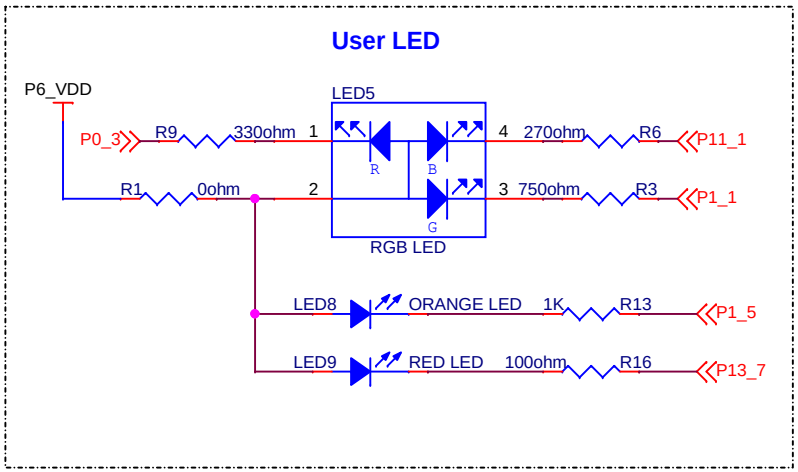
CMOD, CSH & CINT



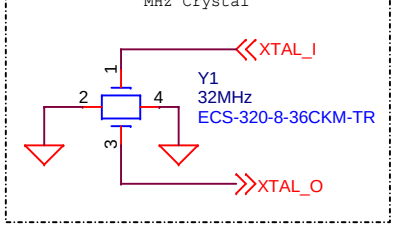
CapSense Shield



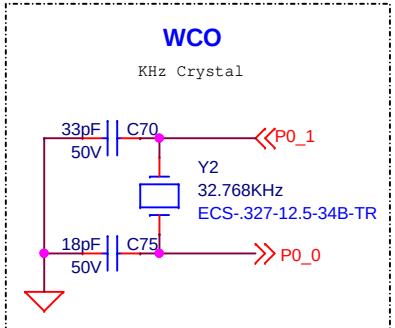
User LED



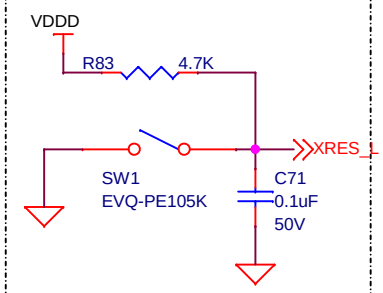
ECO for BLE



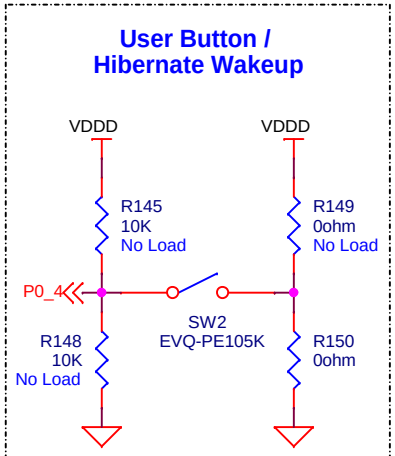
WCO



Reset Button



User Button / Hibernate Wakeup



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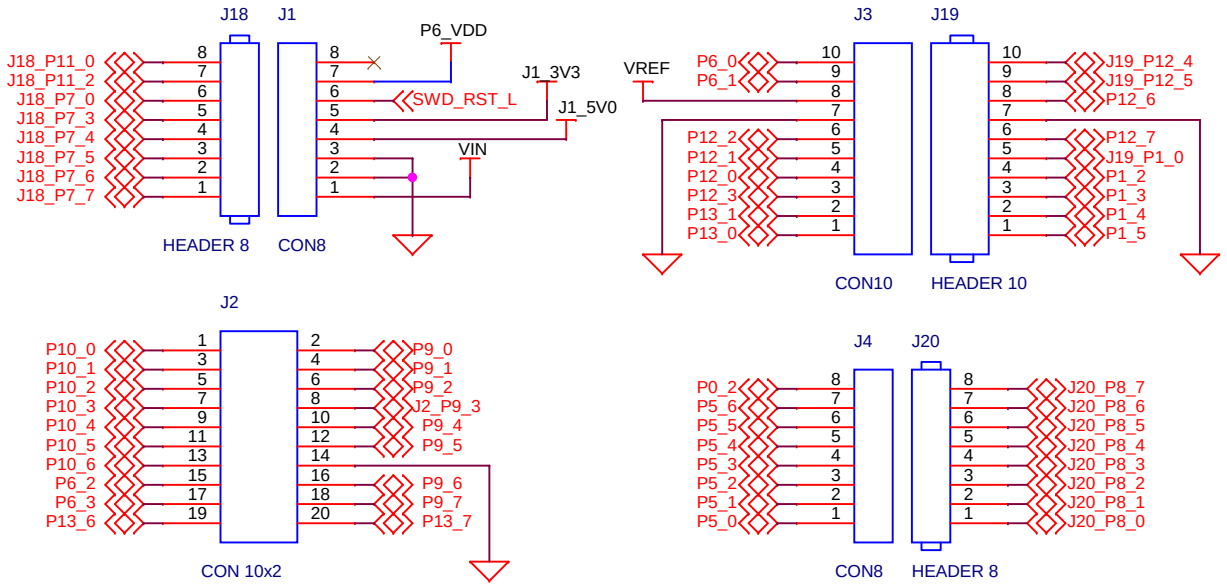
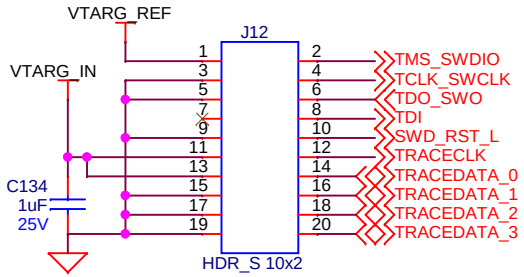
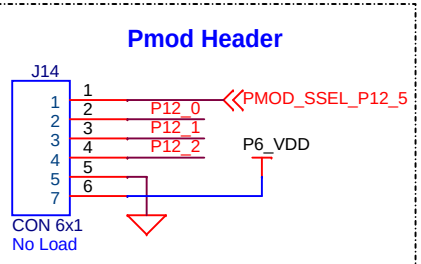
Arduino Headers



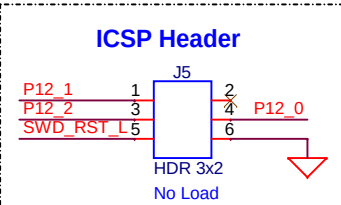
ETM Header



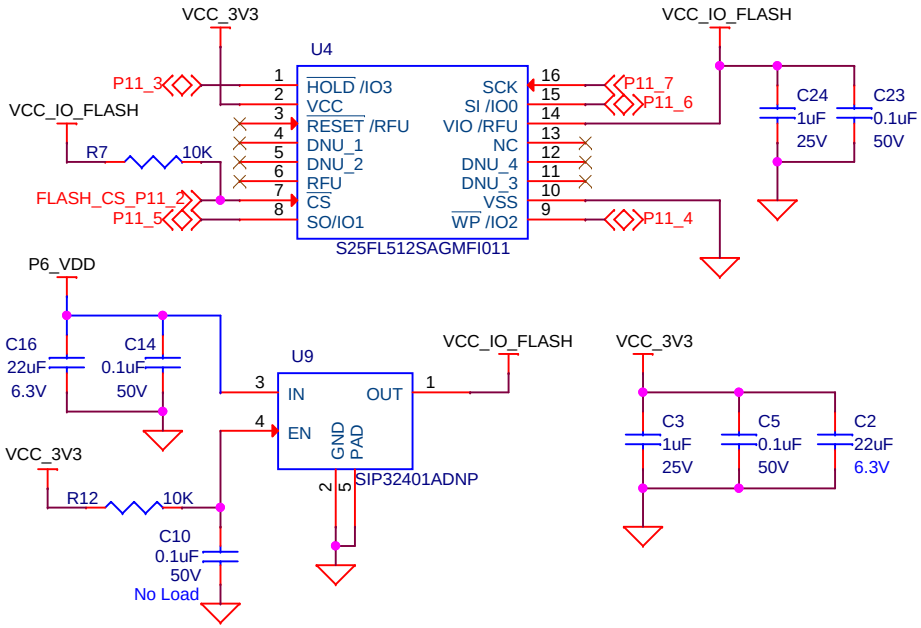
Pmod Header



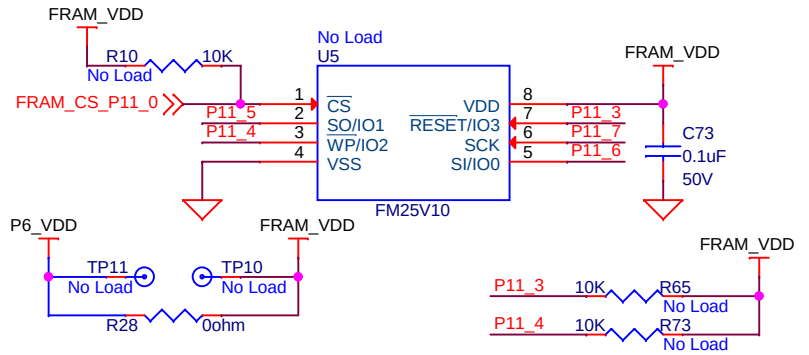
ICSP Header



QSPI Flash (SMIF)



F-RAM



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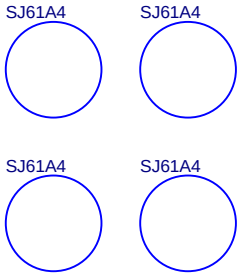
Accessories

Cylindrical Bumper

SJ61A4 SJ61A4

SJ61A4 SJ61A4



Acrylic Overlay

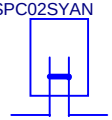
Acrylic Overlay

Color: Clear, Transparent
Finish: Matt

40 x 34 x 1mm

Jumper Shunt

SPC02SYAN

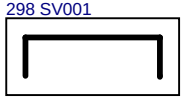


PCBA label

LBL PCA Label LBL QR Code

USB connector bracket

298 SV001



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Revision History

Rev	Description	Date
01	Initial internal release	08/29/2016
02	Internal release	10/03/2016
03	Internal release	11/25/2016
04	Initial external release under NDA	01/19/2017
05	3.3V protection diode changed to load SW (U59) Added PMIC control section (U58, U56, U57 and passives) Removed series resistor for SPI and UART HW flow control Voltage Monitoring domain and Protection Circuit o/p changed to P6_VDD Changed trace data line connection (to 9.3, 7.6, 7.5, 7.4) C22 Cap on VRF changed to 10uF Removed SW8 and connected CINT directly, CMOD connection changed to 7.7 Shield # changed to 13.6/6.3 Added additional LED on pin 13.7 and 1.5 Added option to change logic for user SW and added pull-up/down Back annotated from layout	5/2/2017

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