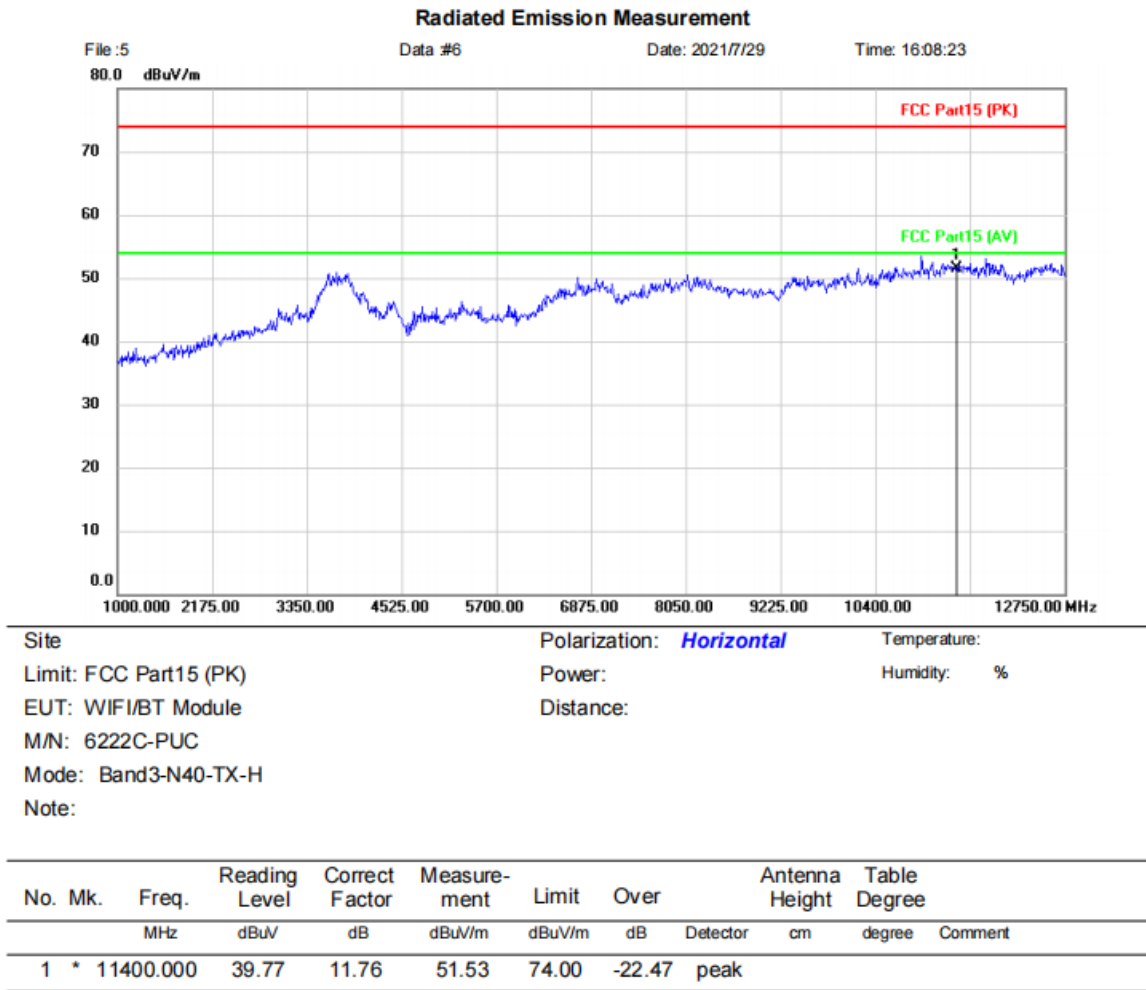


[TestMode: TX N40 5670 channel]; [Polarity: Horizontal]

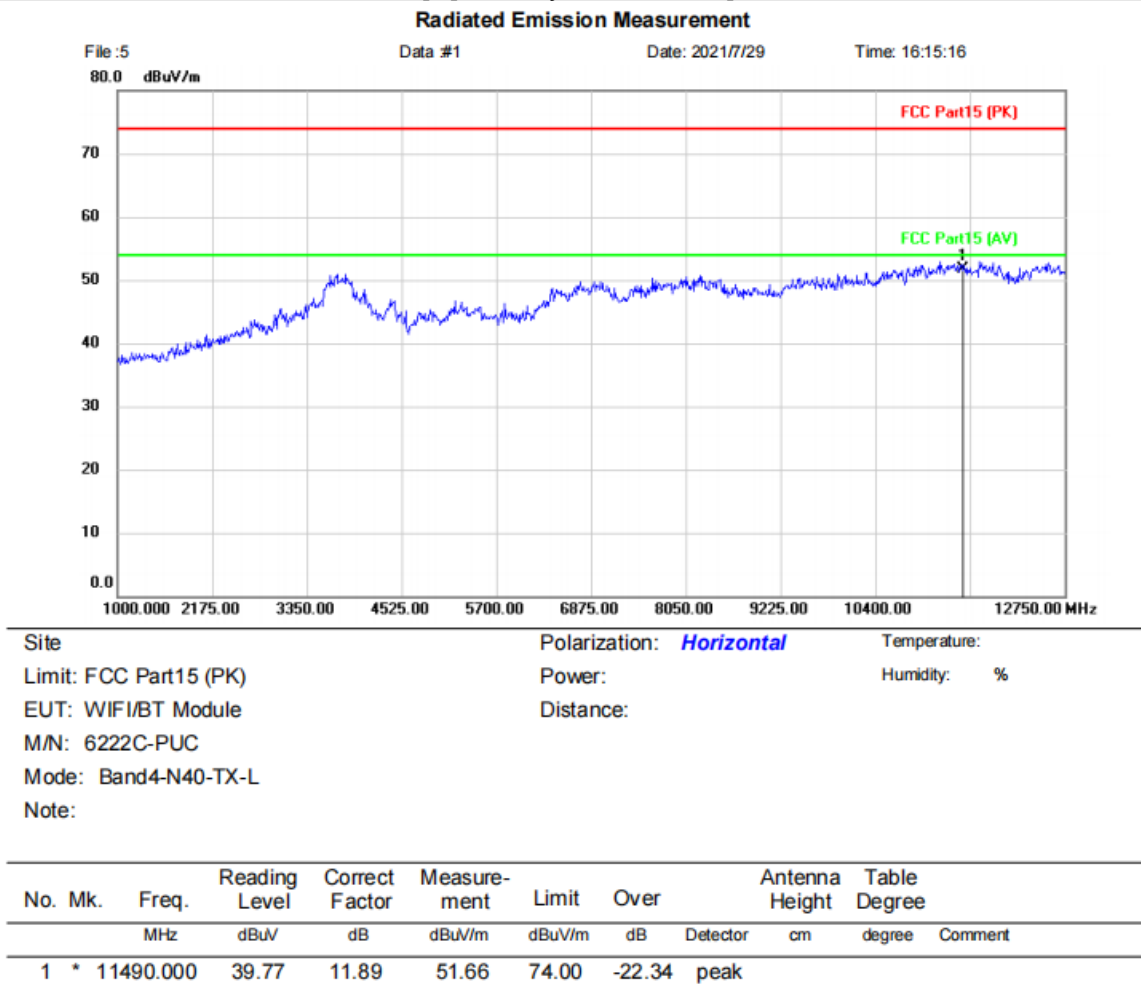


*:Maximum data x:Over limit !:over margin

(Reference Only)

Test Result: Pass

[TestMode: TX N40 5755 channel]; [Polarity: Horizontal]

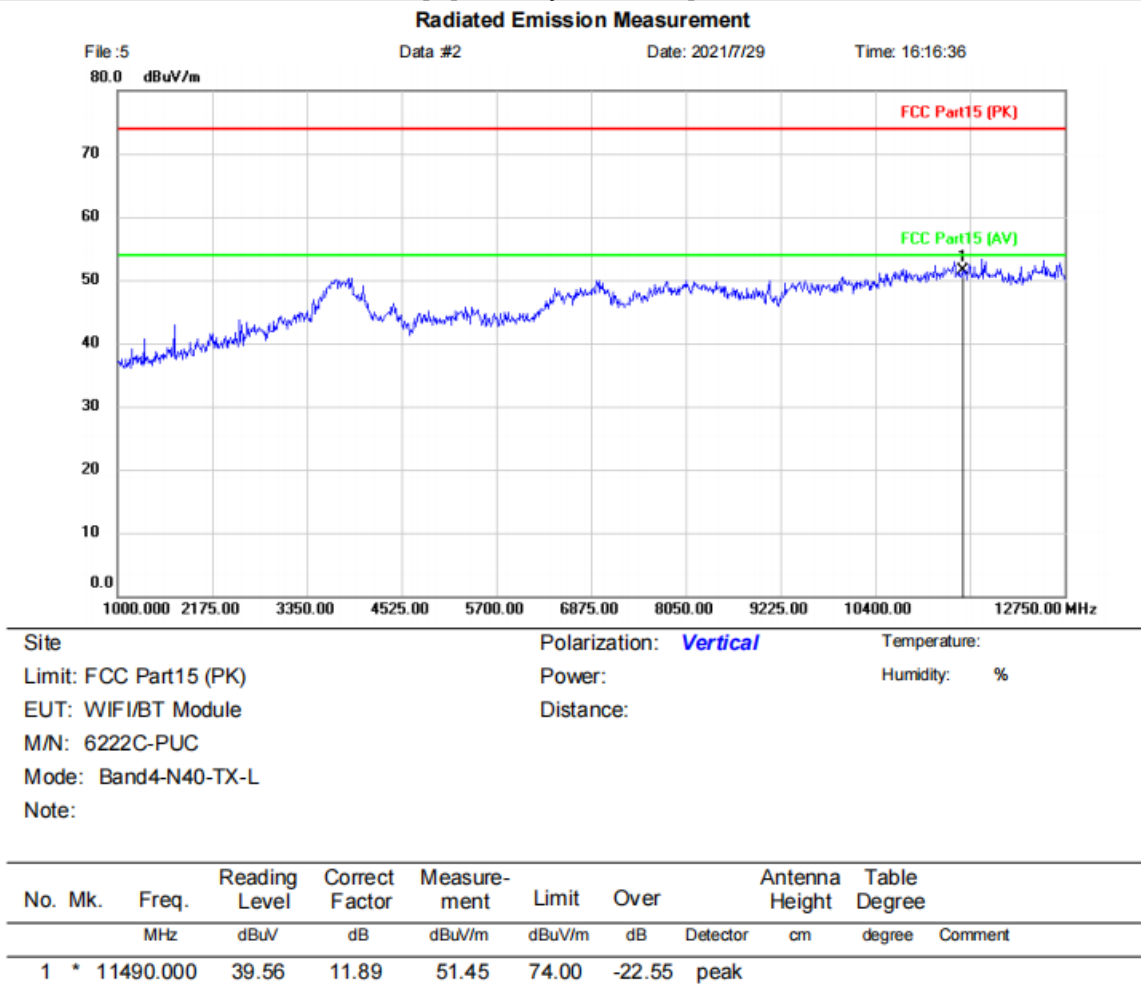


*:Maximum data x:Over limit !:over margin

(Reference Only)

Test Result: Pass

[TestMode: TX N40 5755 channel]; [Polarity: Vertical]

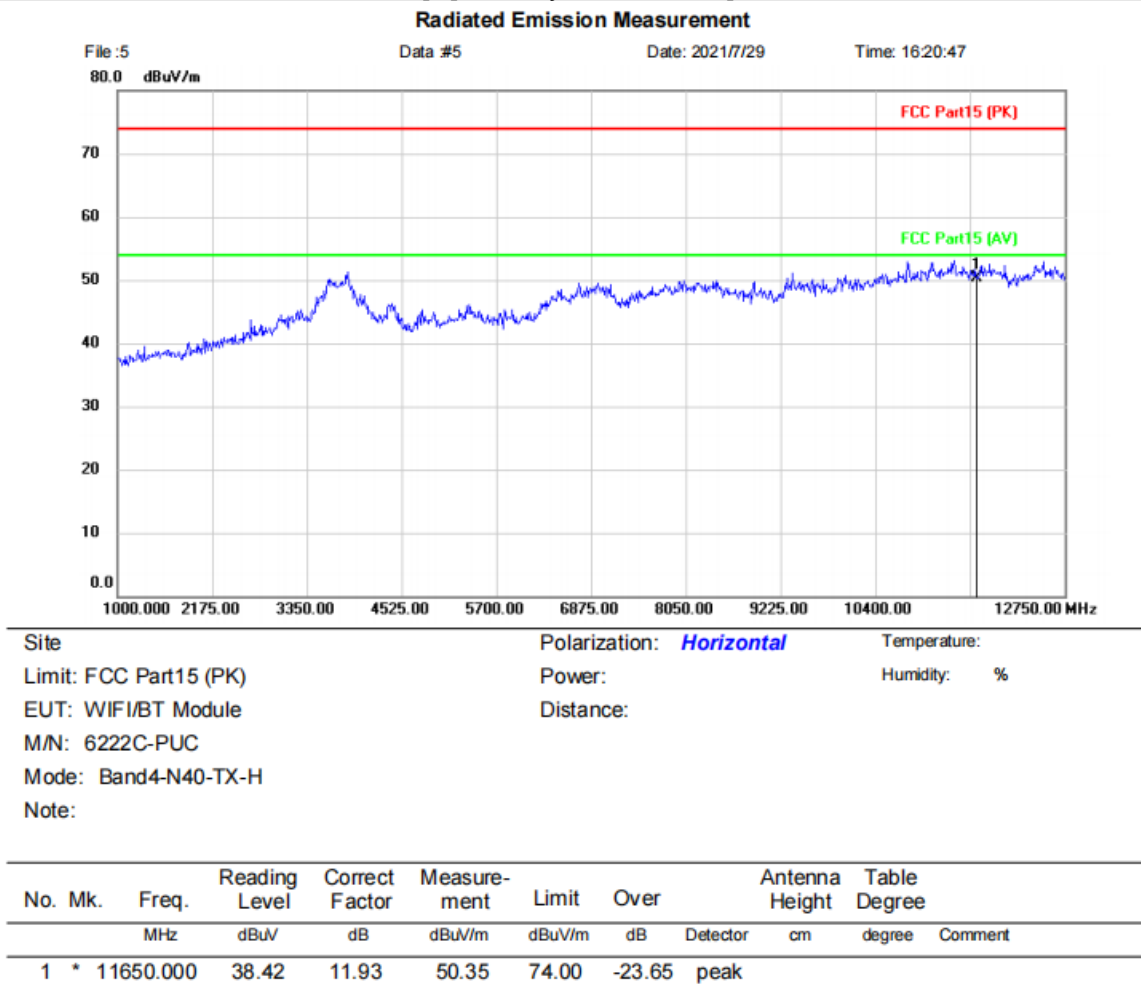


*:Maximum data x:Over limit !:over margin

(Reference Only)

Test Result: Pass

[TestMode: TX N40 5795 channel]; [Polarity: Horizontal]

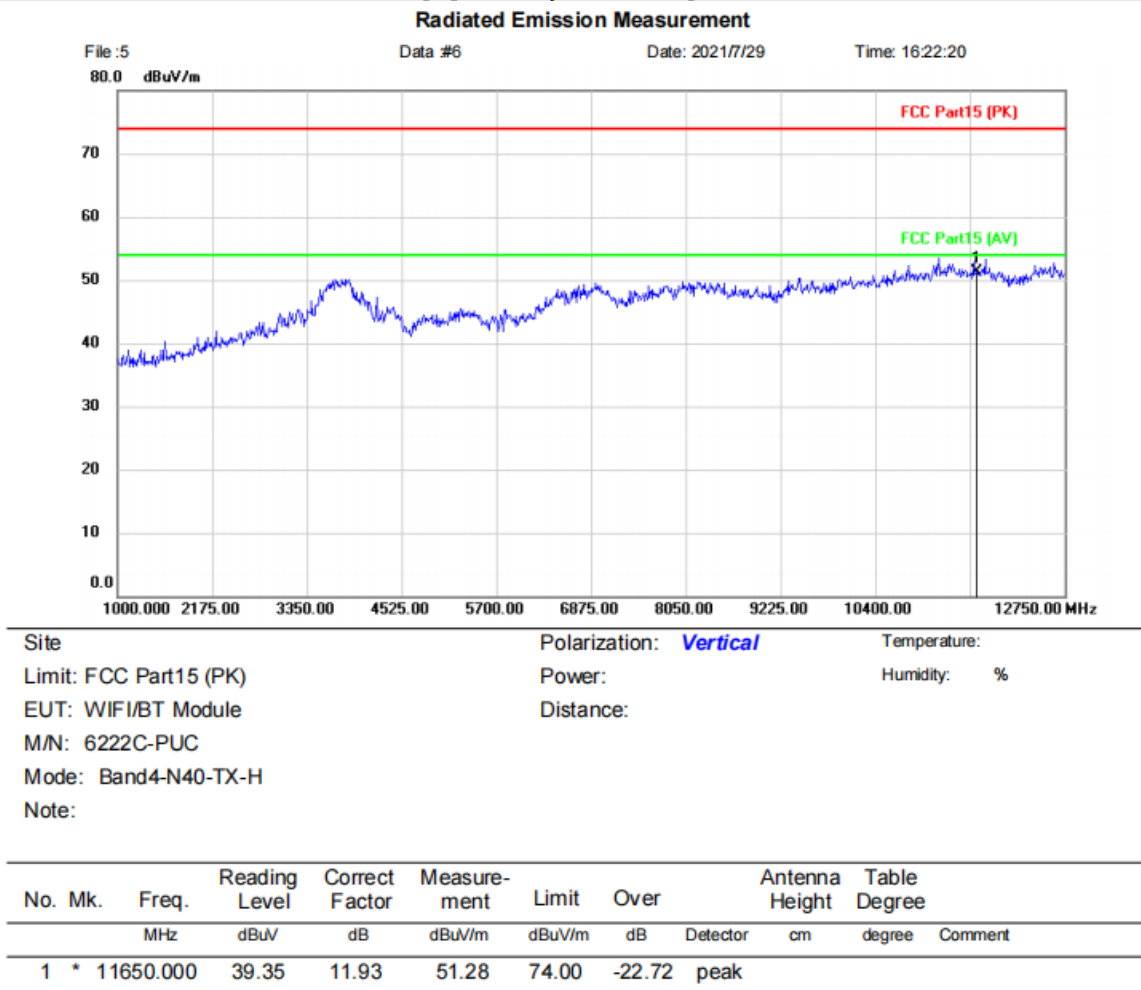


*:Maximum data x:Over limit !:over margin

(Reference Only)

Test Result: Pass

[TestMode: TX N40 5795 channel]; [Polarity: Vertical]



*:Maximum data x:Over limit !:over margin

(Reference Only)

Test Result: Pass

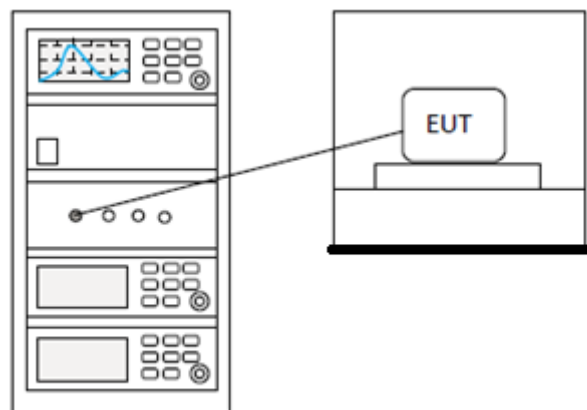
14 DFS: CHANNEL CLOSING TRANSMISSION TIME

| | |
|------------------------|----------------------------------|
| Test Standard | 47 CFR Part 15, Subpart E 15.407 |
| Test Method | KDB 905462 D02 Section 7.8.3 |
| Test Mode (Pre-Scan) | TX |
| Test Mode (Final Test) | TX |
| Tester | Jozu |
| Temperature | 25°C |
| Humidity | 60% |

14.1 LIMITS

| | |
|---------------|---|
| Limit: | 200 milliseconds + an aggregate of 60 milliseconds over remaining 10 second period(should be performed with Radar Type 0. The measurement timing begins at the end of the Radar Type 0 burst. It is comprised of 200 milliseconds starting at the beginning of the Channel Move Time plus any additional intermittent control signals required facilitating a Channel move (an aggregate of 60 milliseconds) during the remainder of the 10 second period. The aggregate duration of control signals will not count quiet periods in between transmissions) |
|---------------|---|

14.2 BLOCK DIAGRAM OF TEST SETUP



14.3 PROCEDURE

- 1) The radar pulse generator is setup to provide a pulse at frequency that the master and client are operating. A type 0 radar pulse with a 1us pulse width and a 1428us PRI is used for the testing.
- 2) The vector signal generator is adjusted to provide the radar burst (18 pulses) at the level of approximately -61dBm at the antenna port of the master device.
- 3) A trigger is provided from the pulse generator to the DFS monitoring system in order to capture the traffic and the occurrence of the radar pulse.
- 4) EUT will associate with the master at channel. The file `j°iperf.exe`± specified by the FCC is streamed

from the PC 2 through the master and the client device to the PC 1 and played in full motion video using Media Player Classic Ver. 6.4.8.6 in order to properly load the network for the entire period of the test.

5) When radar burst with a level equal to the DFS Detection Threshold +1dB is generated on the operating channel of the U-NII device. At time T0 the radar waveform generator sends a burst of pulse of the radar waveform at Detection Threshold +1dB.

6) Observe the transmissions of the EUT at the end of the radar Burst on the Operating Channel. Measure and record the transmissions from the UUT during the observation time (Channel Move Time). One 15 seconds plot is reported for the Short Pulse Radar Type 0. The plot for the Short Pulse Radar Types start at the end of the radar burst. The Channel Move Time will be calculated based on the zoom in 600ms plot of the Short Pulse Radar Type.

7) Measurement of the aggregate duration of the Channel Closed Transmission Time method. With the spectrum analyzer set to zero span tuned to the center frequency of the EUT operating channel at the radar simulated frequency, peak detection, and max hold, the dwell time per bin is given by: $Dwell (0.3ms) = S (12000ms) / B (4000)$; where Dwell is the dwell time per spectrum analyzer sampling bin, S is sweep time and B is the number of spectrum analyzer sampling bins. An upper bound of the aggregate duration of the intermittent control signals of Channel Closing Transmission Time is calculated by: $C (ms) = N \times Dwell (0.3ms)$; where C is the Closing Time, N is the number of spectrum analyzer sampling bins (intermittent control signals) showing a U-NII transmission and Dwell is the dwell time per bin.

8) Measurement the EUT for more than 30 minutes following the channel move time to verify that no transmission or beacons occur on this channel.

14.4 TEST DATA

Pass: Please Refer To DFS Report: BLA-EMC-202106-A6605

BlueAsia

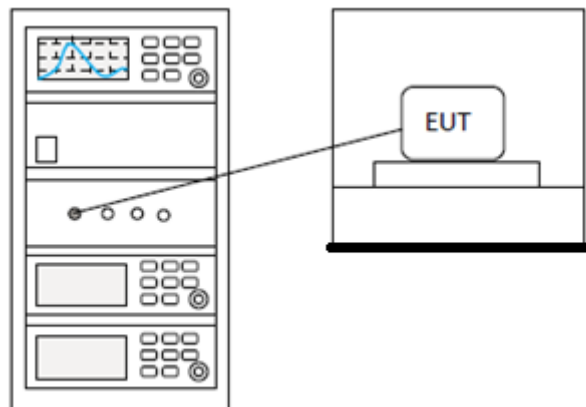
15 DFS: NON-OCCUPANCY PERIOD

| | |
|------------------------|----------------------------------|
| Test Standard | 47 CFR Part 15, Subpart E 15.407 |
| Test Method | KDB 905462 D02 Section 7.8.3 |
| Test Mode (Pre-Scan) | TX |
| Test Mode (Final Test) | TX |
| Tester | Jozu |
| Temperature | 25°C |
| Humidity | 60% |

15.1 LIMITS

| | |
|---------------|--------------------|
| Limit: | Minimum 30 minutes |
|---------------|--------------------|

15.2 BLOCK DIAGRAM OF TEST SETUP



15.3 PROCEDURE

- 1) The radar pulse generator is setup to provide a pulse at frequency that the master and client are operating. A type 0 radar pulse with a 1us pulse width and a 1428us PRI is used for the testing.
- 2) The vector signal generator is adjusted to provide the radar burst (18 pulses) at the level of approximately -61dBm at the antenna port of the master device.
- 3) A trigger is provided from the pulse generator to the DFS monitoring system in order to capture the traffic and the occurrence of the radar pulse.
- 4) EUT will associate with the master at channel. The file `iperf.exe` specified by the FCC is streamed from the PC 2 through the master and the client device to the PC 1 and played in full motion video using Media Player Classic Ver. 6.4.8.6 in order to properly load the network for the entire period of the test.
- 5) When radar burst with a level equal to the DFS Detection Threshold +1dB is generated on the operating channel of the U-NII device. At time T0 the radar waveform generator sends a burst of pulse of the radar waveform at Detection Threshold +1dB.
- 6) Observe the transmissions of the EUT at the end of the radar Burst on the Operating Channel.

Measure and record the transmissions from the UUT during the observation time (Channel Move Time). One 15 seconds plot is reported for the Short Pulse Radar Type 0. The plot for the Short Pulse Radar Types start at the end of the radar burst. The Channel Move Time will be calculated based on the zoom in 600ms plot of the Short Pulse Radar Type.

7) Measurement of the aggregate duration of the Channel Closed Transmission Time method. With the spectrum analyzer set to zero span tuned to the center frequency of the EUT operating channel at the radar simulated frequency, peak detection, and max hold, the dwell time per bin is given by: $Dwell (0.3ms) = S (12000ms) / B (4000)$; where Dwell is the dwell time per spectrum analyzer sampling bin, S is sweep time and B is the number of spectrum analyzer sampling bins. An upper bound of the aggregate duration of the intermittent control signals of Channel Closing Transmission Time is calculated by: $C (ms) = N \times Dwell (0.3ms)$; where C is the Closing Time, N is the number of spectrum analyzer sampling bins (intermittent control signals) showing a U-NII transmission and Dwell is the dwell time per bin.

8) Measurement the EUT for more than 30 minutes following the channel move time to verify that no transmission or beacons occur on this channel.

15.4 TEST DATA**Pass: Please Refer To DFS Report: BLA-EMC-202106-A6605**

BlueAsia

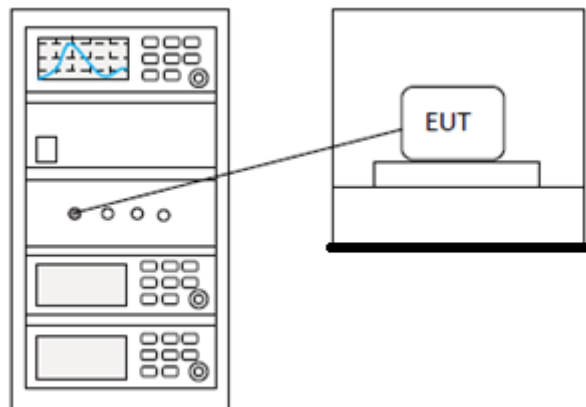
16 PEAK POWER SPECTRUM DENSITY

| | |
|------------------------|----------------------------------|
| Test Standard | 47 CFR Part 15, Subpart E 15.407 |
| Test Method | KDB 789033 D02 II F |
| Test Mode (Pre-Scan) | TX |
| Test Mode (Final Test) | TX |
| Tester | Jozu |
| Temperature | 25℃ |
| Humidity | 60% |

16.1 LIMITS

| Frequency band(MHz) | Limit |
|---------------------|--|
| 5150-5250 | ≤17dBm in 1MHz for master device |
| | ≤11dBm in 1MHz for client device |
| 5250-5350 | ≤11dBm in 1MHz for client device |
| 5470-5725 | ≤11dBm in 1MHz for client device |
| 5725-5850 | ≤30dBm in 500 kHz |
| Remark: | The maximum power spectral density is measured as a conducted emission by direct connection of a calibrated test instrument to the equipment under test. |

16.2 BLOCK DIAGRAM OF TEST SETUP



16.3 TEST DATA

Pass: Please Refer To Appendix: Appendix1 For Details

BlueAsia

17 TRANSMITTER POWER CONTROL

| | |
|---------------|----------------------------------|
| Test Standard | 47 CFR Part 15, Subpart E 15.407 |
| Test Method | KDB 789033 D02 II E |

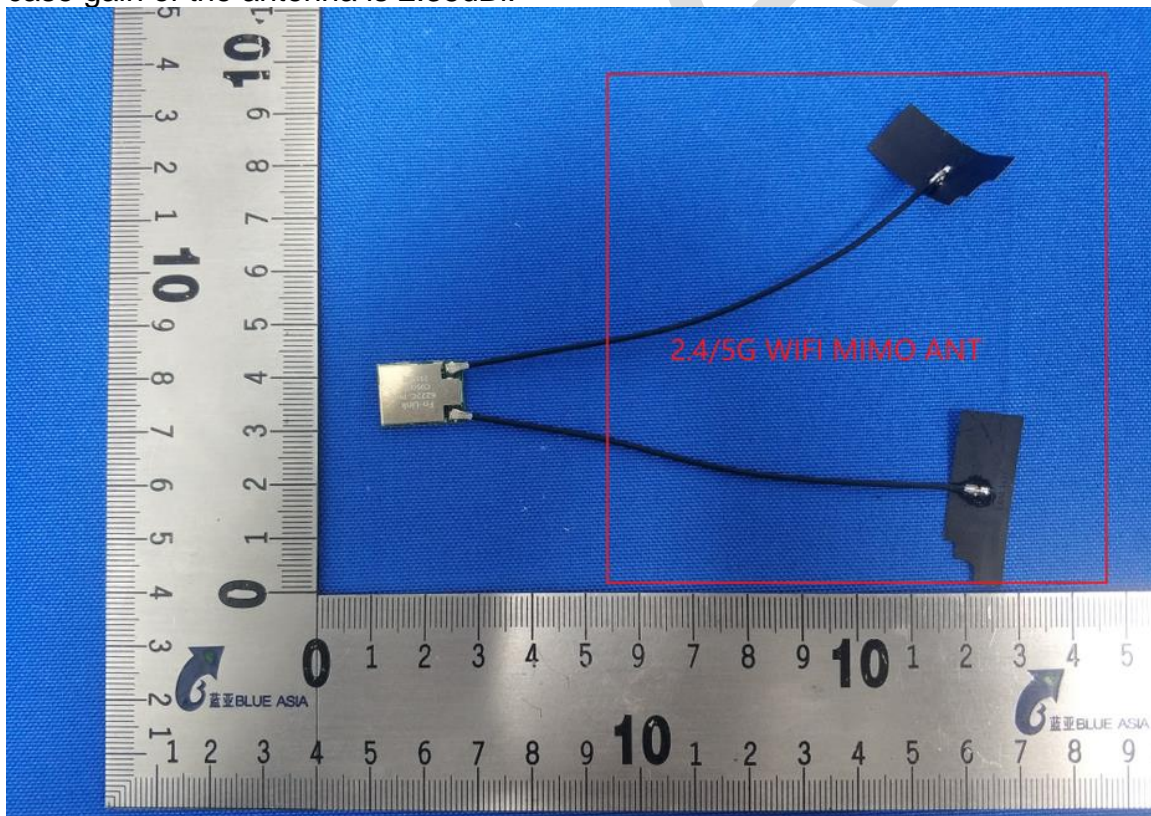
17.1 CONCLUSION

Standard Requirement:

An intentional radiator shall be designed to ensure that no antenna other than that furnished by the responsible party shall be used with the device. The use of a antenna that uses a unique coupling to the intentional radiator, the manufacturer may design the unit permanently attached antenna or of an so that a broken antenna can be replaced by the user, but the use of a standard antenna jack or electrical connector is prohibited.

EUT Antenna:

The antenna is integrated on the main PCB and no consideration of replacement. The best case gain of the antenna is 2.35dBi.



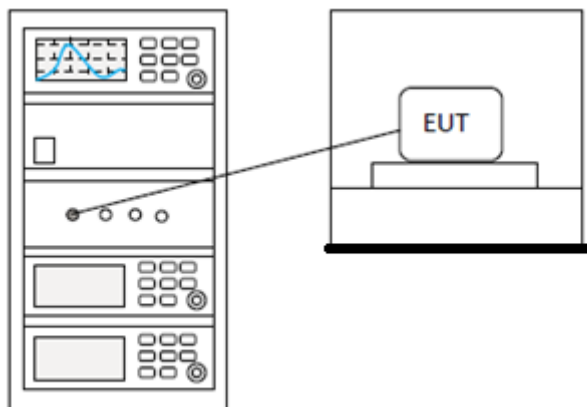
18 MAXIMUM CONDUCTED OUTPUT POWER

| | |
|------------------------|----------------------------------|
| Test Standard | 47 CFR Part 15, Subpart E 15.407 |
| Test Method | KDB 789033 D02 II E |
| Test Mode (Pre-Scan) | TX |
| Test Mode (Final Test) | TX |
| Tester | Jozu |
| Temperature | 25℃ |
| Humidity | 60% |

18.1 LIMITS

| Frequency band(MHz) | Limit |
|---------------------|---|
| 5150-5250 | $\leq 1\text{W}(30\text{dBm})$ for master device |
| | $\leq 250\text{mW}(24\text{dBm})$ for client device |
| 5250-5350 | $\leq 250\text{mW}(24\text{dBm})$ for client device or $11\text{dBm}+10\log B^*$ |
| 5470-5725 | $\leq 250\text{mW}(24\text{dBm})$ for client device or $11\text{dBm}+10\log B^*$ |
| 5725-5850 | $\leq 1\text{W}(30\text{dBm})$ |
| Remark: | <p>* Where B is the 26dB emission bandwidth in MHz.</p> <p>The maximum conducted output power must be measured over any interval of continuous transmission using instrumentation calibrated in terms of an rms-equivalent voltage.</p> |

18.2 BLOCK DIAGRAM OF TEST SETUP



18.3 TEST DATA

Pass: Please Refer To Appendix: Appendix1 For Details

BlueAsia

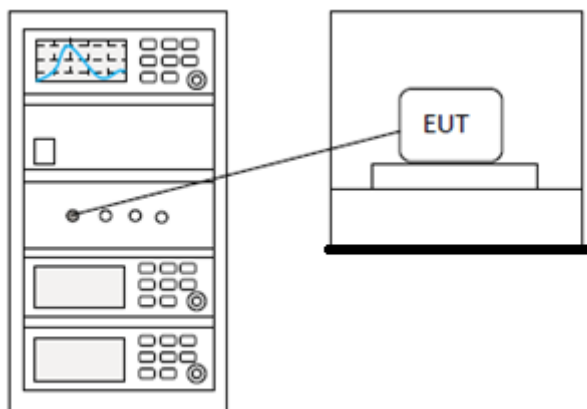
19 MINIMUM 6 DB BANDWIDTH (5.725-5.85 GHZ BAND)

| | |
|------------------------|----------------------------------|
| Test Standard | 47 CFR Part 15, Subpart E 15.407 |
| Test Method | KDB 789033 D02 II C 2 |
| Test Mode (Pre-Scan) | TX |
| Test Mode (Final Test) | TX |
| Tester | Jozu |
| Temperature | 25℃ |
| Humidity | 60% |

19.1 LIMITS

| | |
|---------------|----------------|
| Limit: | ≥ 500 kHz |
|---------------|----------------|

19.2 BLOCK DIAGRAM OF TEST SETUP



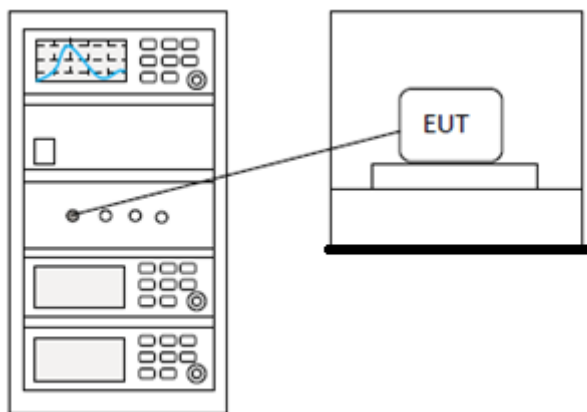
19.3 TEST DATA

Pass: Please Refer To Appendix: Appendix1 For Details

20 26DB EMISSION BANDWIDTH

| | |
|------------------------|----------------------------------|
| Test Standard | 47 CFR Part 15, Subpart E 15.407 |
| Test Method | KDB 789033 D02 II C 1 |
| Test Mode (Pre-Scan) | TX |
| Test Mode (Final Test) | TX |
| Tester | Jozu |
| Temperature | 25℃ |
| Humidity | 60% |

20.1 BLOCK DIAGRAM OF TEST SETUP



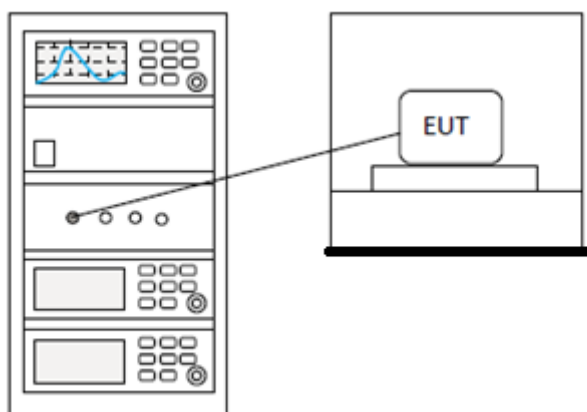
20.2 TEST DATA

Pass: Please Refer To Appendix: Appendix1 For Details

21 99% BANDWIDTH

| | |
|------------------------|----------------------------------|
| Test Standard | 47 CFR Part 15, Subpart E 15.407 |
| Test Method | KDB 789033 II D |
| Test Mode (Pre-Scan) | TX |
| Test Mode (Final Test) | TX |
| Tester | Jozu |
| Temperature | 25℃ |
| Humidity | 60% |

21.1 BLOCK DIAGRAM OF TEST SETUP



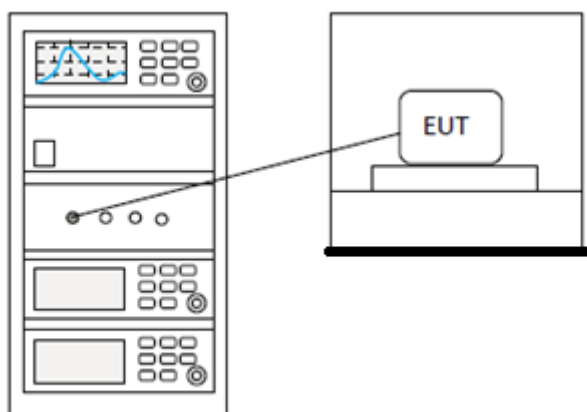
21.2 TEST DATA

Pass: Please Refer To Appendix: Appendix1 For Details

22 DUTY CYCLE

| | |
|------------------------|----------------------------------|
| Test Standard | 47 CFR Part 15, Subpart E 15.407 |
| Test Method | KDB 789033 II B 1 |
| Test Mode (Pre-Scan) | TX |
| Test Mode (Final Test) | TX |
| Tester | Jozu |
| Temperature | 25℃ |
| Humidity | 60% |

22.1 BLOCK DIAGRAM OF TEST SETUP



22.2 TEST DATA

Pass: Please Refer To Appendix: Appendix1 For Details

23 CONDUCTED EMISSIONS AT AC POWER LINE (150KHZ-30MHZ)

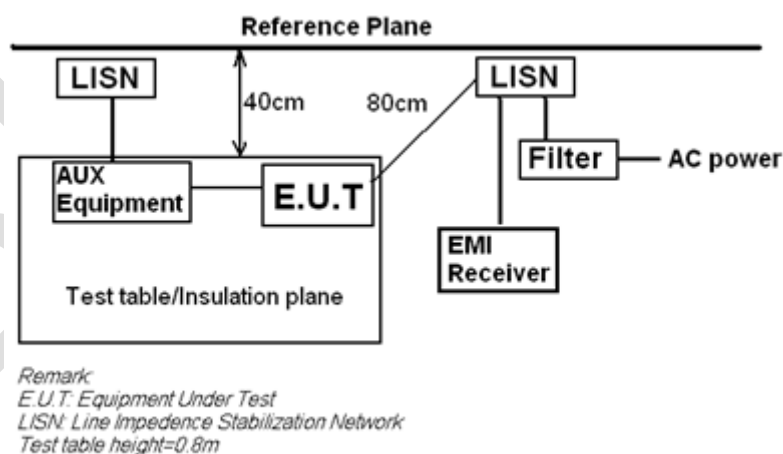
| | |
|------------------------|----------------------------------|
| Test Standard | 47 CFR Part 15, Subpart E 15.407 |
| Test Method | ANSI C63.10 (2013) Section 6.2 |
| Test Mode (Pre-Scan) | Transmitting mode |
| Test Mode (Final Test) | Transmitting mode |
| Tester | Jozu |
| Temperature | 25°C |
| Humidity | 60% |

23.1 LIMITS

| Frequency of emission(MHz) | Conducted limit(dBμV) | |
|----------------------------|-----------------------|-----------|
| | Quasi-peak | Average |
| 0.15-0.5 | 66 to 56* | 56 to 46* |
| 0.5-5 | 56 | 46 |
| 5-30 | 60 | 50 |

*Decreases with the logarithm of the frequency.

23.2 BLOCK DIAGRAM OF TEST SETUP



23.3 PROCEDURE

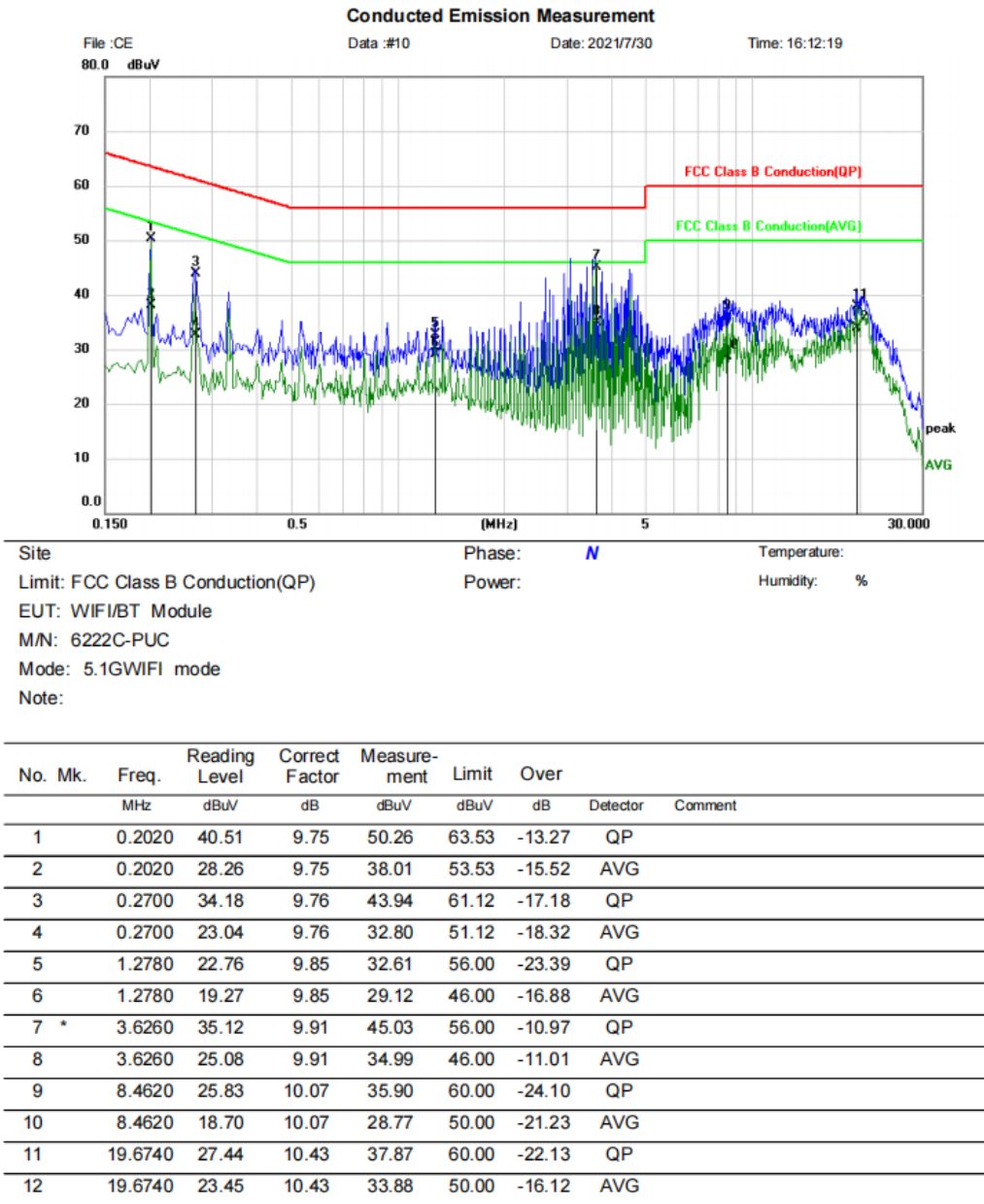
- 1) The mains terminal disturbance voltage test was conducted in a shielded room.
- 2) The EUT was connected to AC power source through a LISN 1 (Line Impedance Stabilization Network) which provides a 50ohm/50H + 5ohm linear impedance. The power cables of all other units of the EUT were connected to a second LISN 2, which was bonded to the ground reference plane in the same way as the LISN 1 for the unit being measured. A multiple socket outlet strip was used to connect multiple power cables to a single LISN provided the rating of the LISN was not exceeded.

- 3) The tabletop EUT was placed upon a non-metallic table 0.8m above the ground reference plane. And for floor-standing arrangement, the EUT was placed on the horizontal ground reference plane,
- 4) The test was performed with a vertical ground reference plane. The rear of the EUT shall be 0.4 m from the vertical ground reference plane. The vertical ground reference plane was bonded to the horizontal ground reference plane. The LISN 1 was placed 0.8 m from the boundary of the unit under test and bonded to a ground reference plane for LISNs mounted on top of the ground reference plane. This distance was between the closest points of the LISN 1 and the EUT. All other units of the EUT and associated equipment was at least 0.8 m from the LISN 2.
- 5) In order to find the maximum emission, the relative positions of equipment and all of the interface cables must be changed according to ANSI C63.10 on conducted measurement.

Remark: $LISN = Read\ Level + Cable\ Loss + LISN\ Factor$

23.4 TEST DATA

[TestMode: Transmitting mode]; [Line: Nutral]

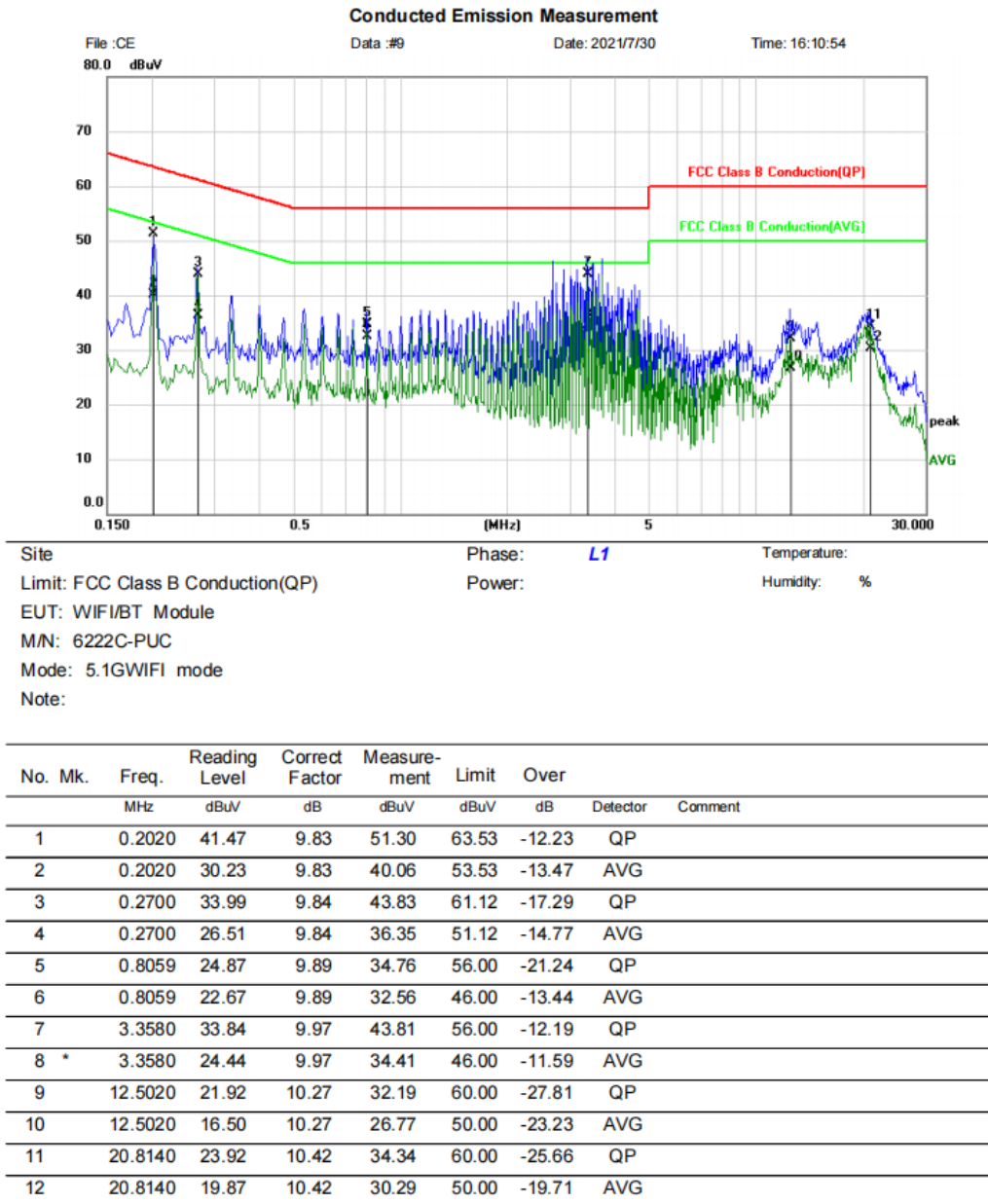


*:Maximum data x:Over limit !:over margin

(Reference Only)

Test Result: Pass

[TestMode: Transmitting mode]; [Line: Line]



*:Maximum data x:Over limit !:over margin

(Reference Only)

Test Result: Pass

24 ANTENNA REQUIREMENT

| | |
|---------------|----------------------------------|
| Test Standard | 47 CFR Part 15, Subpart E 15.407 |
| Test Method | N/A |

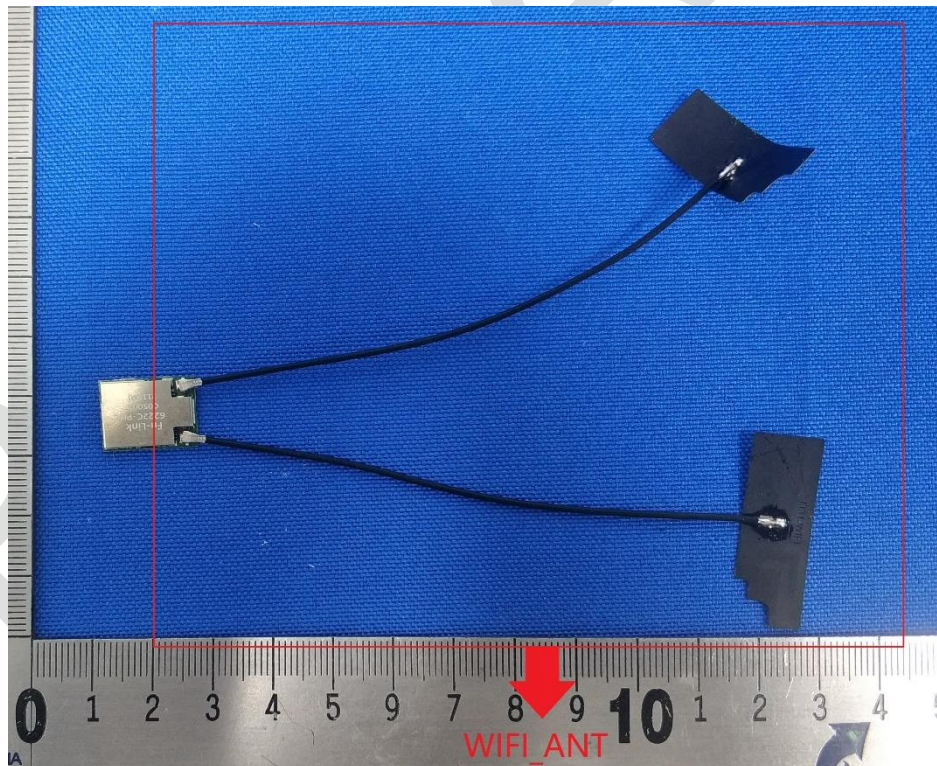
24.1 CONCLUSION

Standard Requirement:

An intentional radiator shall be designed to ensure that no antenna other than that furnished by the responsible party shall be used with the device. The use of an antenna that uses a unique coupling to the intentional radiator, the manufacturer may design the unit permanently attached antenna or of an so that a broken antenna can be replaced by the user, but the use of a standard antenna jack or electrical connector is prohibited.

EUT Antenna:

The antenna is integrated on the main PCB and no consideration of replacement. The best case gain of the antenna is 2.35dBi.



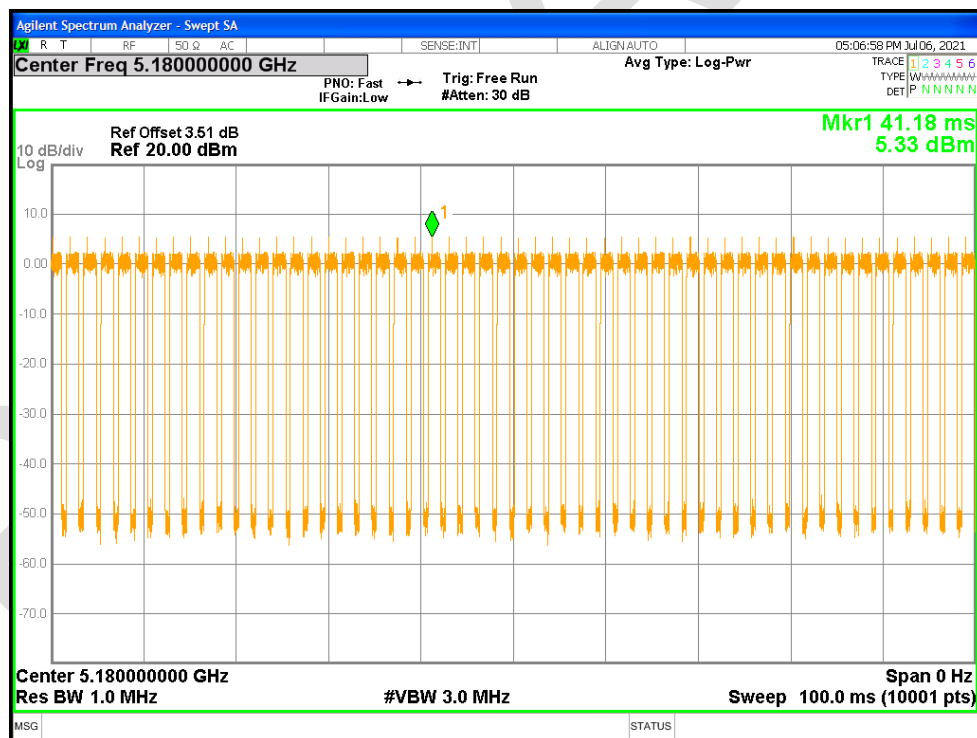
25 APPENDIX

25.1 DUTY CYCLE

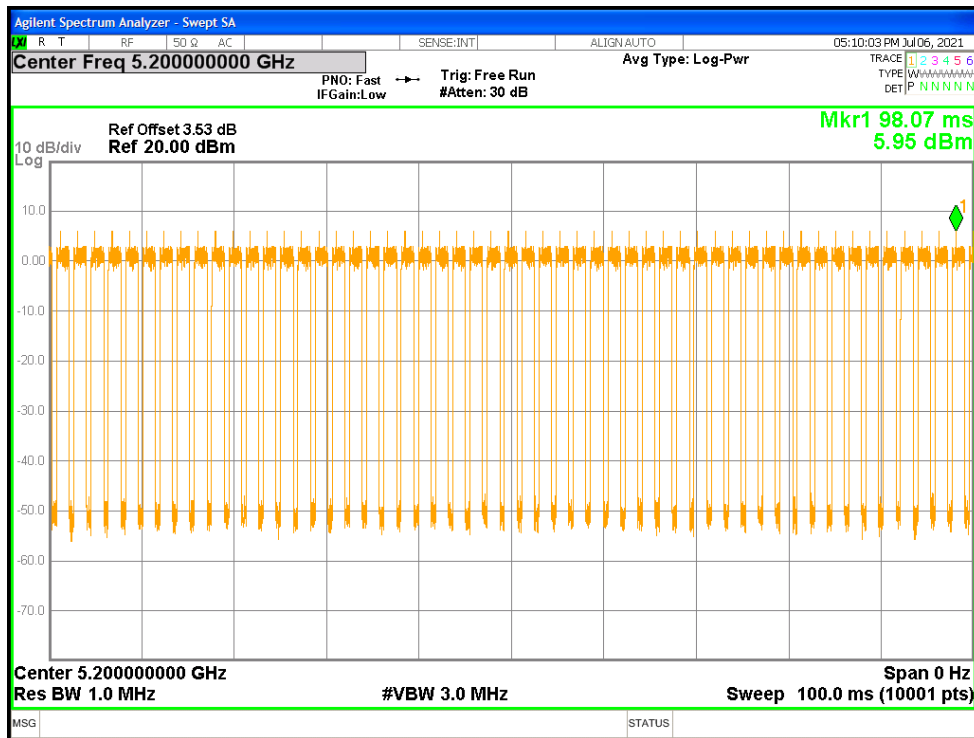
| Condition | Mode | Frequency (MHz) | Antenna | Duty Cycle (%) | Correction Factor (dB) |
|-----------|------|-----------------|---------|----------------|------------------------|
| NVNT | a | 5180 | Sum | 73.88 | 1.31 |
| NVNT | a | 5200 | Sum | 73.54 | 1.33 |
| NVNT | a | 5240 | Sum | 73.55 | 1.33 |
| NVNT | a | 5260 | Sum | 73.54 | 1.33 |
| NVNT | a | 5280 | Sum | 73.54 | 1.33 |
| NVNT | a | 5320 | Sum | 73.83 | 1.32 |
| NVNT | a | 5500 | Sum | 73.55 | 1.33 |
| NVNT | a | 5600 | Sum | 73.55 | 1.33 |
| NVNT | a | 5700 | Sum | 74.01 | 1.31 |
| NVNT | a | 5745 | Sum | 73.72 | 1.32 |
| NVNT | a | 5785 | Sum | 73.55 | 1.33 |
| NVNT | a | 5825 | Sum | 73.73 | 1.32 |
| NVNT | ac20 | 5180 | Sum | 26.5 | 5.77 |
| NVNT | ac20 | 5200 | Sum | 26.43 | 5.78 |
| NVNT | ac20 | 5240 | Sum | 26.54 | 5.76 |
| NVNT | ac20 | 5260 | Sum | 26.49 | 5.77 |
| NVNT | ac20 | 5280 | Sum | 26.45 | 5.78 |
| NVNT | ac20 | 5320 | Sum | 26.5 | 5.77 |
| NVNT | ac20 | 5500 | Sum | 26.51 | 5.77 |
| NVNT | ac20 | 5600 | Sum | 26.51 | 5.77 |
| NVNT | ac20 | 5700 | Sum | 26.49 | 5.77 |
| NVNT | ac20 | 5745 | Sum | 26.43 | 5.78 |
| NVNT | ac20 | 5785 | Sum | 26.44 | 5.78 |
| NVNT | ac20 | 5825 | Sum | 26.51 | 5.77 |
| NVNT | ac40 | 5190 | Sum | 18.66 | 7.29 |
| NVNT | ac40 | 5230 | Sum | 18.66 | 7.29 |
| NVNT | ac40 | 5270 | Sum | 18.67 | 7.29 |
| NVNT | ac40 | 5310 | Sum | 18.62 | 7.3 |
| NVNT | ac40 | 5510 | Sum | 18.62 | 7.3 |
| NVNT | ac40 | 5590 | Sum | 18.63 | 7.3 |
| NVNT | ac40 | 5670 | Sum | 18.58 | 7.31 |
| NVNT | ac40 | 5755 | Sum | 18.64 | 7.3 |
| NVNT | ac40 | 5795 | Sum | 18.62 | 7.3 |
| NVNT | ac80 | 5210 | Sum | 40.05 | 3.97 |
| NVNT | ac80 | 5290 | Sum | 40.02 | 3.98 |
| NVNT | ac80 | 5530 | Sum | 40.05 | 3.97 |
| NVNT | ac80 | 5610 | Sum | 40.01 | 3.98 |
| NVNT | ac80 | 5690 | Sum | 40.11 | 3.97 |
| NVNT | ac80 | 5775 | Sum | 40.19 | 3.96 |
| NVNT | n20 | 5180 | Sum | 26.02 | 5.85 |
| NVNT | n20 | 5200 | Sum | 26.03 | 5.85 |
| NVNT | n20 | 5240 | Sum | 26.05 | 5.84 |

| | | | | | |
|------|-----|------|-----|-------|------|
| NVNT | n20 | 5260 | Sum | 26.02 | 5.85 |
| NVNT | n20 | 5280 | Sum | 26.04 | 5.84 |
| NVNT | n20 | 5320 | Sum | 26.02 | 5.85 |
| NVNT | n20 | 5500 | Sum | 26.02 | 5.85 |
| NVNT | n20 | 5600 | Sum | 26 | 5.85 |
| NVNT | n20 | 5700 | Sum | 26 | 5.85 |
| NVNT | n20 | 5745 | Sum | 26 | 5.85 |
| NVNT | n20 | 5785 | Sum | 26 | 5.85 |
| NVNT | n20 | 5825 | Sum | 26.01 | 5.85 |
| NVNT | n40 | 5190 | Sum | 18.04 | 7.44 |
| NVNT | n40 | 5230 | Sum | 18.05 | 7.44 |
| NVNT | n40 | 5270 | Sum | 18.04 | 7.44 |
| NVNT | n40 | 5310 | Sum | 18.15 | 7.41 |
| NVNT | n40 | 5510 | Sum | 18.03 | 7.44 |
| NVNT | n40 | 5590 | Sum | 18.08 | 7.43 |
| NVNT | n40 | 5670 | Sum | 18.02 | 7.44 |
| NVNT | n40 | 5755 | Sum | 18.04 | 7.44 |
| NVNT | n40 | 5795 | Sum | 18.04 | 7.44 |

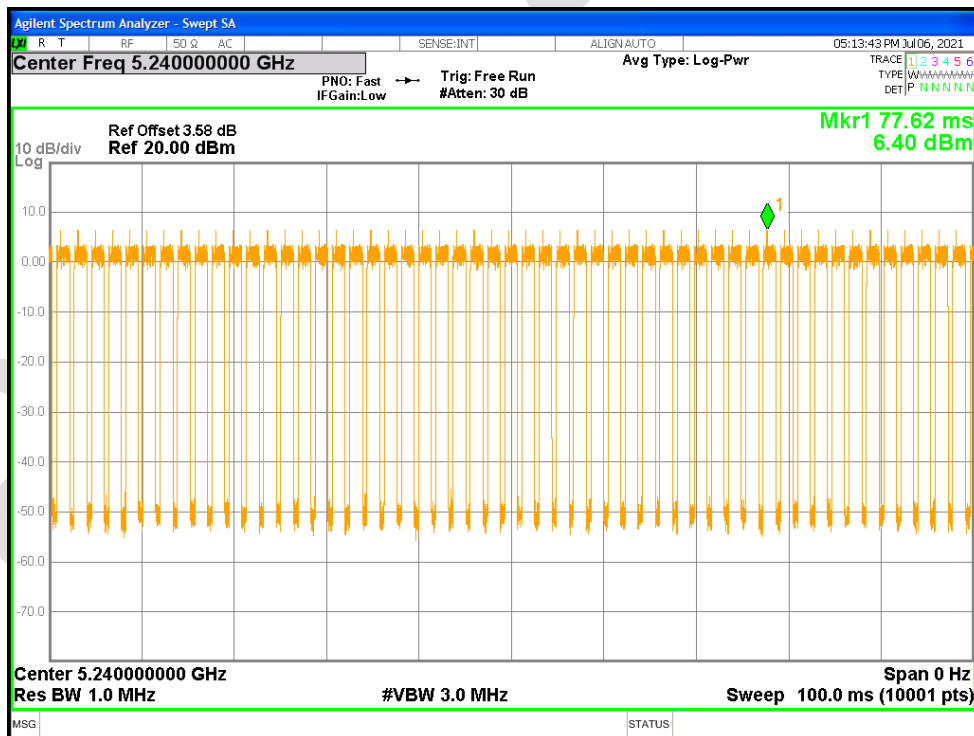
Duty Cycle NVNT a 5180MHz Sum



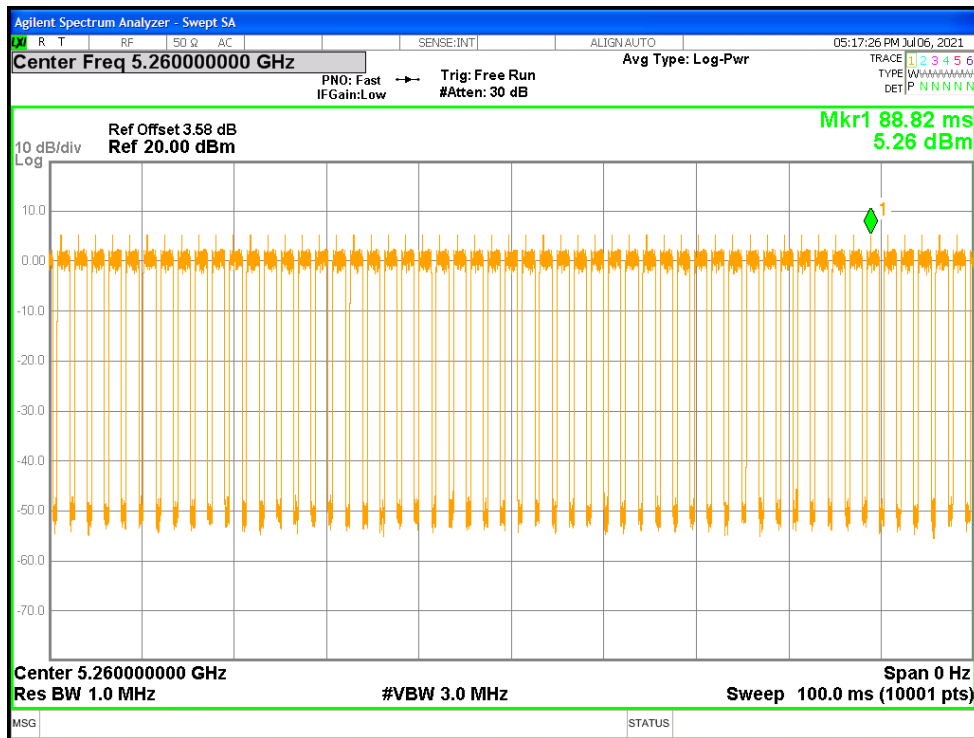
Duty Cycle NVNT a 5200MHz Sum



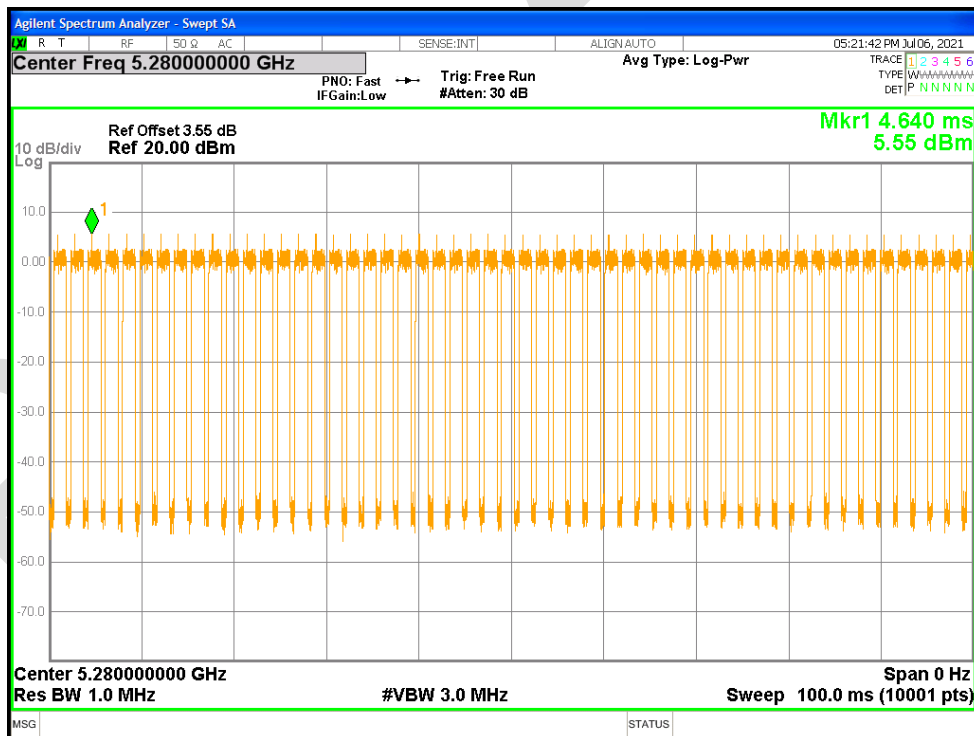
Duty Cycle NVNT a 5240MHz Sum



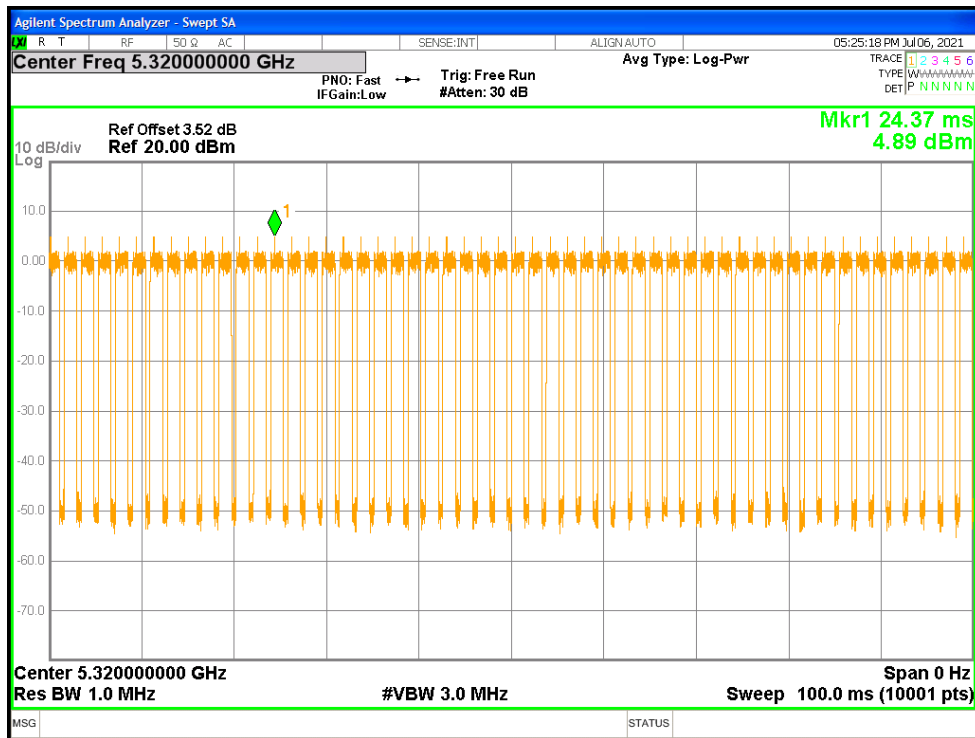
Duty Cycle NVNT a 5260MHz Sum



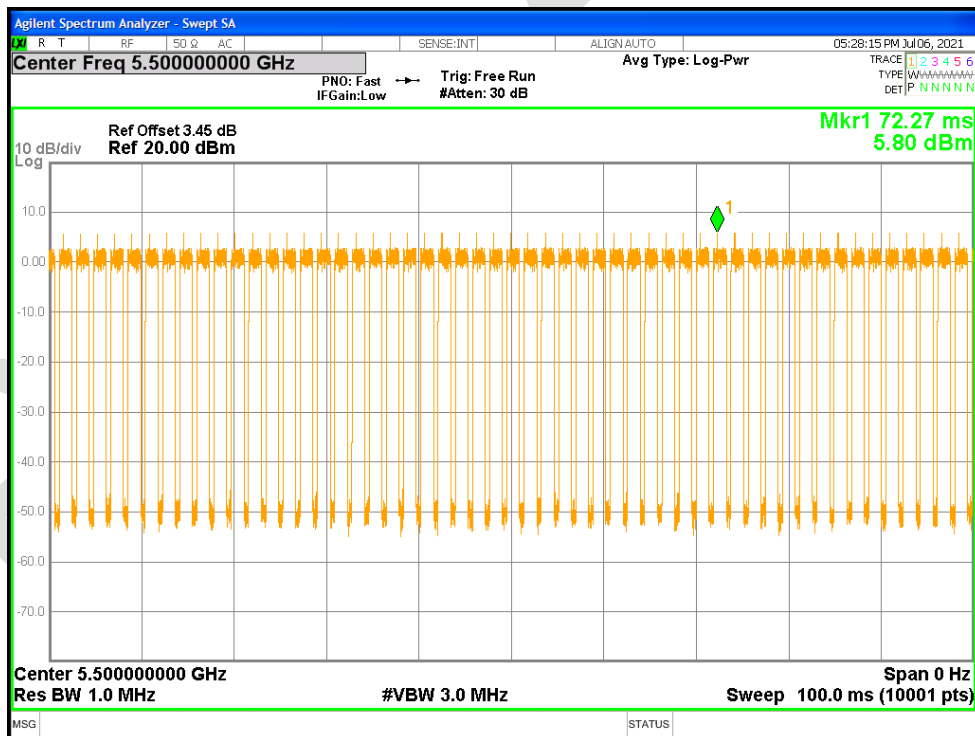
Duty Cycle NVNT a 5280MHz Sum



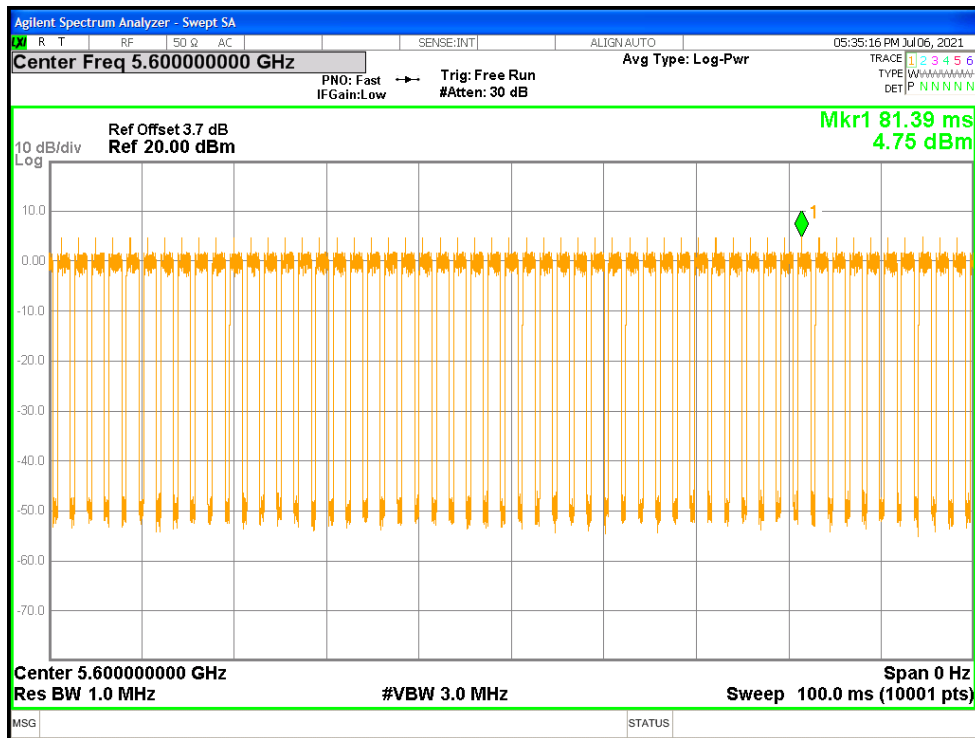
Duty Cycle NVNT a 5320MHz Sum



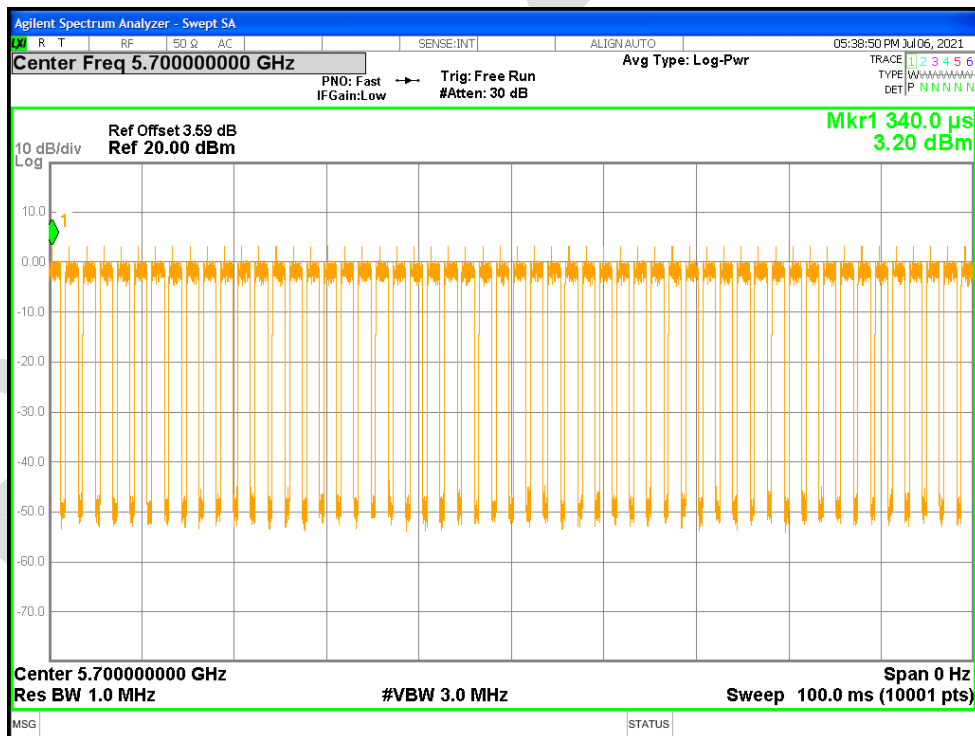
Duty Cycle NVNT a 5500MHz Sum



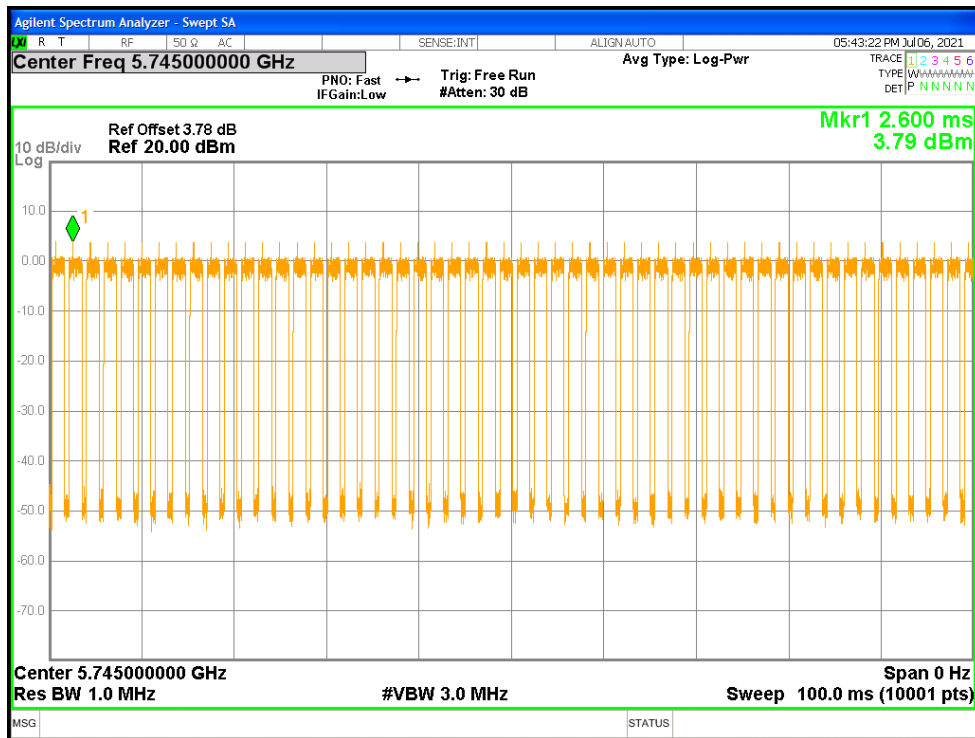
Duty Cycle NVNT a 5600MHz Sum



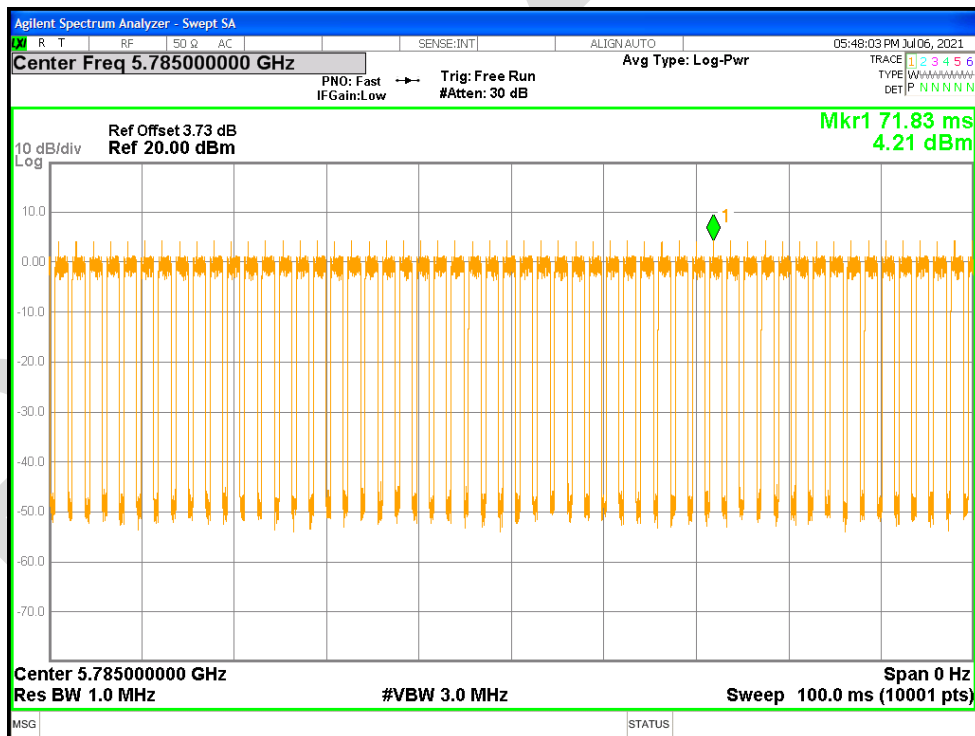
Duty Cycle NVNT a 5700MHz Sum



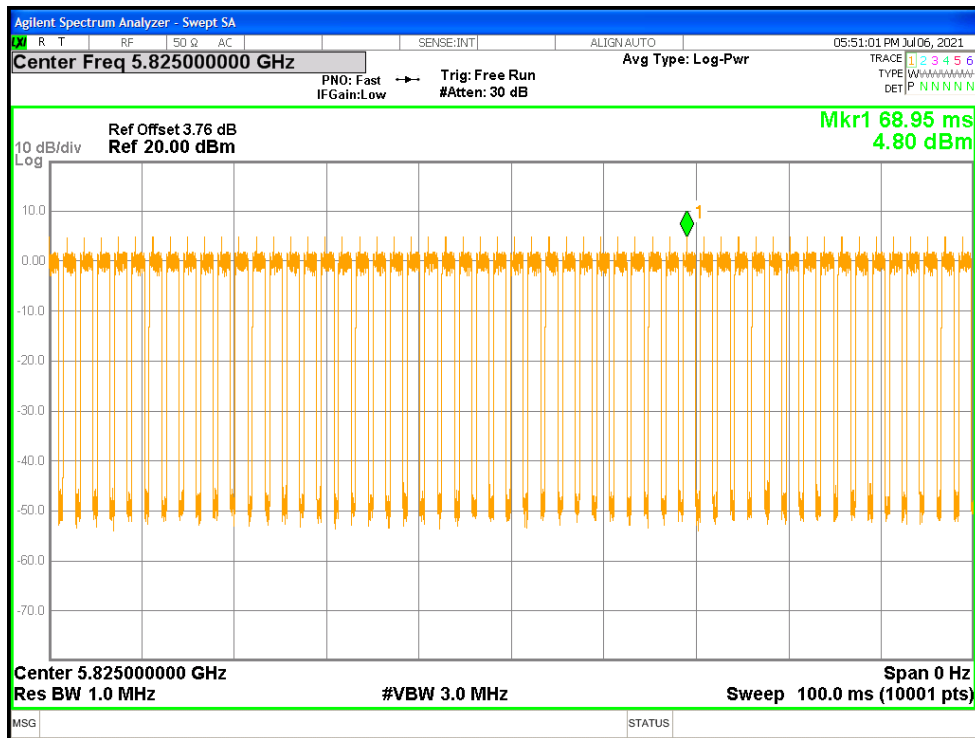
Duty Cycle NVNT a 5745MHz Sum



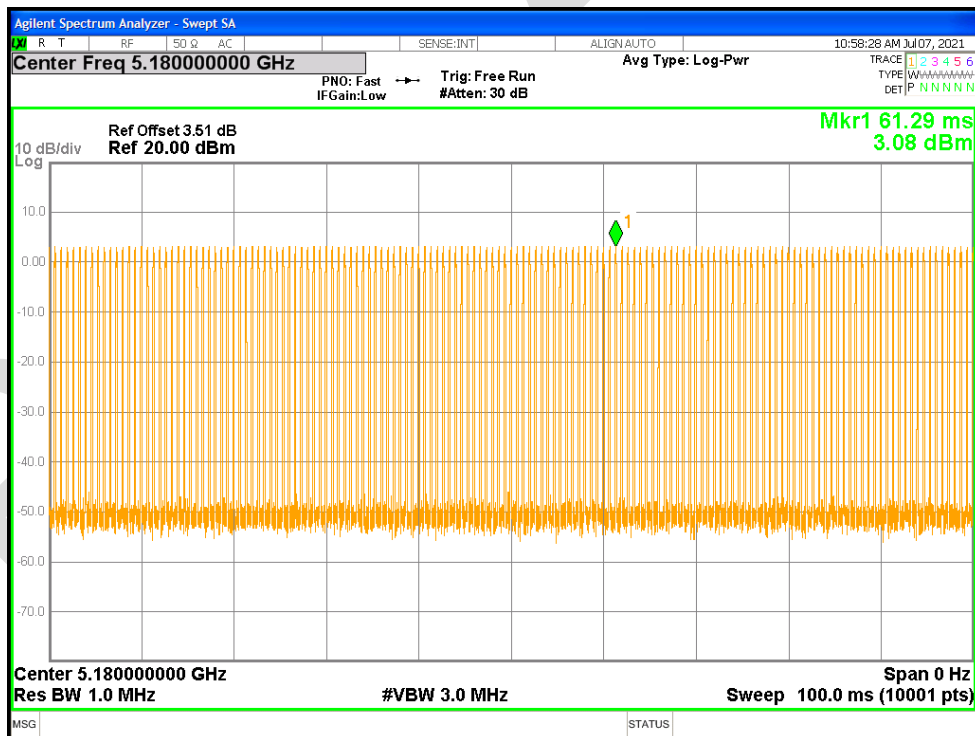
Duty Cycle NVNT a 5785MHz Sum



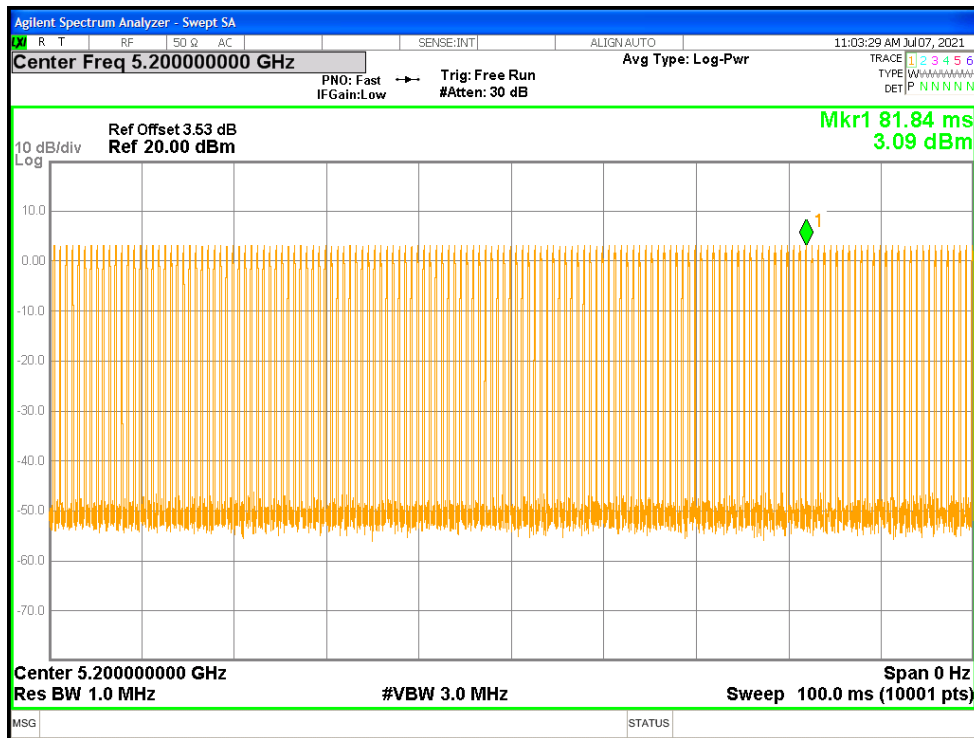
Duty Cycle NVNT a 5825MHz Sum



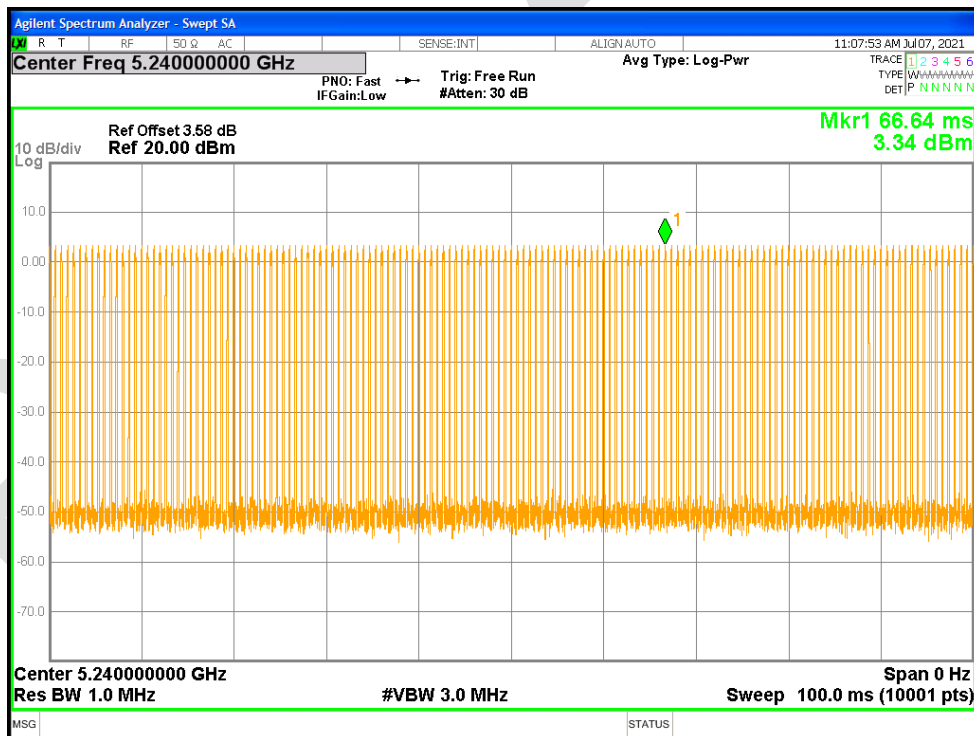
Duty Cycle NVNT ac20 5180MHz Sum



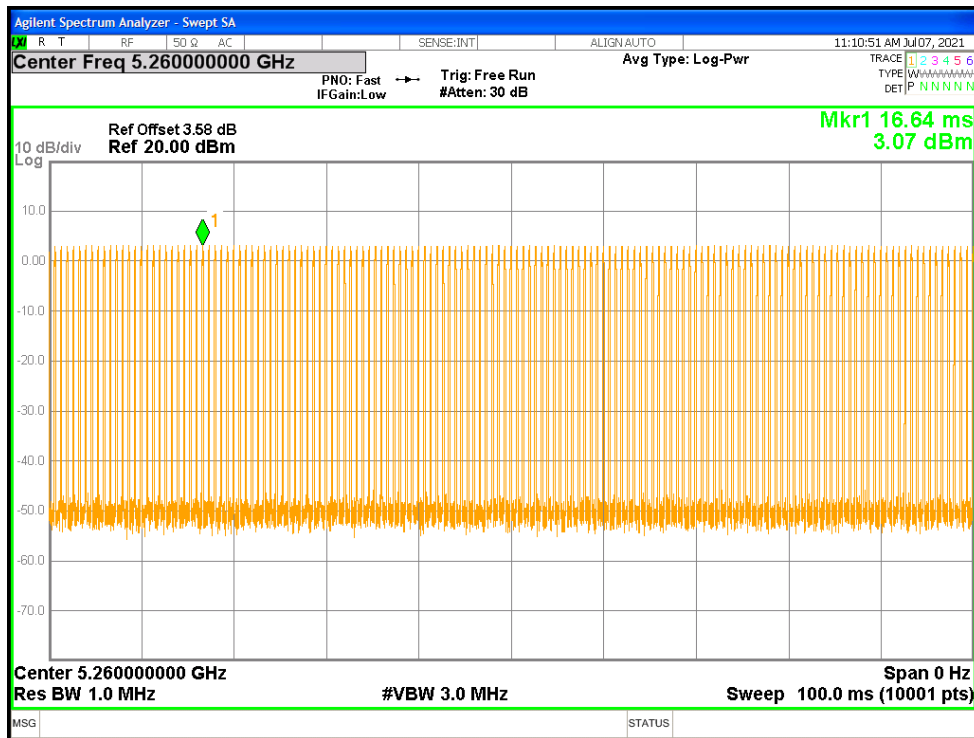
Duty Cycle NVNT ac20 5200MHz Sum



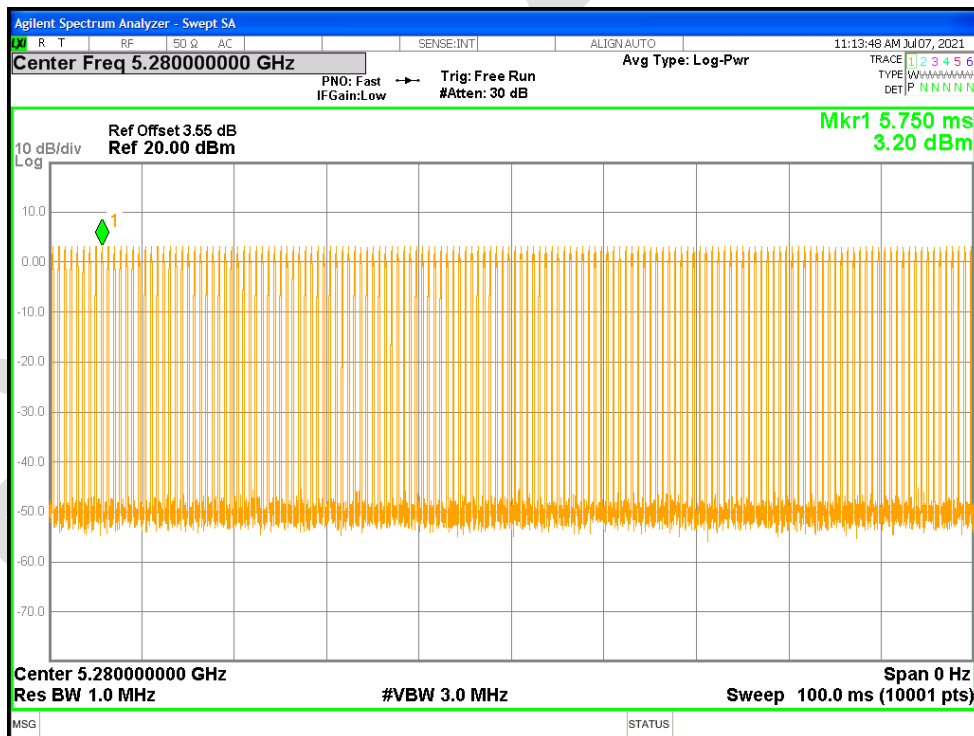
Duty Cycle NVNT ac20 5240MHz Sum



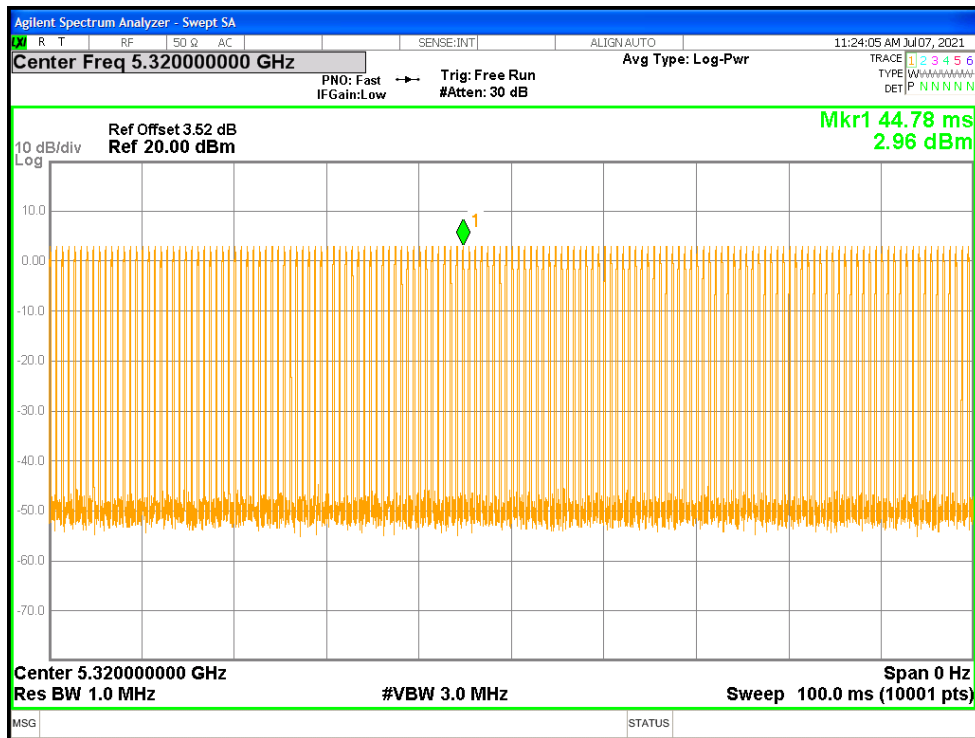
Duty Cycle NVNT ac20 5260MHz Sum



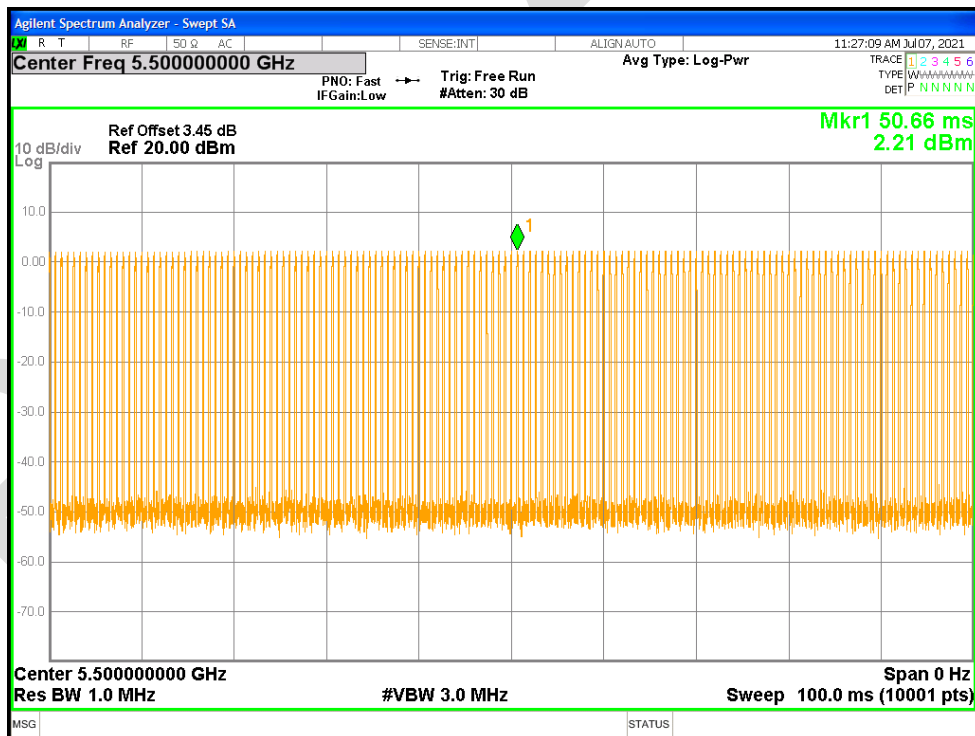
Duty Cycle NVNT ac20 5280MHz Sum



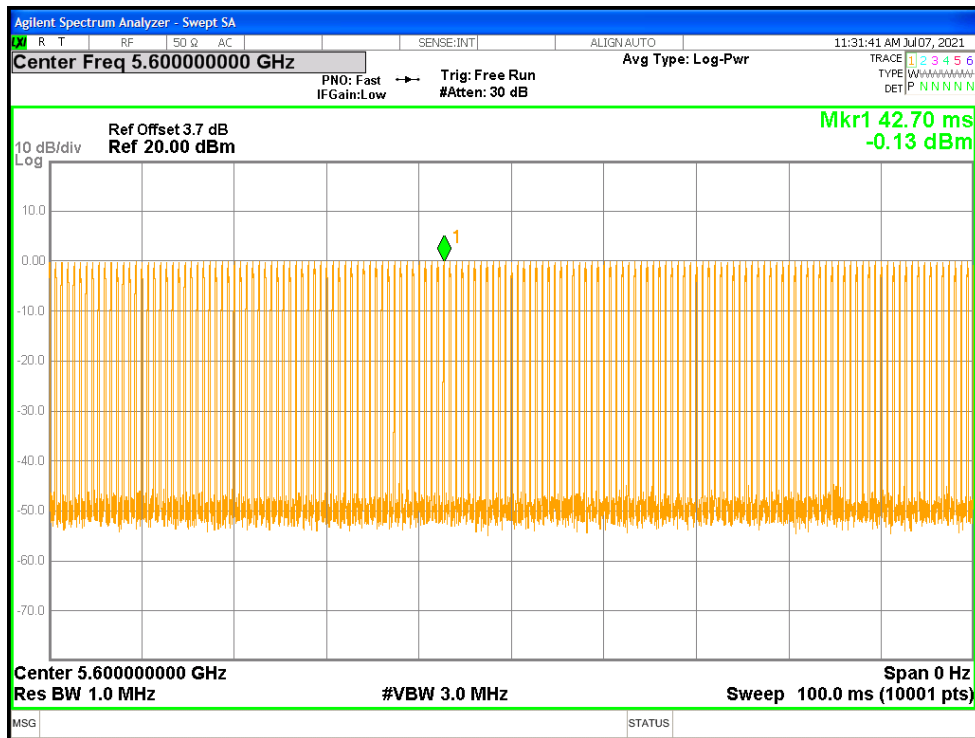
Duty Cycle NVNT ac20 5320MHz Sum



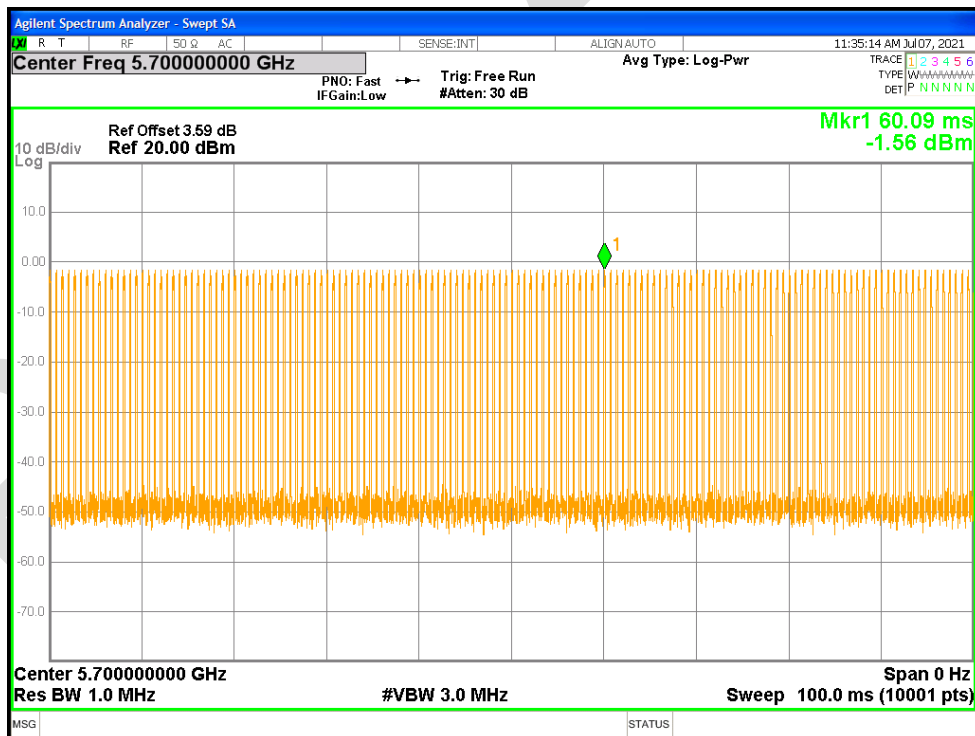
Duty Cycle NVNT ac20 5500MHz Sum



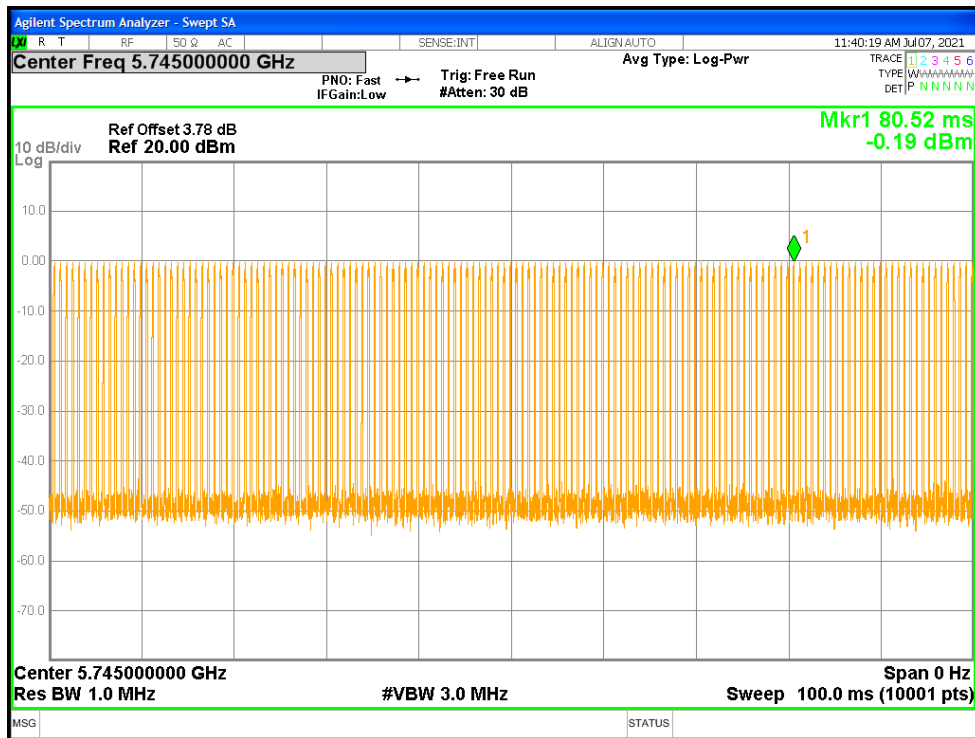
Duty Cycle NVNT ac20 5600MHz Sum



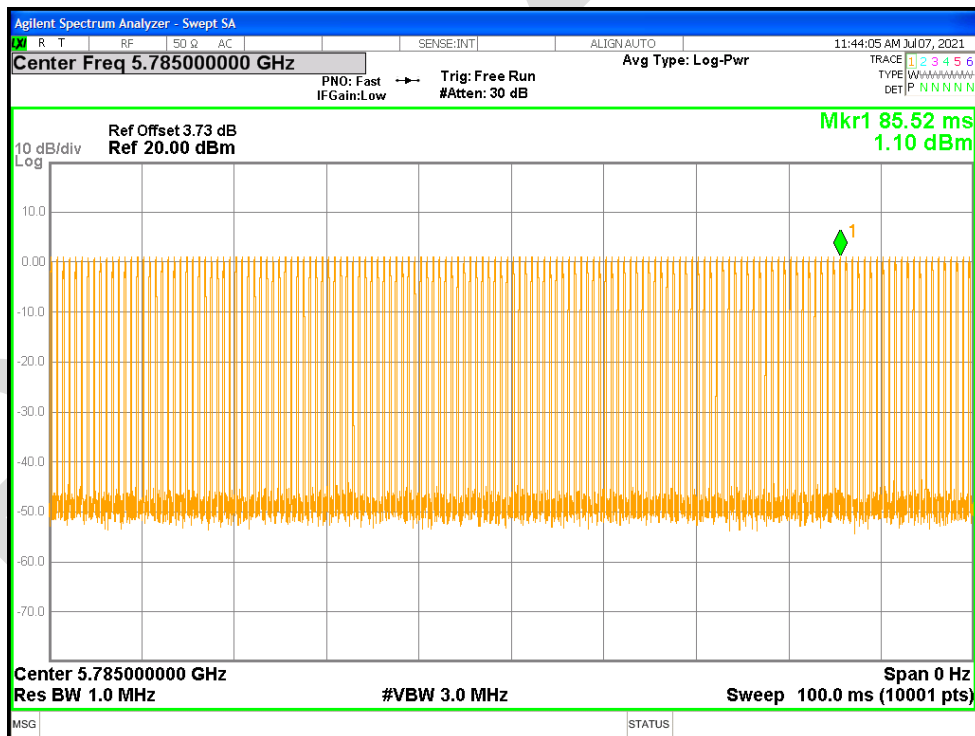
Duty Cycle NVNT ac20 5700MHz Sum



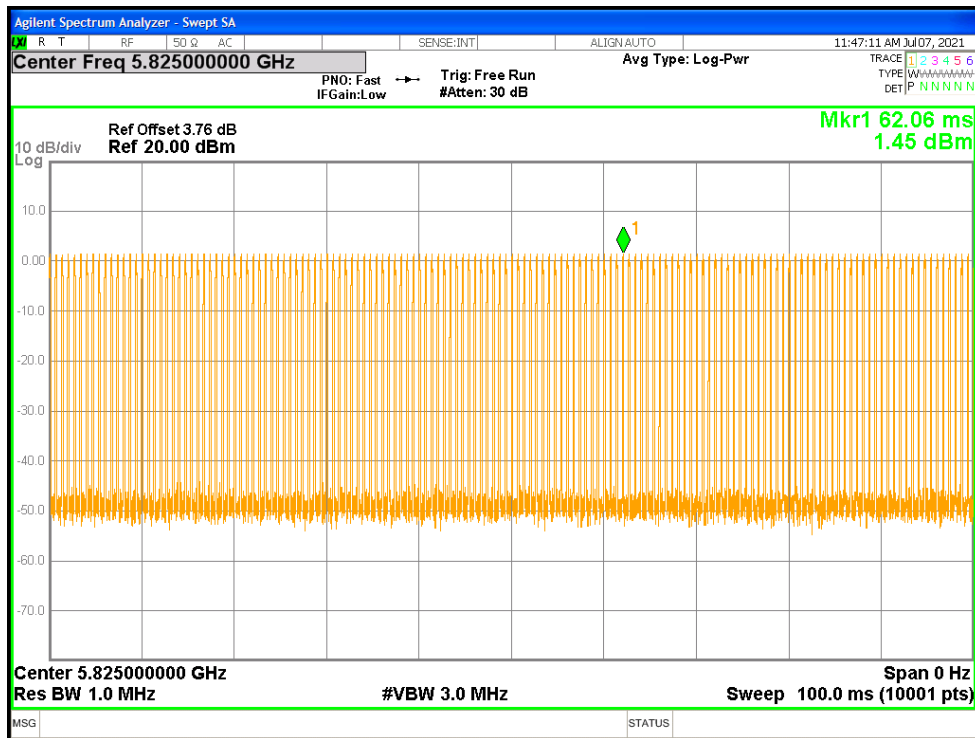
Duty Cycle NVNT ac20 5745MHz Sum



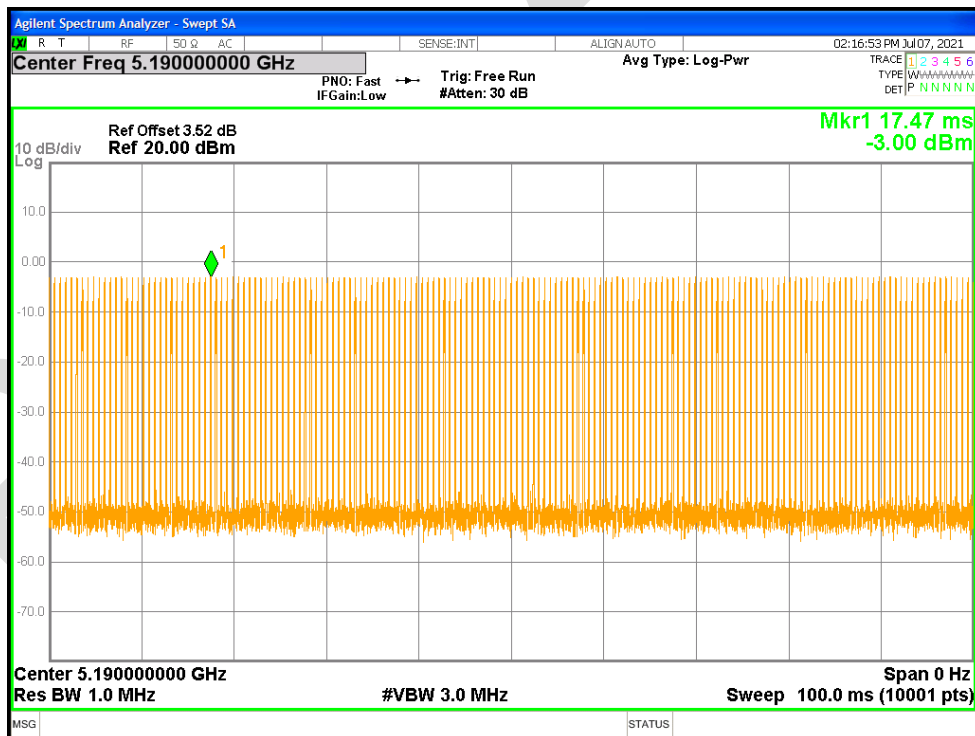
Duty Cycle NVNT ac20 5785MHz Sum



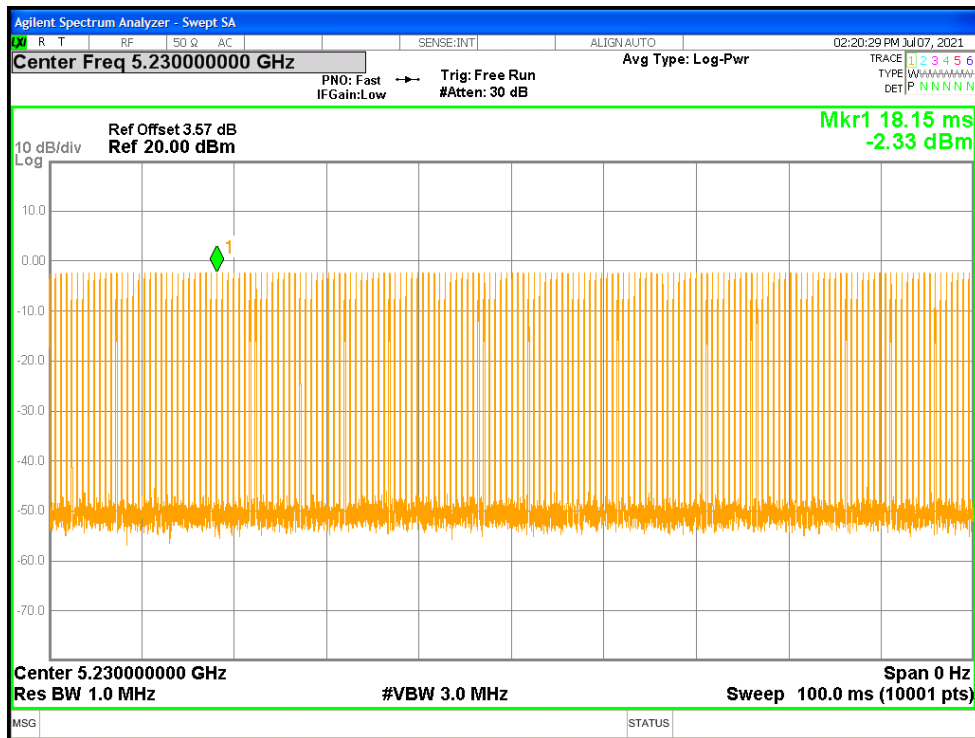
Duty Cycle NVNT ac20 5825MHz Sum



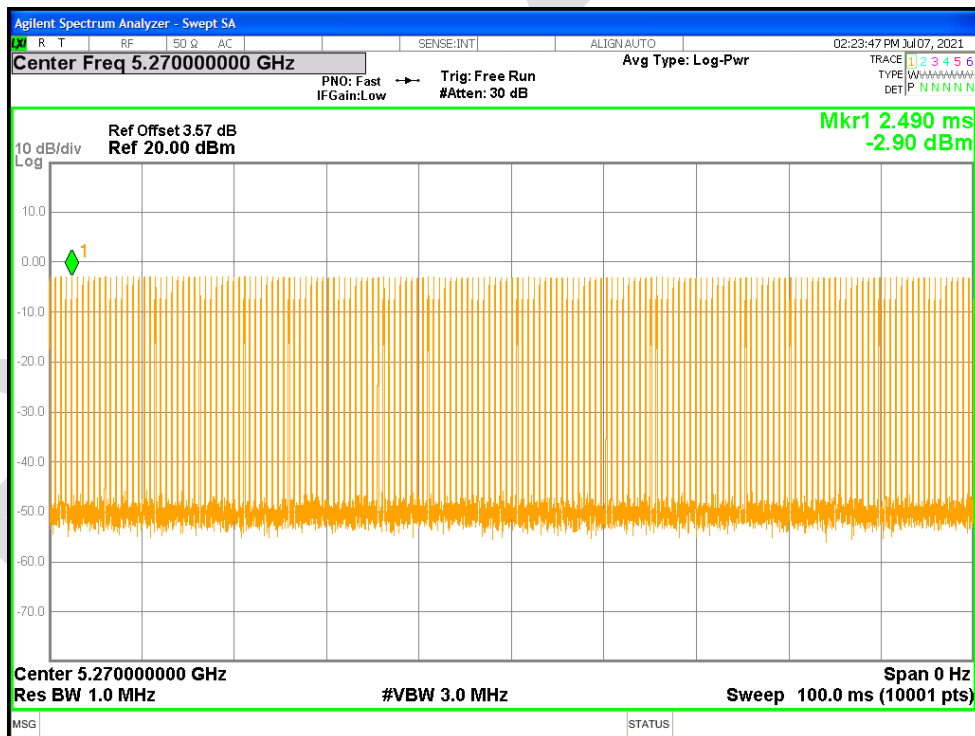
Duty Cycle NVNT ac40 5190MHz Sum



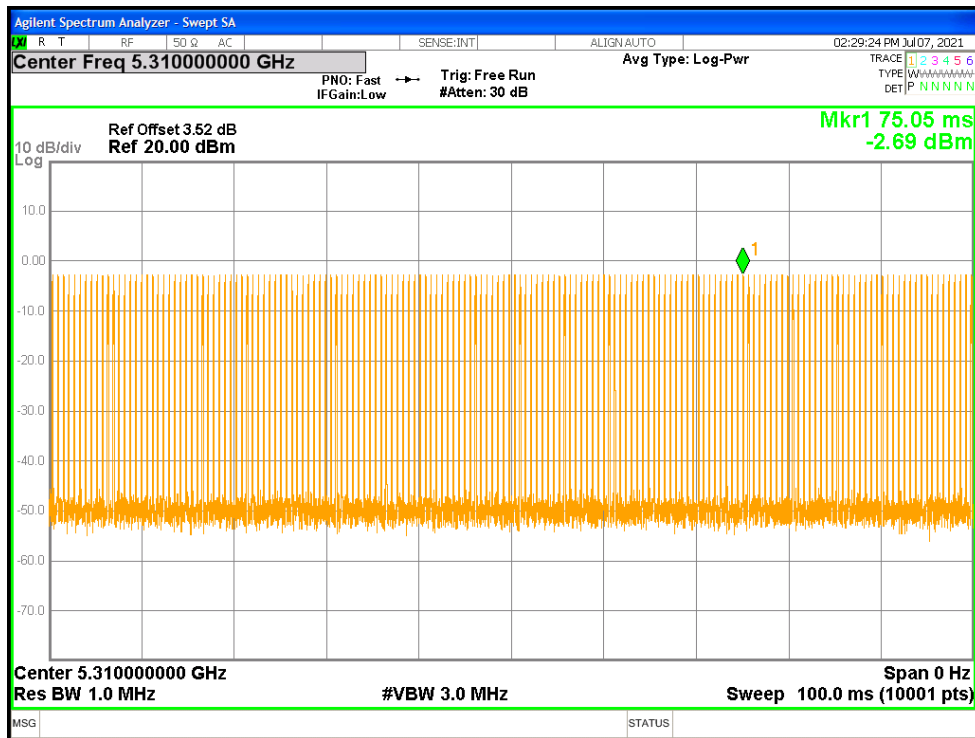
Duty Cycle NVNT ac40 5230MHz Sum



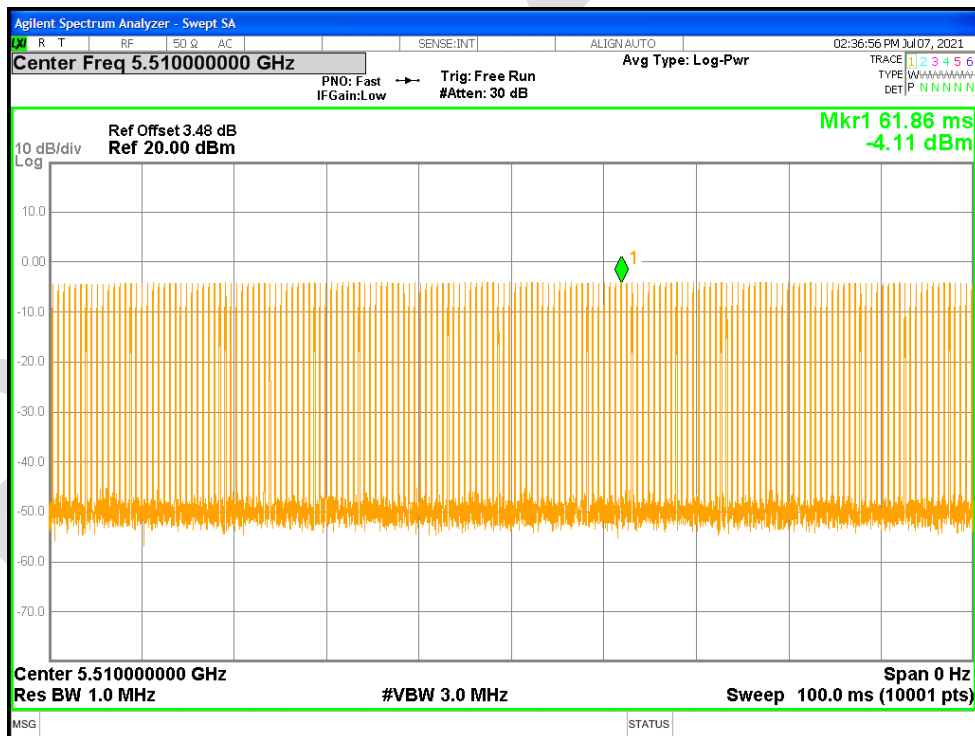
Duty Cycle NVNT ac40 5270MHz Sum



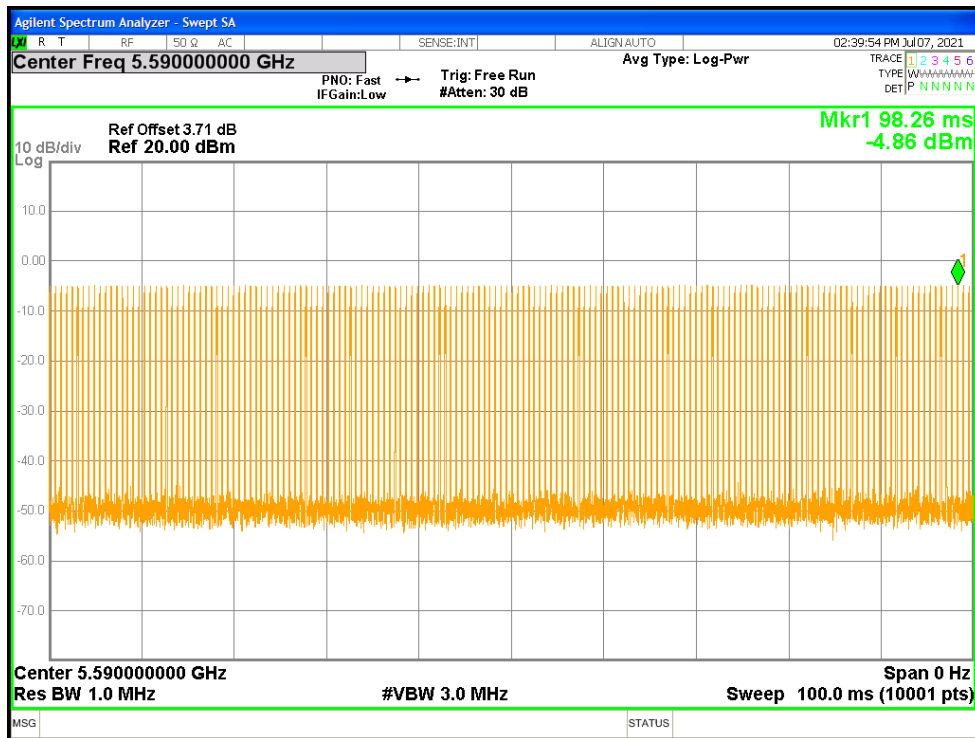
Duty Cycle NVNT ac40 5310MHz Sum



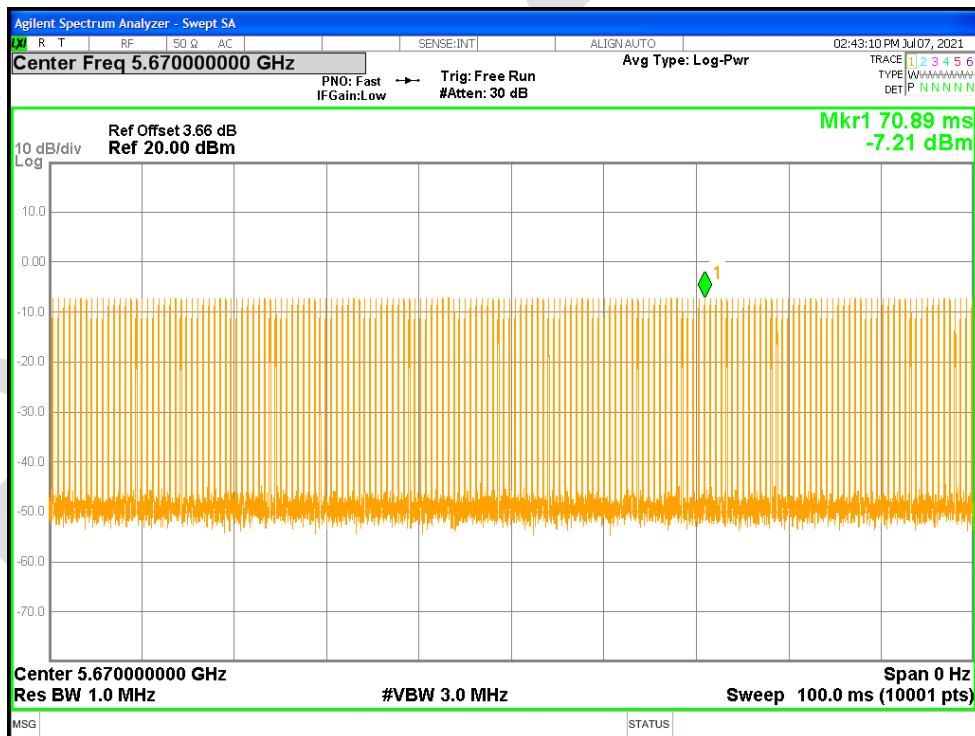
Duty Cycle NVNT ac40 5510MHz Sum



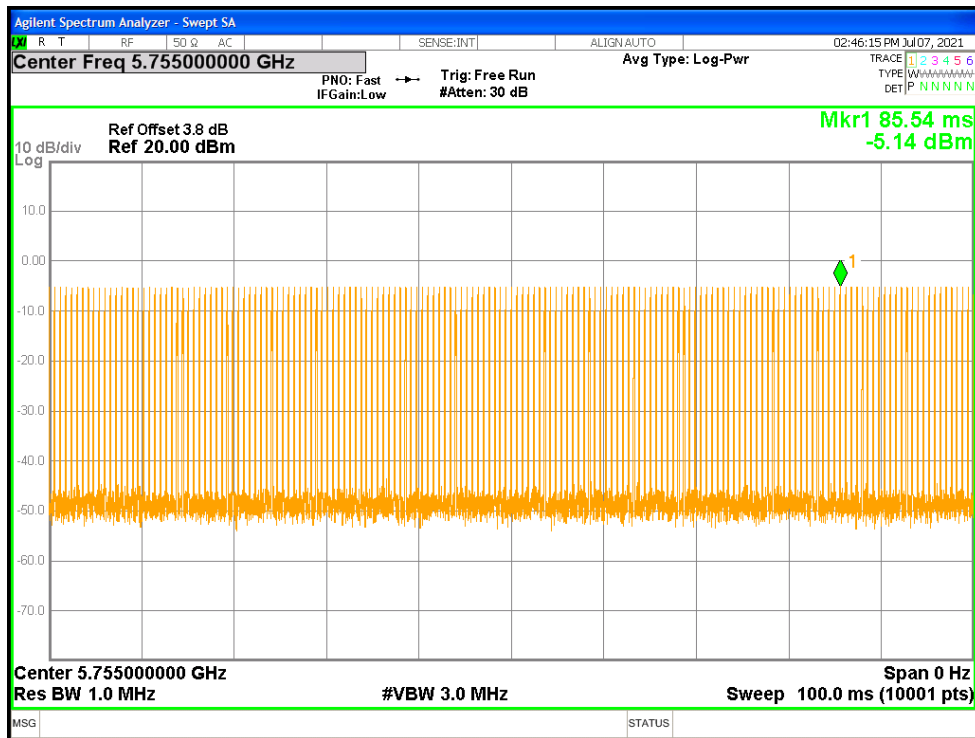
Duty Cycle NVNT ac40 5590MHz Sum



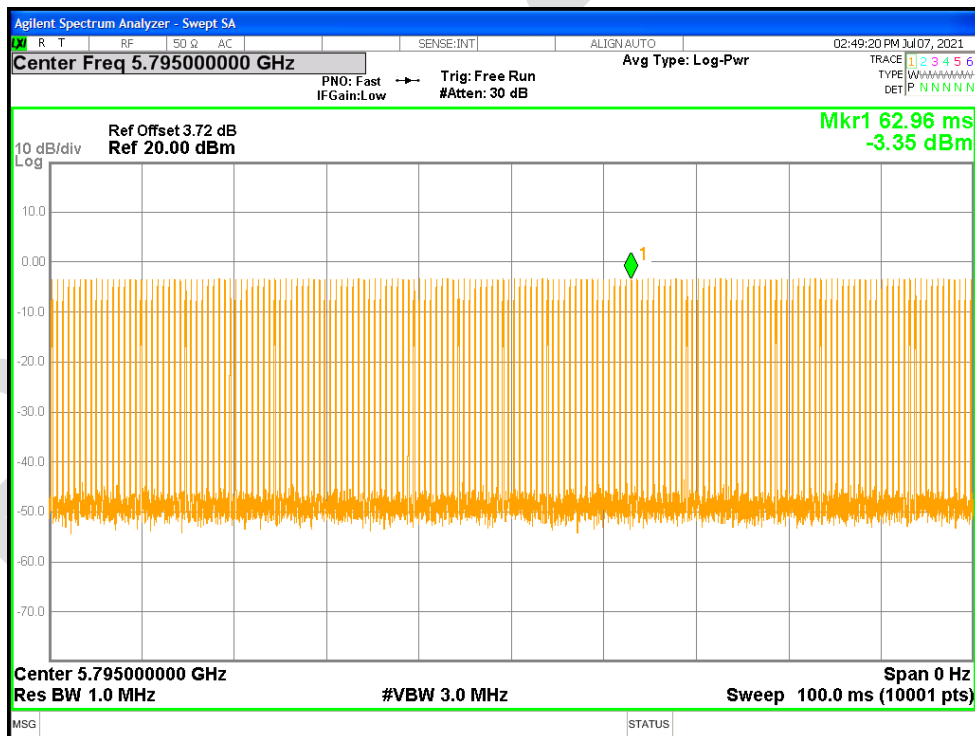
Duty Cycle NVNT ac40 5670MHz Sum



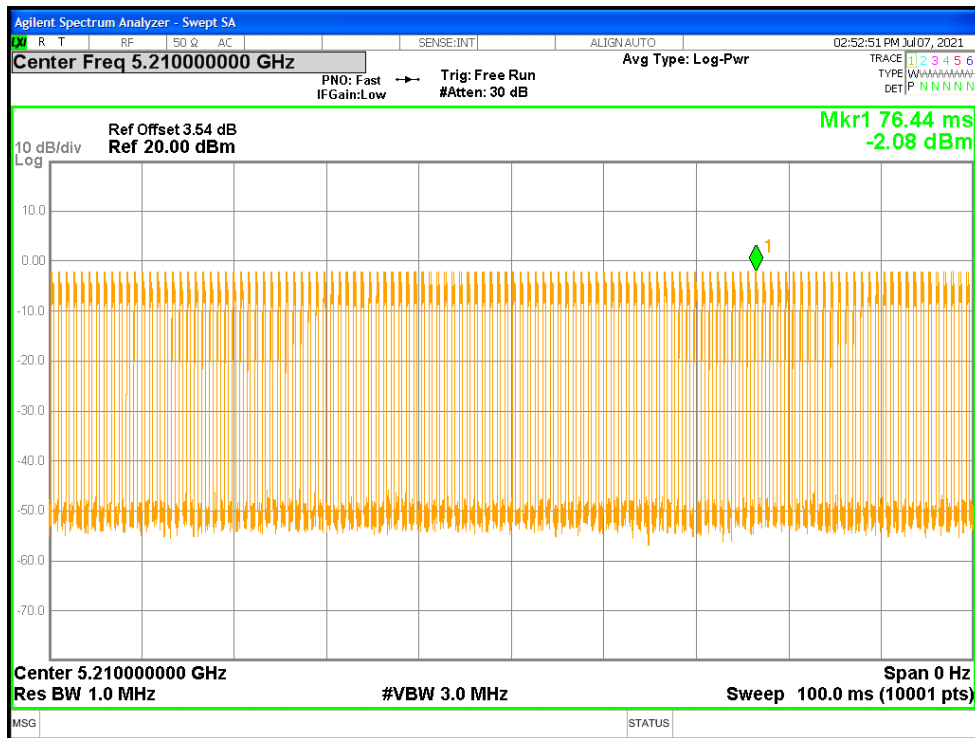
Duty Cycle NVNT ac40 5755MHz Sum



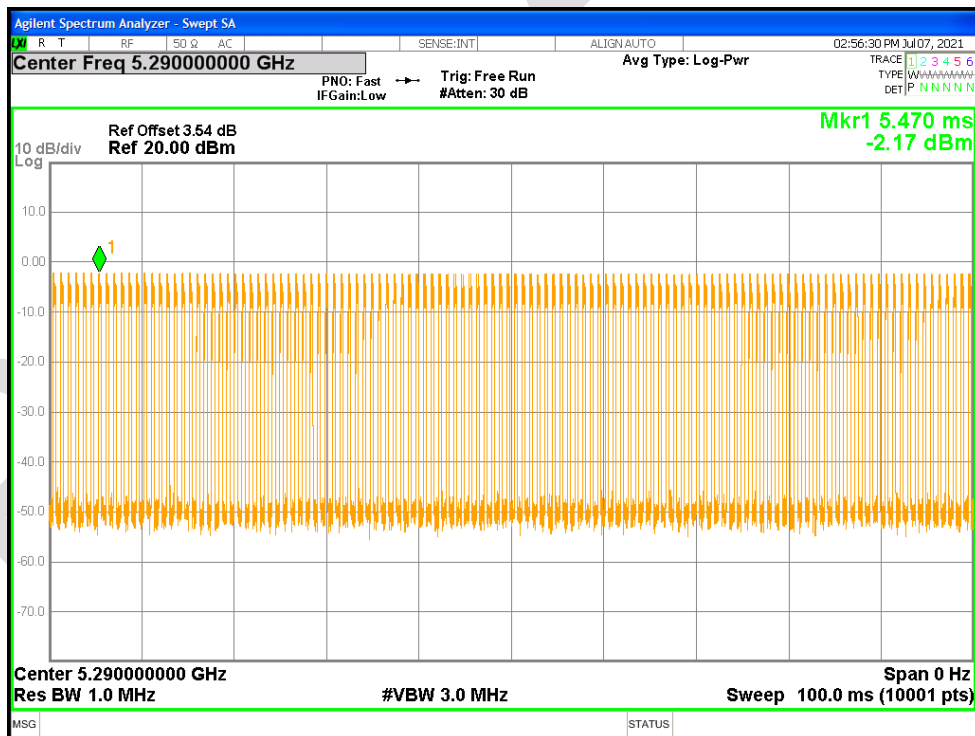
Duty Cycle NVNT ac40 5795MHz Sum



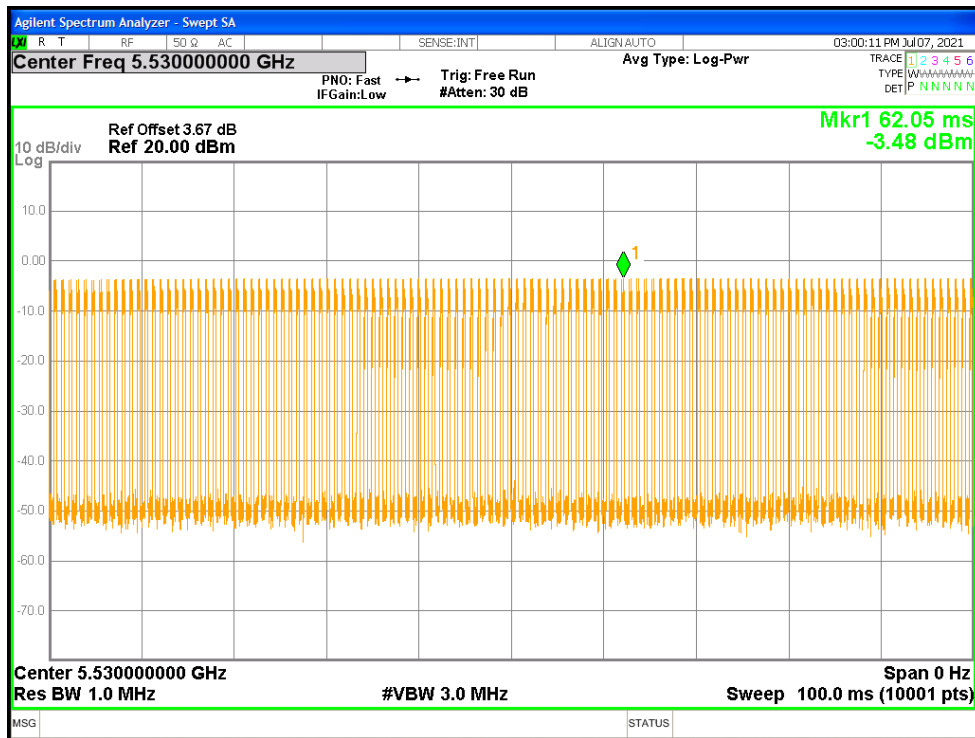
Duty Cycle NVNT ac80 5210MHz Sum



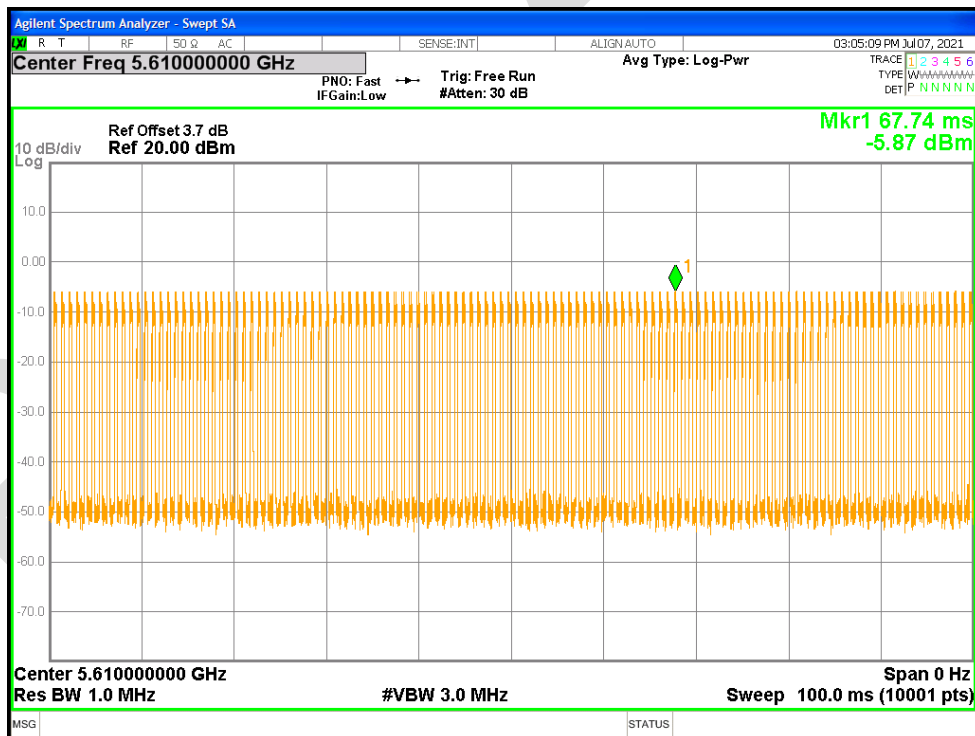
Duty Cycle NVNT ac80 5290MHz Sum



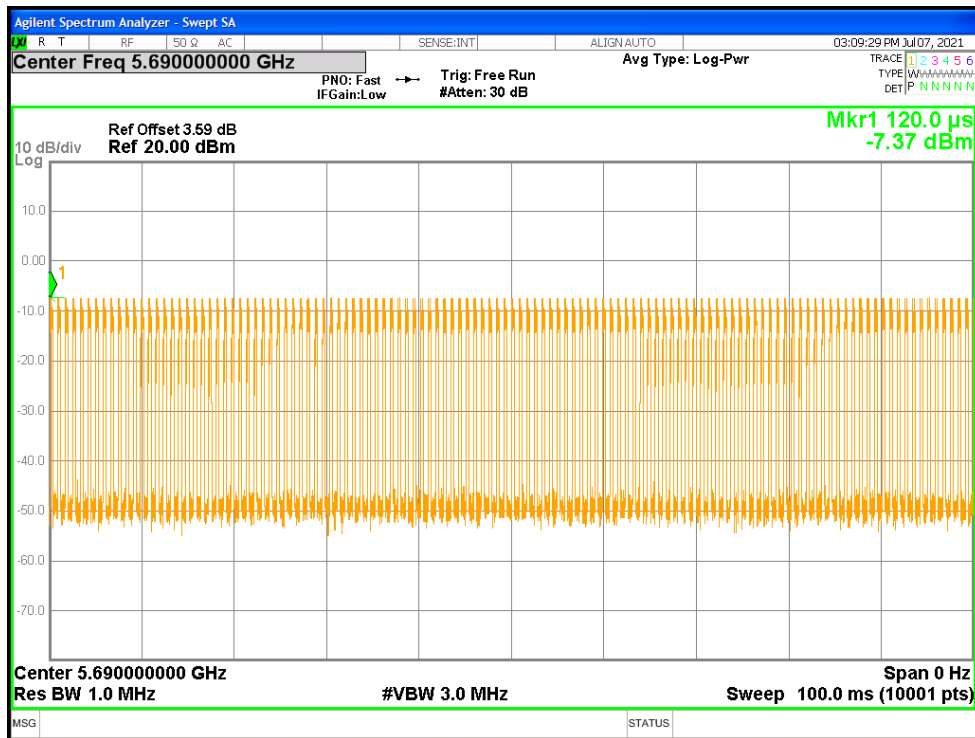
Duty Cycle NVNT ac80 5530MHz Sum



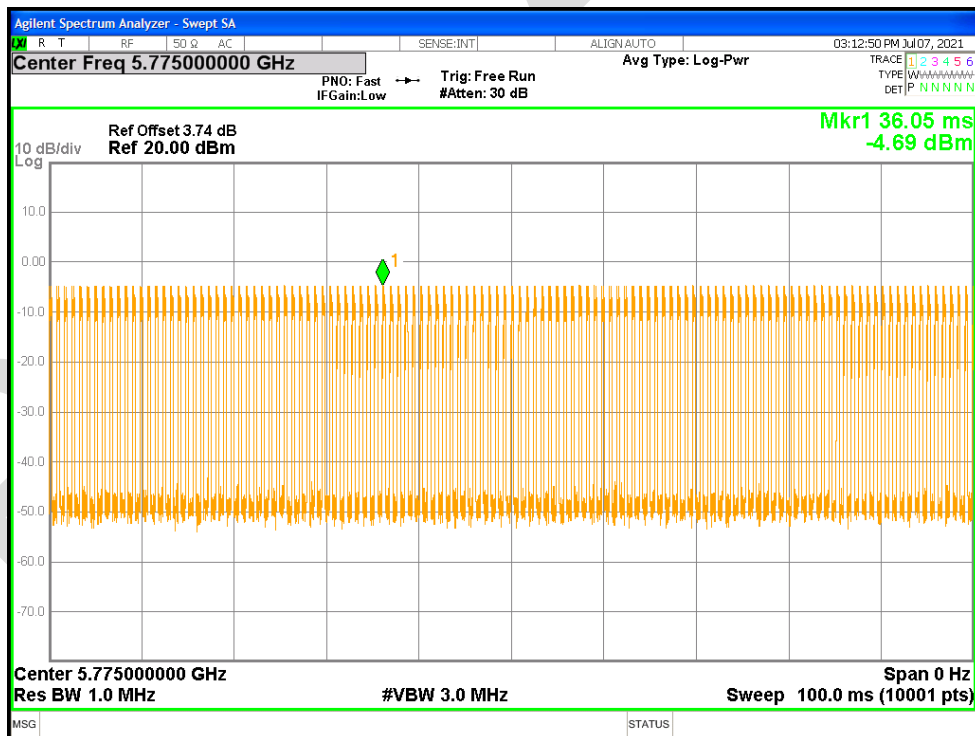
Duty Cycle NVNT ac80 5610MHz Sum



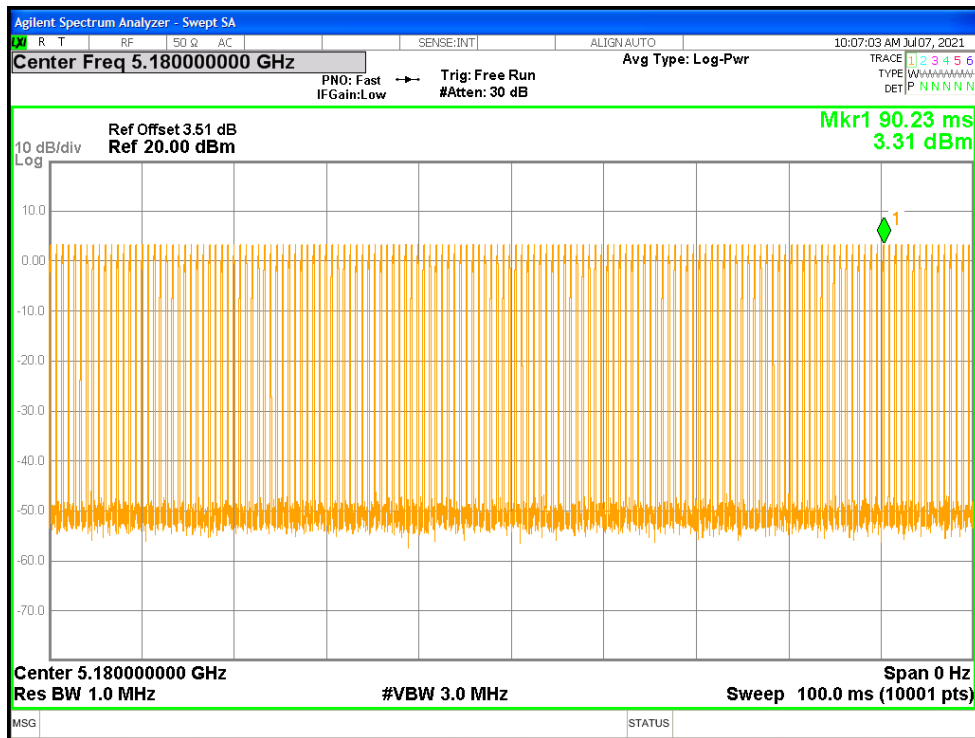
Duty Cycle NVNT ac80 5690MHz Sum



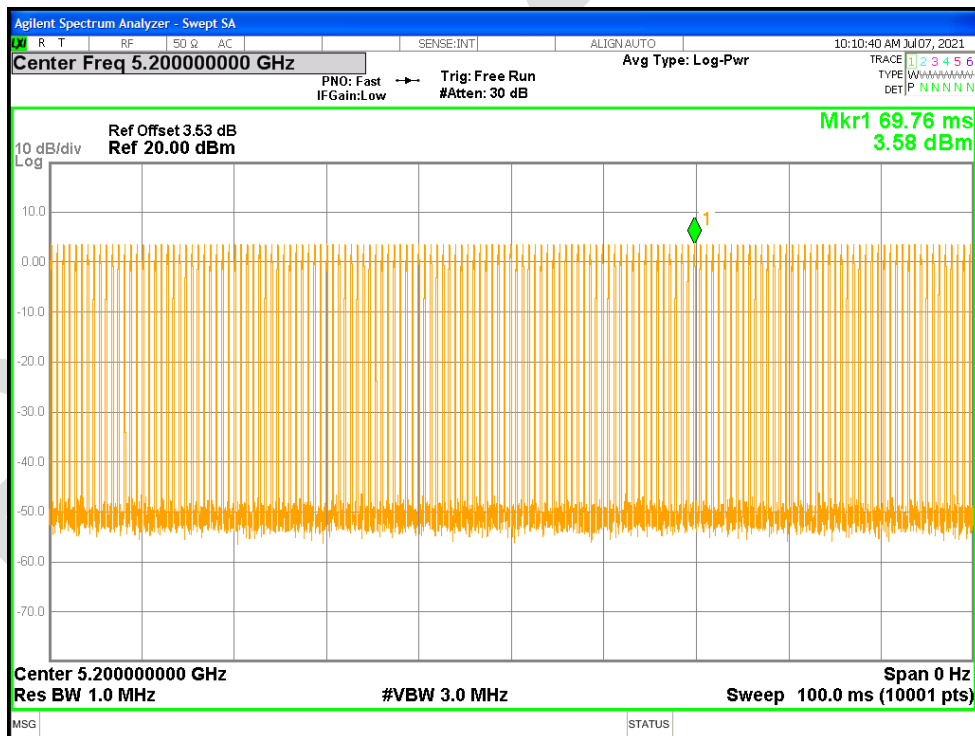
Duty Cycle NVNT ac80 5775MHz Sum



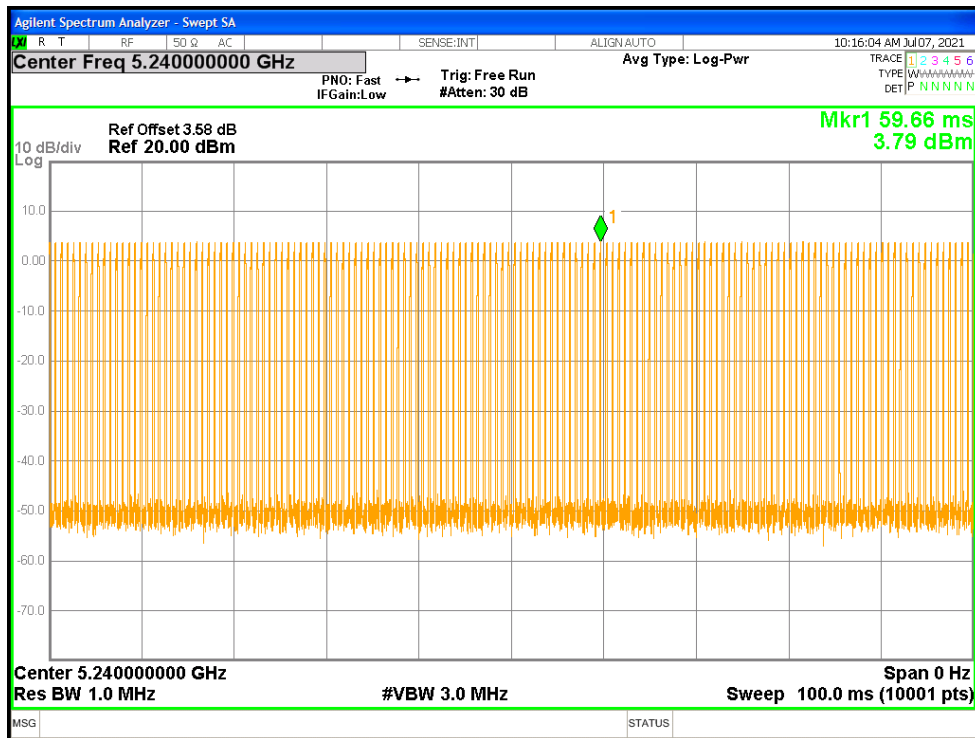
Duty Cycle NVNT n20 5180MHz Sum



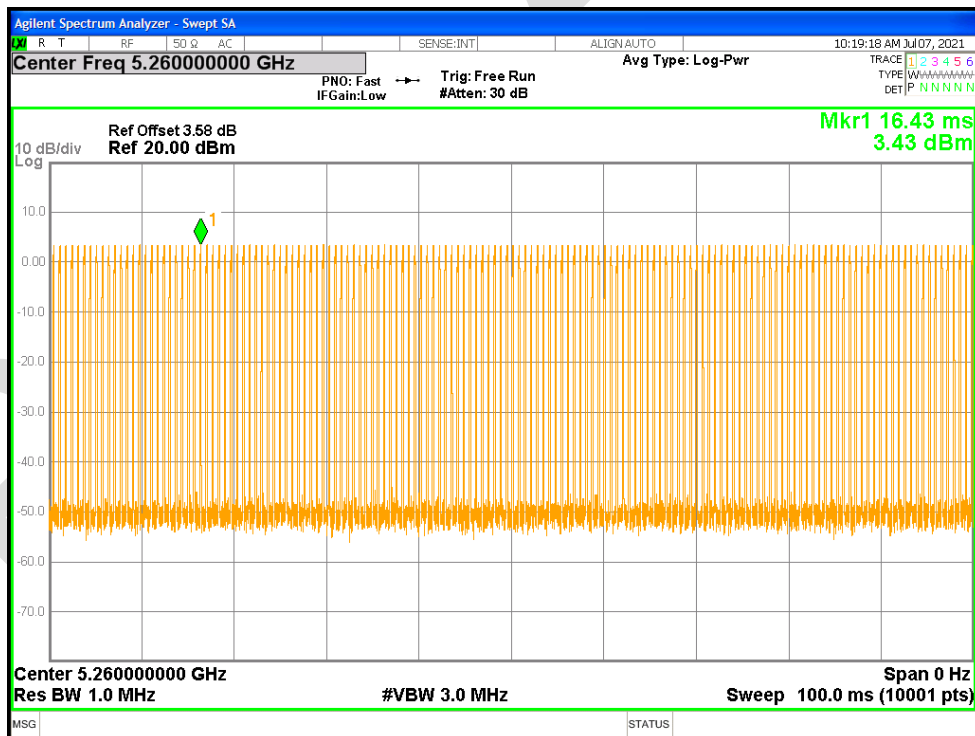
Duty Cycle NVNT n20 5200MHz Sum



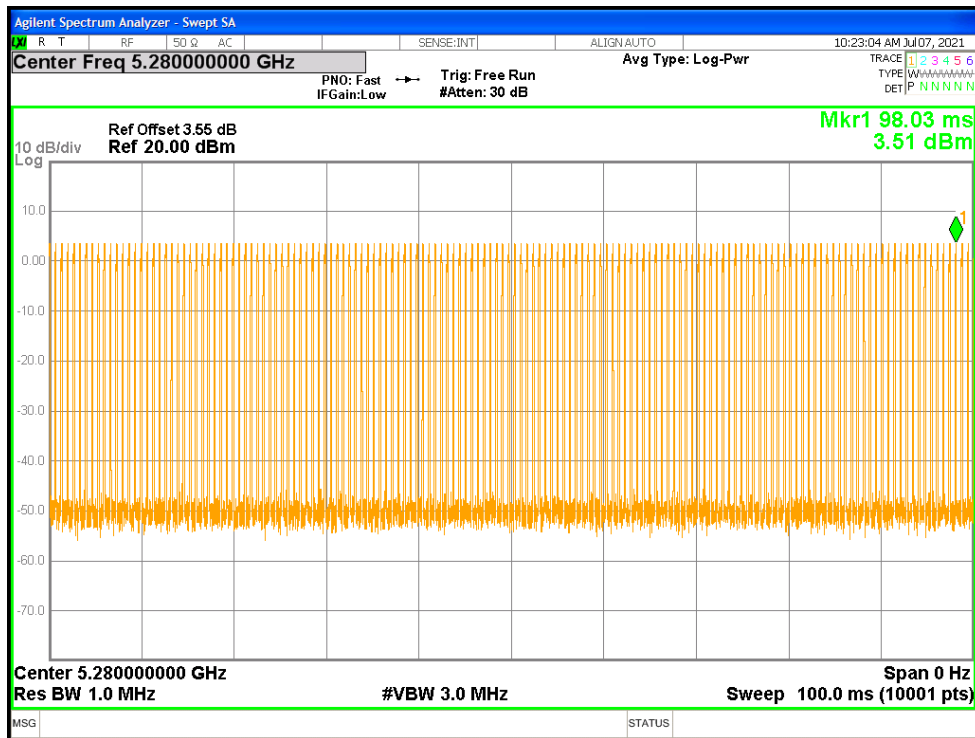
Duty Cycle NVNT n20 5240MHz Sum



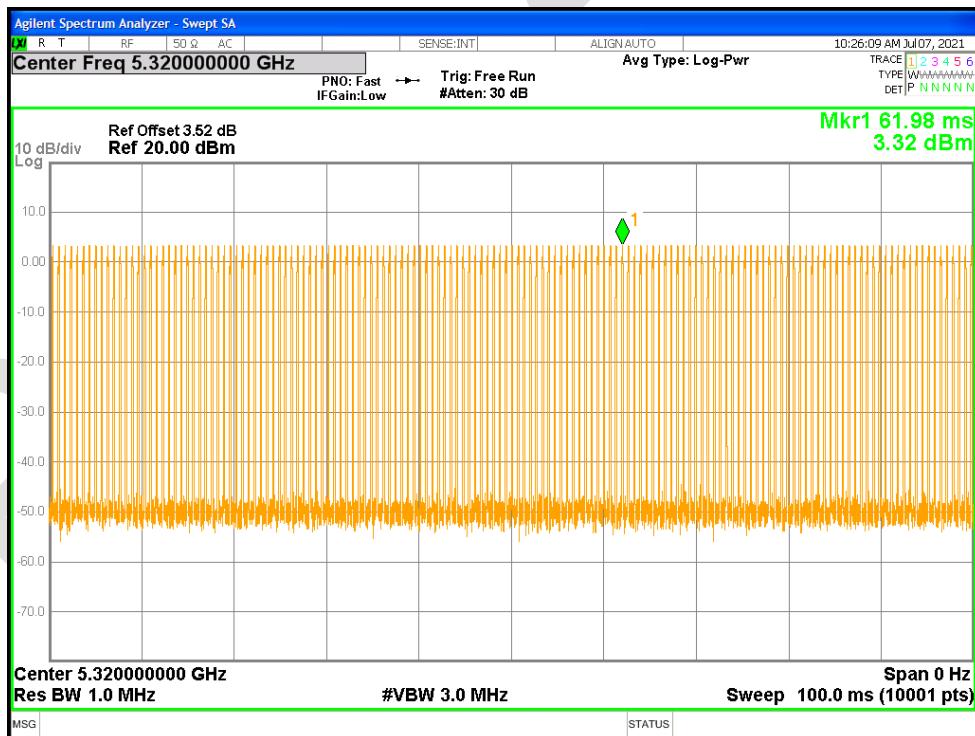
Duty Cycle NVNT n20 5260MHz Sum



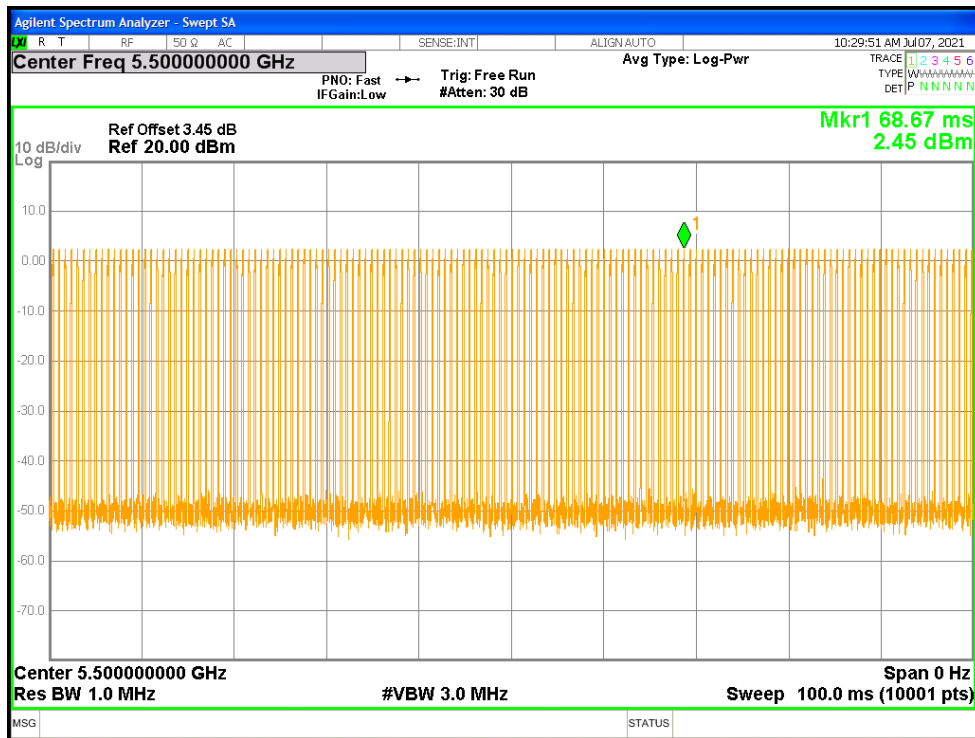
Duty Cycle NVNT n20 5280MHz Sum



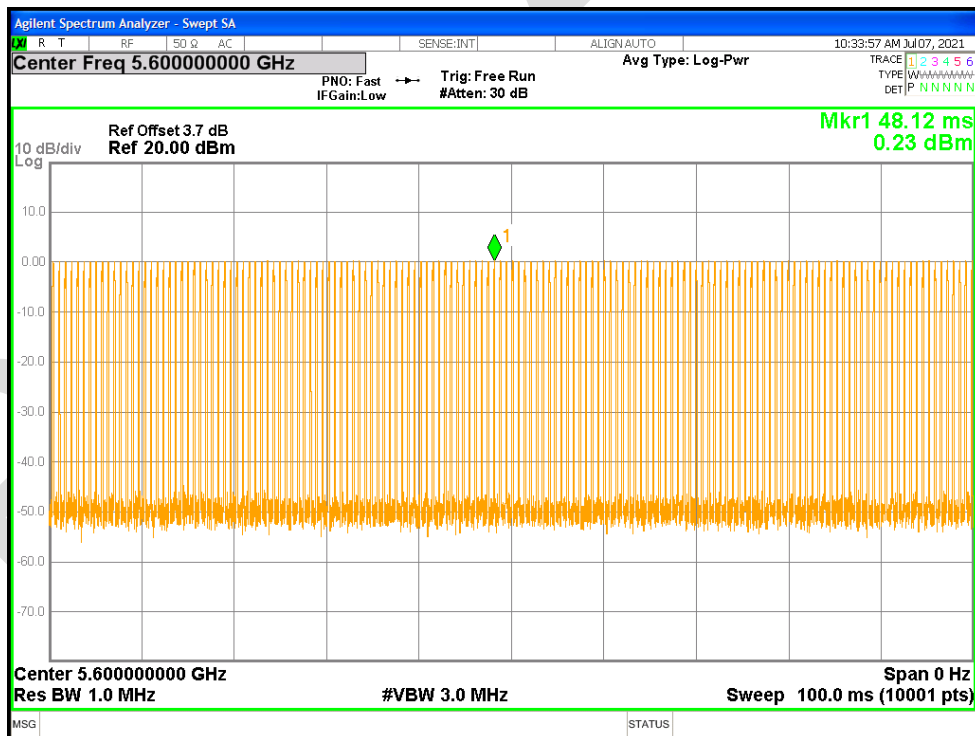
Duty Cycle NVNT n20 5320MHz Sum



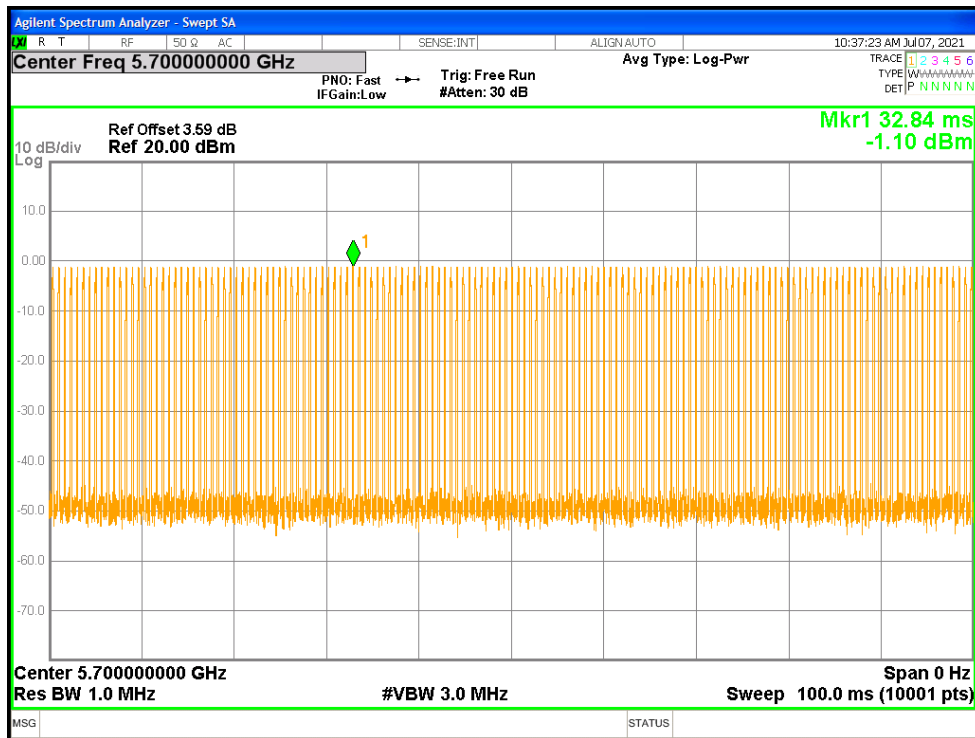
Duty Cycle NVNT n20 5500MHz Sum



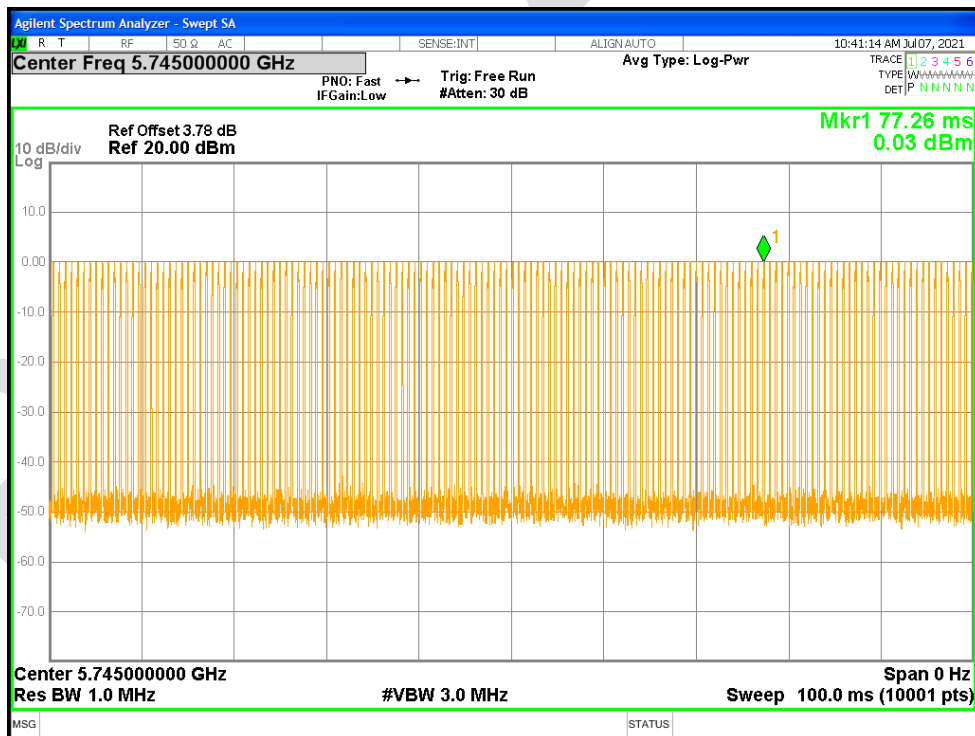
Duty Cycle NVNT n20 5600MHz Sum



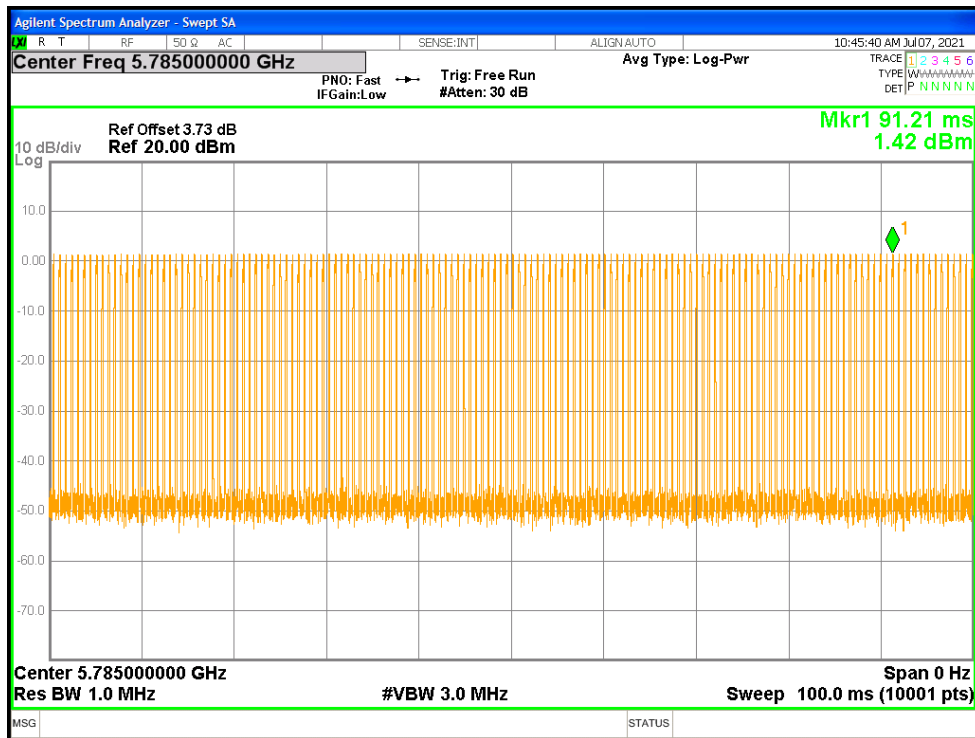
Duty Cycle NVNT n20 5700MHz Sum



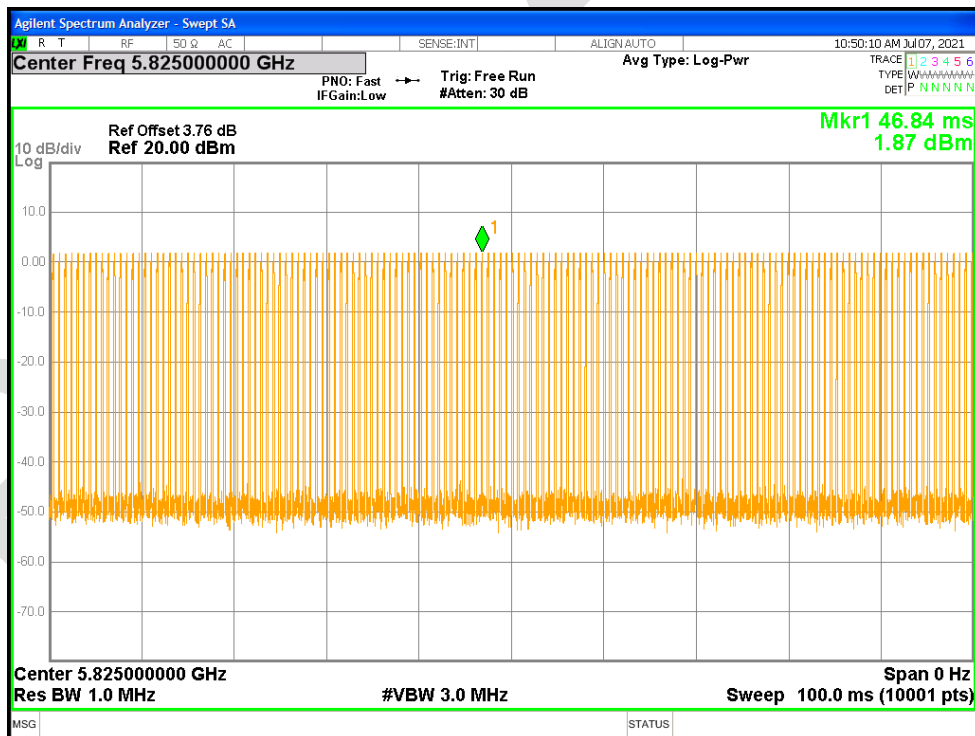
Duty Cycle NVNT n20 5745MHz Sum



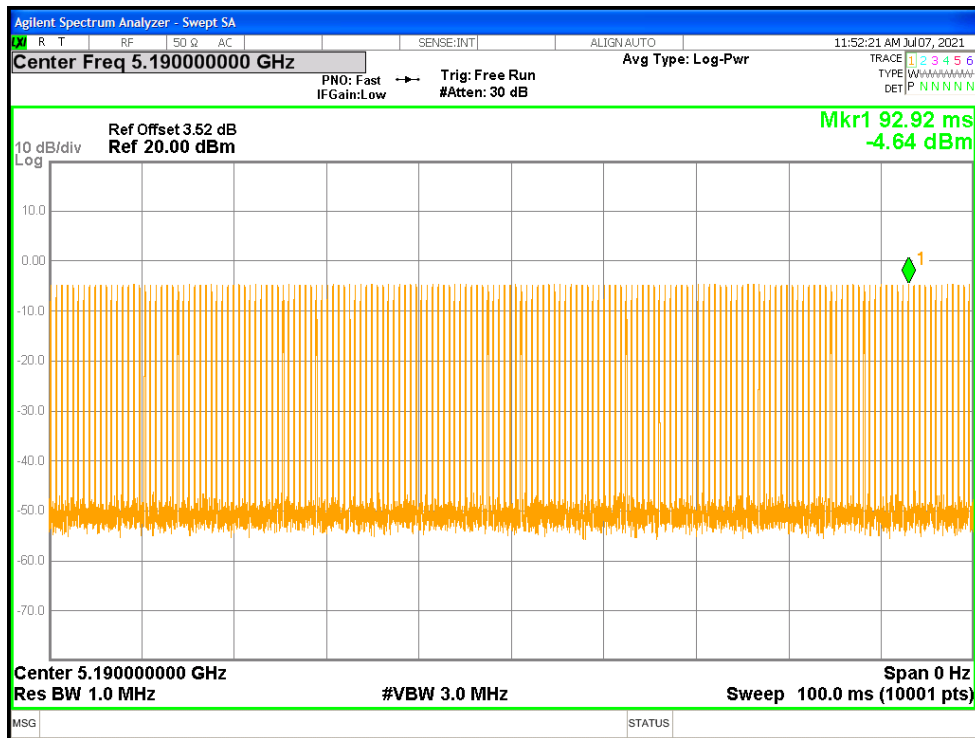
Duty Cycle NVNT n20 5785MHz Sum



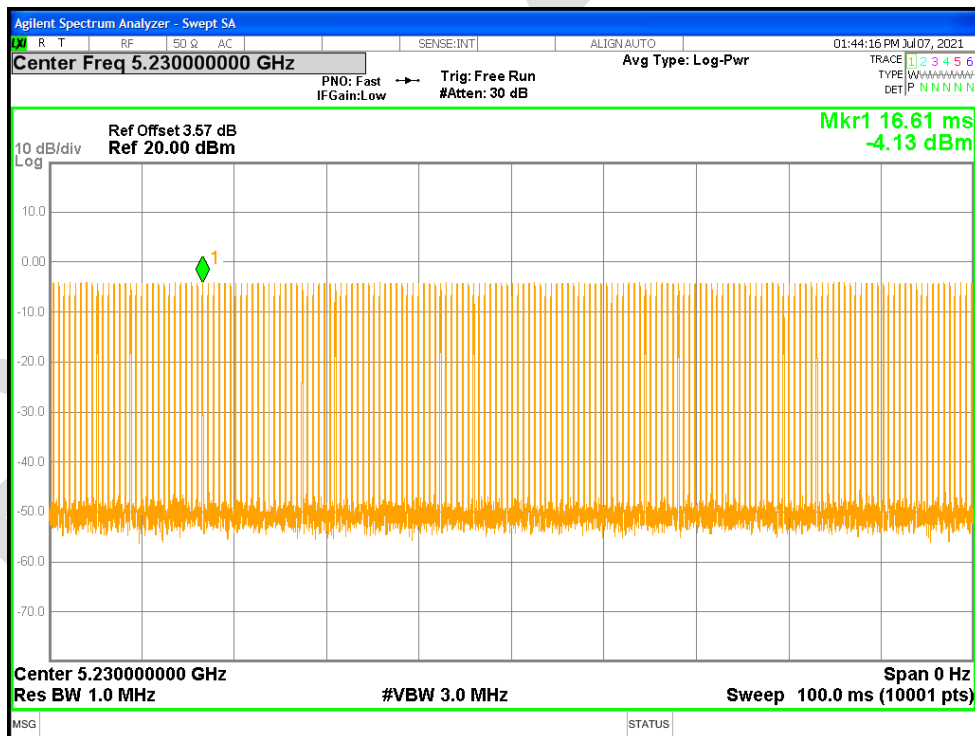
Duty Cycle NVNT n20 5825MHz Sum



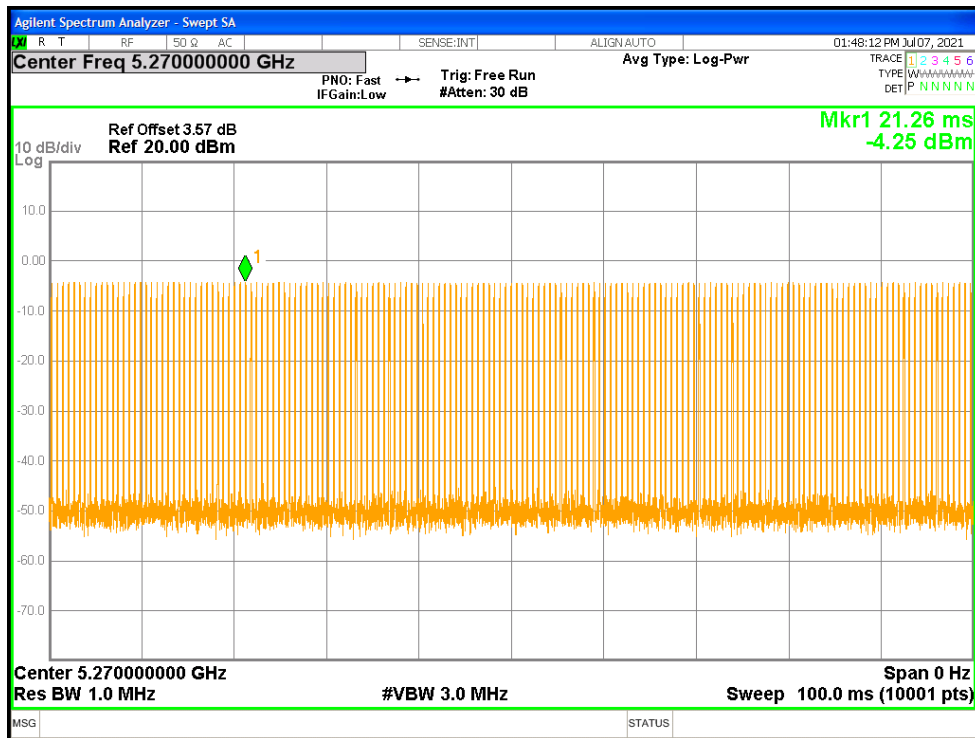
Duty Cycle NVNT n40 5190MHz Sum



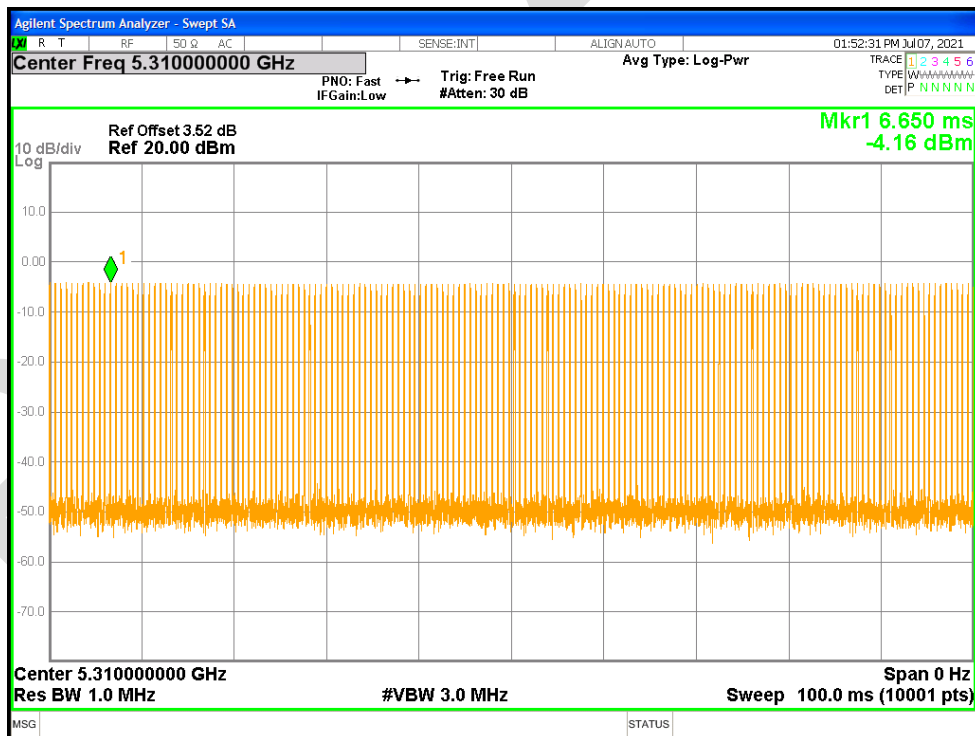
Duty Cycle NVNT n40 5230MHz Sum



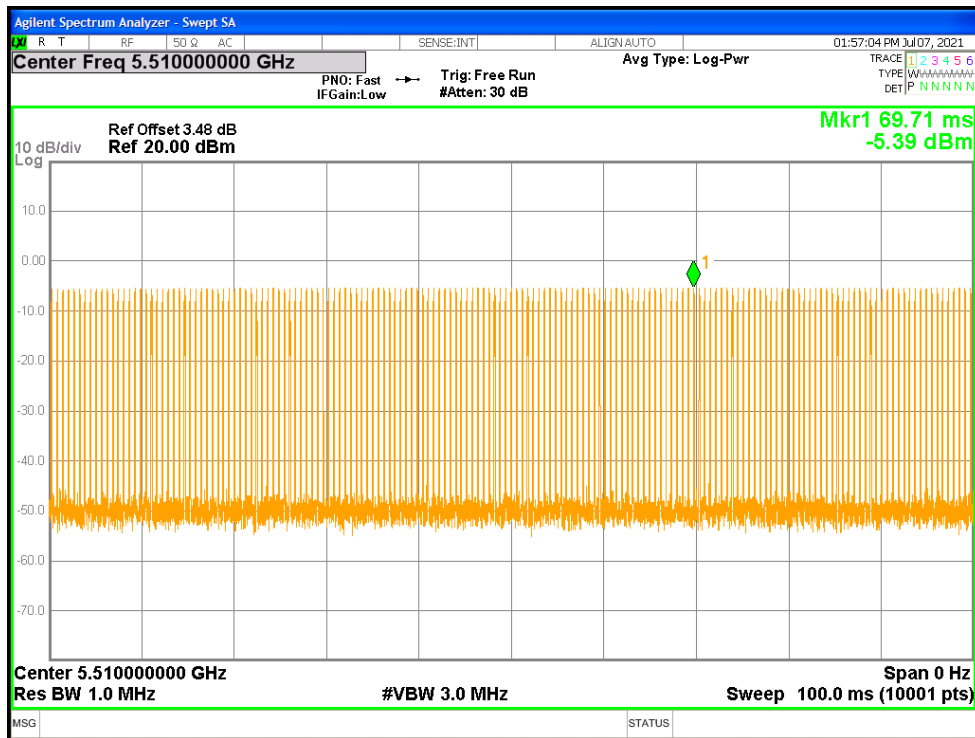
Duty Cycle NVNT n40 5270MHz Sum



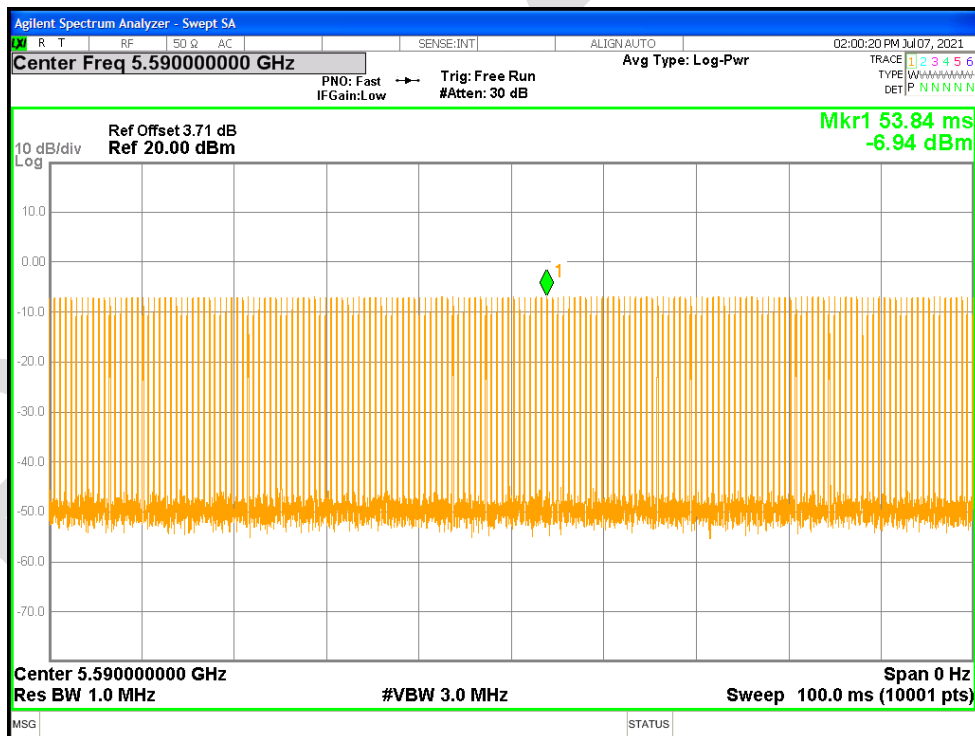
Duty Cycle NVNT n40 5310MHz Sum



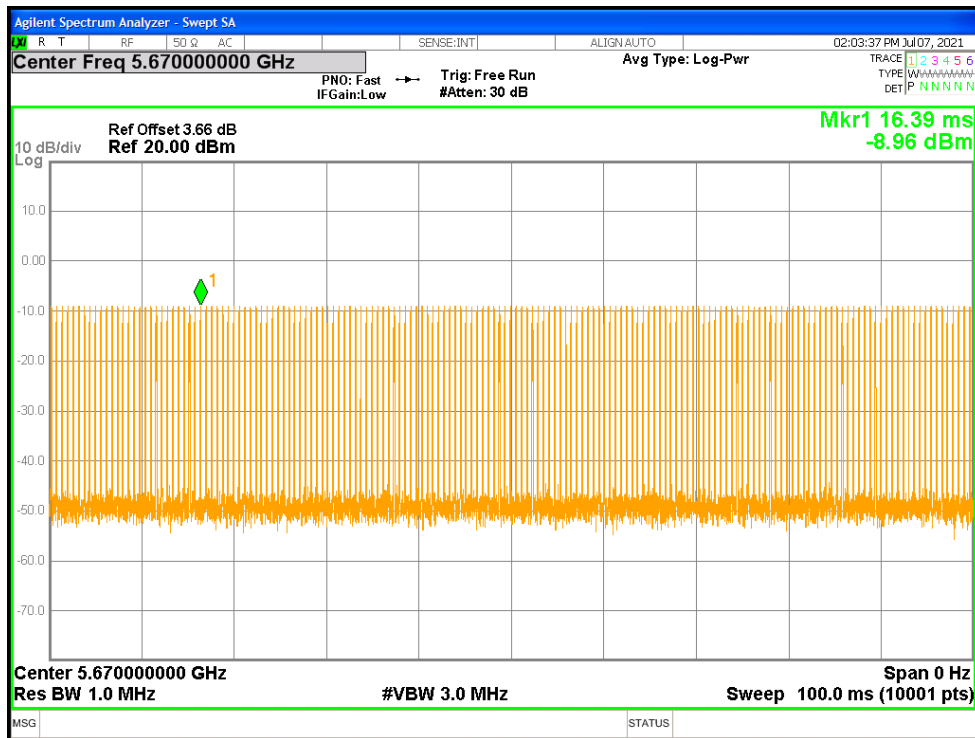
Duty Cycle NVNT n40 5510MHz Sum



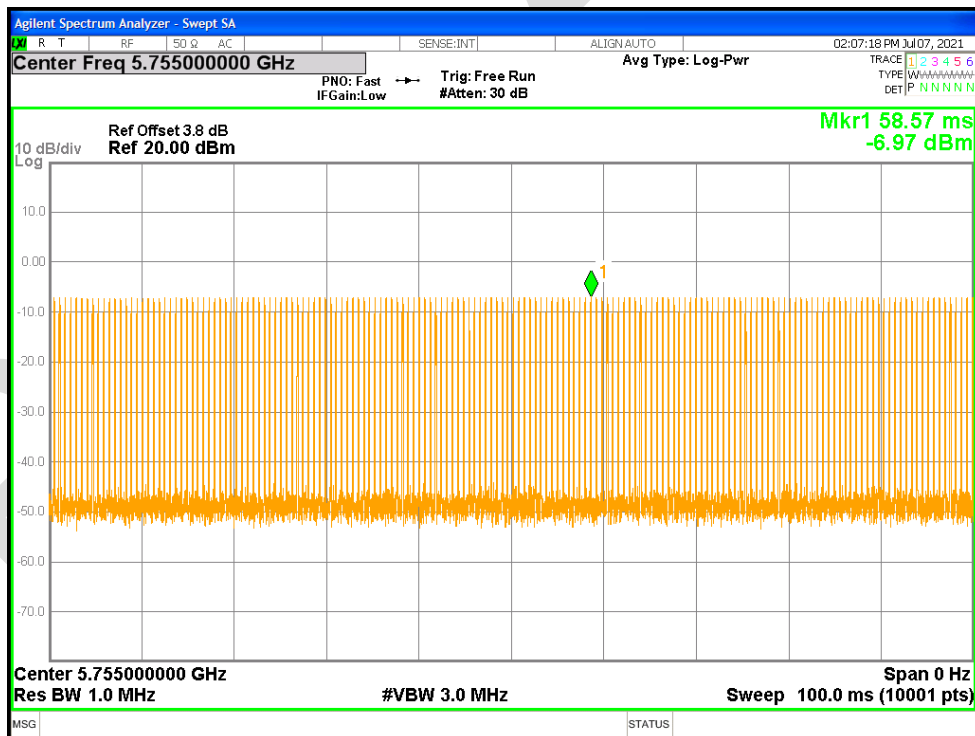
Duty Cycle NVNT n40 5590MHz Sum



Duty Cycle NVNT n40 5670MHz Sum



Duty Cycle NVNT n40 5755MHz Sum



Duty Cycle NVNT n40 5795MHz Sum

