

Test Plots:

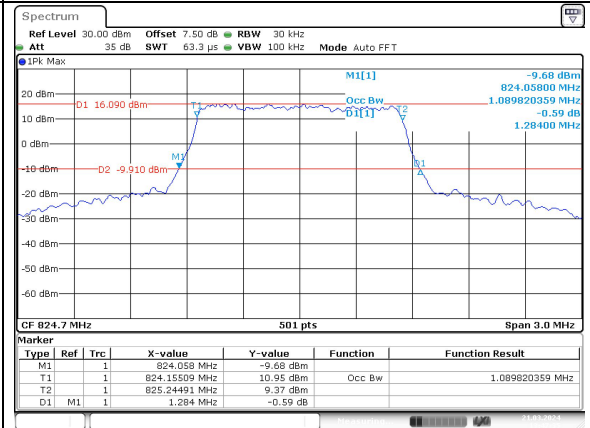
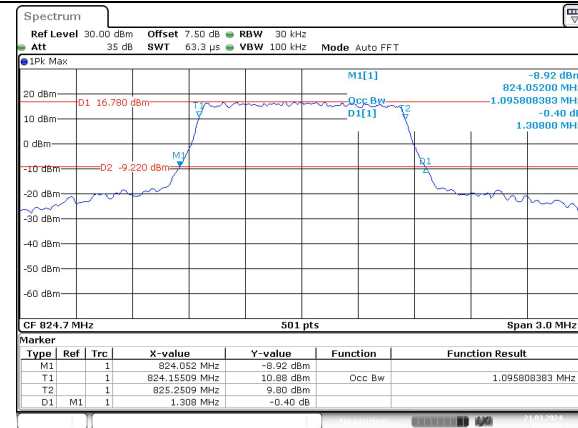
Occupied Bandwidth

Channel

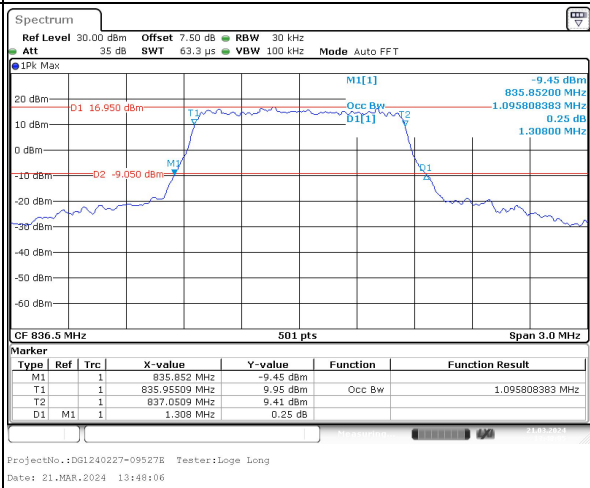
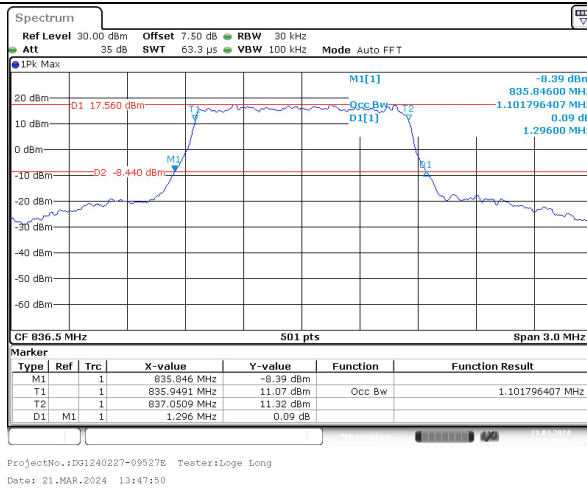
1.4MHz Bandwidth QPSK

1.4MHz Bandwidth 16QAM

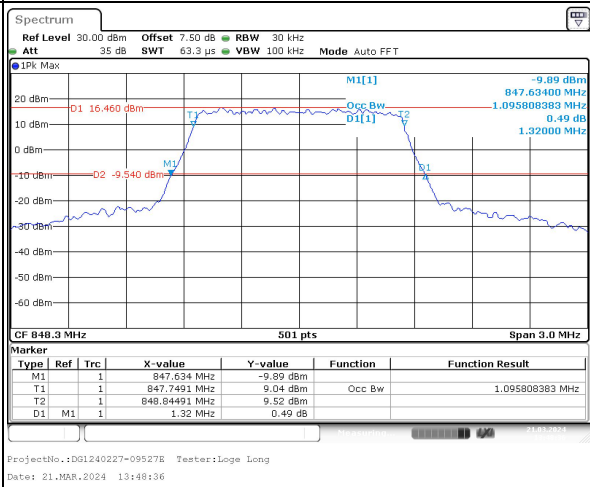
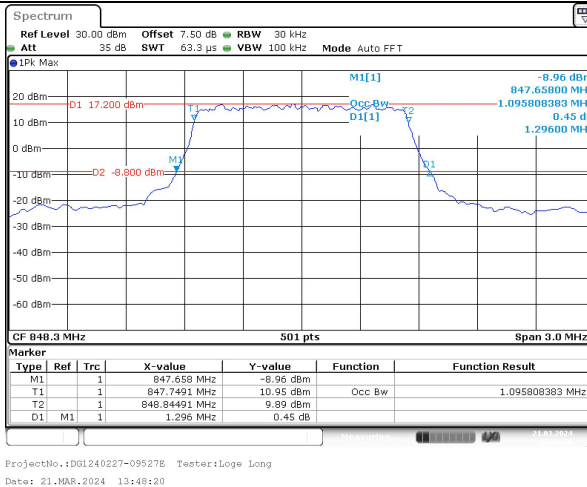
Lowest



Middle



Highest



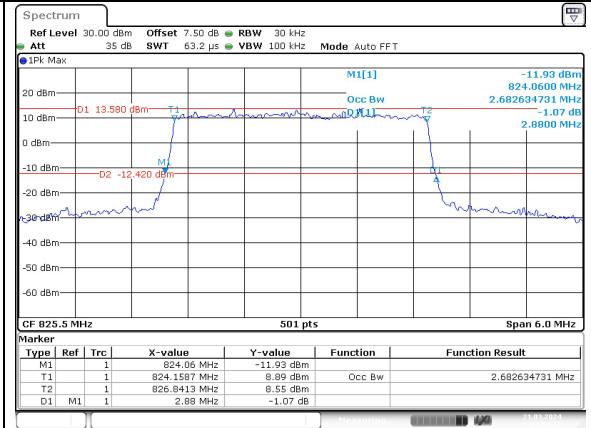
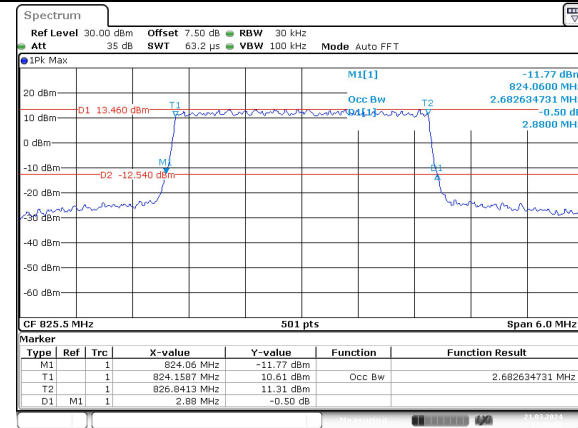
Occupied Bandwidth

Channel

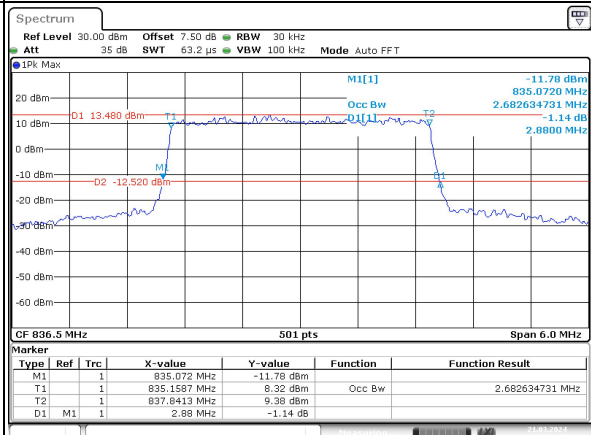
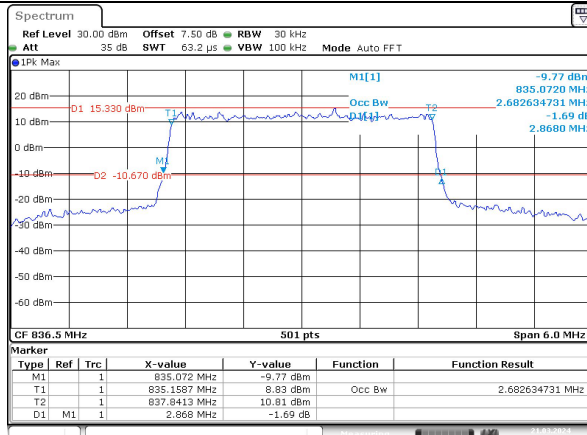
3MHz Bandwidth QPSK

3MHz Bandwidth 16QAM

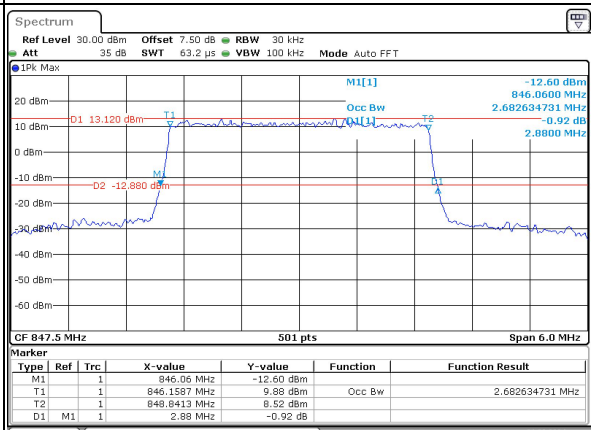
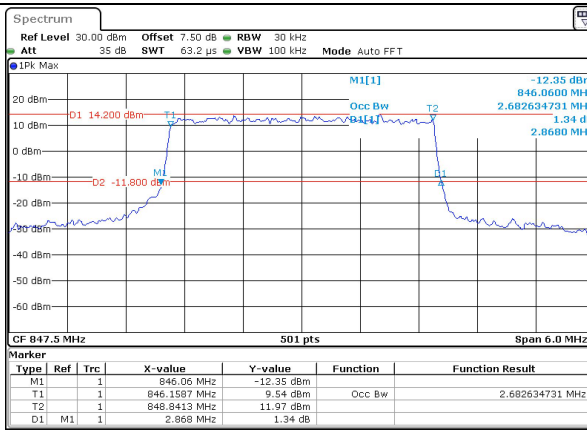
Lowest



Middle



Highest



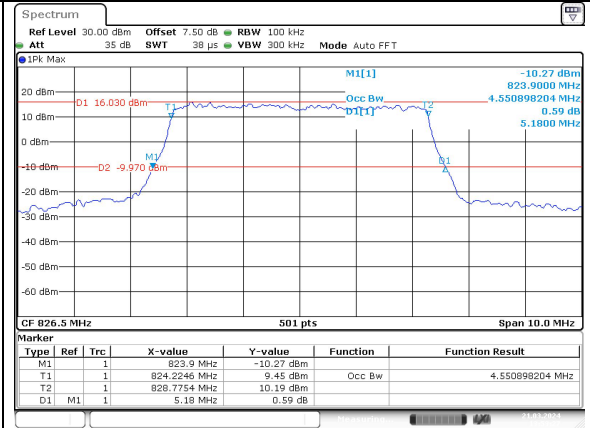
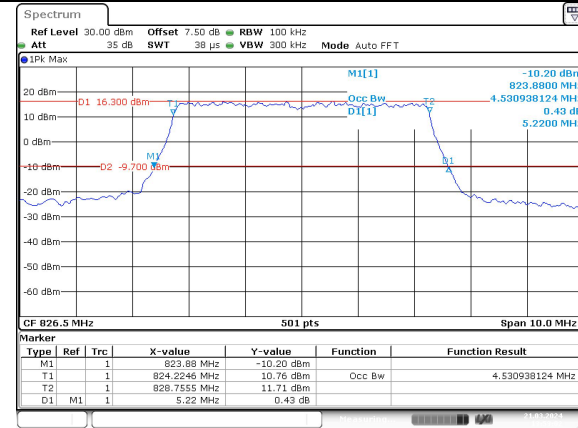
Occupied Bandwidth

Channel

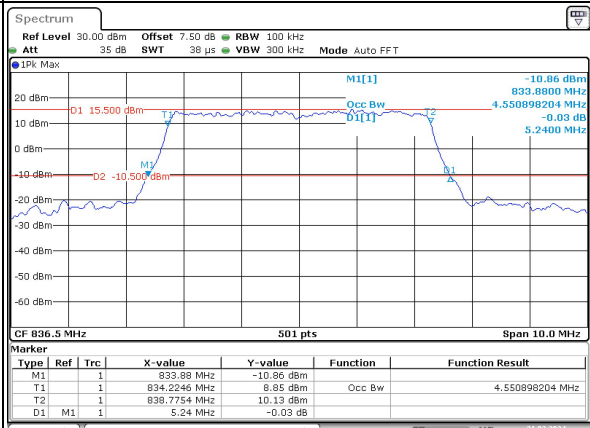
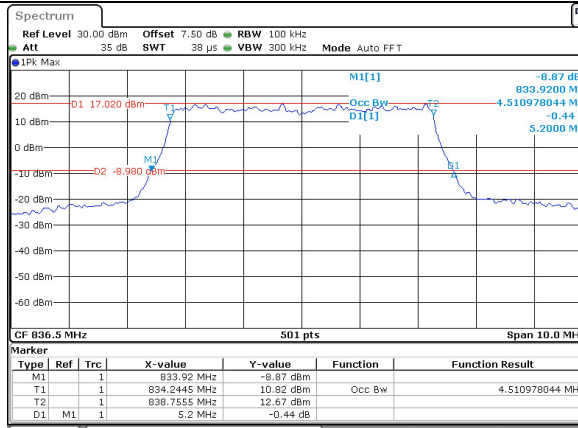
5MHz Bandwidth QPSK

5MHz Bandwidth 16QAM

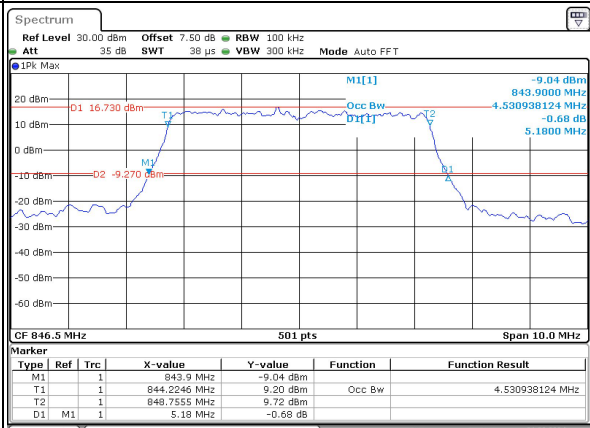
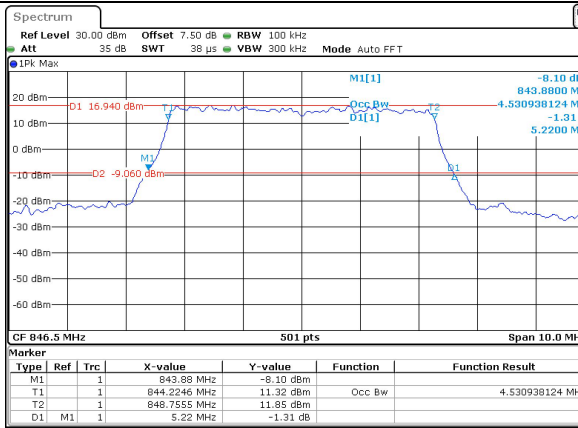
Lowest



Middle



Highest



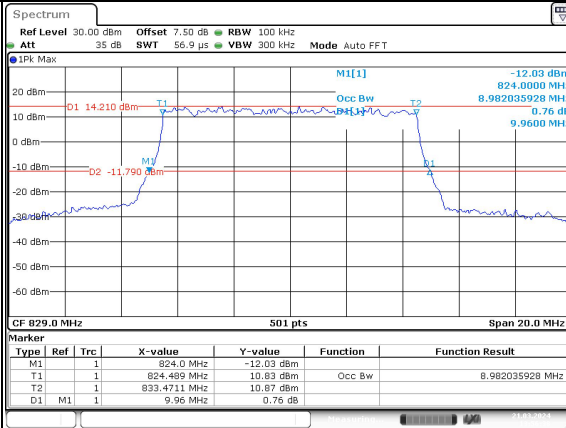
Occupied Bandwidth

Channel

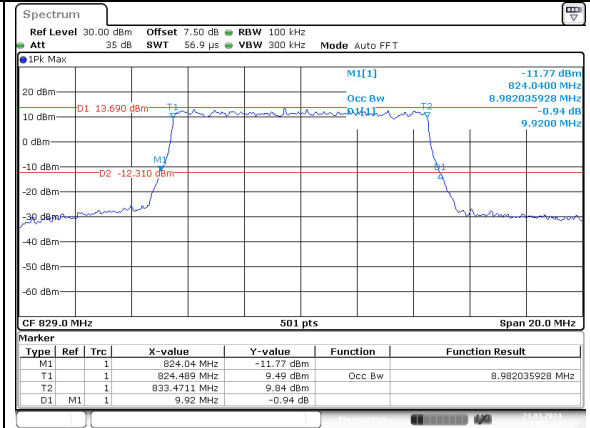
10MHz Bandwidth QPSK

10MHz Bandwidth 16QAM

Lowest

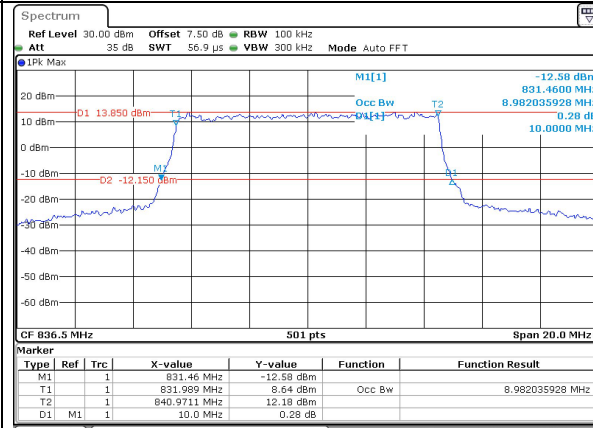


ProjectNo.:DG1240227-09527E Tester:Loge Long
Date: 21.MAR.2024 13:56:38

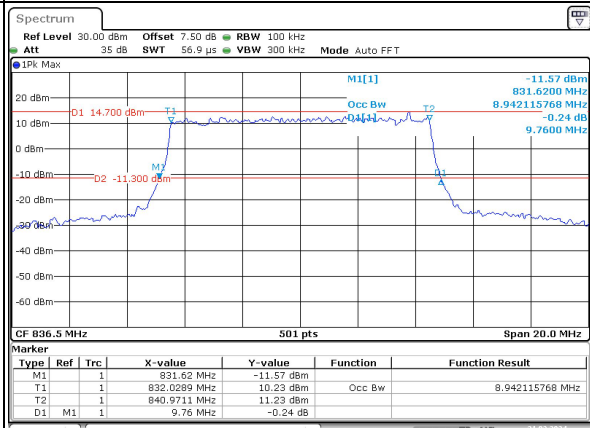


ProjectNo.:DG1240227-09527E Tester:Loge Long
Date: 21.MAR.2024 13:57:07

Middle

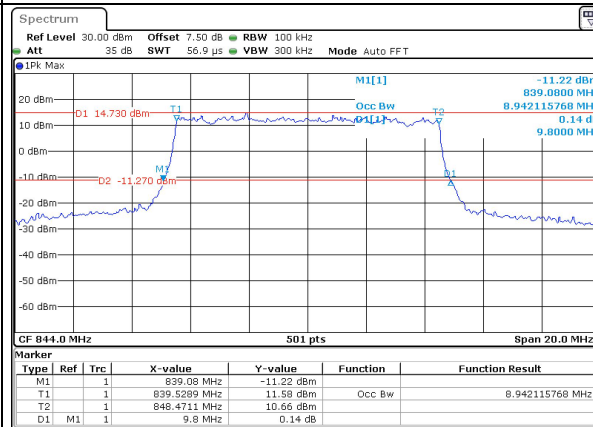


ProjectNo.:DG1240227-09527E Tester:Loge Long
Date: 21.MAR.2024 13:57:34

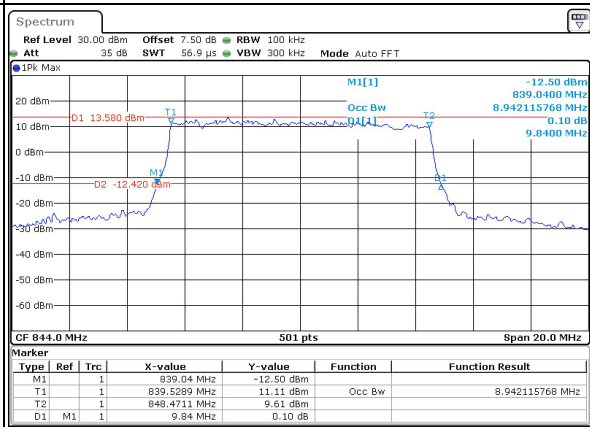


ProjectNo.:DG1240227-09527E Tester:Loge Long
Date: 21.MAR.2024 13:57:56

Highest



ProjectNo.:DG1240227-09527E Tester:Loge Long
Date: 21.MAR.2024 13:58:17



ProjectNo.:DG1240227-09527E Tester:Loge Long
Date: 21.MAR.2024 13:58:40