



11AC40SISO_Ant1_5510



11AC40SISO_Ant0_5550



11AC40SISO_Ant1_5550



11AC40SISO_Ant0_5670



11AC40SISO_Ant1_5670



11AC40SISO_Ant0_5755



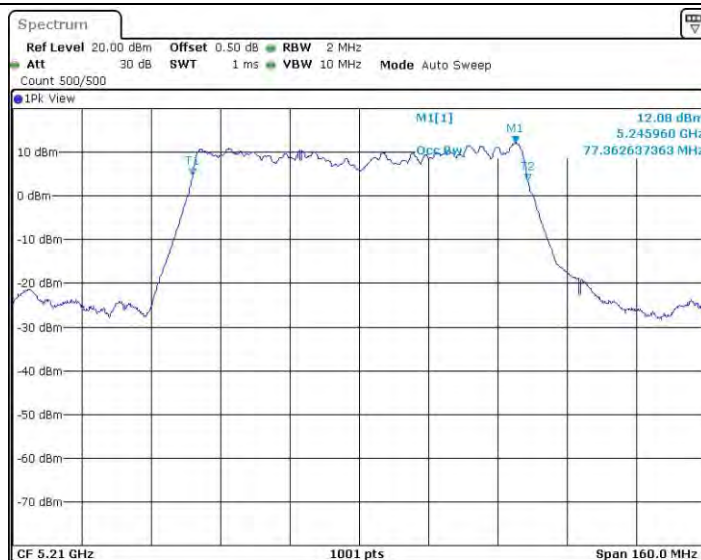
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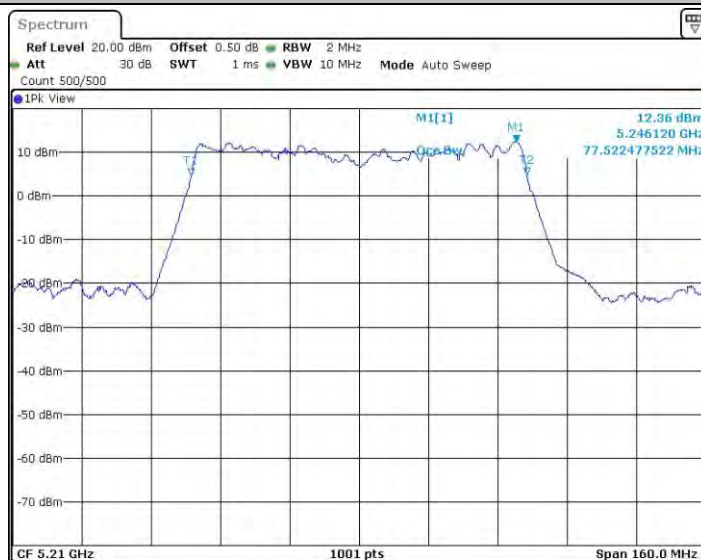
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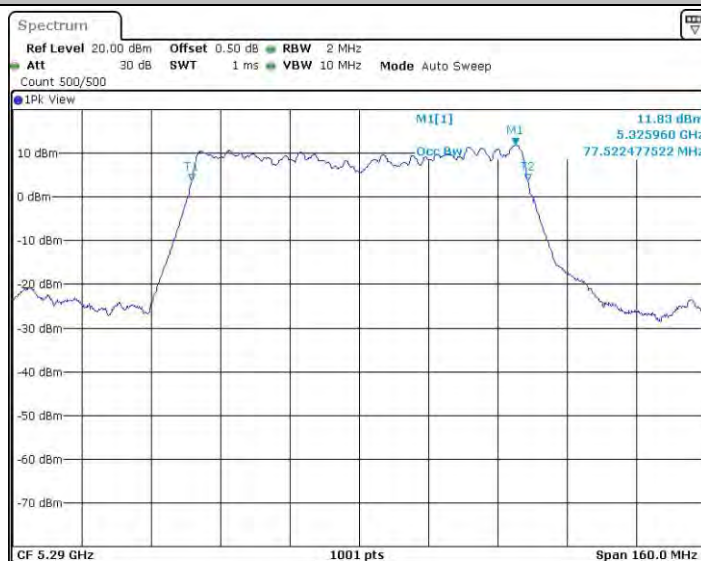
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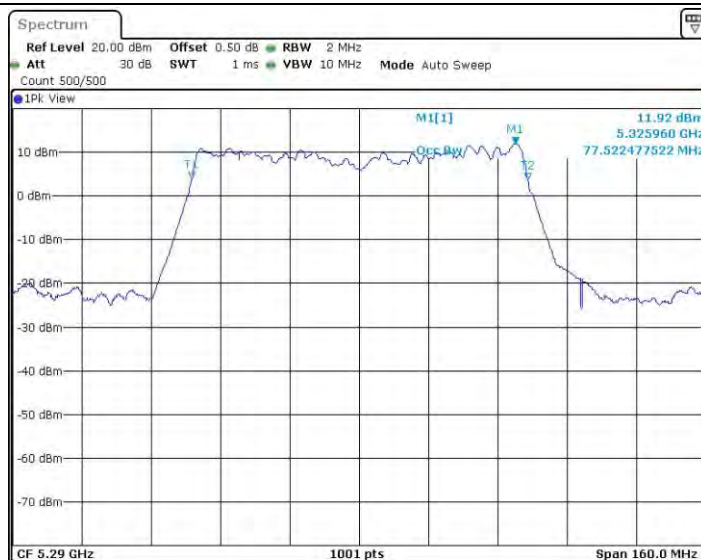
11AC80SISO_Ant0_5210



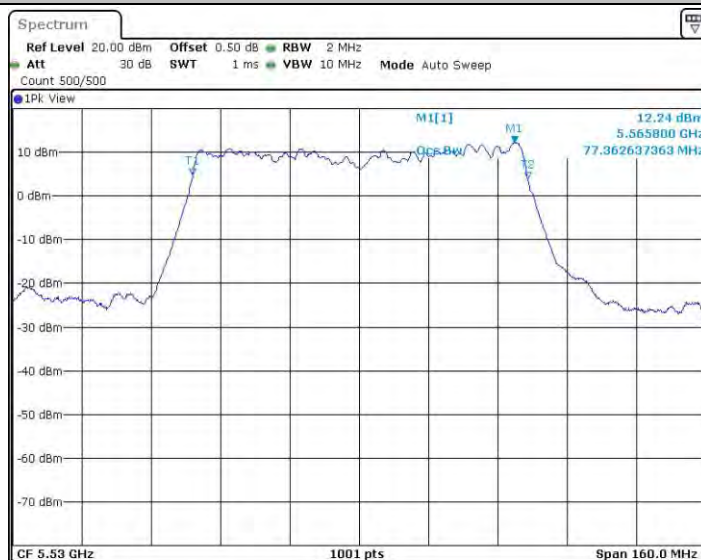
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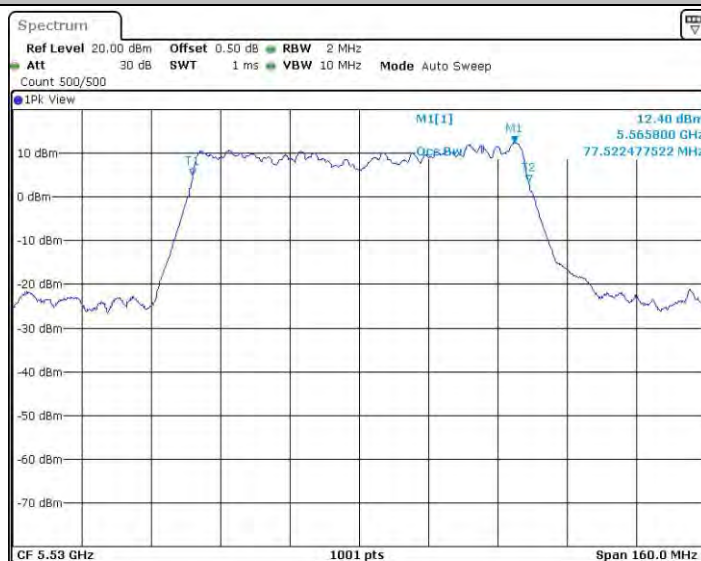
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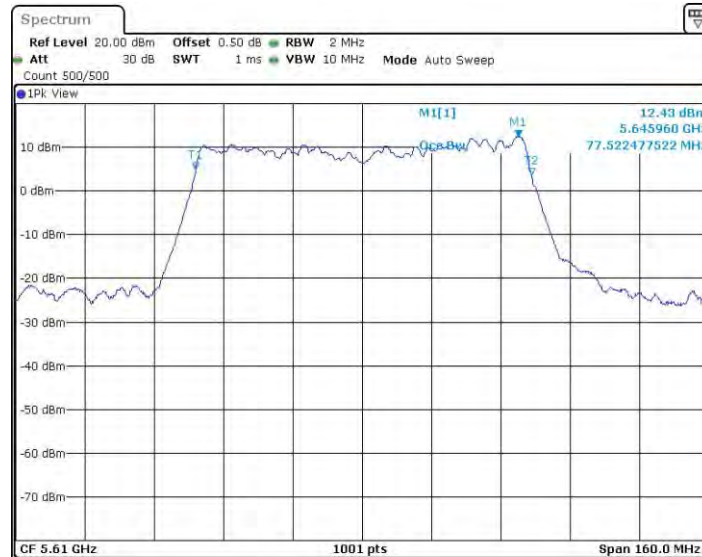
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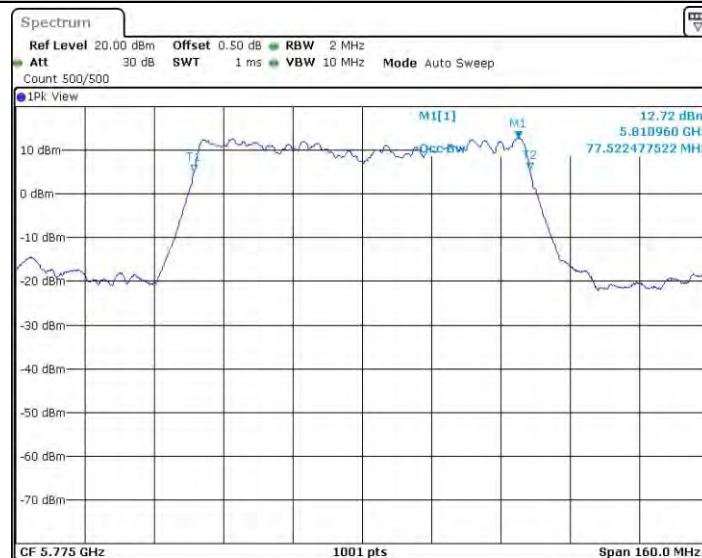
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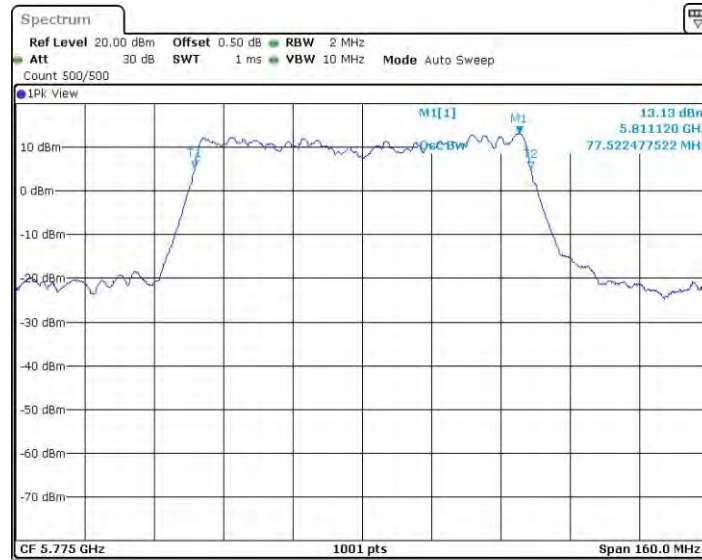
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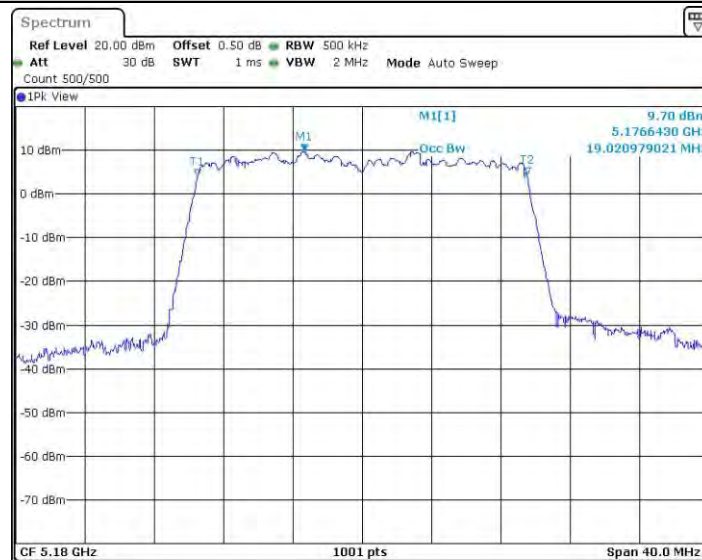
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11AC80SISO_Ant1_5775



11AX20SISO_Ant0_5180



11AX20SISO_Ant1_5180



11AX20SISO_Ant0_5200



11AX20SISO_Ant1_5200



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11AX20SISO_Ant1_5240



11AX20SISO_Ant0_5260



11AX20SISO_Ant1_5260



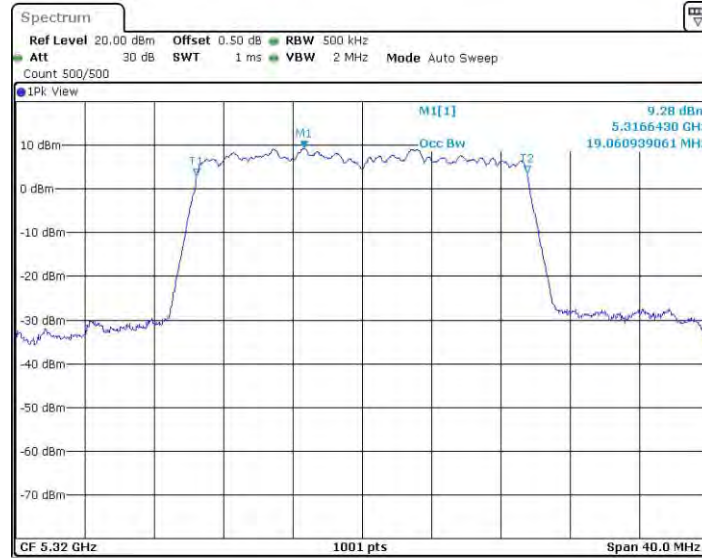
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11AX20SISO_Ant1_5500



11AX20SISO_Ant0_5580



11AX20SISO_Ant1_5580



11AX20SISO_Ant0_5700



11AX20SISO_Ant1_5700



11AX20SISO_Ant0_5745



11AX20SISO_Ant1_5745



11AX20SISO_Ant0_5785



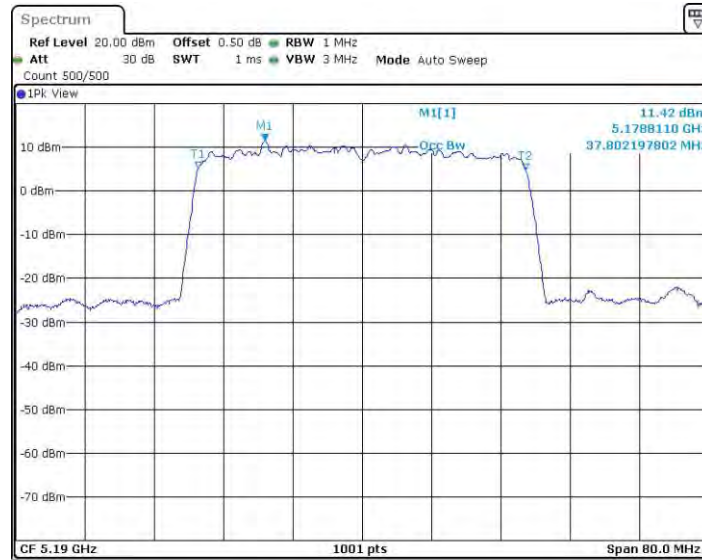
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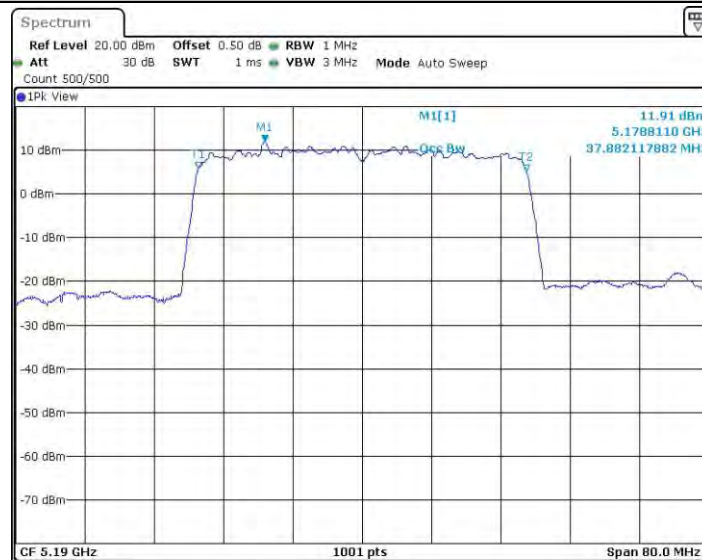
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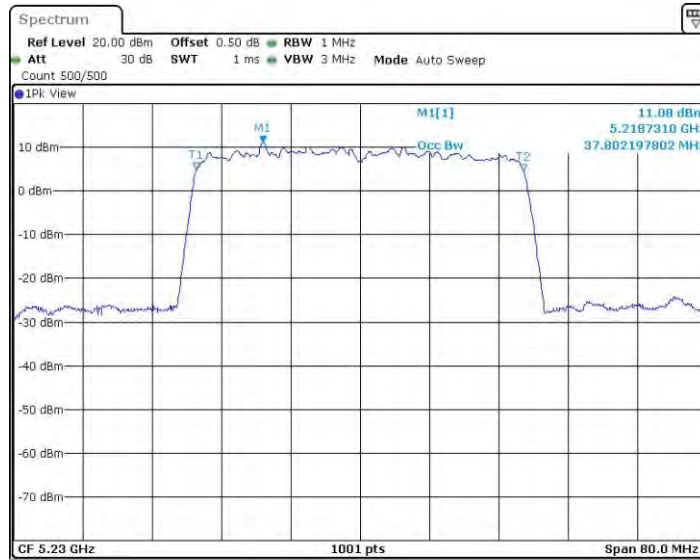
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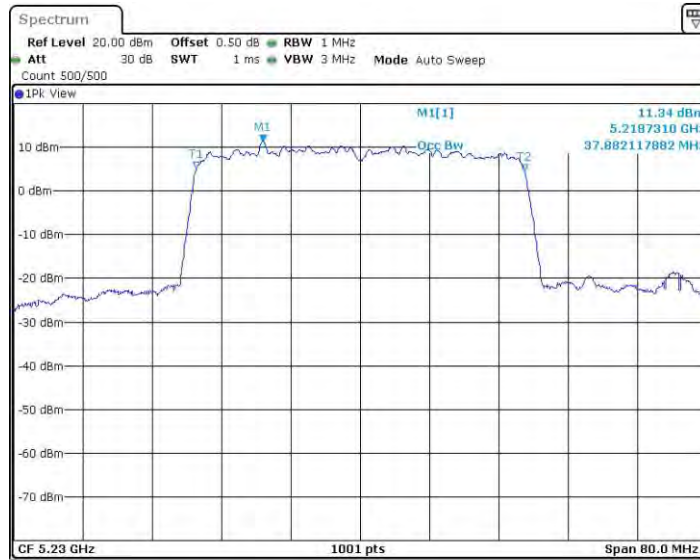
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11AX40SISO_Ant1_5190



11AX40SISO_Ant0_5230



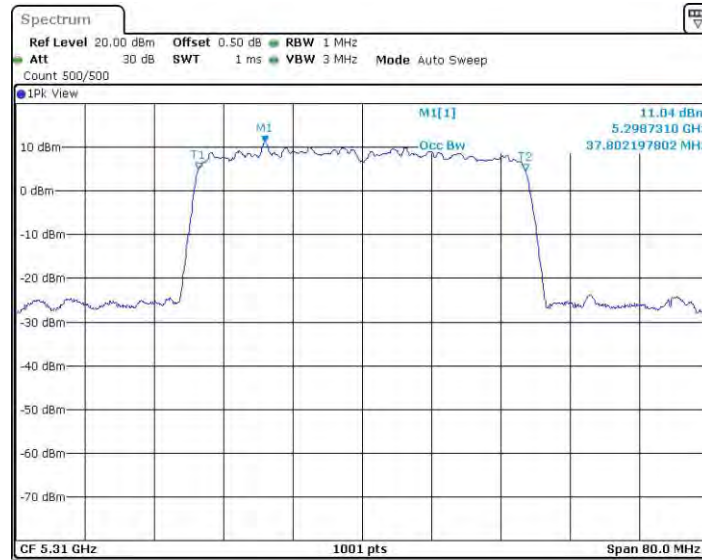
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11AX40SISO_Ant0_5270



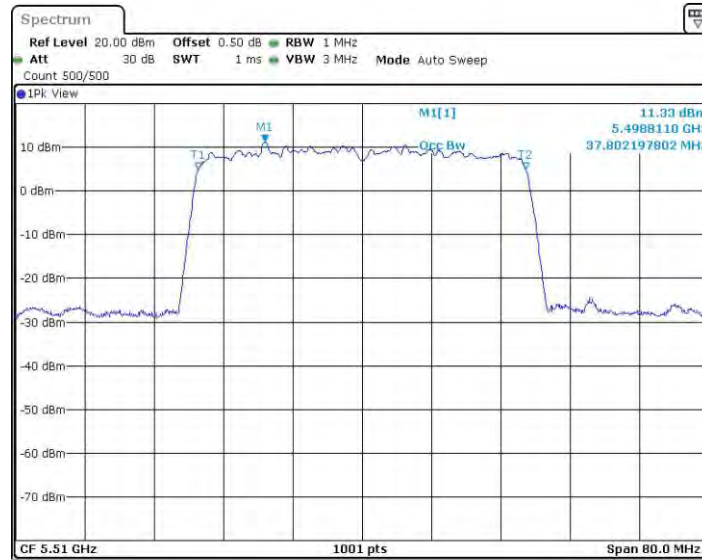
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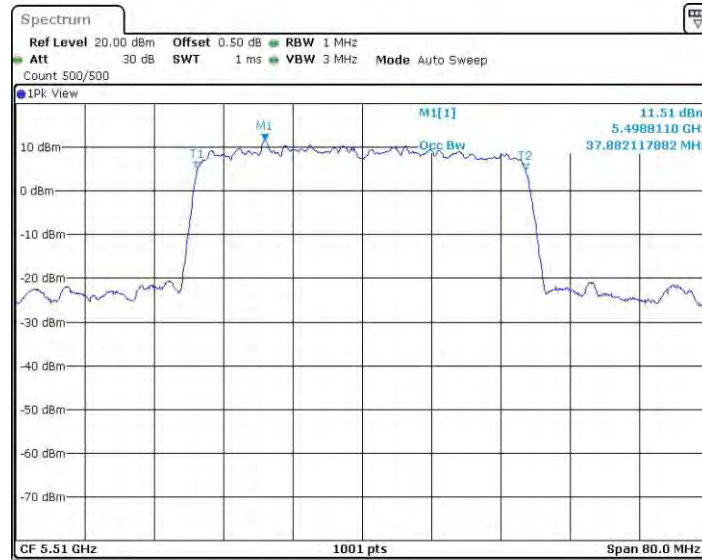
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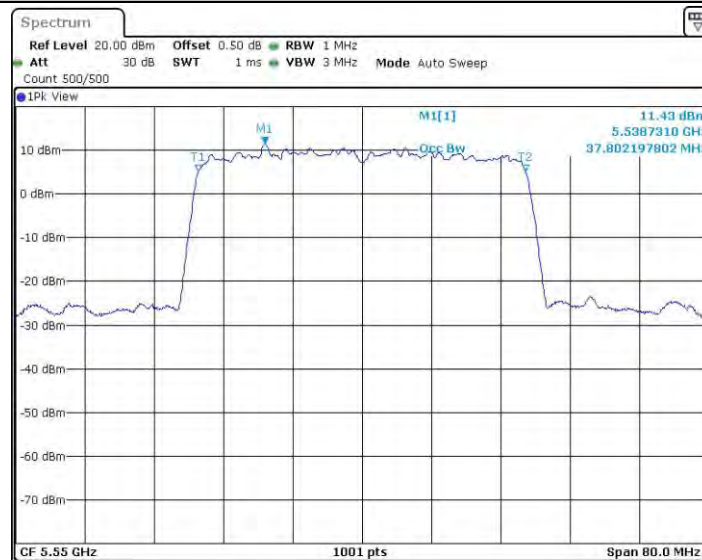
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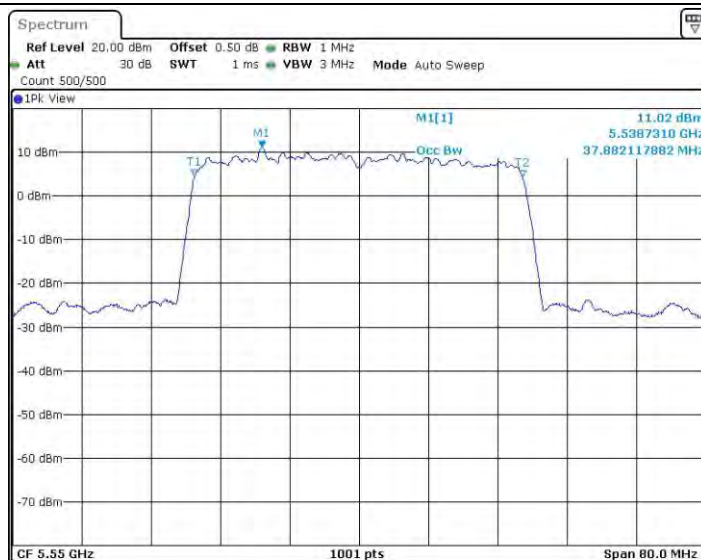
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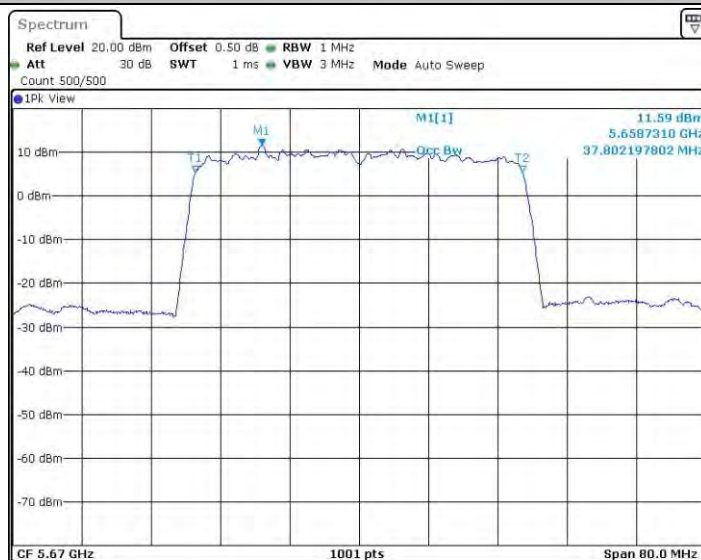
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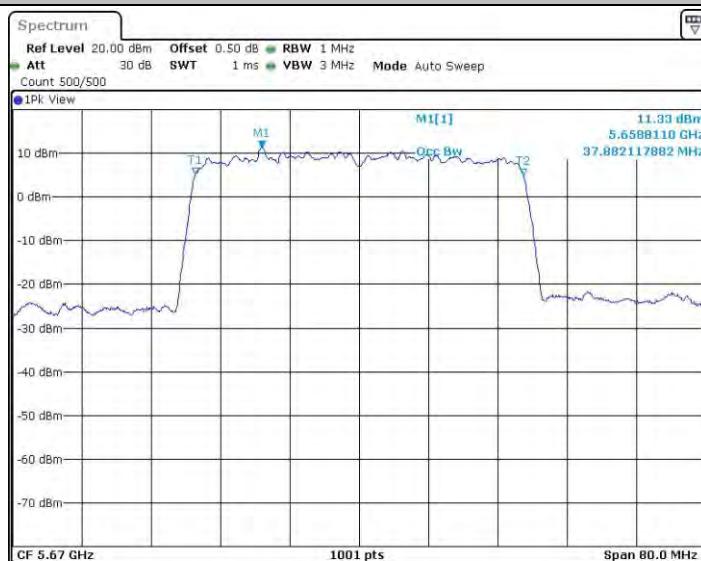
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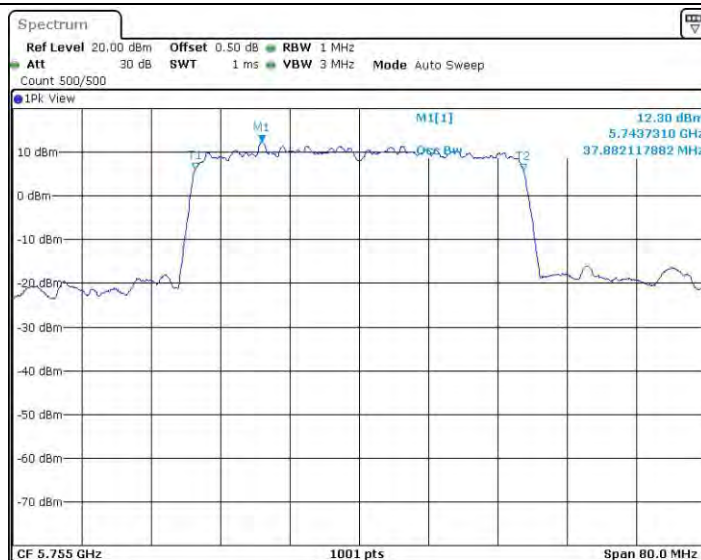
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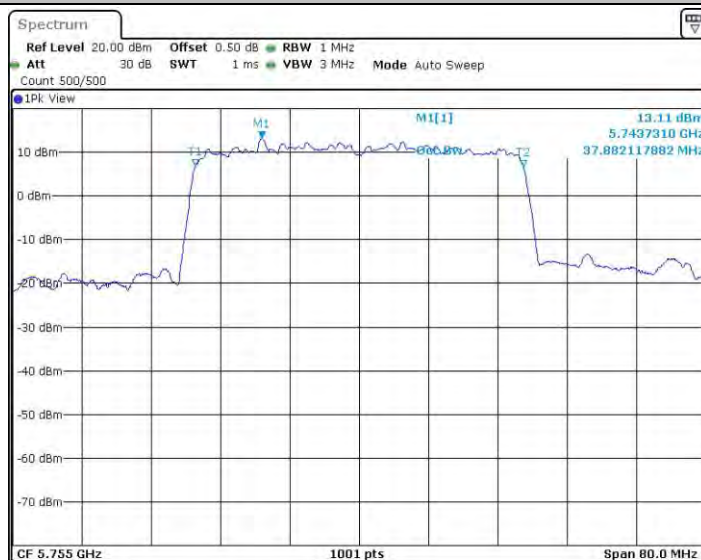
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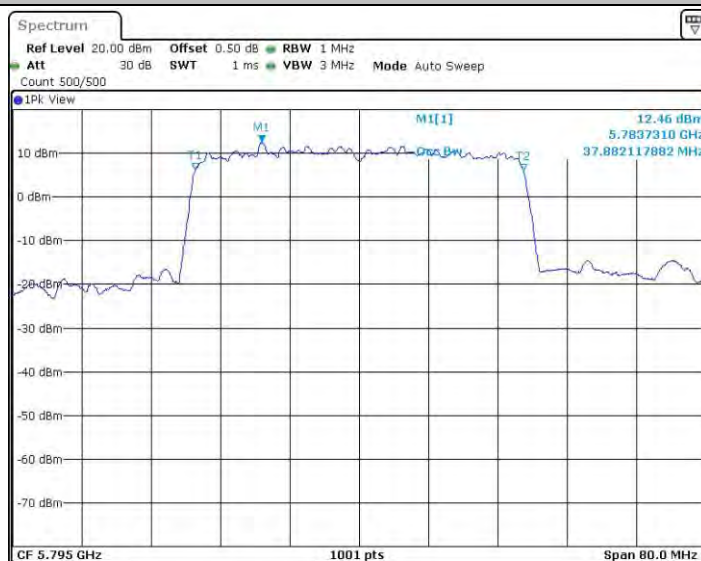
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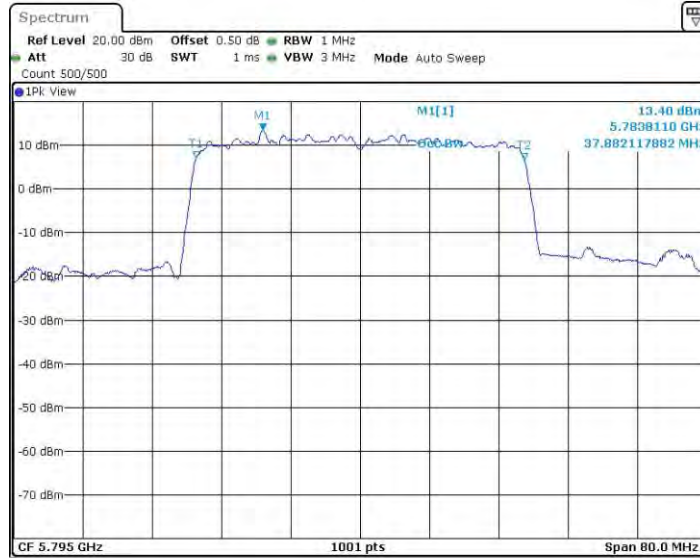
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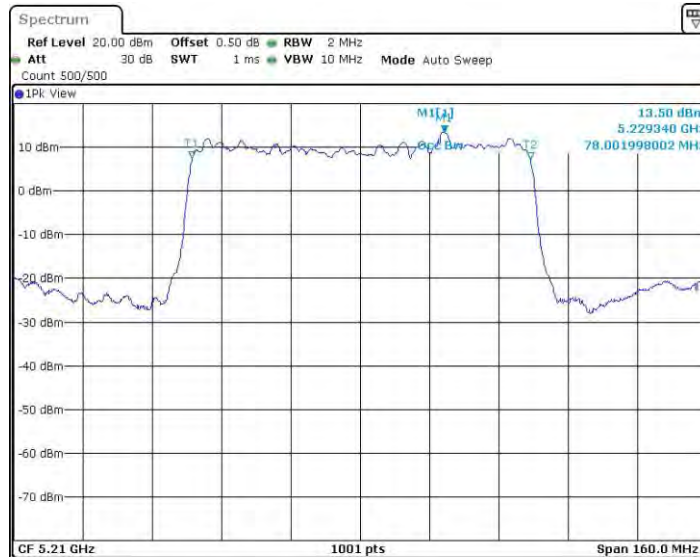
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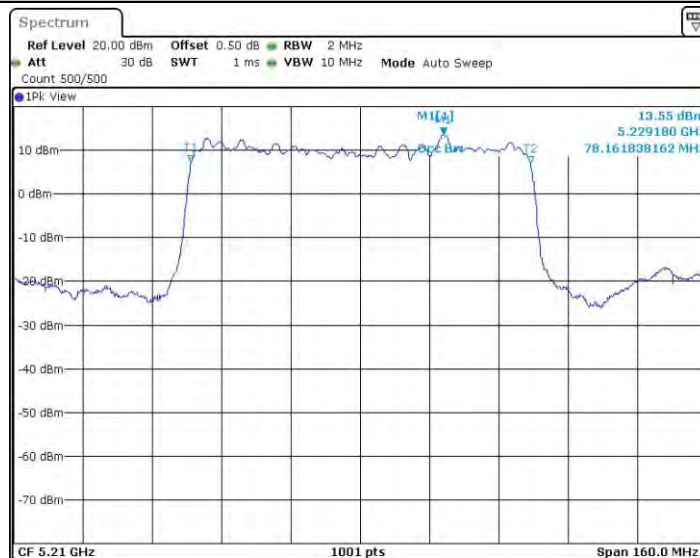
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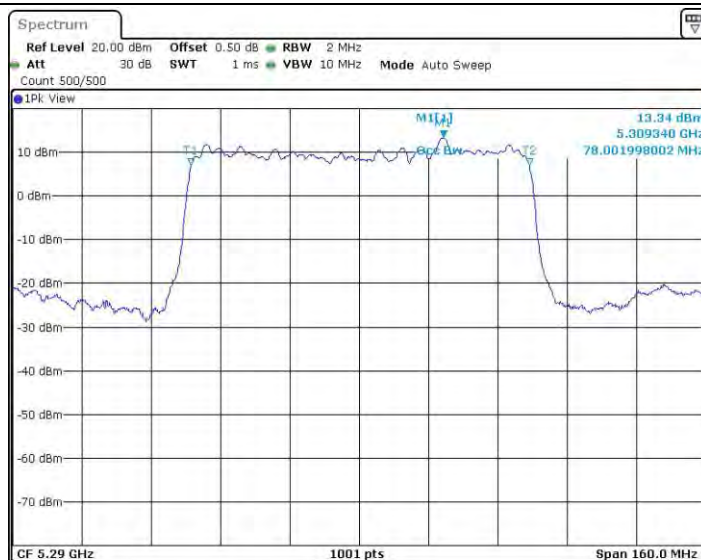
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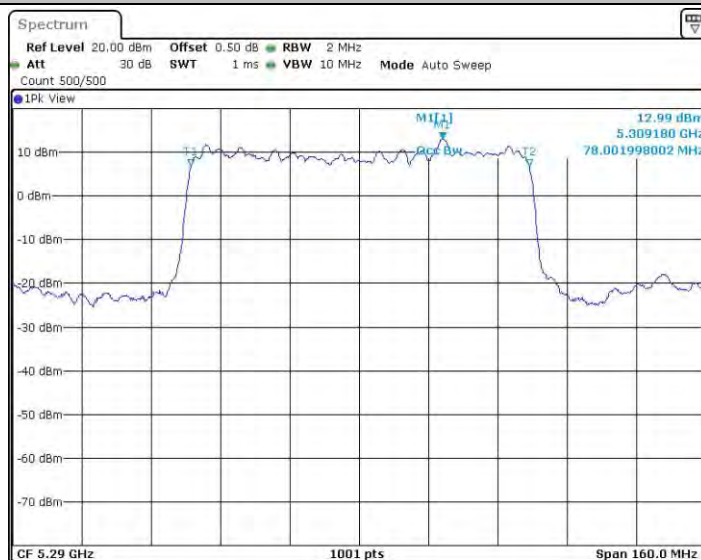
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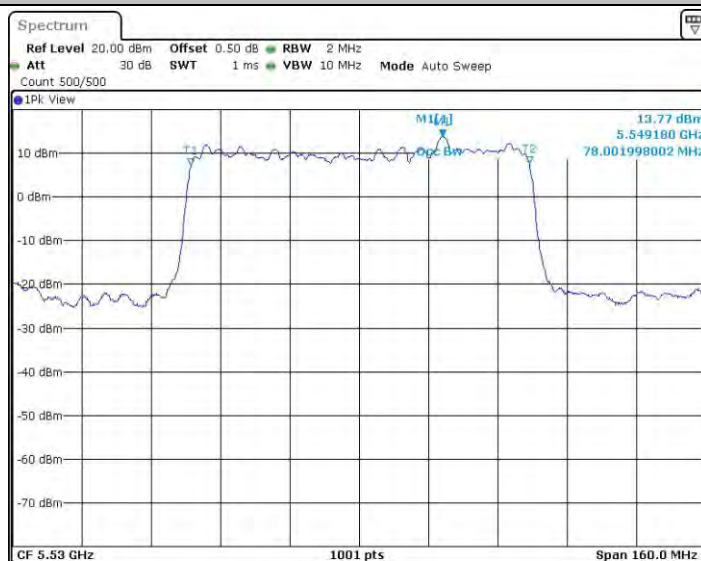
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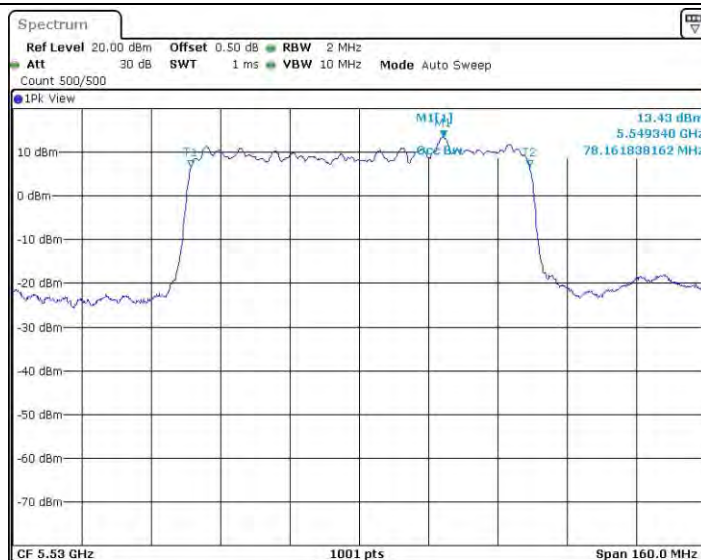
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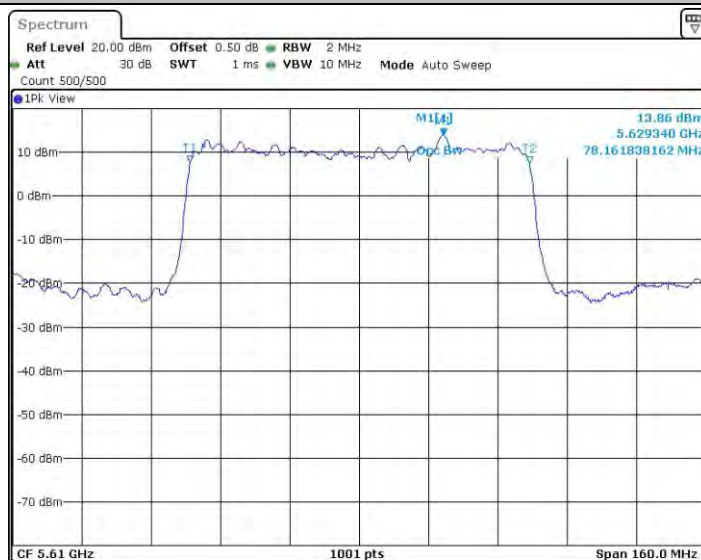
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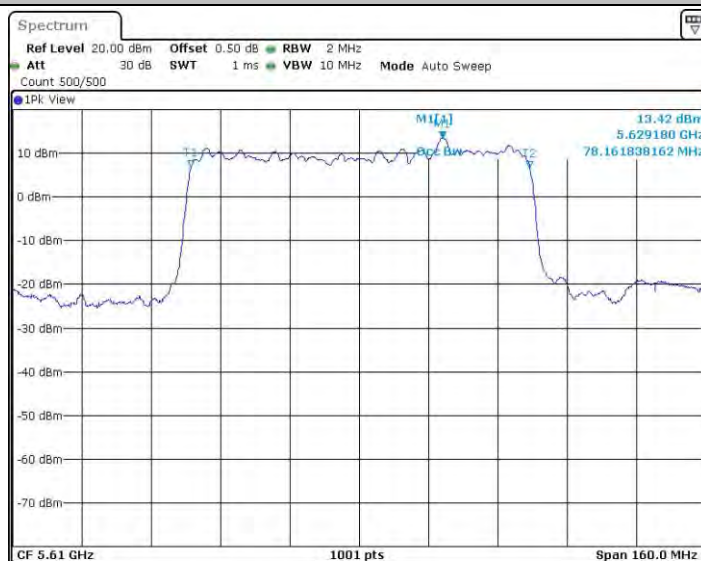
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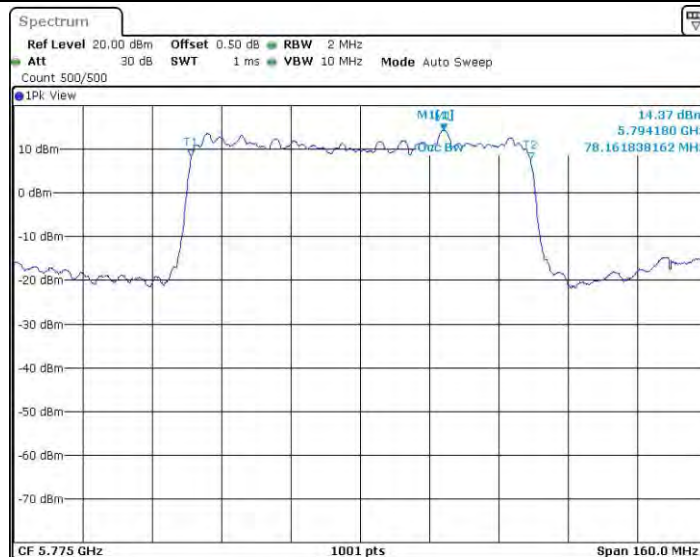
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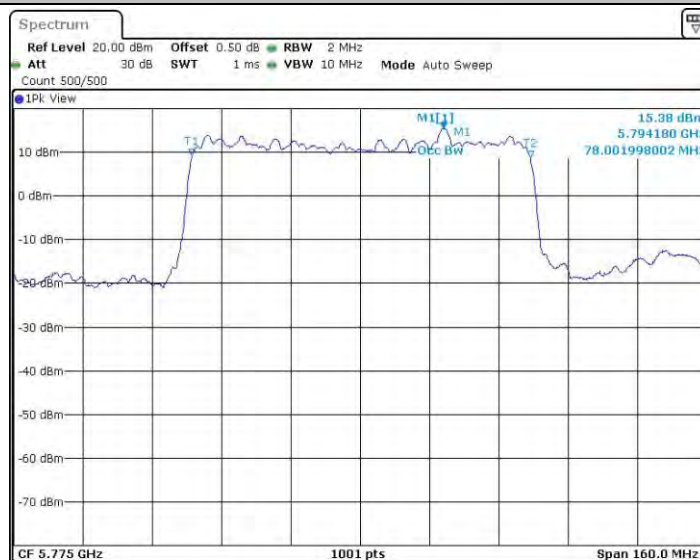
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11AX80SISO_Ant1_5610



11AX80SISO_Ant0_5775



11AX80SISO_Ant1_5775

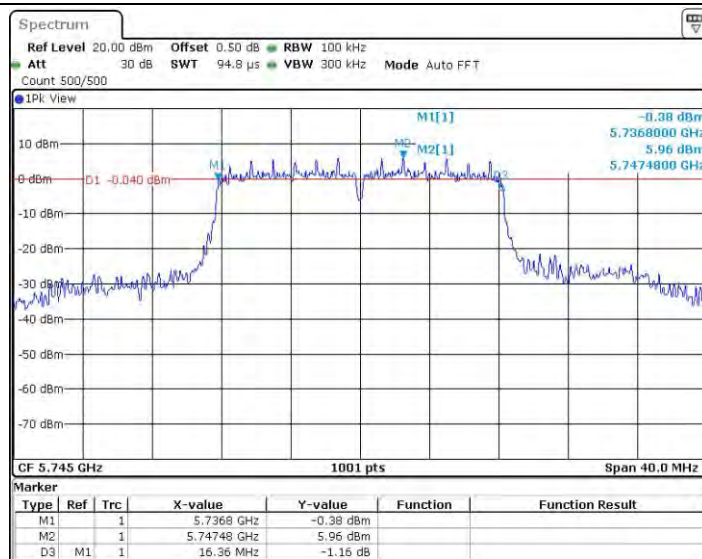
Appendix A3: Min emission bandwidth

Test Result

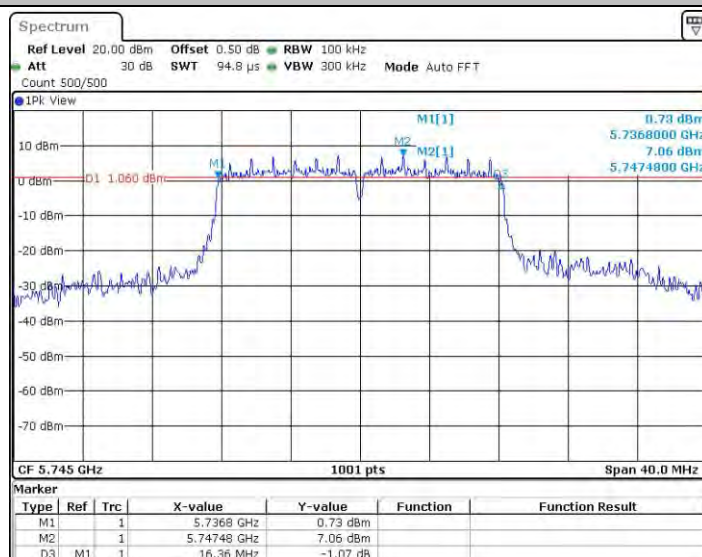
TestMode	Antenna	Channel	6db EBW [MHz]	FL[MHz]	FH[MHz]	Limit[MHz]	Verdict
11A	Ant0	5745	16.360	5736.800	5753.160	0.5	PASS
	Ant1	5745	16.360	5736.800	5753.160	0.5	PASS
	Ant0	5785	16.400	5776.760	5793.160	0.5	PASS
	Ant1	5785	16.400	5776.760	5793.160	0.5	PASS
	Ant0	5825	16.360	5816.800	5833.160	0.5	PASS
	Ant1	5825	16.400	5816.760	5833.160	0.5	PASS
11N20SISO	Ant0	5745	17.000	5736.160	5753.160	0.5	PASS
	Ant1	5745	16.760	5736.400	5753.160	0.5	PASS
	Ant0	5785	17.360	5776.160	5793.520	0.5	PASS
	Ant1	5785	17.000	5776.400	5793.400	0.5	PASS
	Ant0	5825	17.240	5816.160	5833.400	0.5	PASS
	Ant1	5825	16.760	5816.400	5833.160	0.5	PASS
11N40SISO	Ant0	5755	35.520	5737.160	5772.680	0.5	PASS
	Ant1	5755	35.520	5737.160	5772.680	0.5	PASS
	Ant0	5795	35.520	5777.160	5812.680	0.5	PASS
	Ant1	5795	35.680	5777.000	5812.680	0.5	PASS
11AC20SISO	Ant0	5745	17.400	5736.160	5753.560	0.5	PASS
	Ant1	5745	17.360	5736.160	5753.520	0.5	PASS
	Ant0	5785	17.040	5776.520	5793.560	0.5	PASS
	Ant1	5785	17.400	5776.160	5793.560	0.5	PASS
	Ant0	5825	17.000	5816.520	5833.520	0.5	PASS
	Ant1	5825	17.360	5816.160	5833.520	0.5	PASS
11AC40SISO	Ant0	5755	35.840	5737.160	5773.000	0.5	PASS
	Ant1	5755	35.840	5737.160	5773.000	0.5	PASS
	Ant0	5795	35.840	5777.160	5813.000	0.5	PASS
	Ant1	5795	35.840	5777.160	5813.000	0.5	PASS
11AC80SISO	Ant0	5775	76.160	5736.600	5812.760	0.5	PASS
	Ant1	5775	76.640	5736.600	5813.240	0.5	PASS
11AX20SISO	Ant0	5745	18.080	5735.680	5753.760	0.5	PASS
	Ant1	5745	18.000	5736.120	5754.120	0.5	PASS
	Ant0	5785	18.080	5775.680	5793.760	0.5	PASS
	Ant1	5785	18.280	5775.680	5793.960	0.5	PASS
	Ant0	5825	17.840	5815.920	5833.760	0.5	PASS
	Ant1	5825	18.080	5815.680	5833.760	0.5	PASS
11AX40SISO	Ant0	5755	37.680	5736.360	5774.040	0.5	PASS
	Ant1	5755	37.680	5736.360	5774.040	0.5	PASS
	Ant0	5795	37.680	5776.360	5814.040	0.5	PASS
	Ant1	5795	37.680	5776.360	5814.040	0.5	PASS
11AX80SISO	Ant0	5775	77.760	5735.960	5813.720	0.5	PASS

	Ant1	5775	77.760	5735.960	5813.720	0.5	PASS
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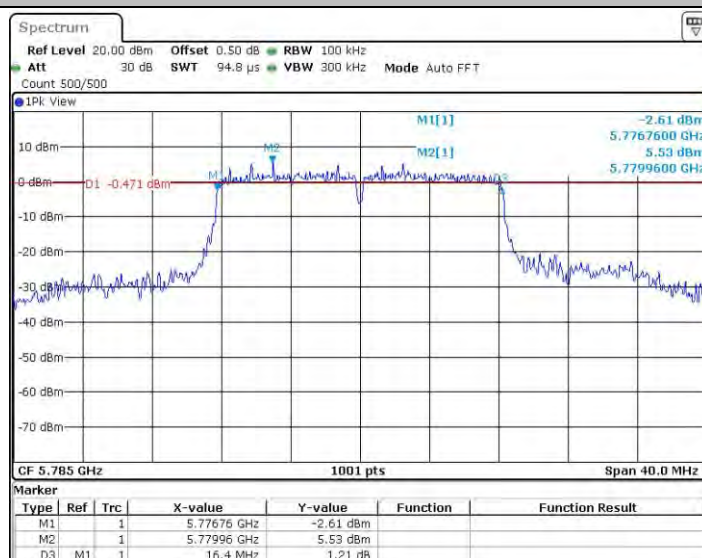
Test Graphs



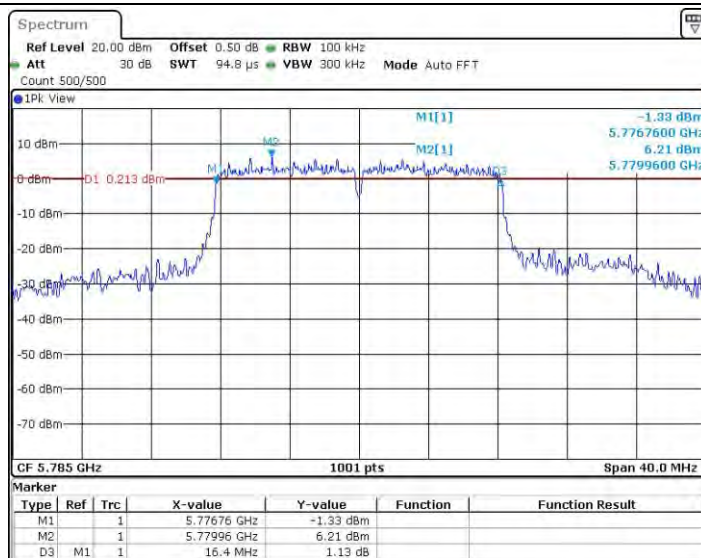
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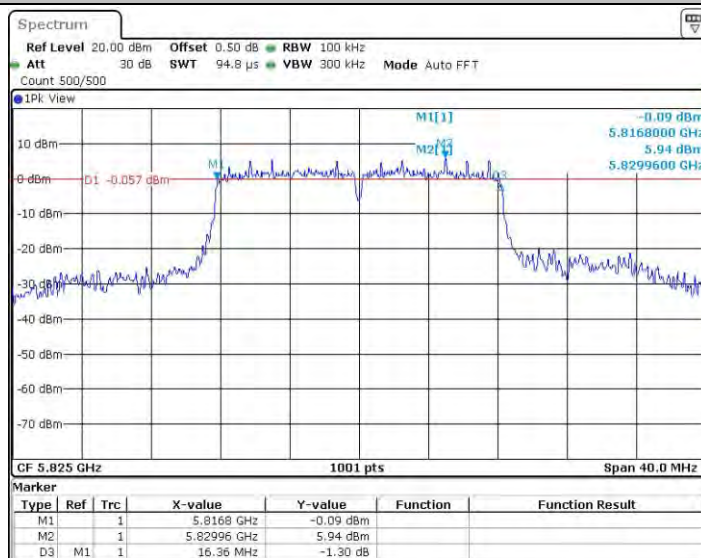
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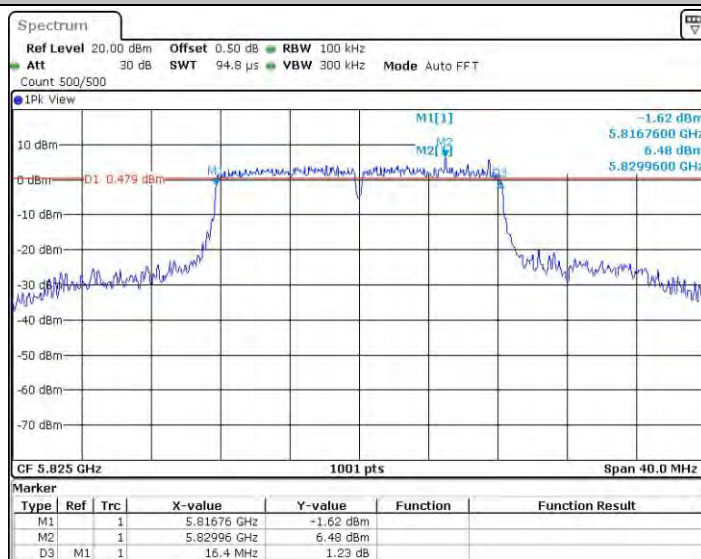
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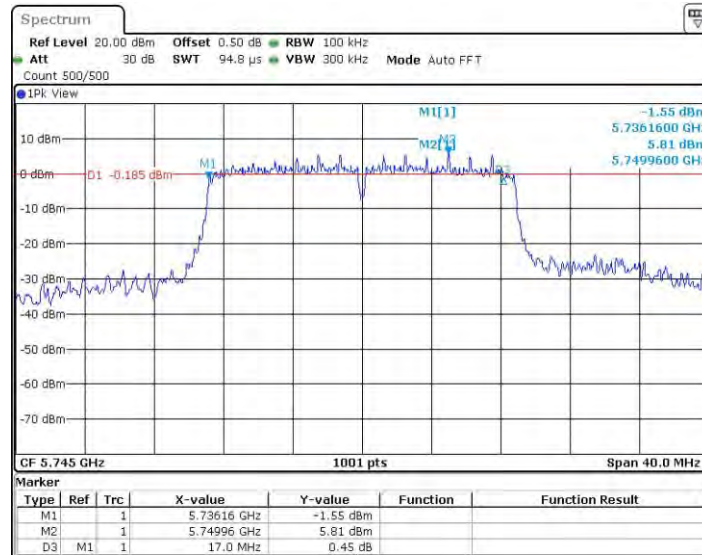
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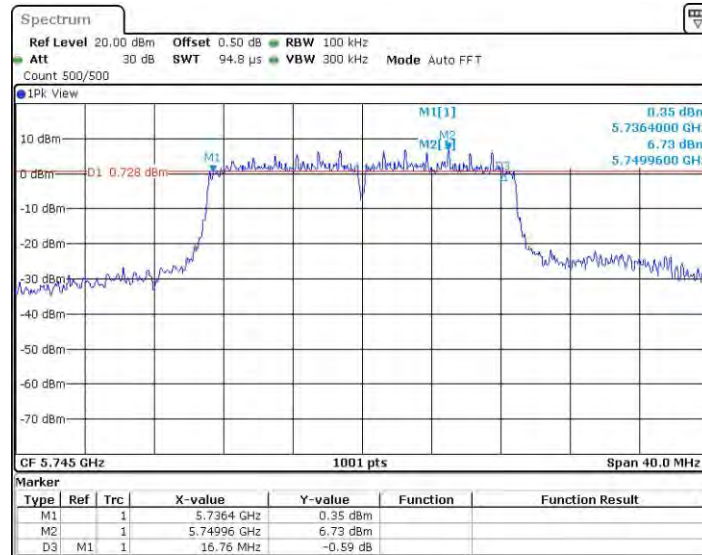
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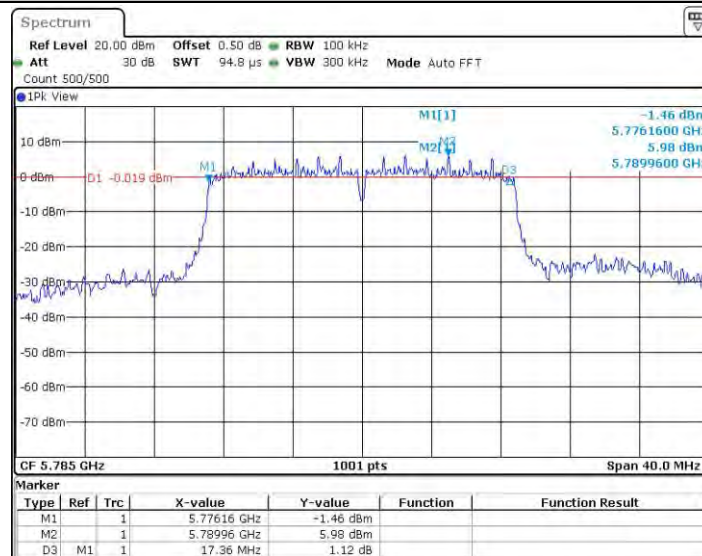
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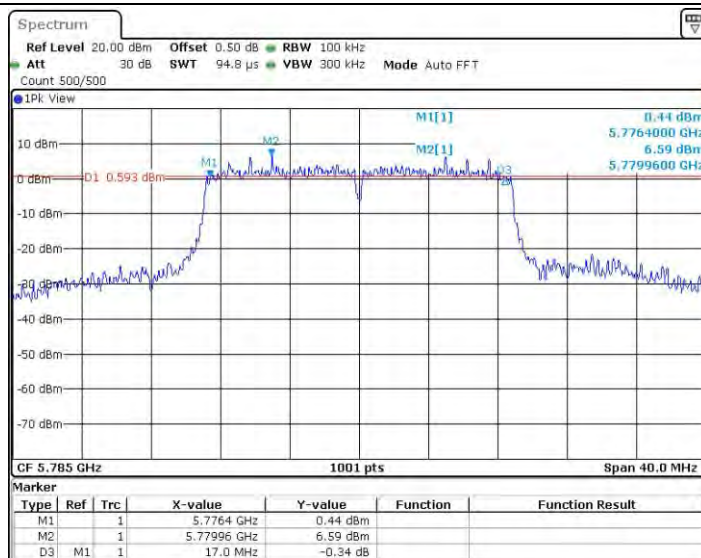
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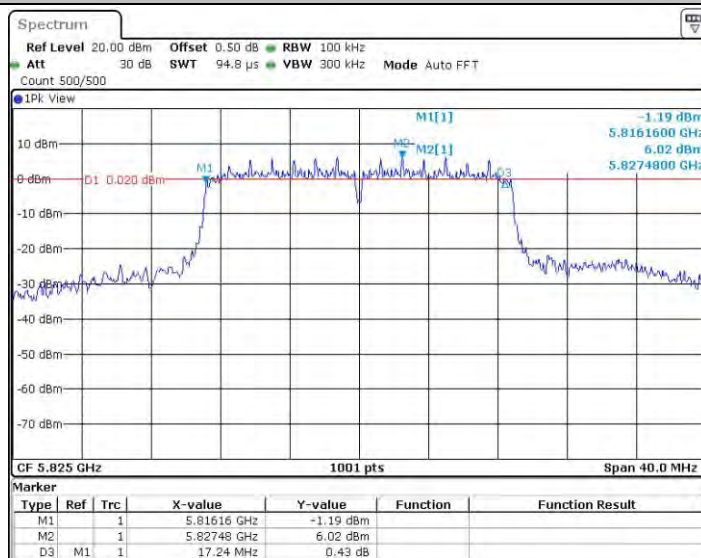
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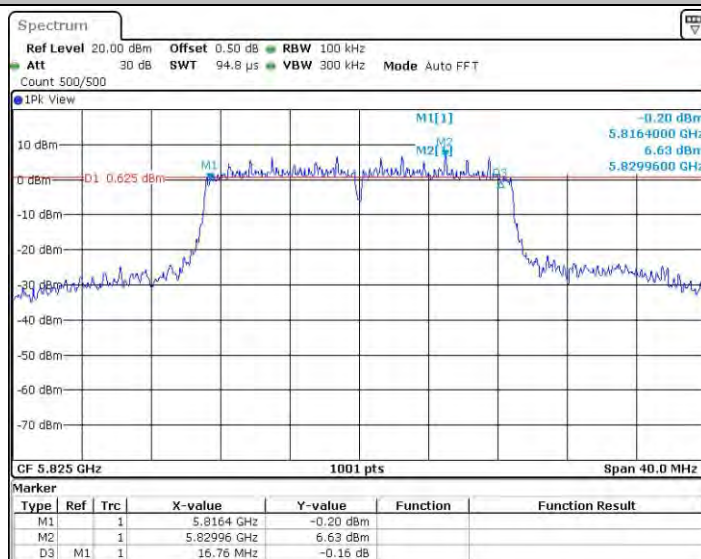
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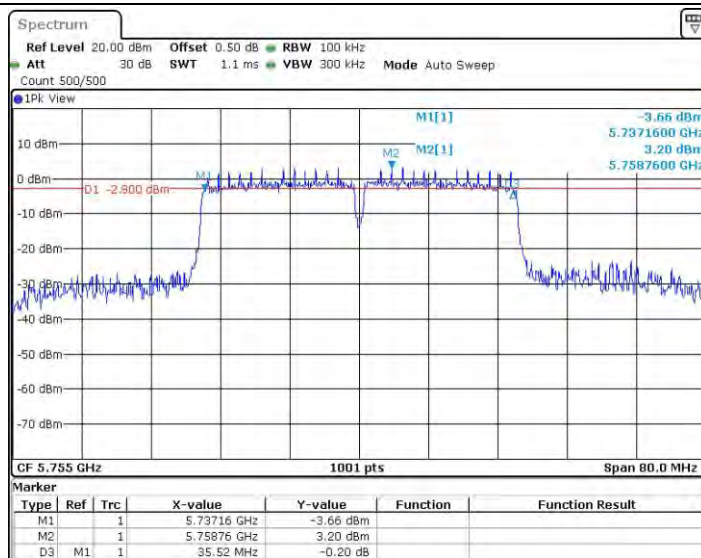
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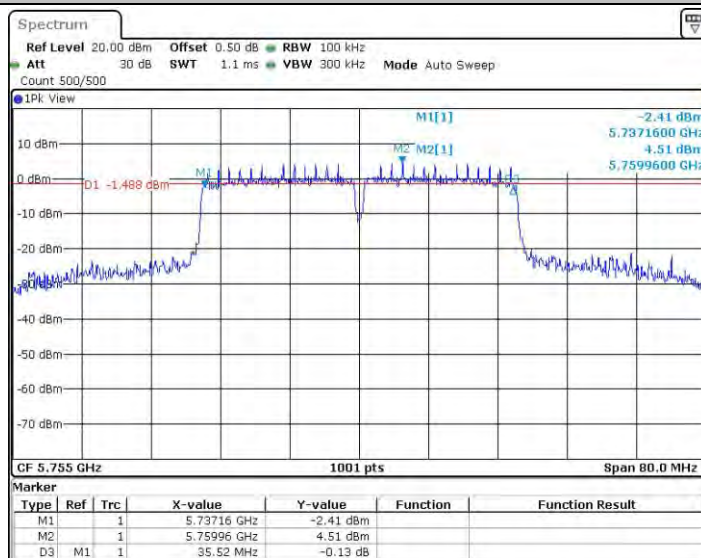
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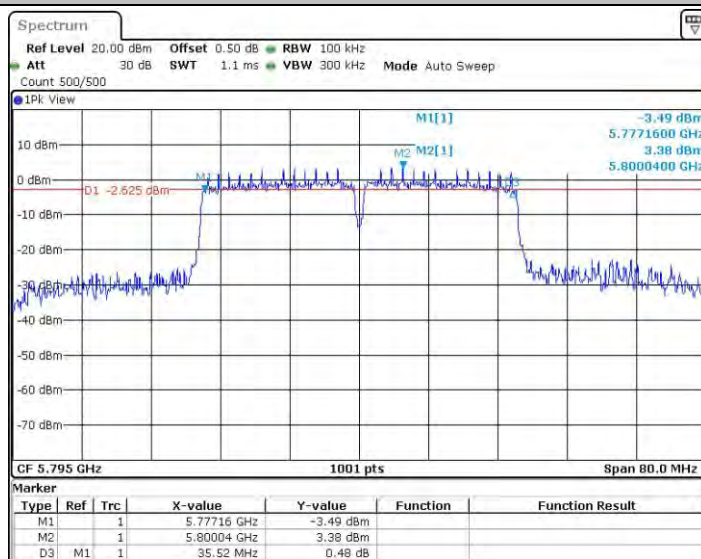
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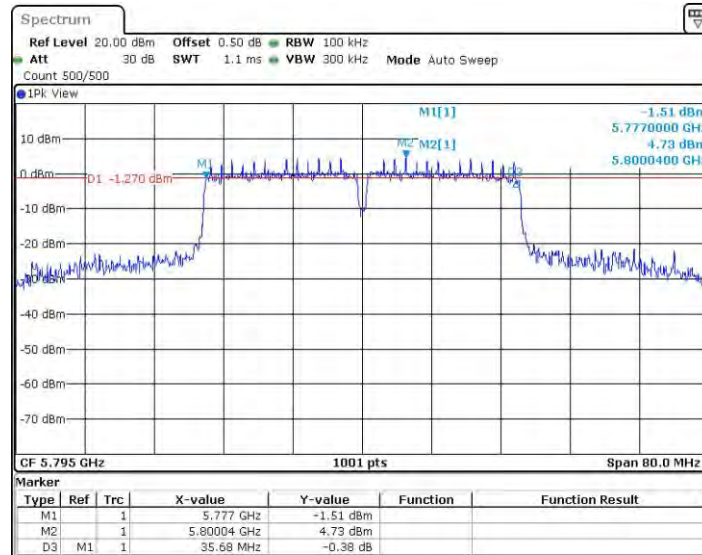
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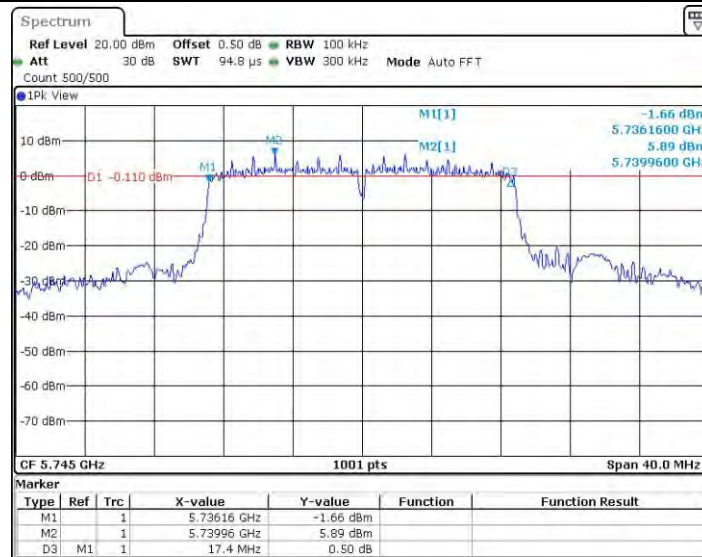
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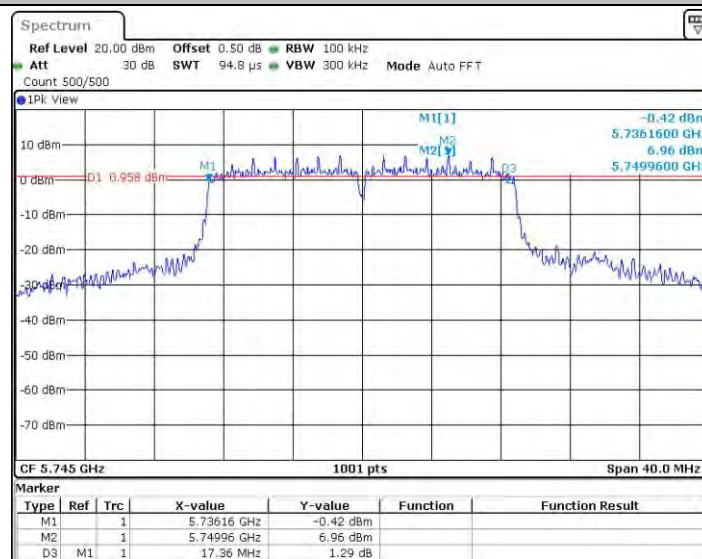
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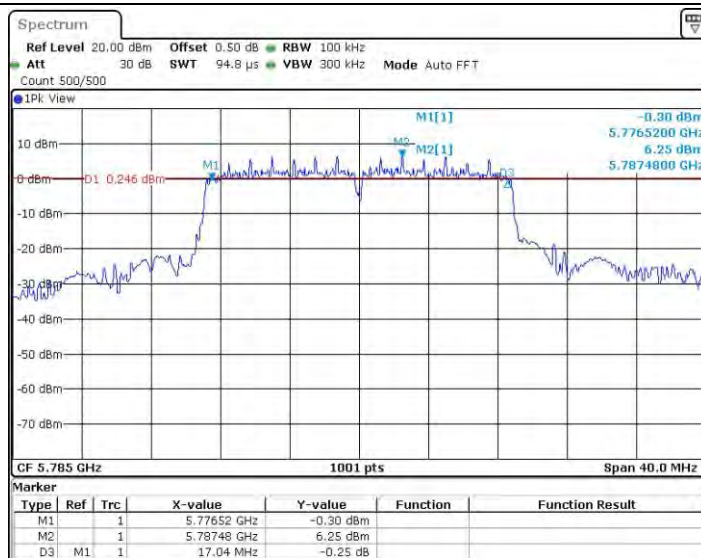
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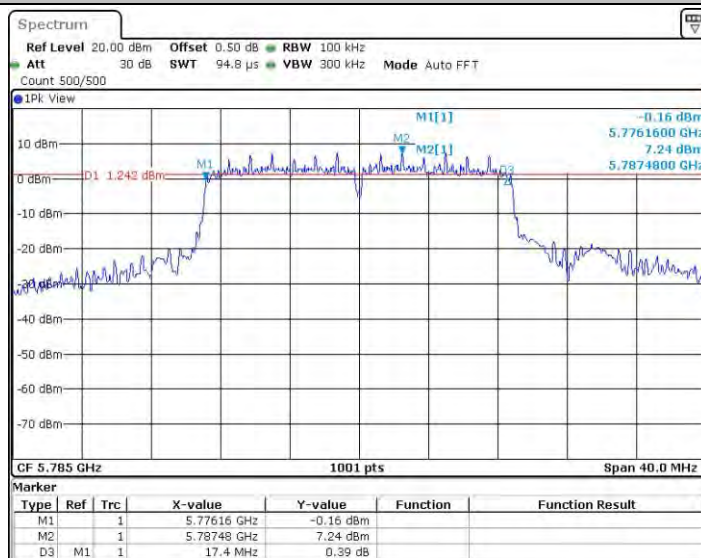
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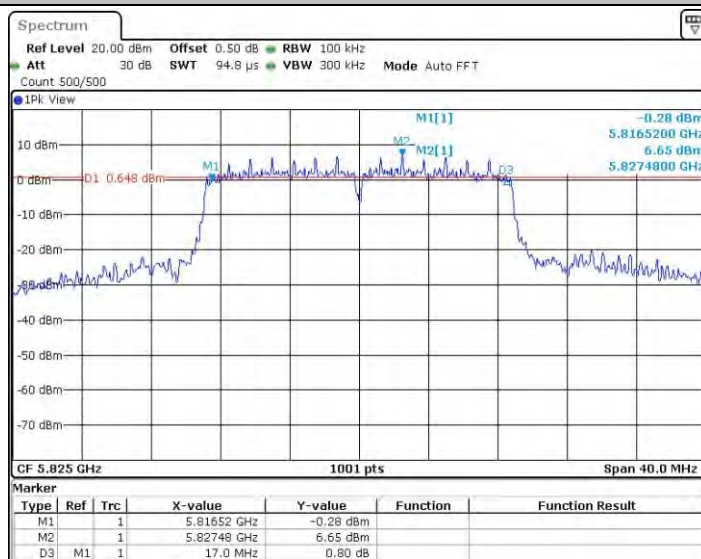
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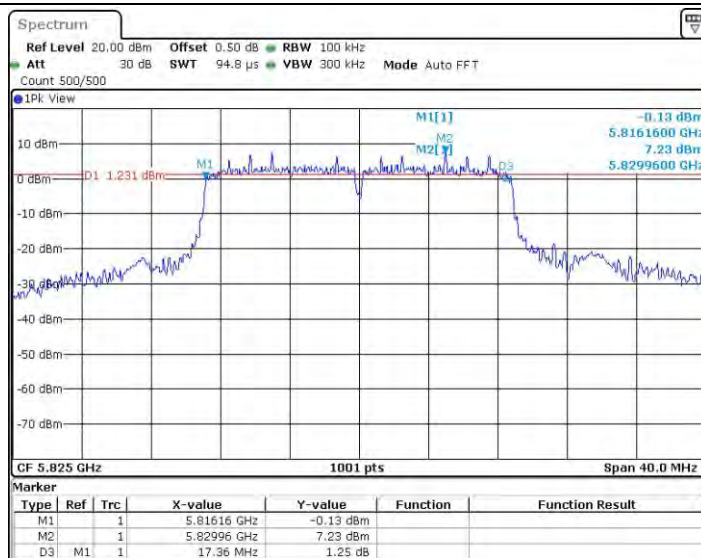
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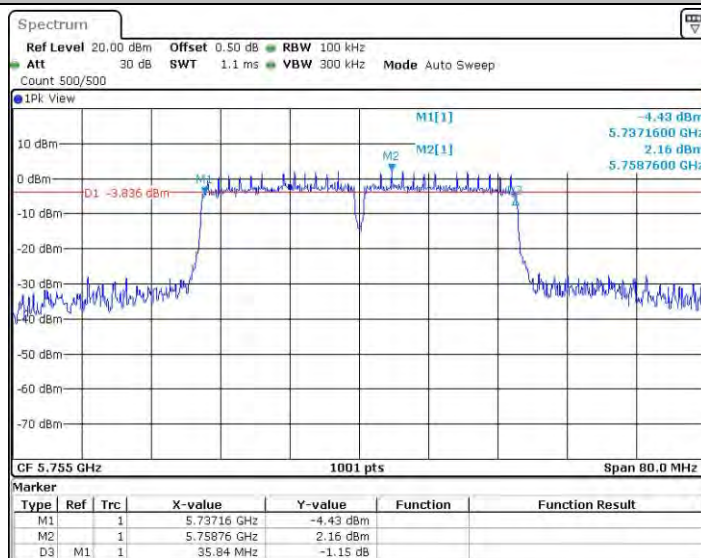
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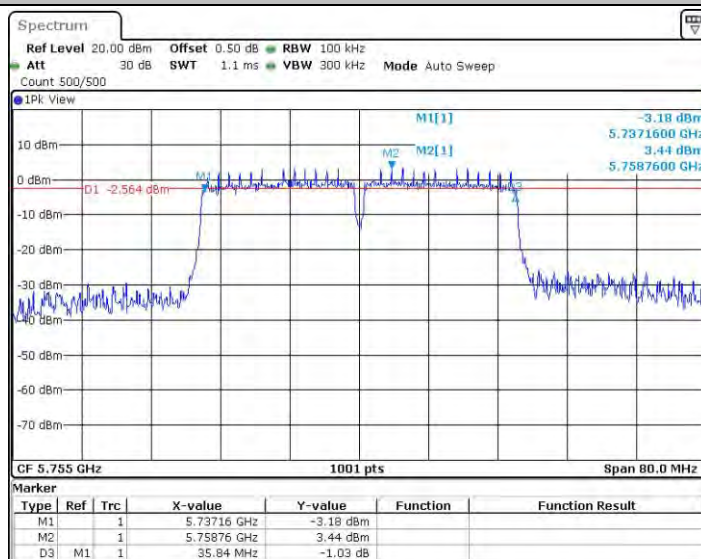
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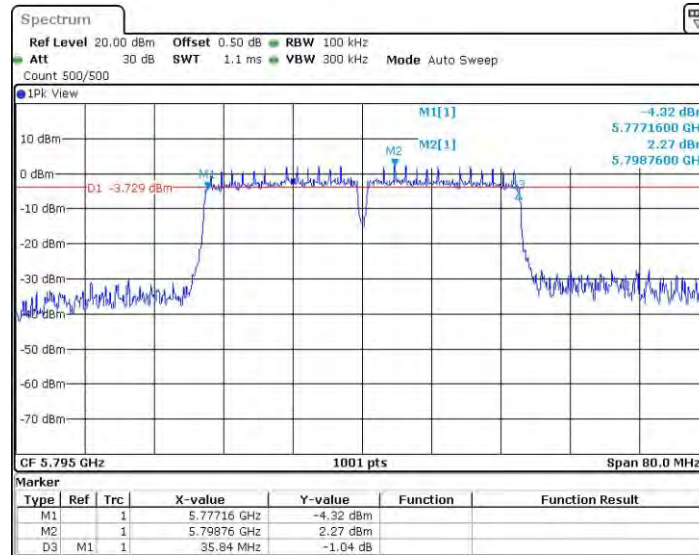
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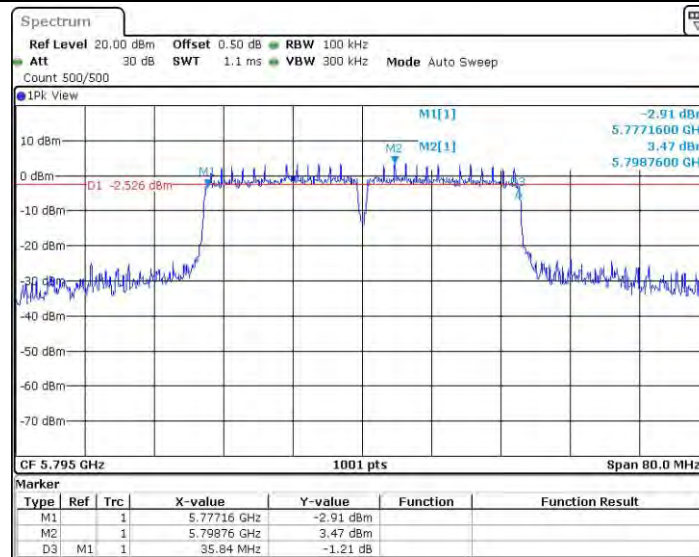
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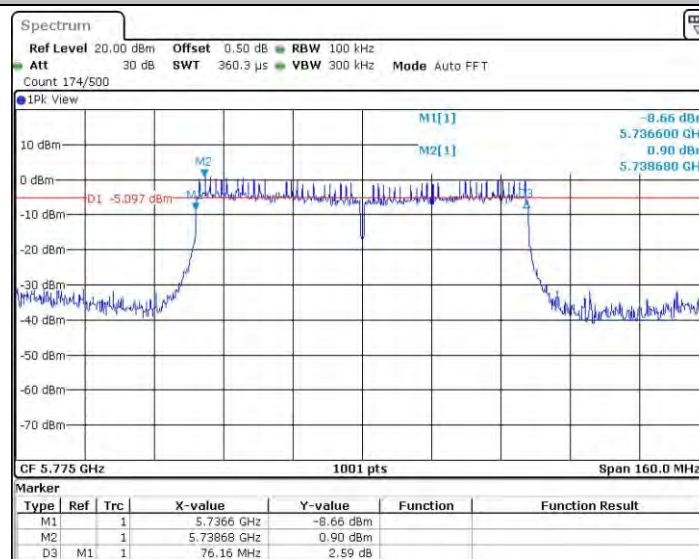
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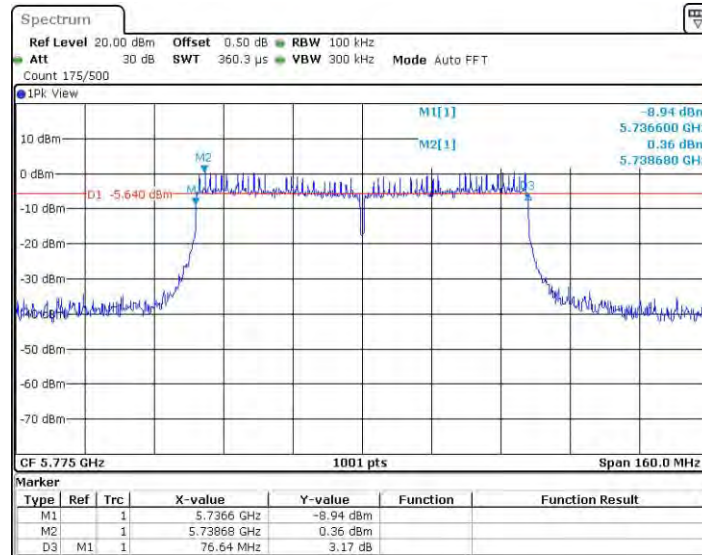
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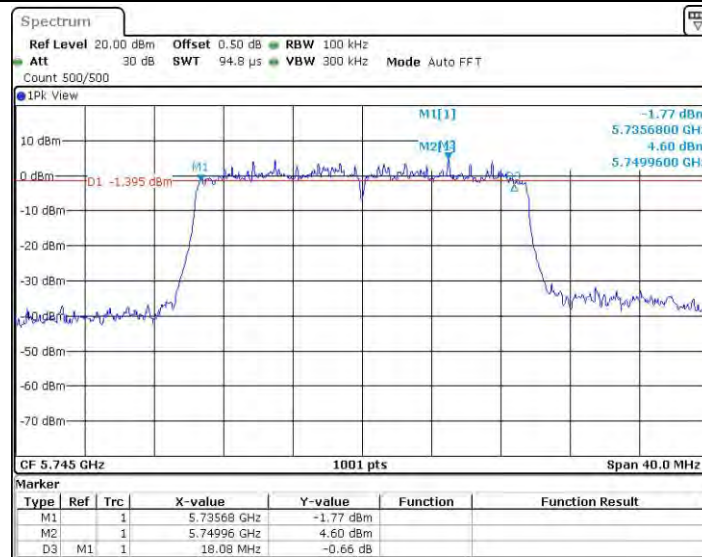
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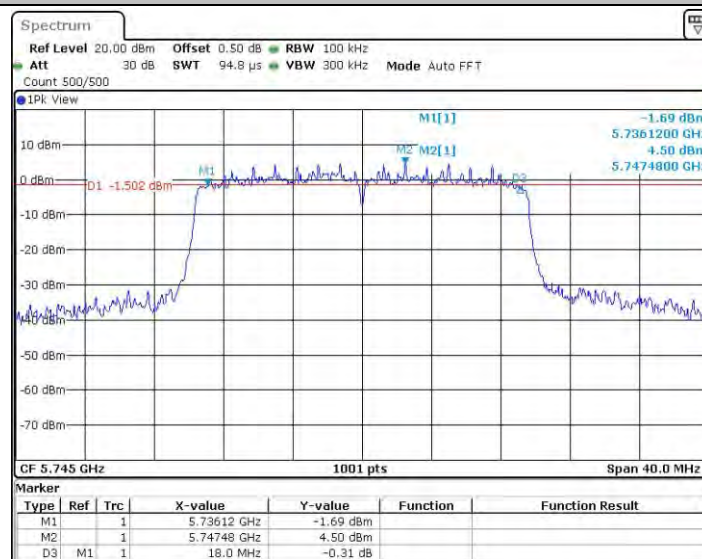
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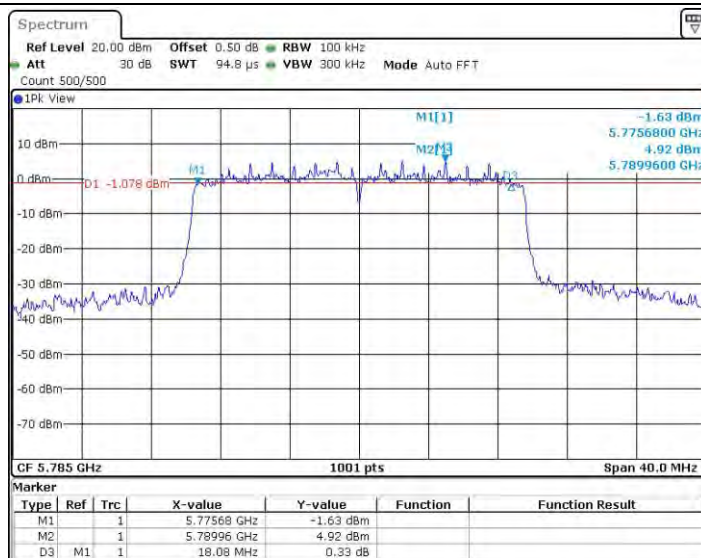
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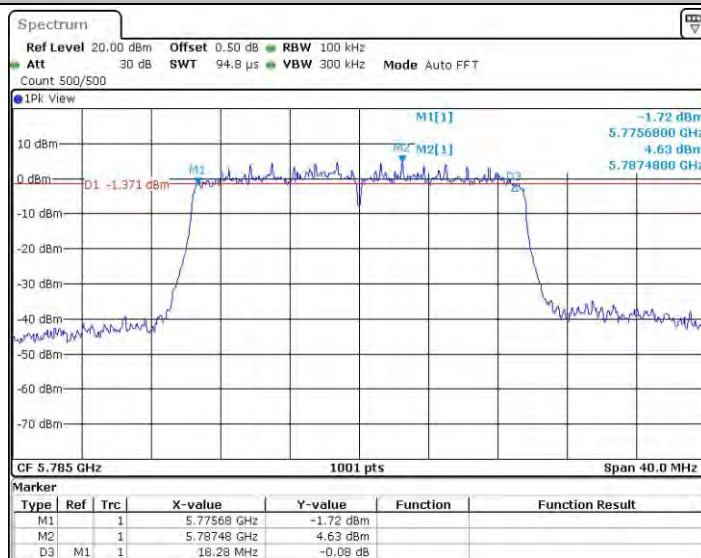
11AX20SISO_Ant0_5745



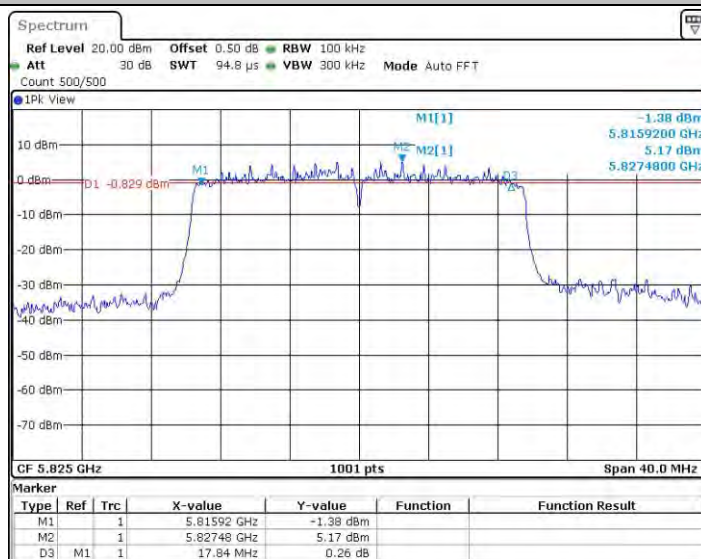
11AX20SISO_Ant1_5745



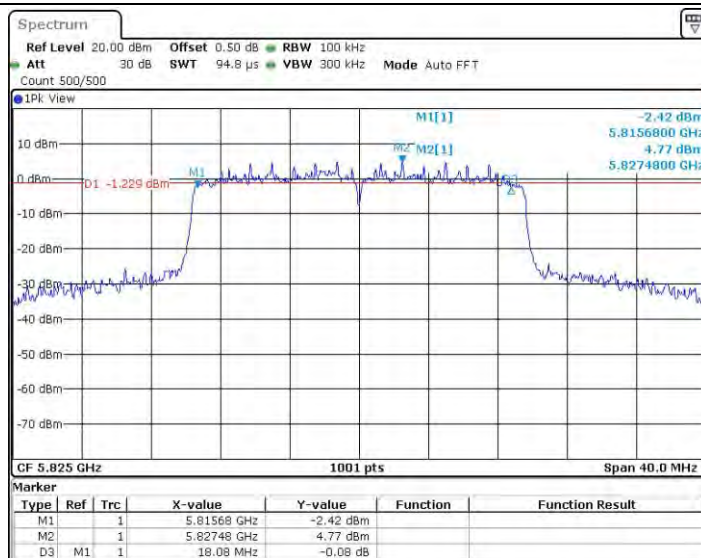
11AX20SISO_Ant0_5785



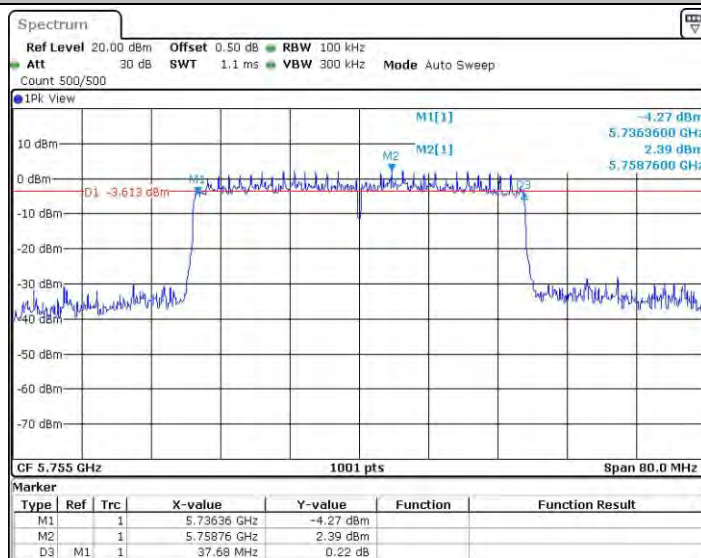
11AX20SISO_Ant1_5785



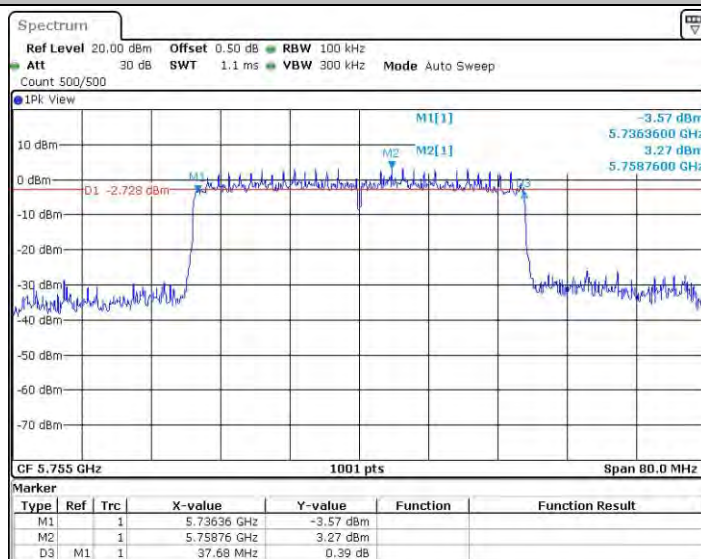
11AX20SISO_Ant0_5825



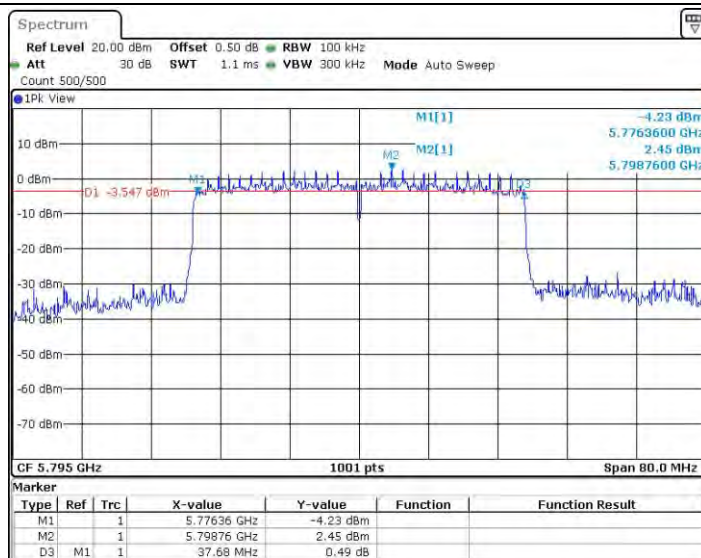
11AX20SISO_Ant1_5825



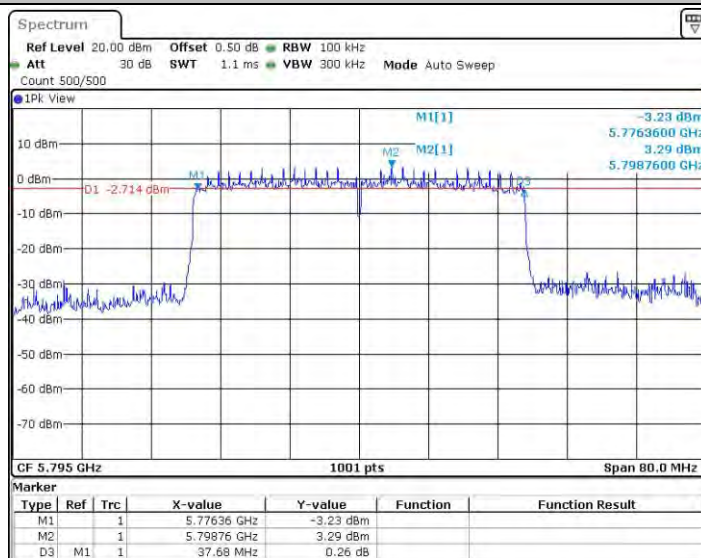
11AX40SISO_Ant0_5755



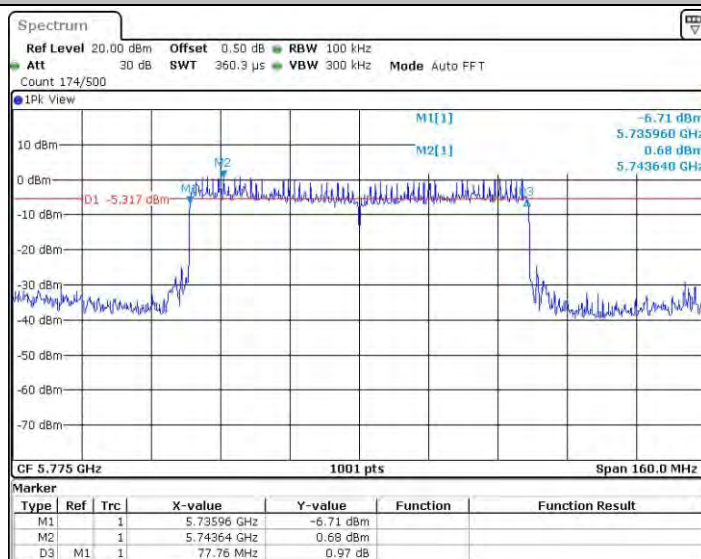
11AX40SISO_Ant1_5755



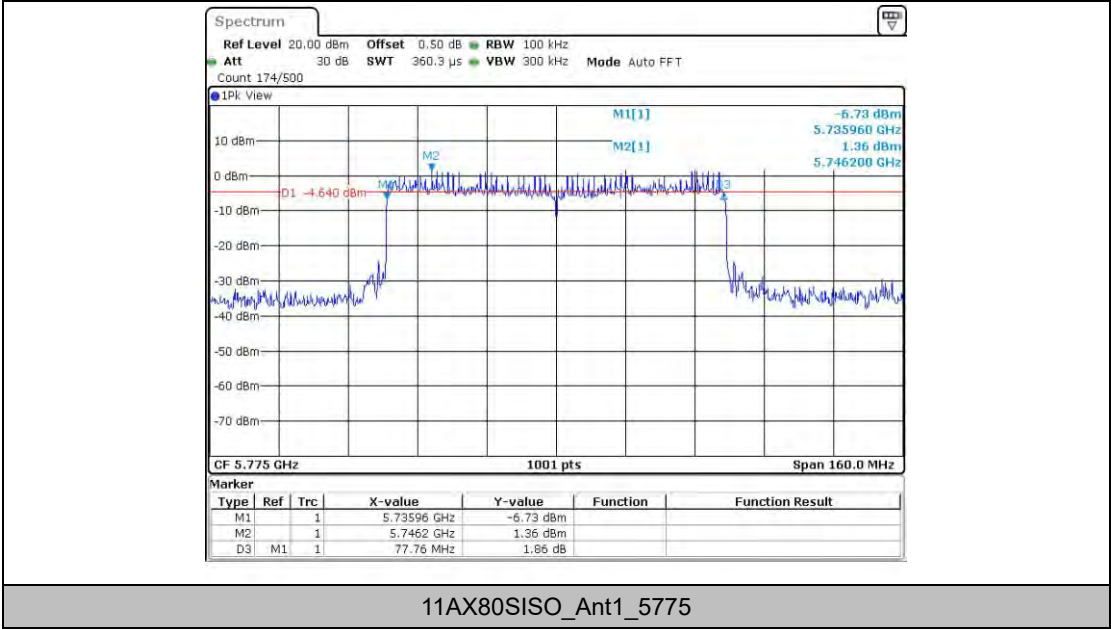
11AX40SISO_Ant0_5795



11AX40SISO_Ant1_5795



11AX80SISO_Ant0_5775



Appendix B: Maximum average conducted output power

Test Result

TestMode	Antenna	Channel	Result	Limit	Verdict
11A	Ant0	5180	15.49	<=23.98	PASS
	Ant1	5180	16.53	<=23.98	PASS
	Ant0	5200	15.38	<=23.98	PASS
	Ant1	5200	16.28	<=23.98	PASS
	Ant0	5240	14.86	<=23.98	PASS
	Ant1	5240	15.73	<=23.98	PASS
	Ant0	5260	16.02	<=23.65	PASS
	Ant1	5260	16.33	<=23.65	PASS
	Ant0	5280	15.98	<=23.64	PASS
	Ant1	5280	16.18	<=23.68	PASS
	Ant0	5320	15.86	<=23.68	PASS
	Ant1	5320	16.27	<=23.70	PASS
	Ant0	5500	16.27	<=23.64	PASS
	Ant1	5500	15.26	<=23.65	PASS
	Ant0	5580	16.62	<=23.64	PASS
	Ant1	5580	15.04	<=23.67	PASS
	Ant0	5700	16.07	<=23.67	PASS
	Ant1	5700	15.80	<=23.67	PASS
	Ant0	5745	17.09	<=30	PASS
	Ant1	5745	17.74	<=30	PASS
	Ant0	5785	16.23	<=30	PASS
	Ant1	5785	17.77	<=30	PASS
	Ant0	5825	16.46	<=30	PASS
	Ant1	5825	18.05	<=30	PASS
11N20SISO	Ant0	5180	16.32	<=23.98	PASS
	Ant1	5180	16.06	<=23.98	PASS
	Ant0	5200	16.26	<=23.98	PASS
	Ant1	5200	15.80	<=23.98	PASS
	Ant0	5240	15.78	<=23.98	PASS
	Ant1	5240	15.23	<=23.98	PASS
	Ant0	5260	17.00	<=23.87	PASS
	Ant1	5260	16.68	<=23.98	PASS
	Ant0	5280	16.97	<=23.88	PASS
	Ant1	5280	16.51	<=23.91	PASS
	Ant0	5320	16.82	<=23.84	PASS
	Ant1	5320	16.68	<=23.90	PASS
	Ant0	5500	16.19	<=23.89	PASS
	Ant1	5500	15.91	<=23.87	PASS
	Ant0	5580	16.62	<=23.88	PASS
	Ant1	5580	15.74	<=23.98	PASS

	Ant0	5700	16.05	<=23.88	PASS
	Ant1	5700	16.60	<=23.87	PASS
	Ant0	5745	15.82	<=30	PASS
	Ant1	5745	17.64	<=30	PASS
	Ant0	5785	16.03	<=30	PASS
	Ant1	5785	17.79	<=30	PASS
	Ant0	5825	16.45	<=30	PASS
	Ant1	5825	17.98	<=30	PASS
11N40SISO	Ant0	5190	15.76	<=23.98	PASS
	Ant1	5190	16.52	<=23.98	PASS
	Ant0	5230	16.16	<=23.98	PASS
	Ant1	5230	15.89	<=23.98	PASS
	Ant0	5270	15.04	<=23.98	PASS
	Ant1	5270	15.30	<=23.98	PASS
	Ant0	5310	14.71	<=23.98	PASS
	Ant1	5310	15.79	<=23.98	PASS
	Ant0	5510	16.39	<=23.98	PASS
	Ant1	5510	15.96	<=23.98	PASS
	Ant0	5550	14.90	<=23.98	PASS
	Ant1	5550	15.83	<=23.98	PASS
	Ant0	5670	14.91	<=23.98	PASS
	Ant1	5670	16.51	<=23.98	PASS
	Ant0	5755	15.78	<=30	PASS
	Ant1	5755	18.13	<=30	PASS
	Ant0	5795	16.13	<=30	PASS
	Ant1	5795	18.37	<=30	PASS
11AC20SISO	Ant0	5180	16.19	<=23.98	PASS
	Ant1	5180	16.16	<=23.98	PASS
	Ant0	5200	16.17	<=23.98	PASS
	Ant1	5200	15.98	<=23.98	PASS
	Ant0	5240	15.75	<=23.98	PASS
	Ant1	5240	15.38	<=23.98	PASS
	Ant0	5260	15.83	<=23.86	PASS
	Ant1	5260	16.00	<=23.98	PASS
	Ant0	5280	15.84	<=23.89	PASS
	Ant1	5280	15.82	<=23.98	PASS
	Ant0	5320	14.61	<=23.98	PASS
	Ant1	5320	15.98	<=23.98	PASS
	Ant0	5500	16.25	<=23.87	PASS
	Ant1	5500	16.21	<=23.98	PASS
	Ant0	5580	16.73	<=23.87	PASS
	Ant1	5580	16.16	<=23.98	PASS
	Ant0	5700	16.22	<=23.87	PASS
	Ant1	5700	16.97	<=23.98	PASS
	Ant0	5745	15.54	<=30	PASS

	Ant1	5745	18.31	<=30	PASS
	Ant0	5785	15.99	<=30	PASS
	Ant1	5785	18.41	<=30	PASS
	Ant0	5825	16.39	<=30	PASS
	Ant1	5825	18.46	<=30	PASS
11AC40SISO	Ant0	5190	16.50	<=23.98	PASS
	Ant1	5190	17.12	<=23.98	PASS
	Ant0	5230	16.06	<=23.98	PASS
	Ant1	5230	16.55	<=23.98	PASS
	Ant0	5270	17.20	<=23.98	PASS
	Ant1	5270	15.91	<=23.98	PASS
	Ant0	5310	17.09	<=23.98	PASS
	Ant1	5310	16.00	<=23.98	PASS
	Ant0	5510	16.46	<=23.98	PASS
	Ant1	5510	16.38	<=23.98	PASS
	Ant0	5550	16.57	<=23.98	PASS
	Ant1	5550	16.18	<=23.98	PASS
	Ant0	5670	16.67	<=23.98	PASS
	Ant1	5670	16.74	<=23.98	PASS
	Ant0	5755	16.15	<=30	PASS
	Ant1	5755	17.49	<=30	PASS
	Ant0	5795	16.39	<=30	PASS
	Ant1	5795	17.51	<=30	PASS
11AC80SISO	Ant0	5210	16.06	<=23.98	PASS
	Ant1	5210	16.71	<=23.98	PASS
	Ant0	5290	15.74	<=23.98	PASS
	Ant1	5290	16.00	<=23.98	PASS
	Ant0	5530	16.22	<=23.98	PASS
	Ant1	5530	16.14	<=23.98	PASS
	Ant0	5610	16.59	<=23.98	PASS
	Ant1	5610	16.22	<=23.98	PASS
	Ant0	5775	17.32	<=30	PASS
	Ant1	5775	17.33	<=30	PASS
11AX20SISO	Ant0	5180	13.67	<=23.98	PASS
	Ant1	5180	14.33	<=23.98	PASS
	Ant0	5200	13.58	<=23.98	PASS
	Ant1	5200	14.10	<=23.98	PASS
	Ant0	5240	12.97	<=23.98	PASS
	Ant1	5240	13.51	<=23.98	PASS
	Ant0	5260	12.96	<=23.98	PASS
	Ant1	5260	13.78	<=23.98	PASS
	Ant0	5280	12.77	<=23.98	PASS
	Ant1	5280	13.67	<=23.98	PASS
	Ant0	5320	12.47	<=23.98	PASS
	Ant1	5320	13.85	<=23.98	PASS

	Ant0	5500	12.13	<=23.98	PASS
	Ant1	5500	13.10	<=23.98	PASS
	Ant0	5580	12.35	<=23.98	PASS
	Ant1	5580	12.92	<=23.98	PASS
	Ant0	5700	12.08	<=23.98	PASS
	Ant1	5700	13.66	<=23.98	PASS
	Ant0	5745	14.60	<=30	PASS
	Ant1	5745	16.06	<=30	PASS
	Ant0	5785	15.05	<=30	PASS
	Ant1	5785	16.31	<=30	PASS
	Ant0	5825	15.36	<=30	PASS
	Ant1	5825	16.29	<=30	PASS
11AX40SISO	Ant0	5190	15.65	<=23.98	PASS
	Ant1	5190	16.13	<=23.98	PASS
	Ant0	5230	15.28	<=23.98	PASS
	Ant1	5230	15.56	<=23.98	PASS
	Ant0	5270	15.27	<=23.98	PASS
	Ant1	5270	14.98	<=23.98	PASS
	Ant0	5310	15.24	<=23.98	PASS
	Ant1	5310	15.17	<=23.98	PASS
	Ant0	5510	15.65	<=23.98	PASS
	Ant1	5510	15.29	<=23.98	PASS
	Ant0	5550	15.67	<=23.98	PASS
	Ant1	5550	15.07	<=23.98	PASS
	Ant0	5670	15.92	<=23.98	PASS
	Ant1	5670	15.73	<=23.98	PASS
	Ant0	5755	16.69	<=30	PASS
	Ant1	5755	17.52	<=30	PASS
	Ant0	5795	16.91	<=30	PASS
	Ant1	5795	17.66	<=30	PASS
11AX80SISO	Ant0	5210	16.69	<=23.98	PASS
	Ant1	5210	16.99	<=23.98	PASS
	Ant0	5290	16.55	<=23.98	PASS
	Ant1	5290	16.27	<=23.98	PASS
	Ant0	5530	16.88	<=23.98	PASS
	Ant1	5530	16.36	<=23.98	PASS
	Ant0	5610	17.22	<=23.98	PASS
	Ant1	5610	16.46	<=23.98	PASS
	Ant0	5775	17.92	<=30	PASS
	Ant1	5775	18.70	<=30	PASS
11N20MIMO	Ant0	5180	12.33	<=23.98	PASS
	Ant1	5180	12.67	<=23.98	PASS
	total	5180	15.5	<=23.98	PASS
	Ant0	5200	12.17	<=23.98	PASS
	Ant1	5200	12.47	<=23.98	PASS

	total	5200	15.3	<=23.98	PASS
	Ant0	5240	11.74	<=23.98	PASS
	Ant1	5240	11.90	<=23.98	PASS
	total	5240	14.8	<=23.98	PASS
	Ant0	5260	11.75	<=23.98	PASS
	Ant1	5260	11.44	<=23.98	PASS
	total	5260	14.6	<=23.98	PASS
	Ant0	5280	11.68	<=23.98	PASS
	Ant1	5280	11.36	<=23.98	PASS
	total	5280	14.5	<=23.98	PASS
	Ant0	5320	11.52	<=23.98	PASS
	Ant1	5320	11.48	<=23.98	PASS
	total	5320	14.5	<=23.98	PASS
	Ant0	5500	11.50	<=23.98	PASS
	Ant1	5500	11.70	<=23.98	PASS
	total	5500	14.6	<=23.98	PASS
	Ant0	5580	11.83	<=23.98	PASS
	Ant1	5580	11.57	<=23.98	PASS
	total	5580	14.7	<=23.98	PASS
	Ant0	5700	11.21	<=23.98	PASS
	Ant1	5700	12.34	<=23.98	PASS
	total	5700	14.8	<=23.98	PASS
	Ant0	5745	11.13	<=30	PASS
	Ant1	5745	12.56	<=30	PASS
	total	5745	14.9	<=30	PASS
	Ant0	5785	11.34	<=30	PASS
	Ant1	5785	12.67	<=30	PASS
	total	5785	15.1	<=30	PASS
	Ant0	5825	11.44	<=30	PASS
	Ant1	5825	12.82	<=30	PASS
	total	5825	15.2	<=30	PASS
11N40MIMO	Ant0	5190	12.16	<=23.98	PASS
	Ant1	5190	12.87	<=23.98	PASS
	total	5190	15.5	<=23.98	PASS
	Ant0	5230	11.72	<=23.98	PASS
	Ant1	5230	12.35	<=23.98	PASS
	total	5230	15.1	<=23.98	PASS
	Ant0	5270	11.64	<=23.98	PASS
	Ant1	5270	11.81	<=23.98	PASS
	total	5270	14.7	<=23.98	PASS
	Ant0	5310	11.52	<=23.98	PASS
	Ant1	5310	11.97	<=23.98	PASS
	total	5310	14.8	<=23.98	PASS
	Ant0	5510	11.52	<=23.98	PASS
	Ant1	5510	12.04	<=23.98	PASS

	total	5510	14.8	<=23.98	PASS
	Ant0	5550	11.65	<=23.98	PASS
	Ant1	5550	11.73	<=23.98	PASS
	total	5550	14.7	<=23.98	PASS
	Ant0	5670	11.72	<=23.98	PASS
	Ant1	5670	12.29	<=23.98	PASS
	total	5670	15.0	<=23.98	PASS
	Ant0	5755	12.77	<=30	PASS
	Ant1	5755	13.86	<=30	PASS
	total	5755	16.4	<=30	PASS
	Ant0	5795	12.80	<=30	PASS
	Ant1	5795	14.40	<=30	PASS
11AC20MIMO	total	5795	16.7	<=30	PASS
	Ant0	5180	12.50	<=23.98	PASS
	Ant1	5180	14.28	<=23.98	PASS
	total	5180	16.5	<=23.98	PASS
	Ant0	5200	12.30	<=23.98	PASS
	Ant1	5200	14.05	<=23.98	PASS
	total	5200	16.3	<=23.98	PASS
	Ant0	5240	11.37	<=23.98	PASS
	Ant1	5240	13.50	<=23.98	PASS
	total	5240	15.6	<=23.98	PASS
	Ant0	5260	11.26	<=23.98	PASS
	Ant1	5260	14.18	<=23.98	PASS
	total	5260	16.0	<=23.98	PASS
	Ant0	5280	11.60	<=23.98	PASS
	Ant1	5280	13.95	<=23.98	PASS
	total	5280	15.9	<=23.98	PASS
	Ant0	5320	10.40	<=23.98	PASS
	Ant1	5320	14.14	<=23.98	PASS
	total	5320	15.7	<=23.98	PASS
	Ant0	5500	10.71	<=23.98	PASS
	Ant1	5500	13.21	<=23.98	PASS
	total	5500	15.1	<=23.98	PASS
	Ant0	5580	11.93	<=23.98	PASS
	Ant1	5580	13.09	<=23.98	PASS
	total	5580	15.6	<=23.98	PASS
	Ant0	5700	12.82	<=23.98	PASS
	Ant1	5700	13.90	<=23.98	PASS
	total	5700	16.4	<=23.98	PASS
	Ant0	5745	13.25	<=30	PASS
	Ant1	5745	14.09	<=30	PASS
	total	5745	16.7	<=30	PASS
	Ant0	5785	13.61	<=30	PASS
	Ant1	5785	14.25	<=30	PASS

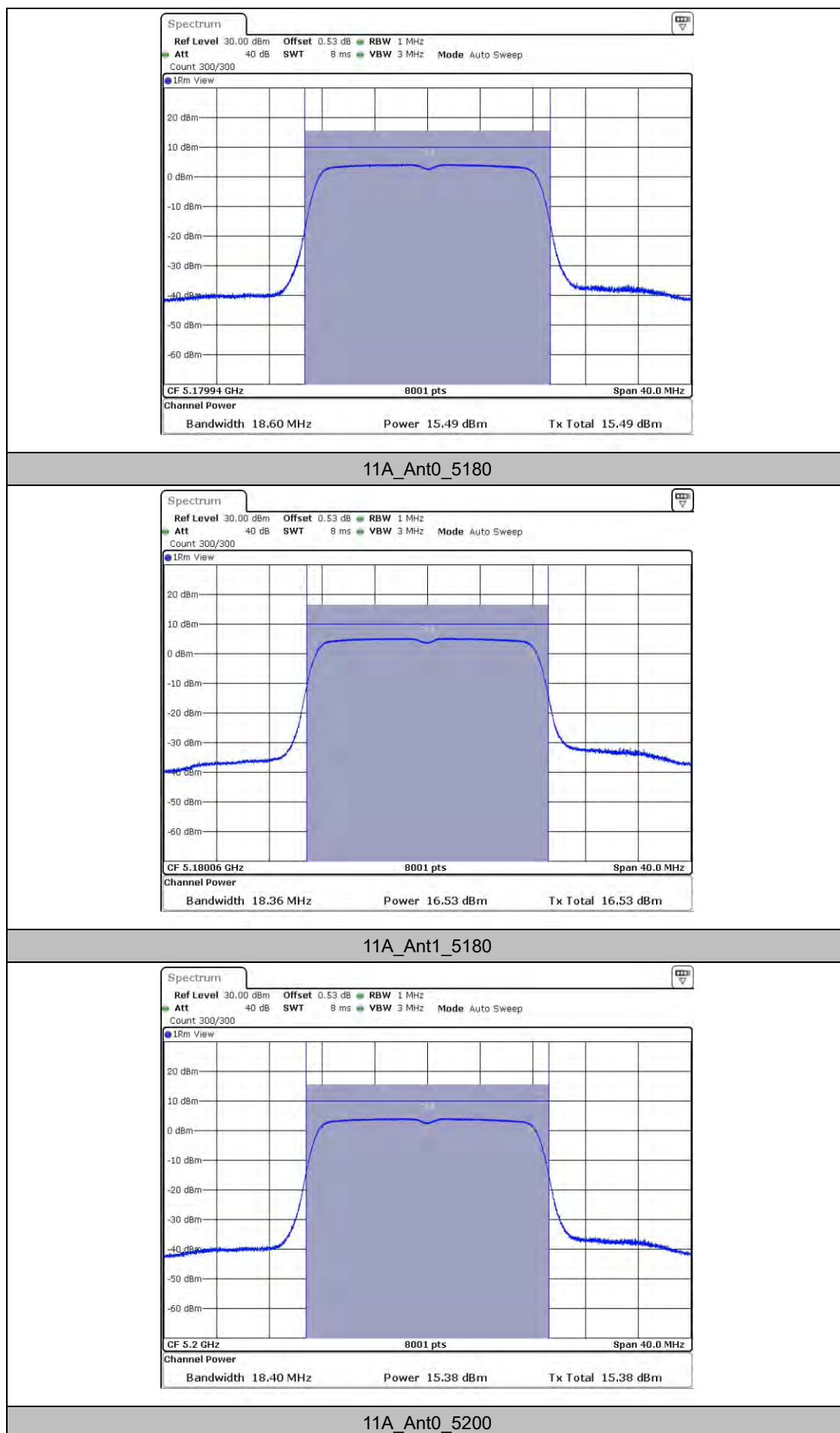
	total	5785	17.0	<=30	PASS
	Ant0	5825	13.97	<=30	PASS
	Ant1	5825	14.34	<=30	PASS
	total	5825	17.2	<=30	PASS
11AC40MIMO	Ant0	5190	12.36	<=23.98	PASS
	Ant1	5190	13.04	<=23.98	PASS
	total	5190	15.7	<=23.98	PASS
	Ant0	5230	11.75	<=23.98	PASS
	Ant1	5230	13.52	<=23.98	PASS
	total	5230	15.7	<=23.98	PASS
	Ant0	5270	12.58	<=23.98	PASS
	Ant1	5270	12.93	<=23.98	PASS
	total	5270	15.8	<=23.98	PASS
	Ant0	5310	12.26	<=23.98	PASS
	Ant1	5310	13.08	<=23.98	PASS
	total	5310	15.7	<=23.98	PASS
	Ant0	5510	11.59	<=23.98	PASS
	Ant1	5510	13.14	<=23.98	PASS
	total	5510	15.4	<=23.98	PASS
	Ant0	5550	11.48	<=23.98	PASS
	Ant1	5550	12.94	<=23.98	PASS
	total	5550	15.3	<=23.98	PASS
	Ant0	5670	11.33	<=23.98	PASS
	Ant1	5670	13.48	<=23.98	PASS
	total	5670	15.5	<=23.98	PASS
	Ant0	5755	12.38	<=30	PASS
	Ant1	5755	15.24	<=30	PASS
	total	5755	17.1	<=30	PASS
	Ant0	5795	12.85	<=30	PASS
	Ant1	5795	15.37	<=30	PASS
	total	5795	17.3	<=30	PASS
11AC80MIMO	Ant0	5210	11.93	<=23.98	PASS
	Ant1	5210	12.62	<=23.98	PASS
	total	5210	15.3	<=23.98	PASS
	Ant0	5290	12.45	<=23.98	PASS
	Ant1	5290	13.02	<=23.98	PASS
	total	5290	15.8	<=23.98	PASS
	Ant0	5530	11.57	<=23.98	PASS
	Ant1	5530	12.91	<=23.98	PASS
	total	5530	15.3	<=23.98	PASS
	Ant0	5610	11.43	<=23.98	PASS
	Ant1	5610	12.89	<=23.98	PASS
	total	5610	15.2	<=23.98	PASS
	Ant0	5775	12.55	<=30	PASS
	Ant1	5775	15.06	<=30	PASS

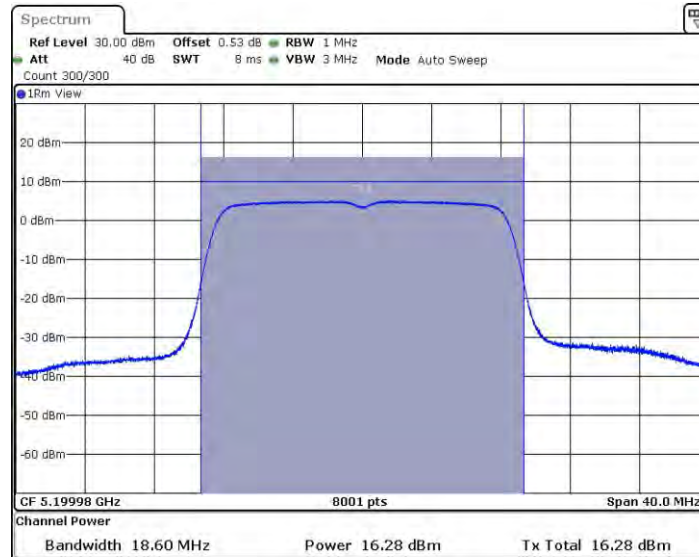
	total	5775	17.0	<=30	PASS
11AX20MIMO	Ant0	5180	11.59	<=23.98	PASS
	Ant1	5180	12.19	<=23.98	PASS
	total	5180	14.9	<=23.98	PASS
	Ant0	5200	11.48	<=23.98	PASS
	Ant1	5200	11.97	<=23.98	PASS
	total	5200	14.7	<=23.98	PASS
	Ant0	5240	11.09	<=23.98	PASS
	Ant1	5240	11.42	<=23.98	PASS
	total	5240	14.3	<=23.98	PASS
	Ant0	5260	10.93	<=23.98	PASS
	Ant1	5260	10.41	<=23.98	PASS
	total	5260	13.7	<=23.98	PASS
	Ant0	5280	10.71	<=23.98	PASS
	Ant1	5280	10.24	<=23.98	PASS
	total	5280	13.5	<=23.98	PASS
	Ant0	5320	10.59	<=23.98	PASS
	Ant1	5320	10.39	<=23.98	PASS
	total	5320	13.5	<=23.98	PASS
	Ant0	5500	10.38	<=23.98	PASS
	Ant1	5500	10.59	<=23.98	PASS
	total	5500	13.5	<=23.98	PASS
	Ant0	5580	10.54	<=23.98	PASS
	Ant1	5580	10.41	<=23.98	PASS
	total	5580	13.5	<=23.98	PASS
	Ant0	5700	9.70	<=23.98	PASS
	Ant1	5700	11.10	<=23.98	PASS
	total	5700	13.5	<=23.98	PASS
	Ant0	5745	12.91	<=30	PASS
	Ant1	5745	13.92	<=30	PASS
	total	5745	16.5	<=30	PASS
	Ant0	5785	13.04	<=30	PASS
	Ant1	5785	14.03	<=30	PASS
	total	5785	16.6	<=30	PASS
	Ant0	5825	13.12	<=30	PASS
	Ant1	5825	14.17	<=30	PASS
	total	5825	16.7	<=30	PASS
11AX40MIMO	Ant0	5190	11.53	<=23.98	PASS
	Ant1	5190	12.09	<=23.98	PASS
	total	5190	14.8	<=23.98	PASS
	Ant0	5230	11.20	<=23.98	PASS
	Ant1	5230	11.48	<=23.98	PASS
	total	5230	14.4	<=23.98	PASS
	Ant0	5270	12.21	<=23.98	PASS
	Ant1	5270	11.93	<=23.98	PASS

	total	5270	15.1	<=23.98	PASS
	Ant0	5310	12.08	<=23.98	PASS
	Ant1	5310	12.00	<=23.98	PASS
	total	5310	15.1	<=23.98	PASS
	Ant0	5510	12.12	<=23.98	PASS
	Ant1	5510	12.10	<=23.98	PASS
	total	5510	15.1	<=23.98	PASS
	Ant0	5550	12.28	<=23.98	PASS
	Ant1	5550	11.81	<=23.98	PASS
	total	5550	15.1	<=23.98	PASS
	Ant0	5670	12.30	<=23.98	PASS
	Ant1	5670	12.32	<=23.98	PASS
	total	5670	15.3	<=23.98	PASS
	Ant0	5755	13.02	<=30	PASS
	Ant1	5755	14.12	<=30	PASS
	total	5755	16.6	<=30	PASS
	Ant0	5795	13.15	<=30	PASS
	Ant1	5795	14.24	<=30	PASS
	total	5795	16.7	<=30	PASS
11AX80MIMO	Ant0	5210	11.50	<=23.98	PASS
	Ant1	5210	11.69	<=23.98	PASS
	total	5210	14.6	<=23.98	PASS
	Ant0	5290	12.22	<=23.98	PASS
	Ant1	5290	11.95	<=23.98	PASS
	total	5290	15.1	<=23.98	PASS
	Ant0	5530	12.28	<=23.98	PASS
	Ant1	5530	11.87	<=23.98	PASS
	total	5530	15.1	<=23.98	PASS
	Ant0	5610	12.52	<=23.98	PASS
	Ant1	5610	11.93	<=23.98	PASS
	total	5610	15.2	<=23.98	PASS
	Ant0	5775	13.18	<=30	PASS
	Ant1	5775	14.11	<=30	PASS
	total	5775	16.7	<=30	PASS

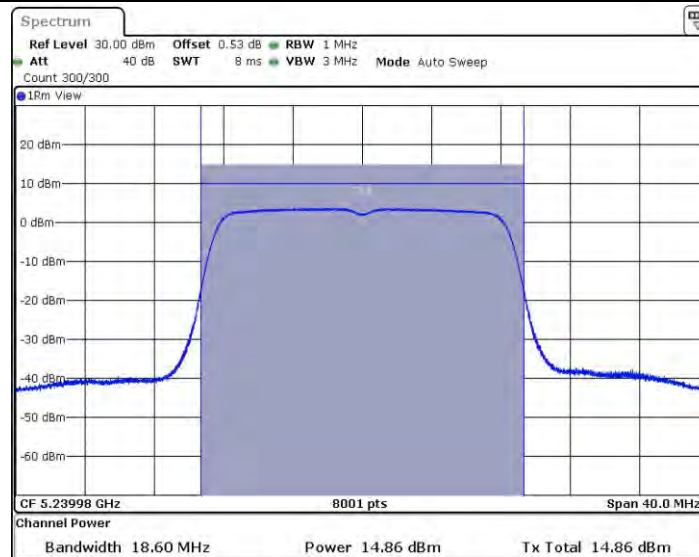
Note: The Duty Cycle Factor is compensated in the graph.

Test Graphs

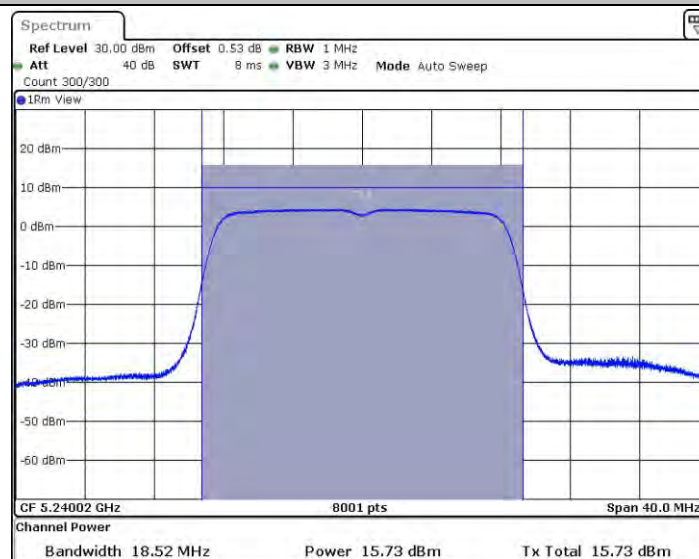




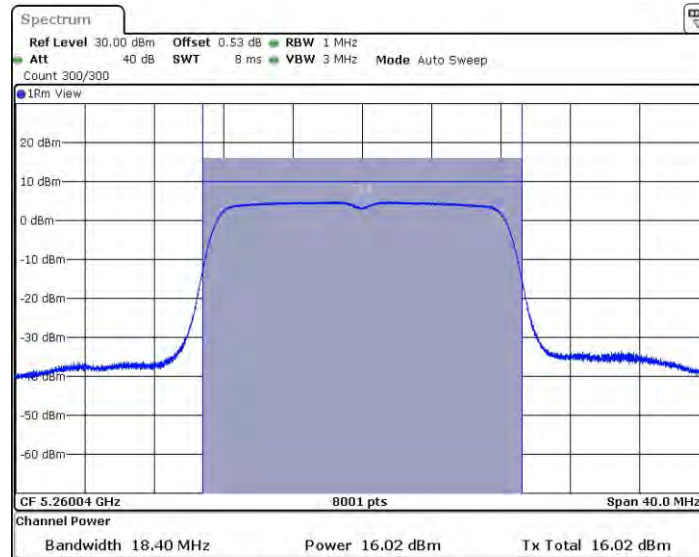
11A_Ant1_5200



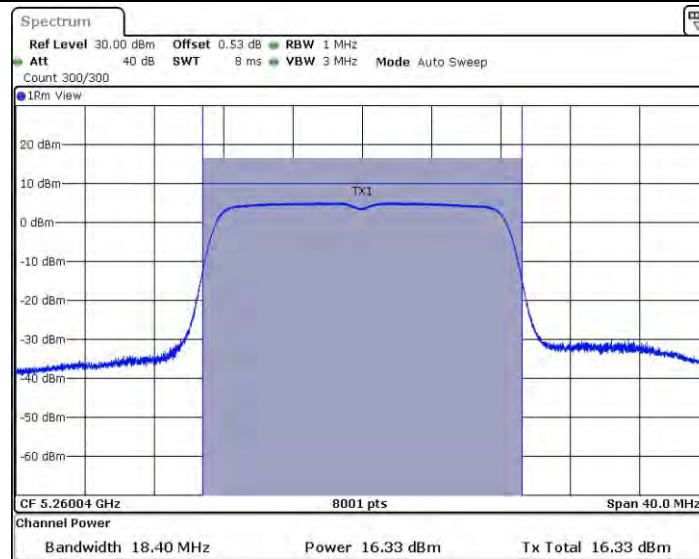
11A_Ant0_5240



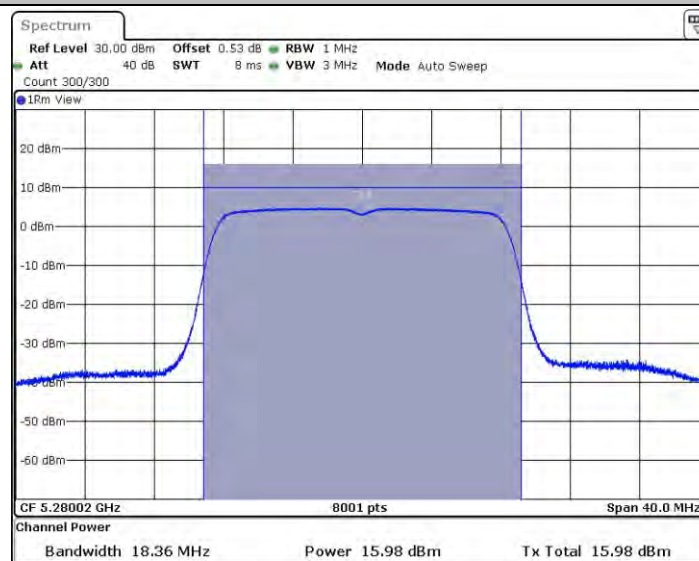
11A_Ant1_5240



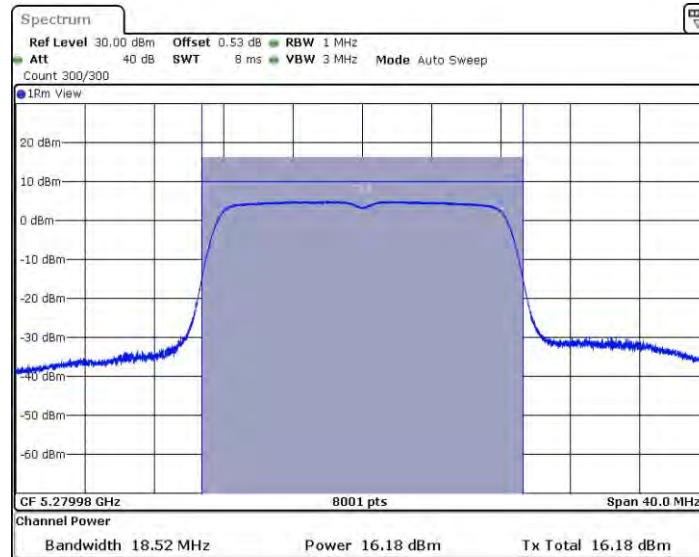
11A_Ant0_5260



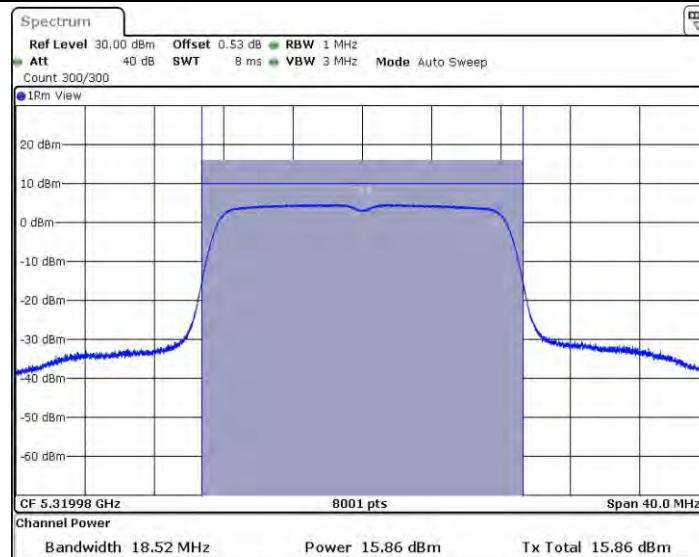
11A_Ant1_5260



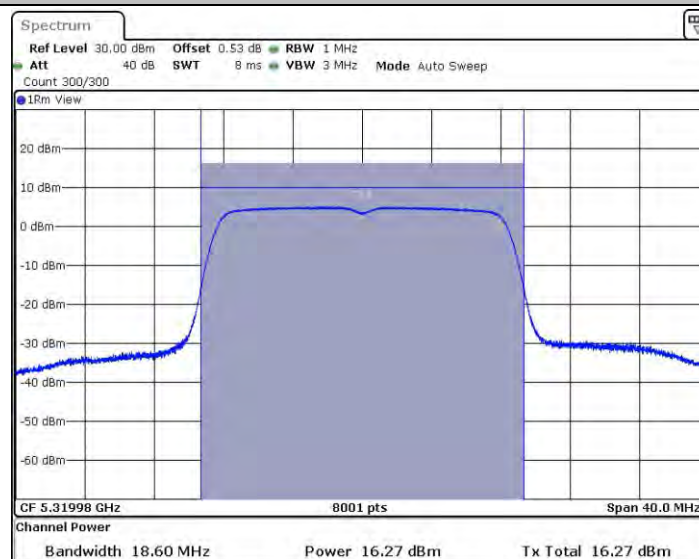
11A_Ant0_5280



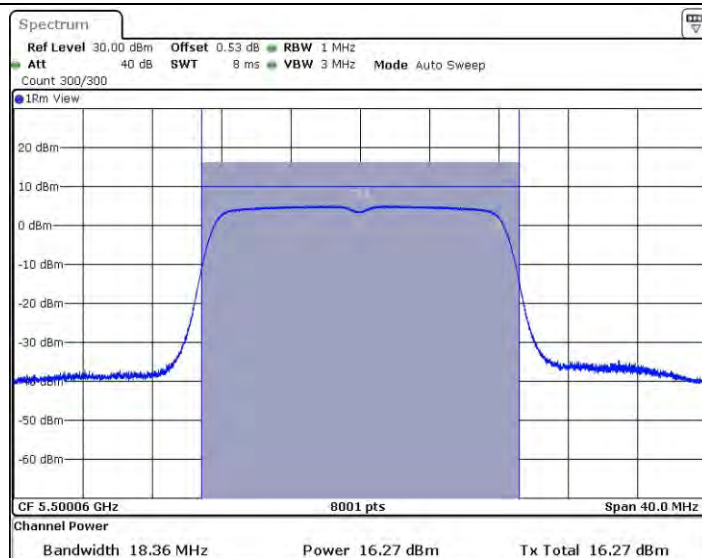
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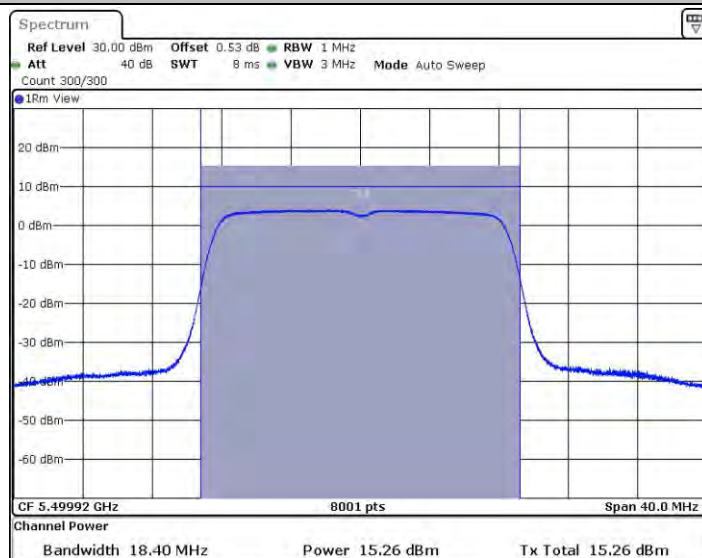
11A_Ant0_5320



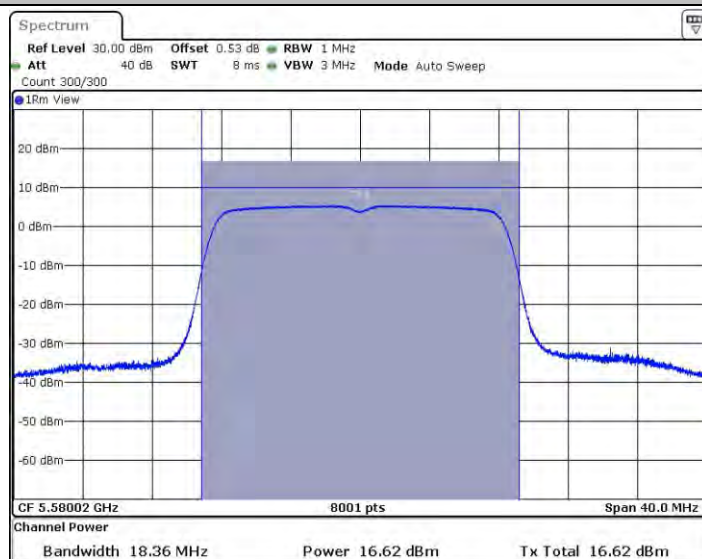
11A_Ant1_5320



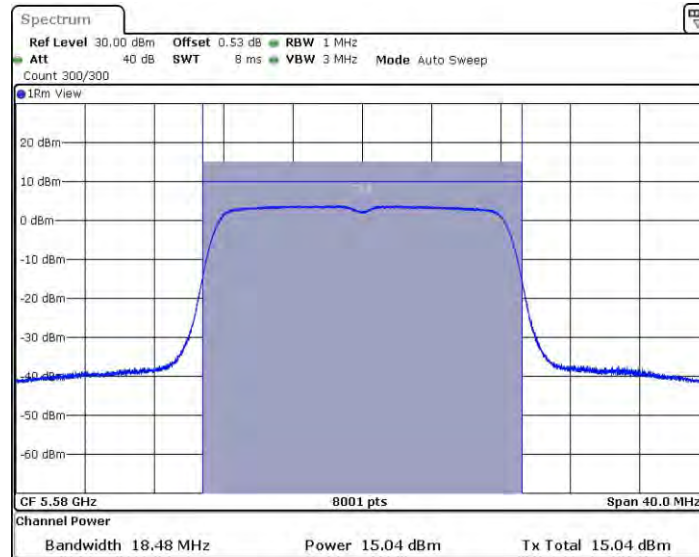
11A_Ant0_5500



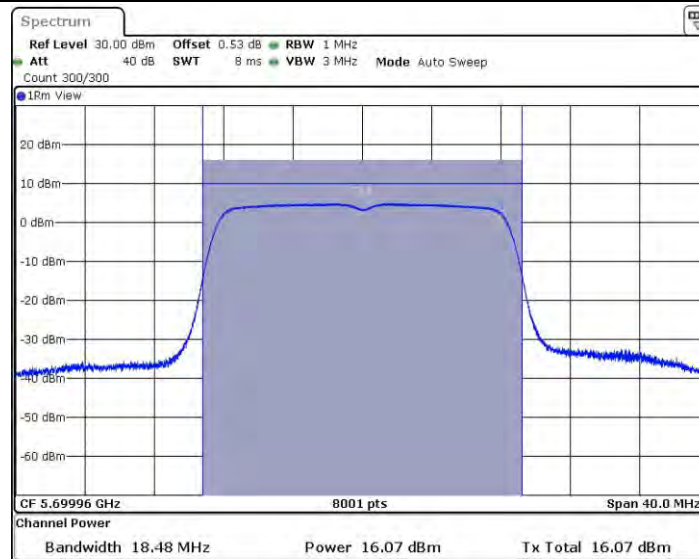
11A_Ant1_5500



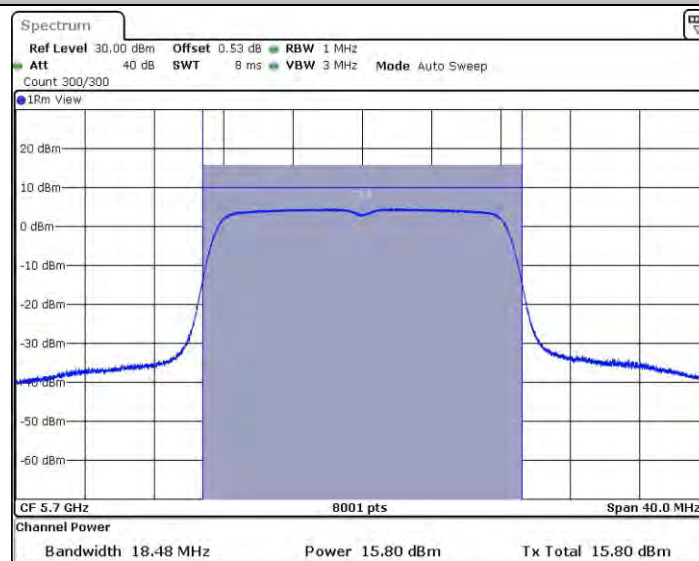
11A_Ant0_5580



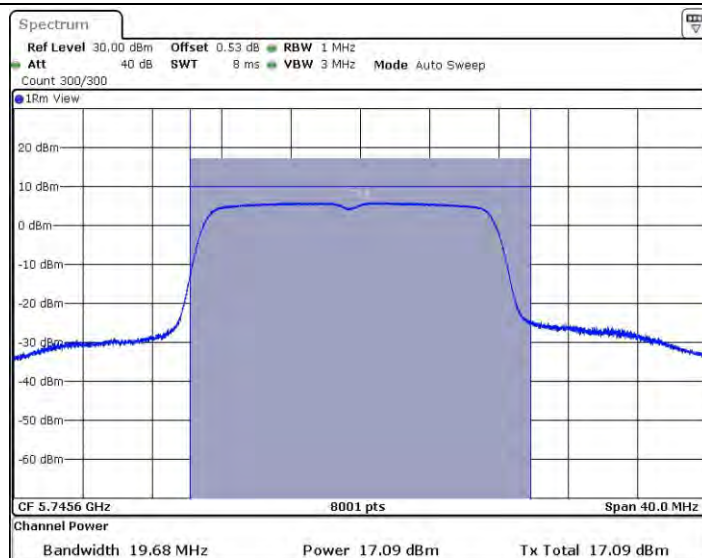
11A_Ant1_5580



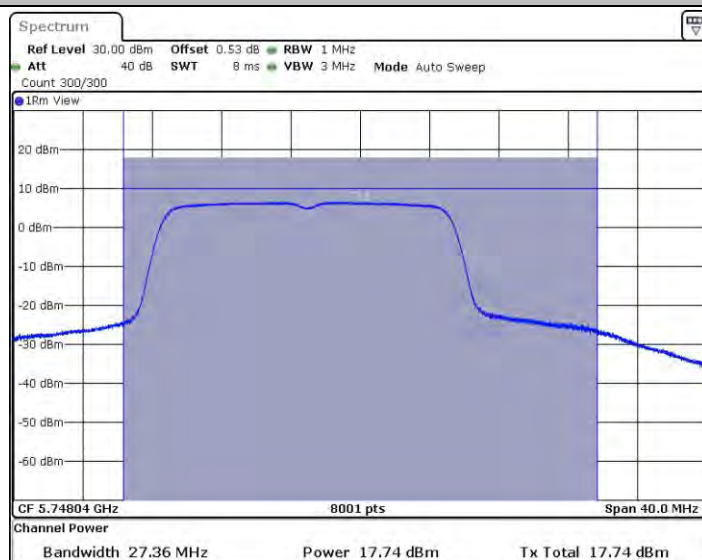
11A_Ant0_5700



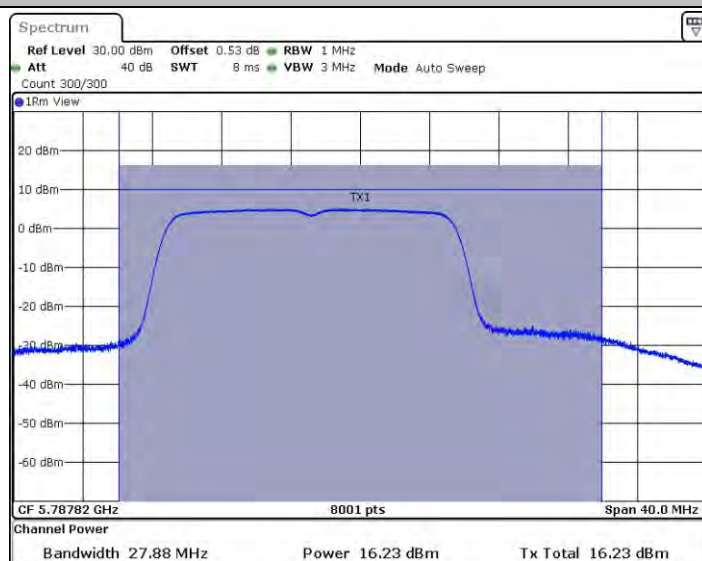
11A_Ant1_5700



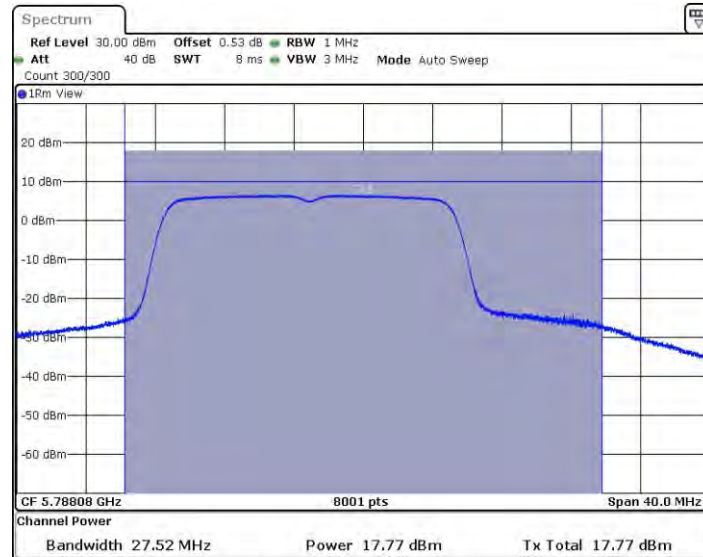
11A_Ant0_5745



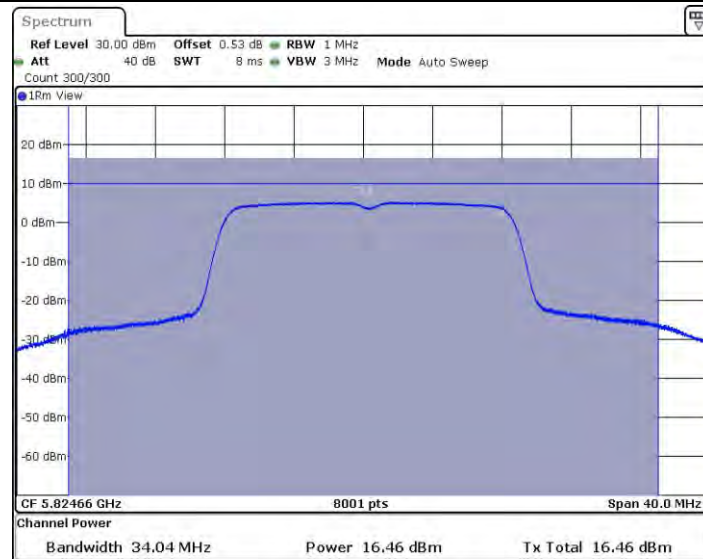
11A_Ant1_5745



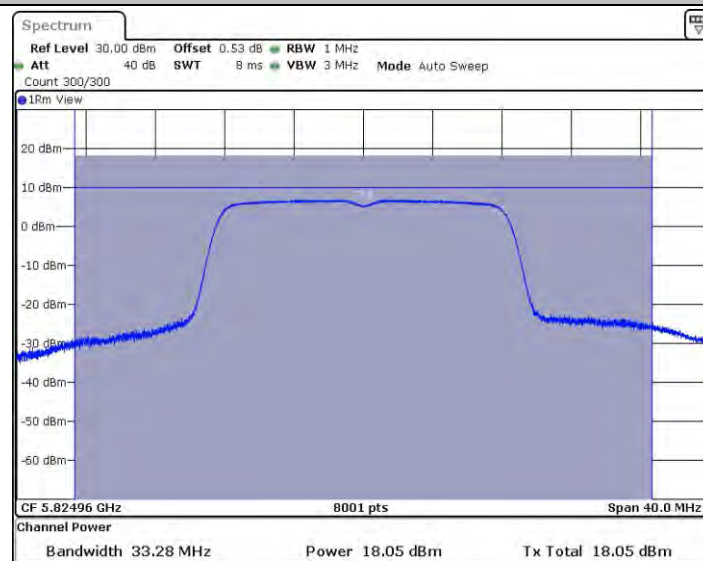
11A_Ant0_5785



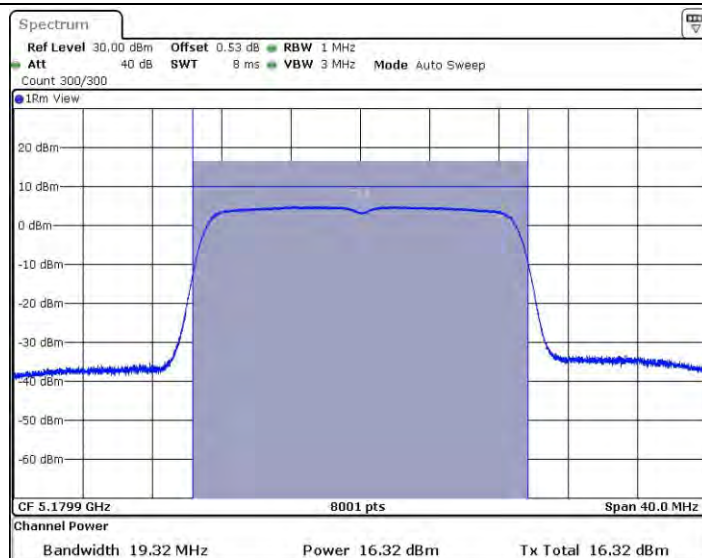
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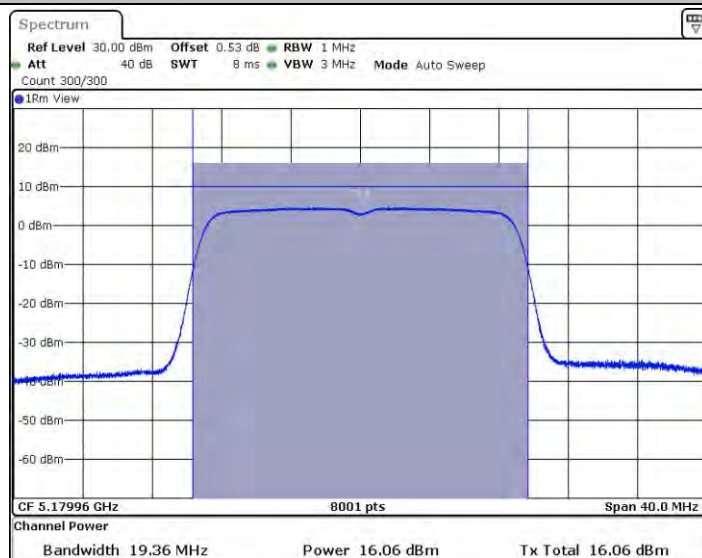
11A_Ant0_5825



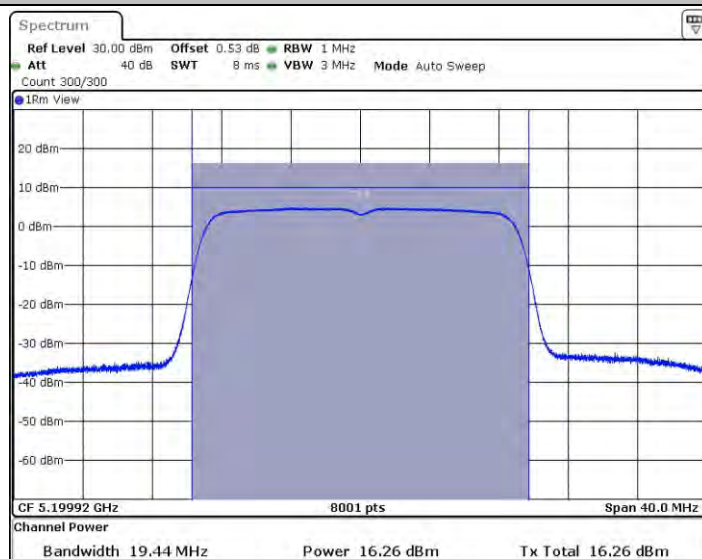
11A_Ant1_5825



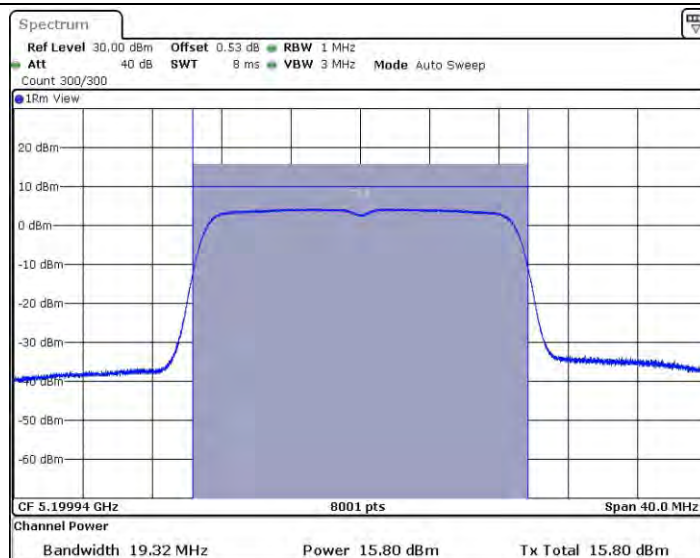
11N20SISO_Ant0_5180



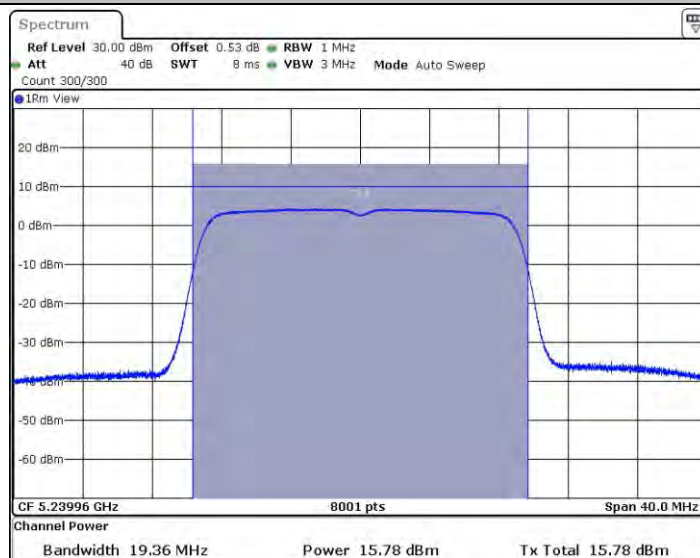
11N20SISO_Ant1_5180



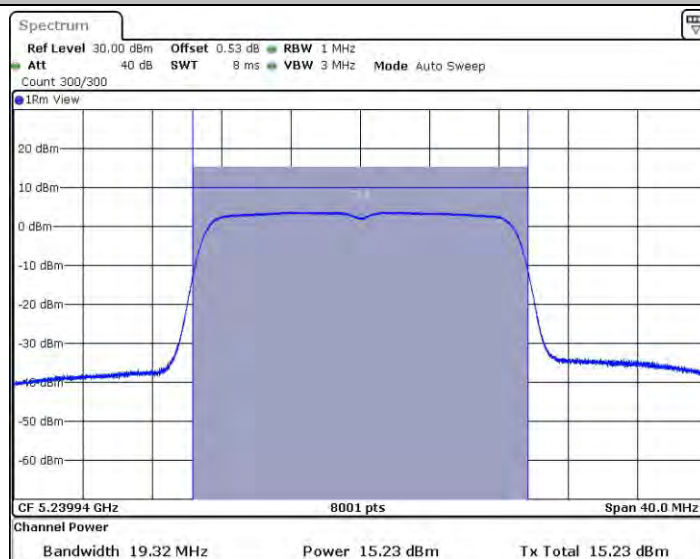
11N20SISO_Ant0_5200



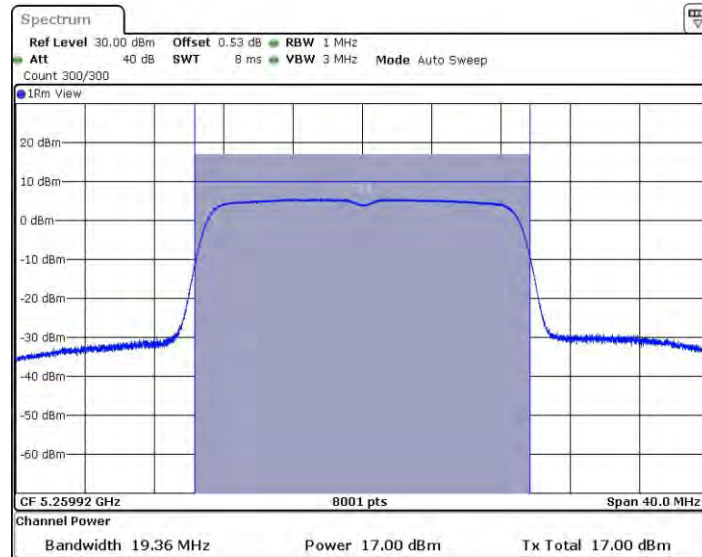
11N20SISO_Ant1_5200



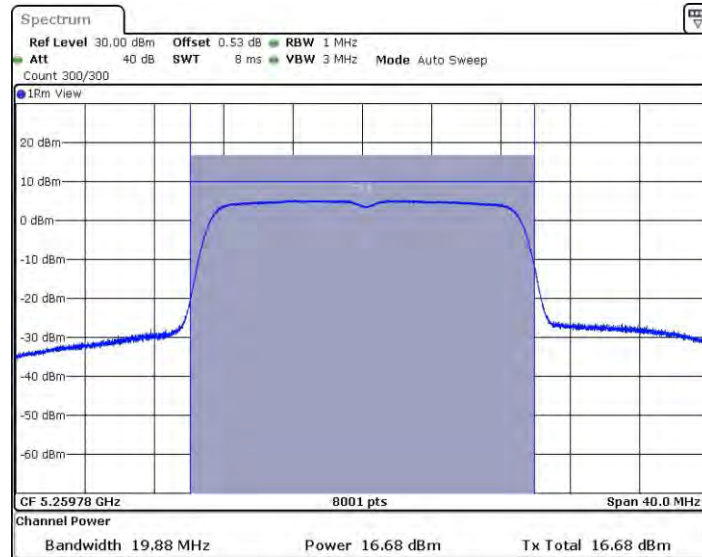
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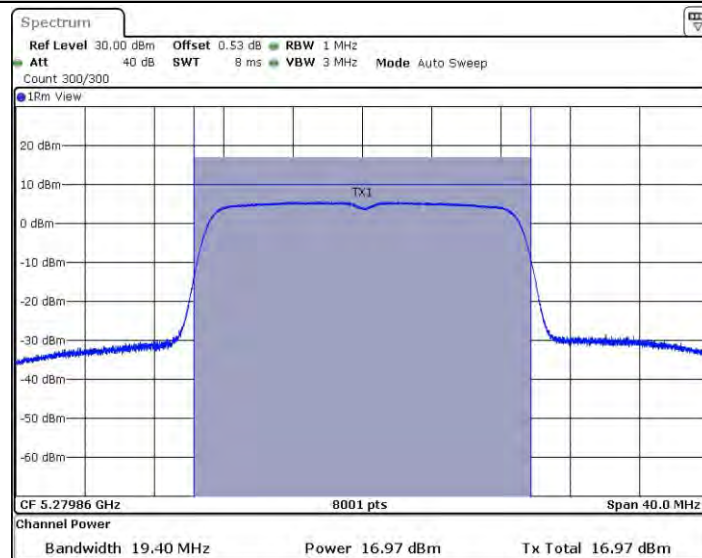
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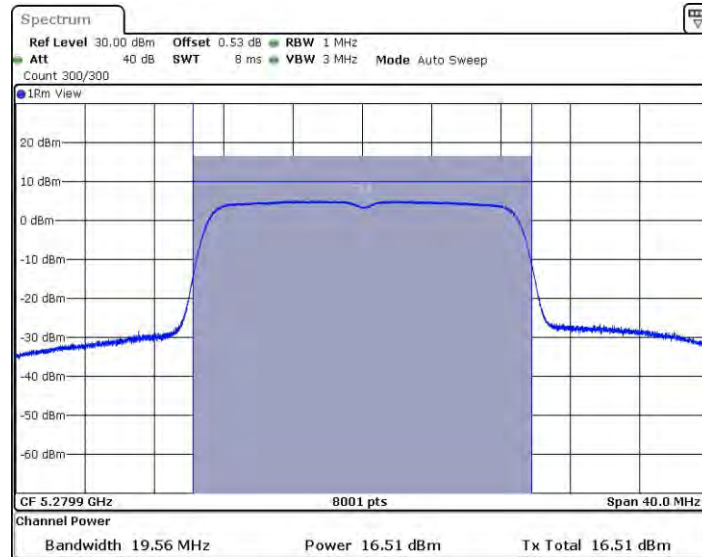
11N20SISO_Ant0_5260



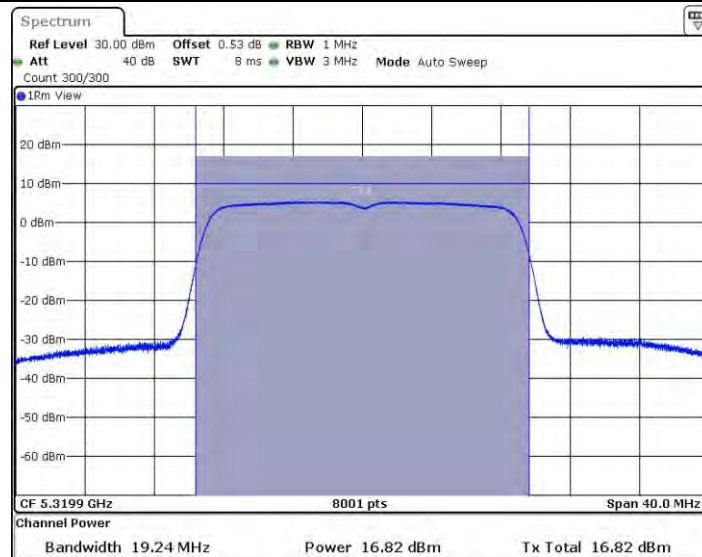
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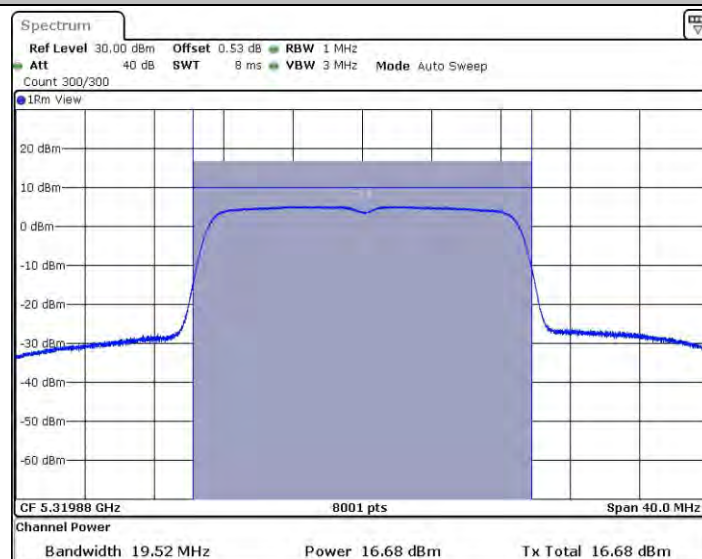
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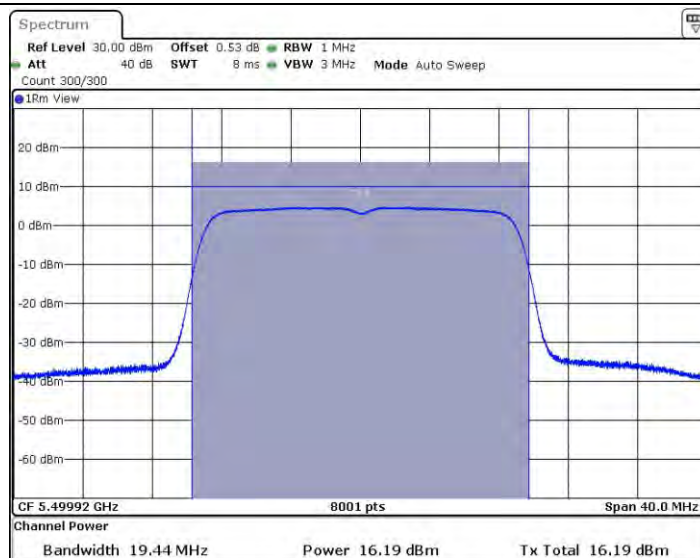
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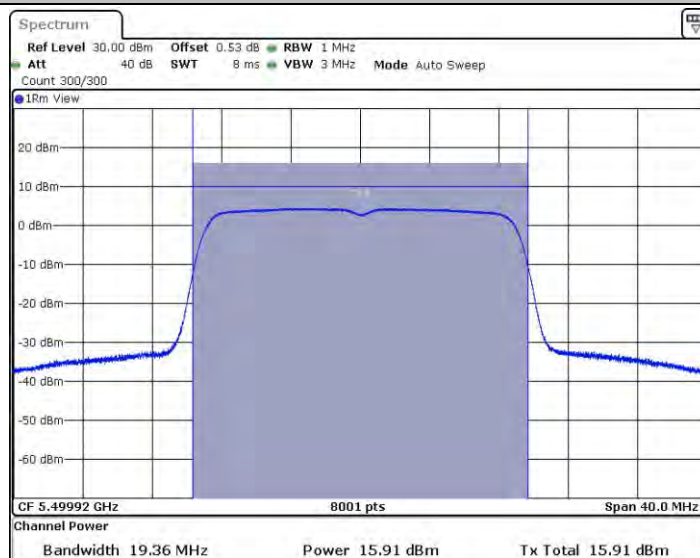
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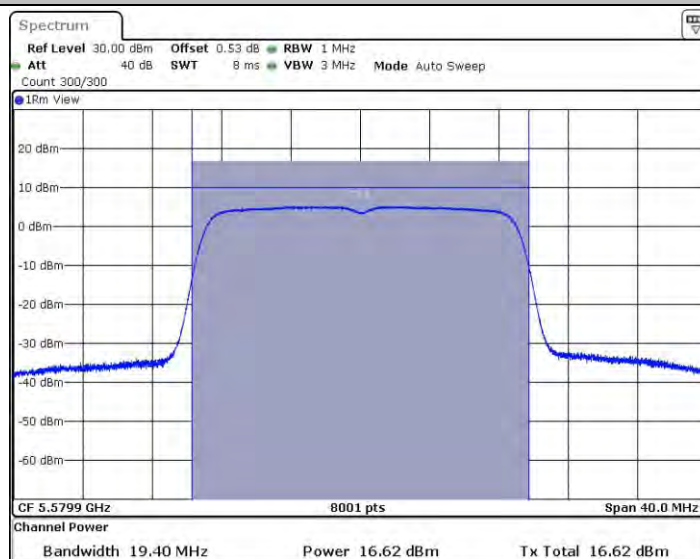
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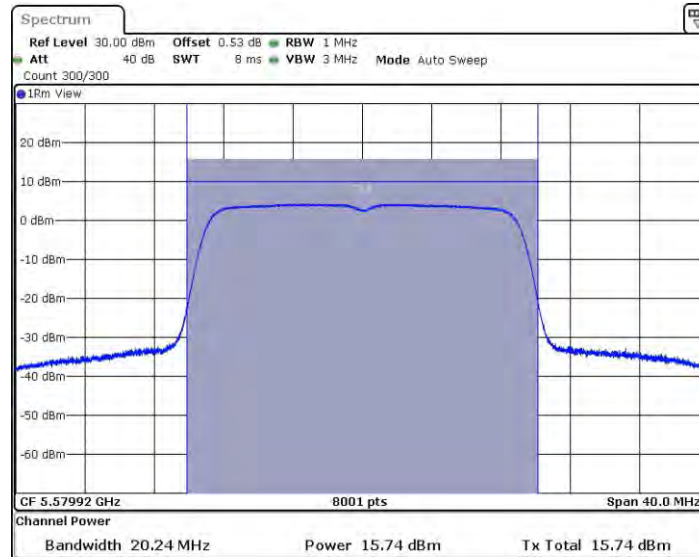
11N20SISO_Ant0_5500



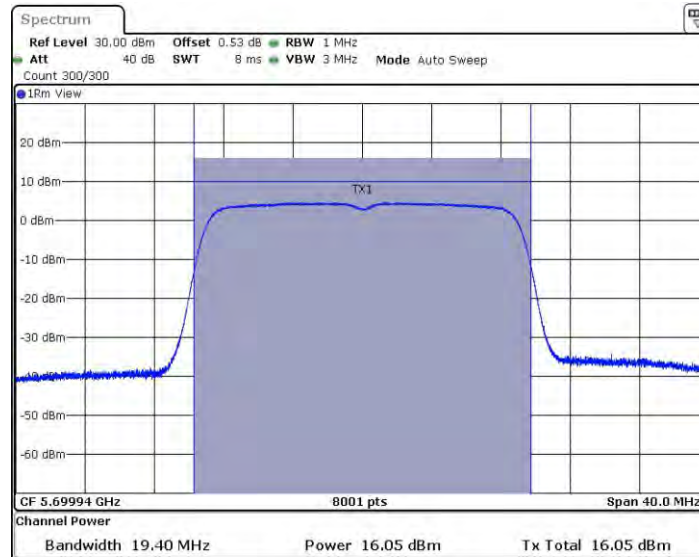
11N20SISO_Ant1_5500



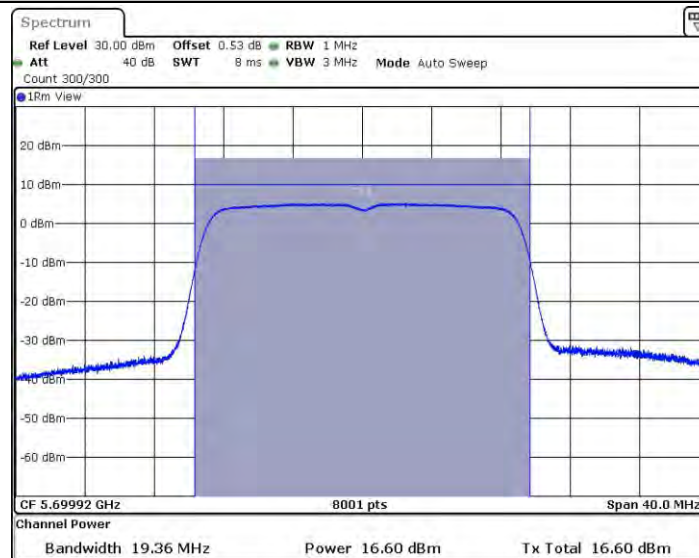
11N20SISO_Ant0_5580



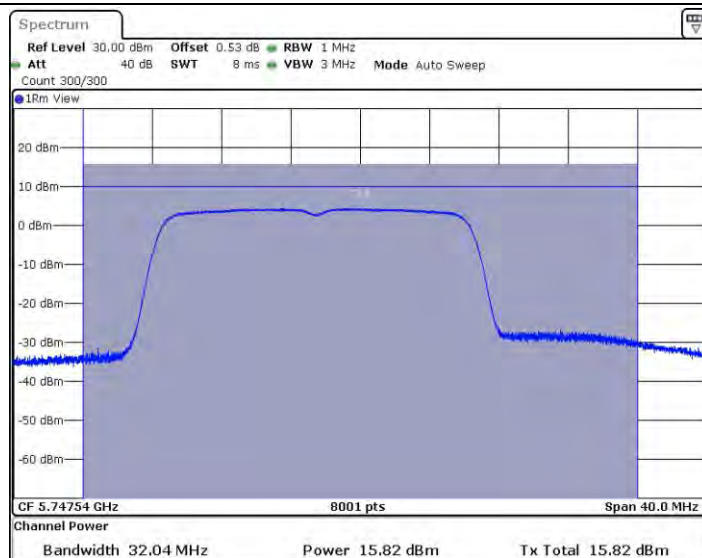
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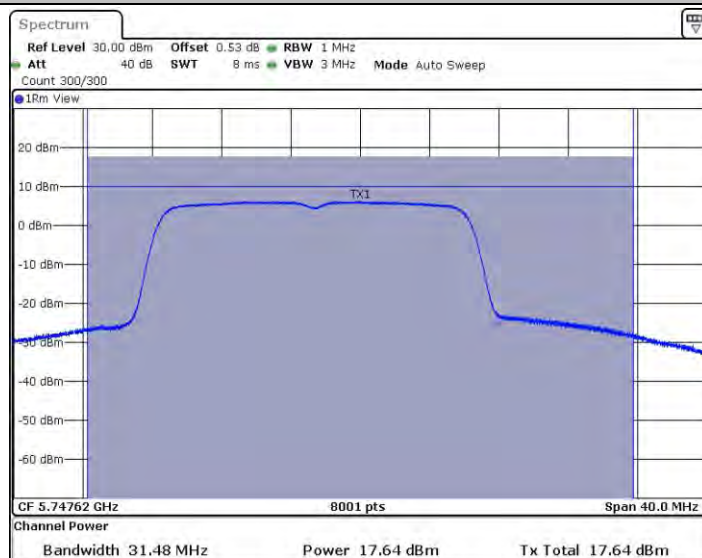
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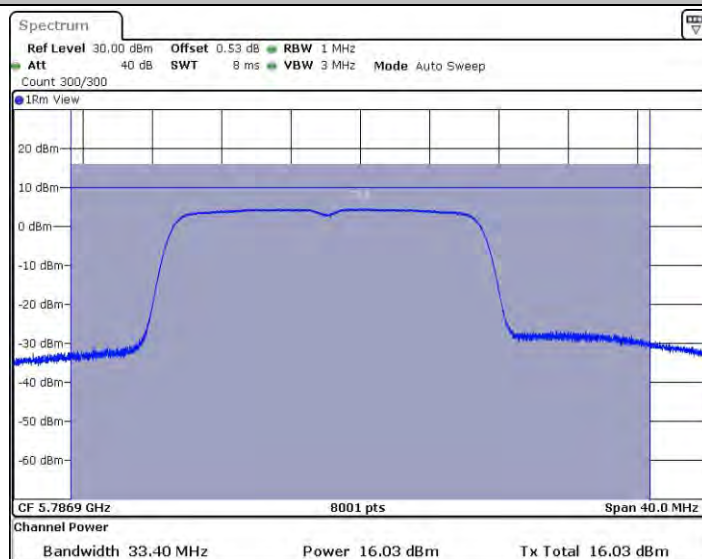
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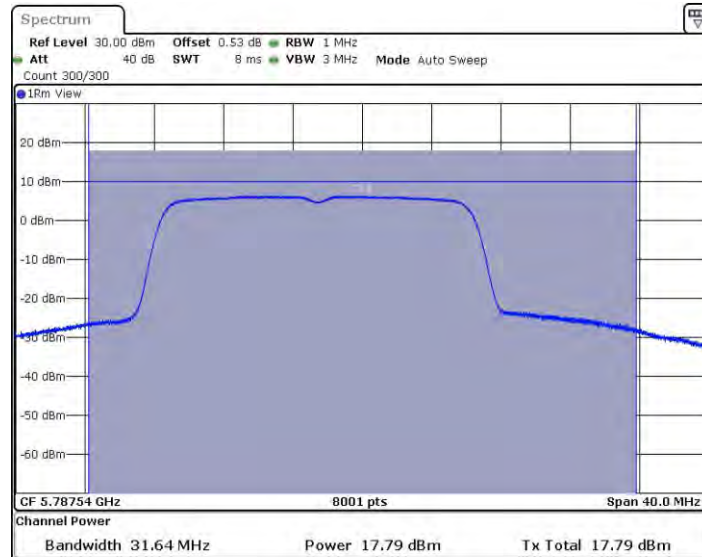
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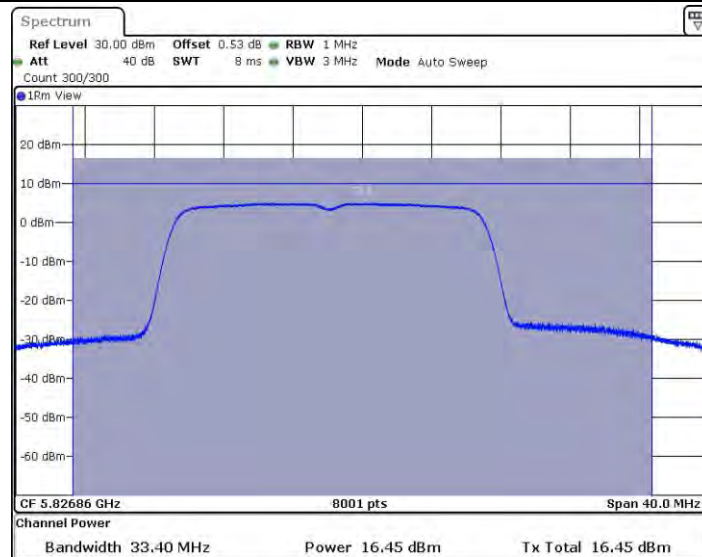
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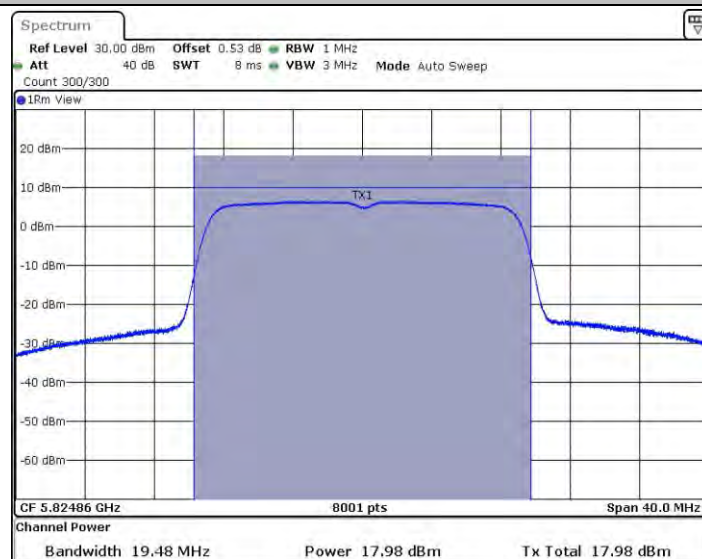
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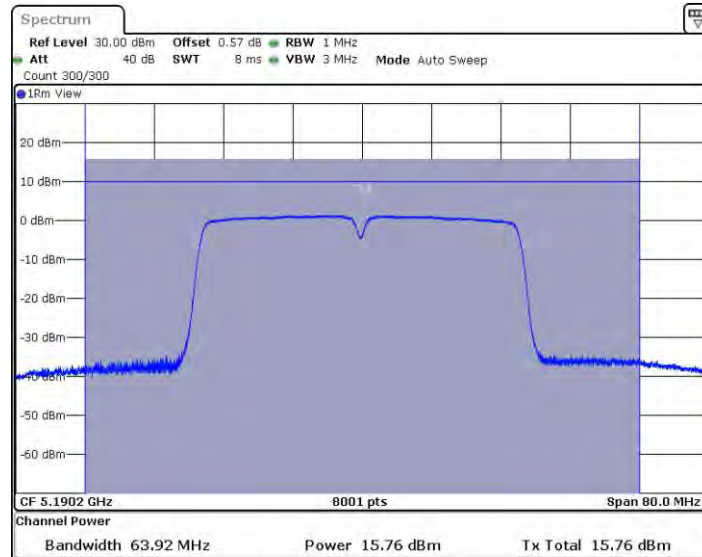
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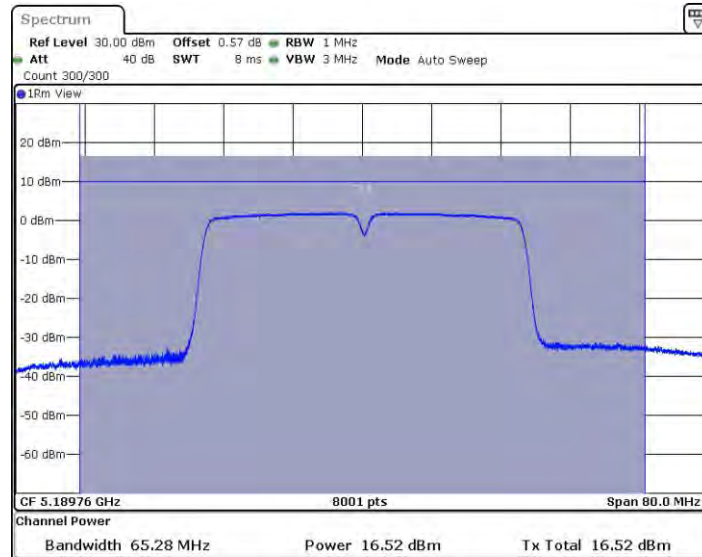
11N20SISO_Ant0_5825



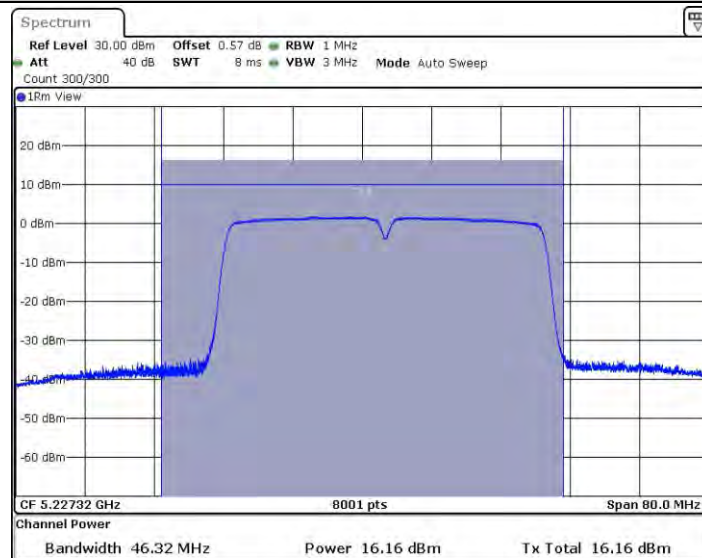
11N20SISO_Ant1_5825



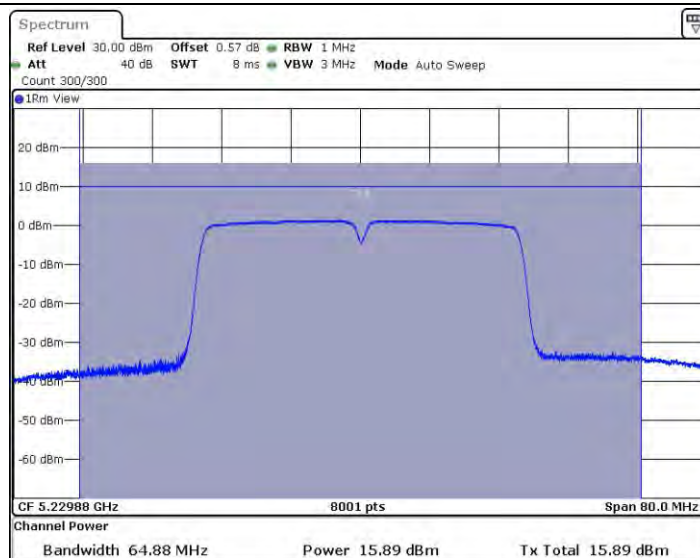
11N40SISO_Ant0_5190



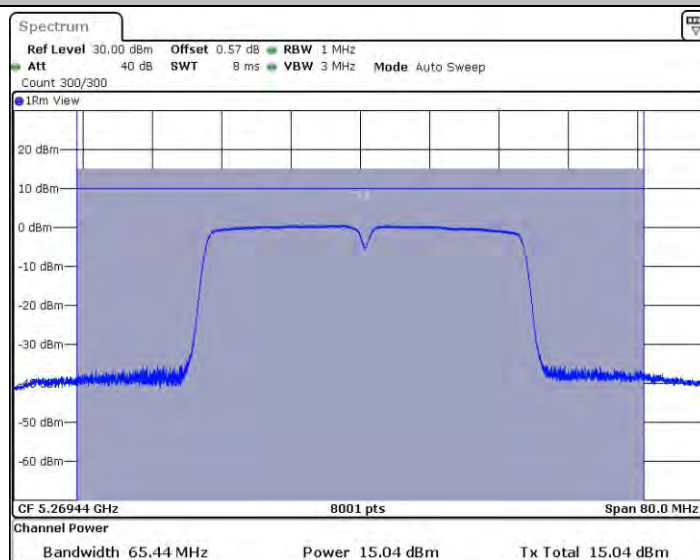
11N40SISO_Ant1_5190



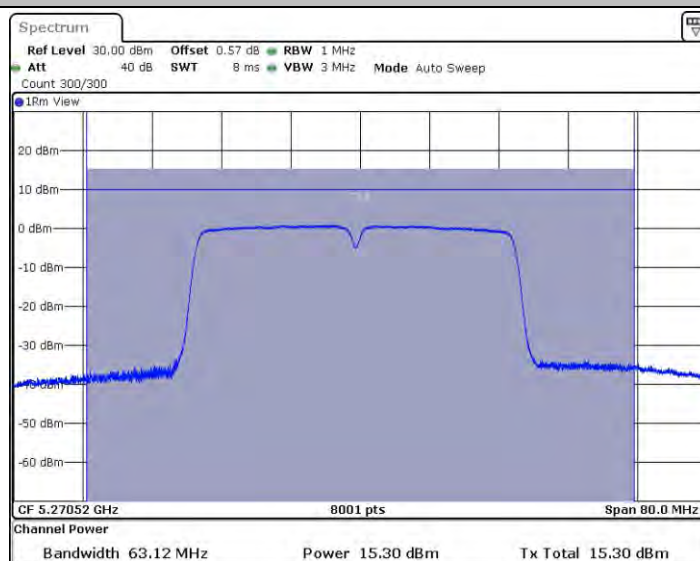
11N40SISO_Ant0_5230



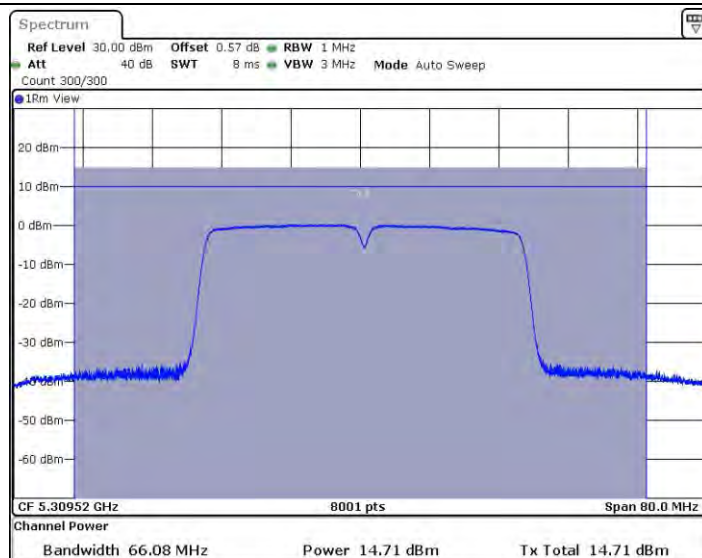
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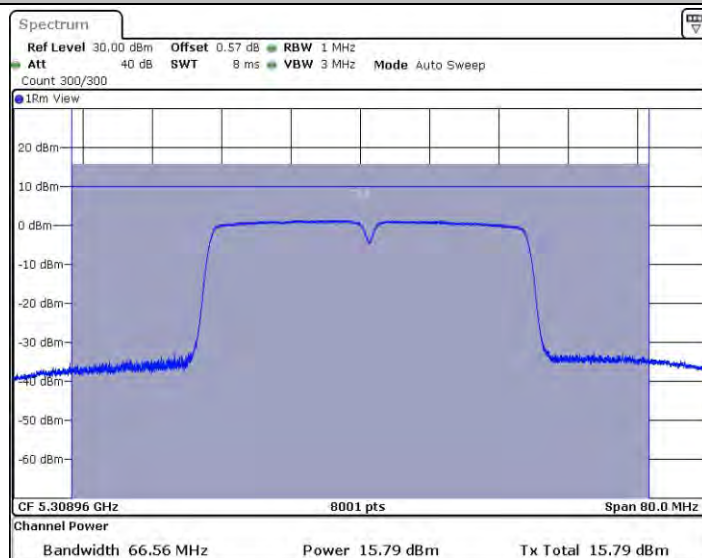
11N40SISO_Ant0_5270



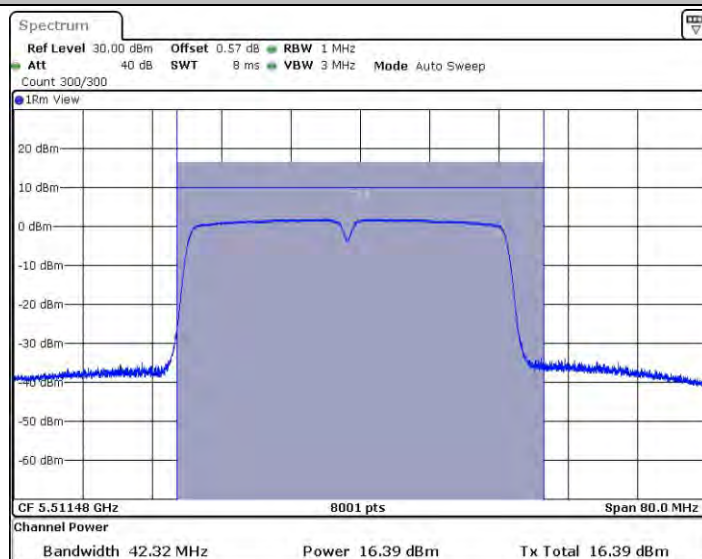
11N40SISO_Ant1_5270



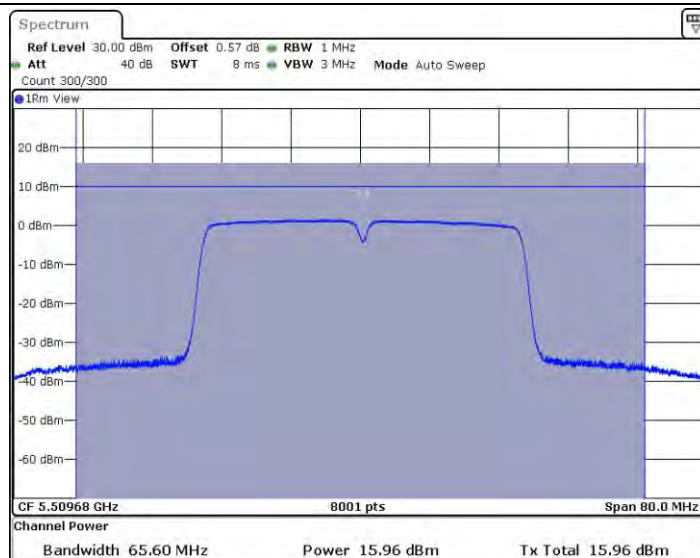
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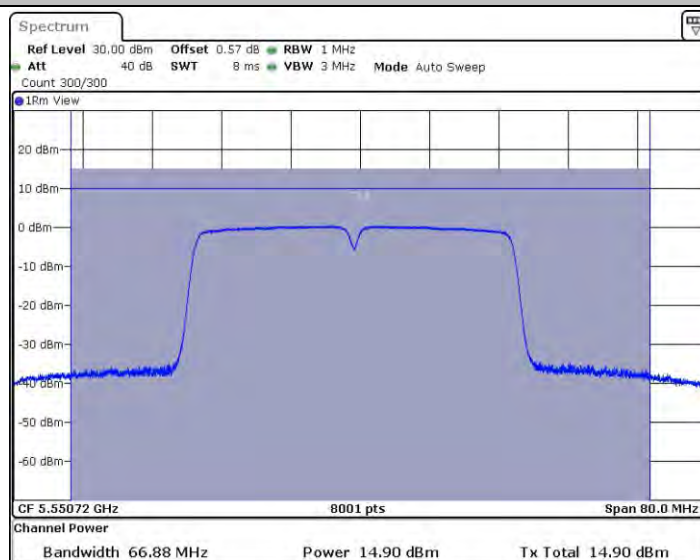
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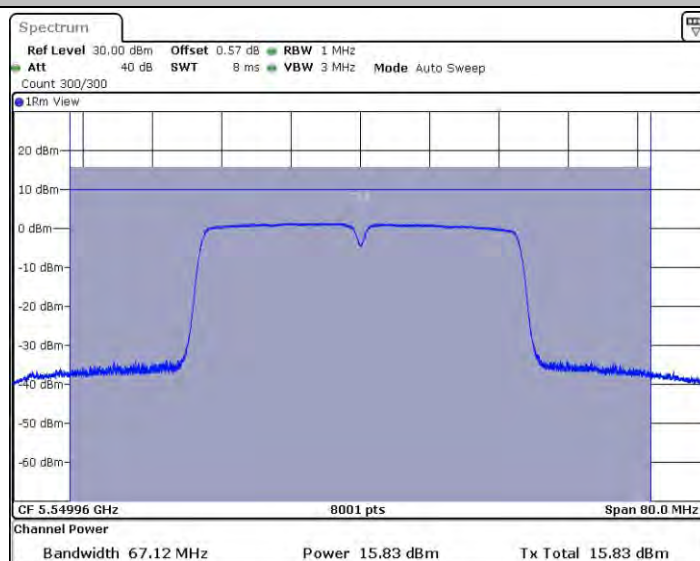
11N40SISO_Ant0_5510



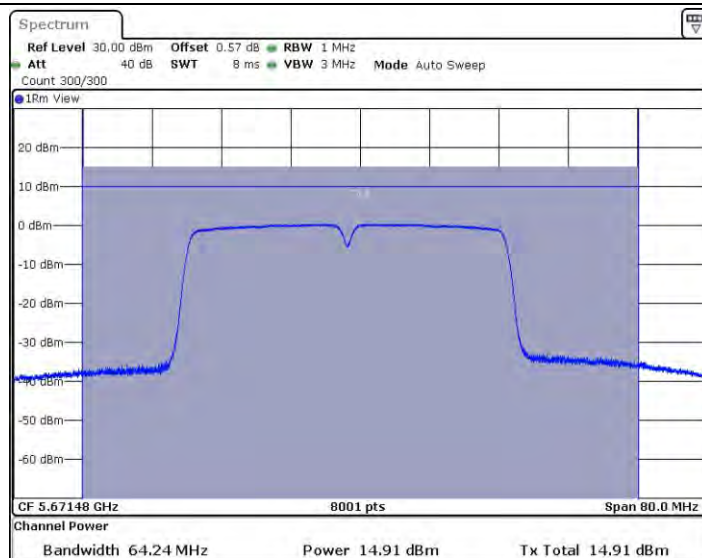
11N40SISO_Ant1_5510



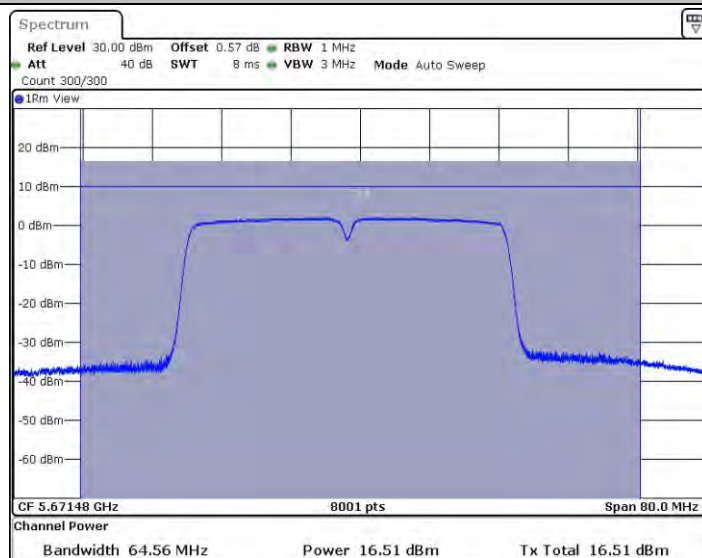
11N40SISO_Ant0_5550



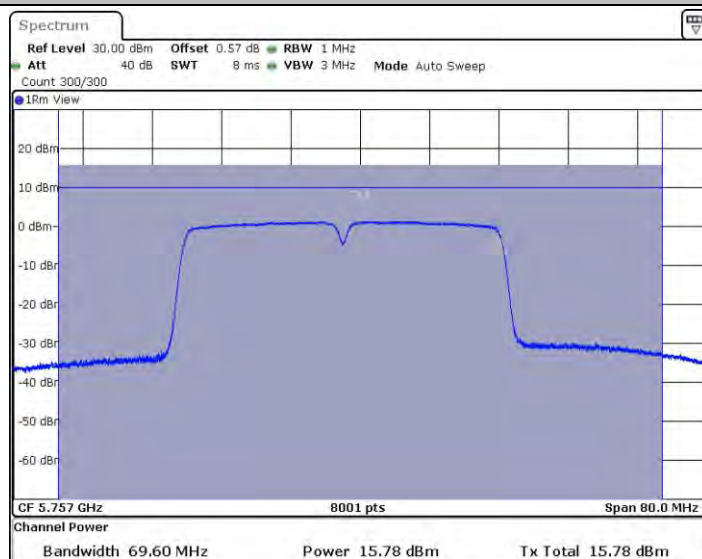
11N40SISO_Ant1_5550



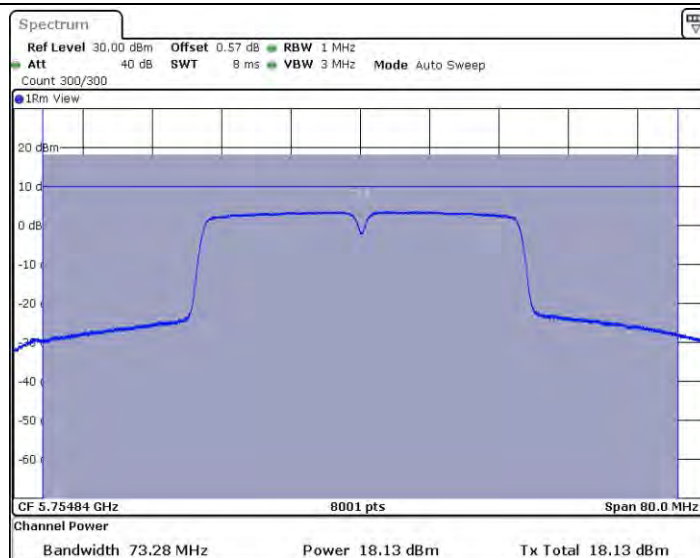
11N40SISO_Ant0_5670



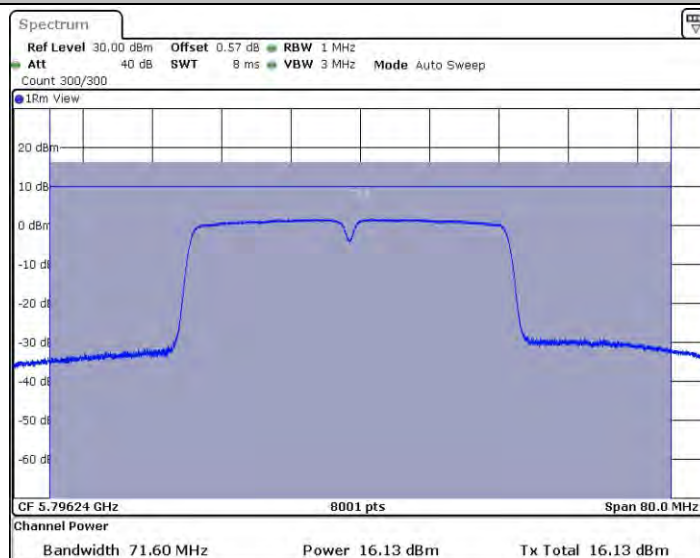
11N40SISO_Ant1_5670



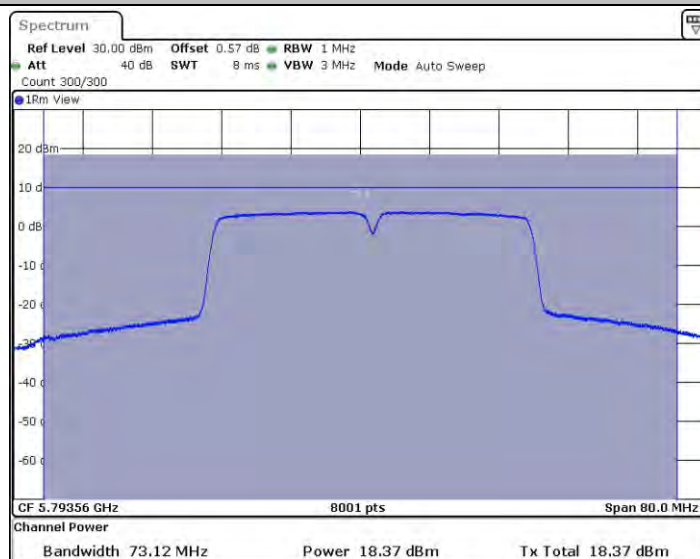
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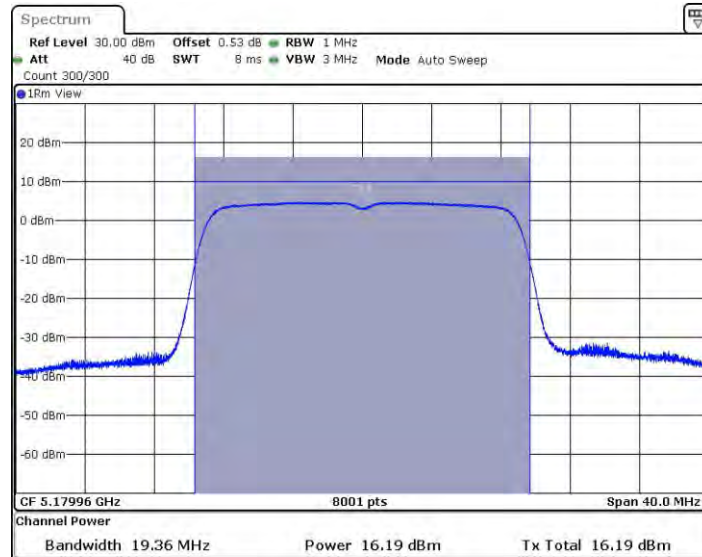
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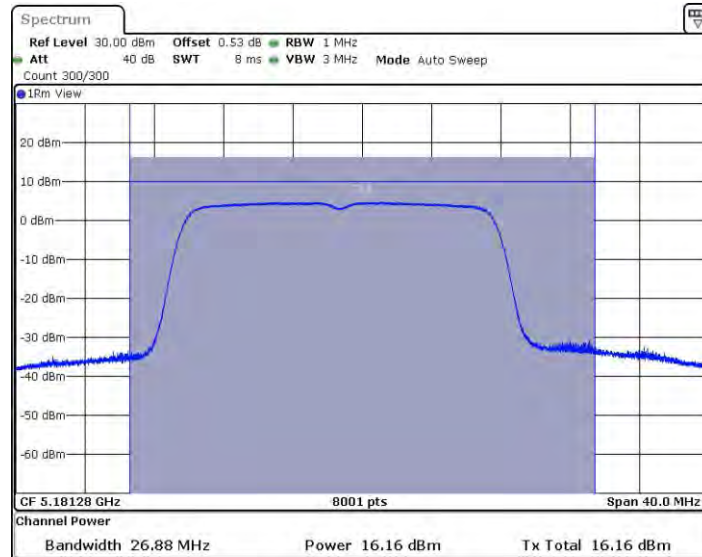
11N40SISO_Ant0_5795



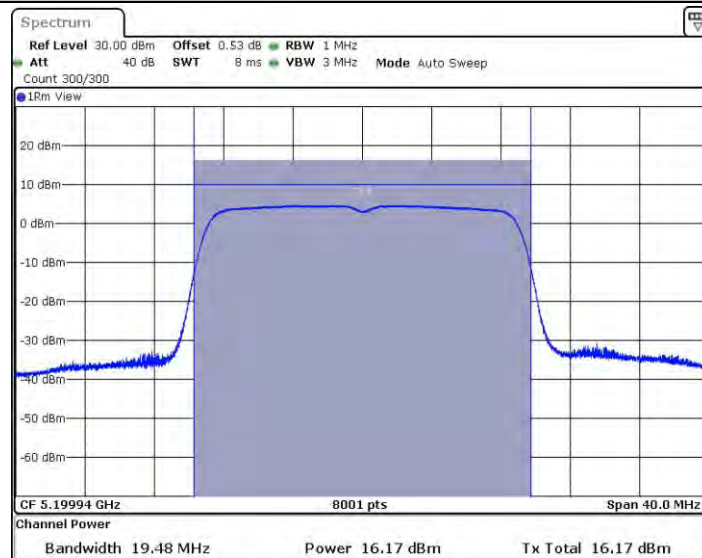
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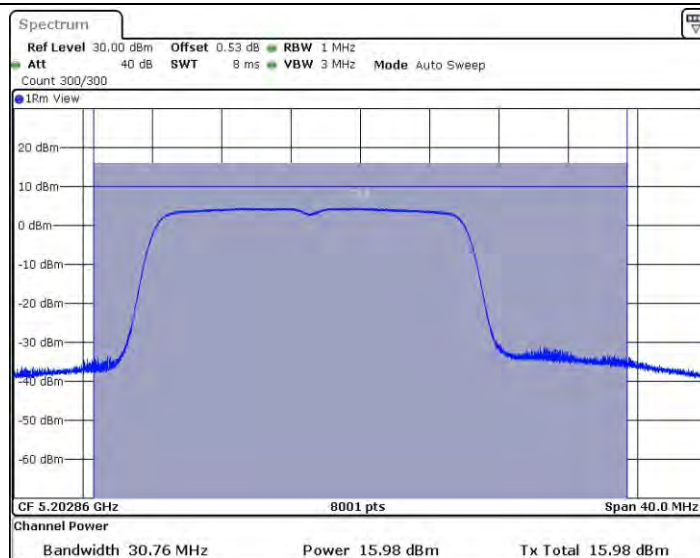
11AC20SISO_Ant0_5180



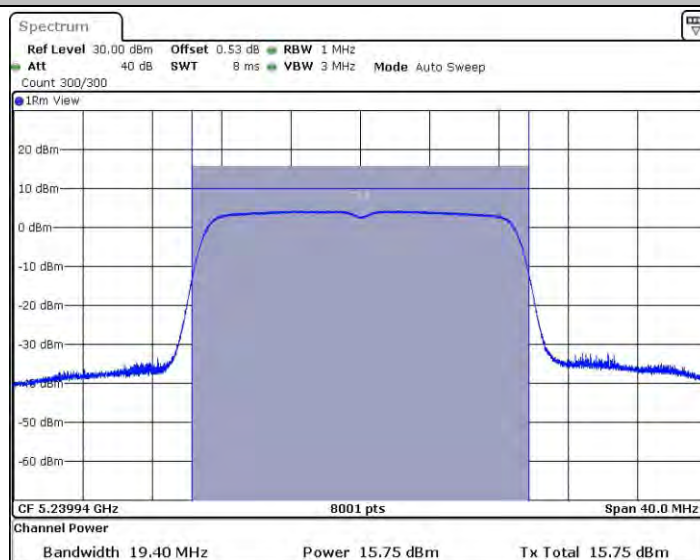
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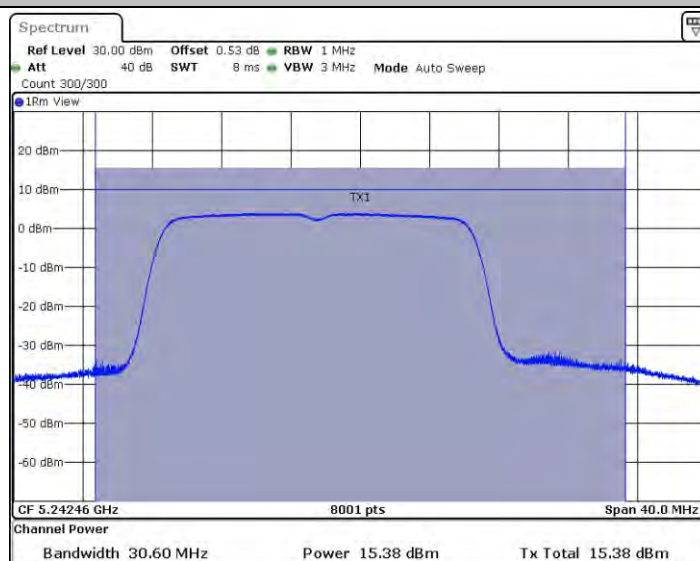
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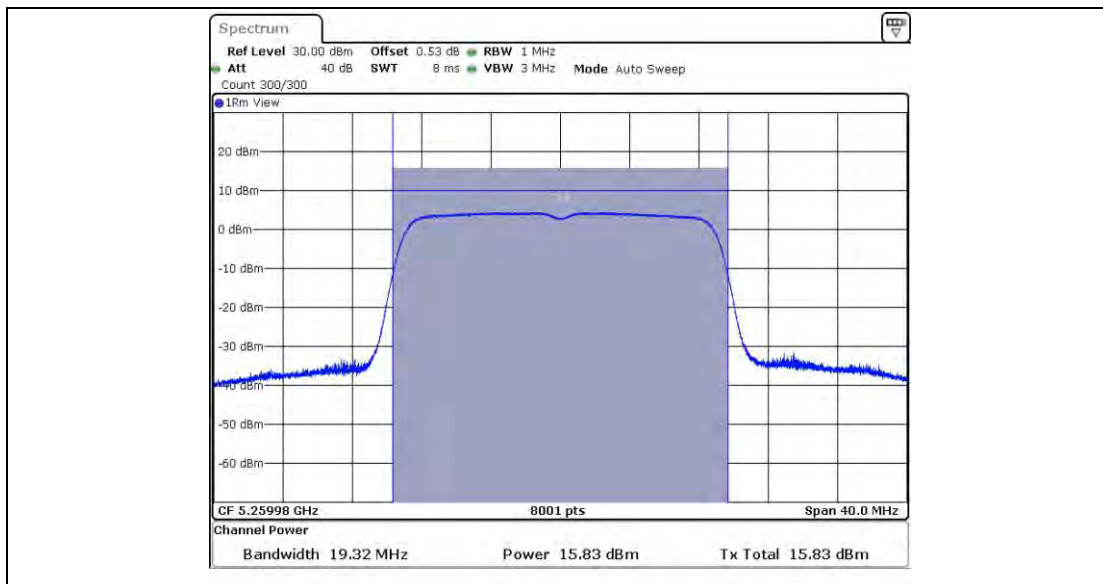
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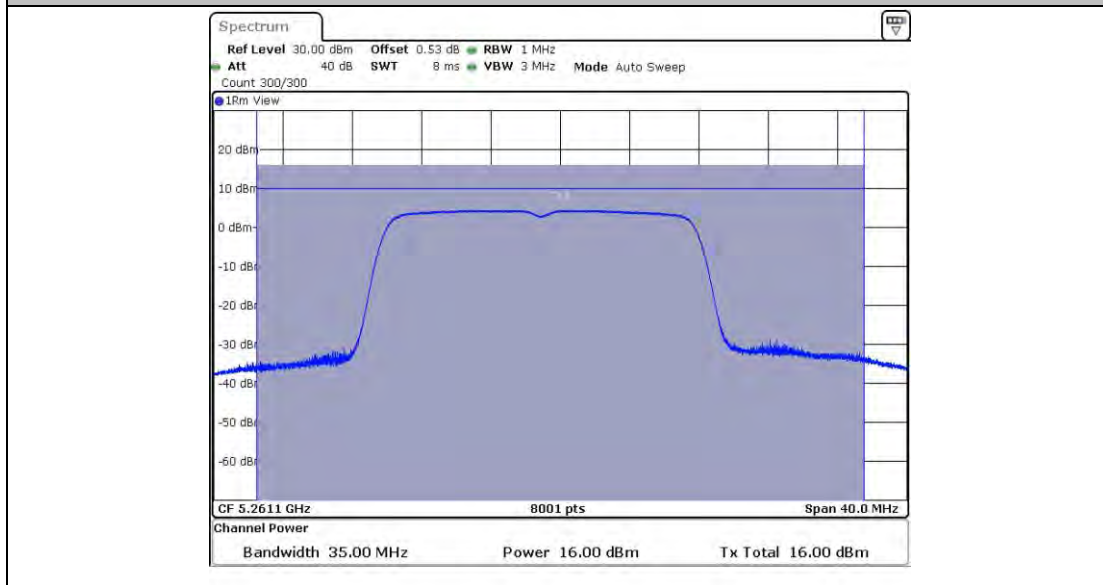
11AC20SISO_Ant0_5240



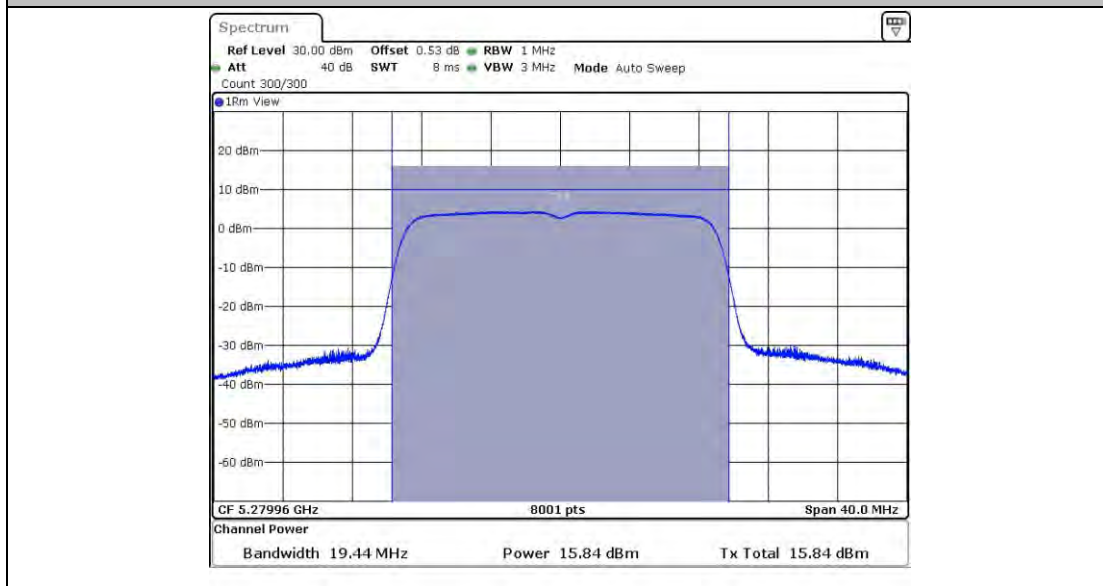
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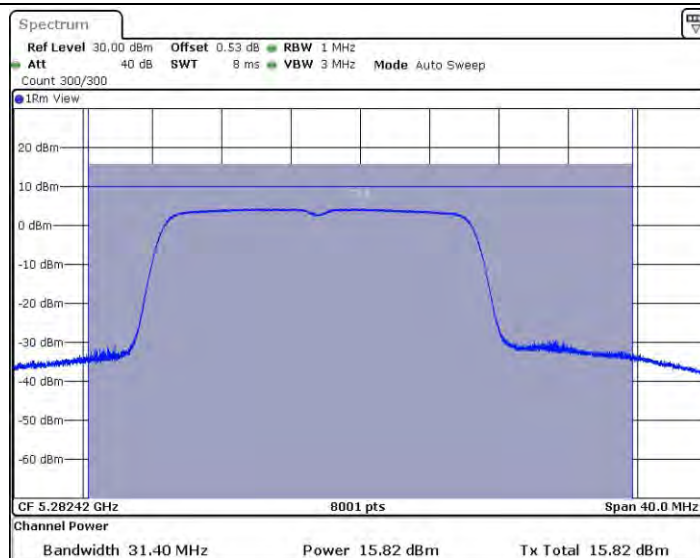
11AC20SISO_Ant0_5260



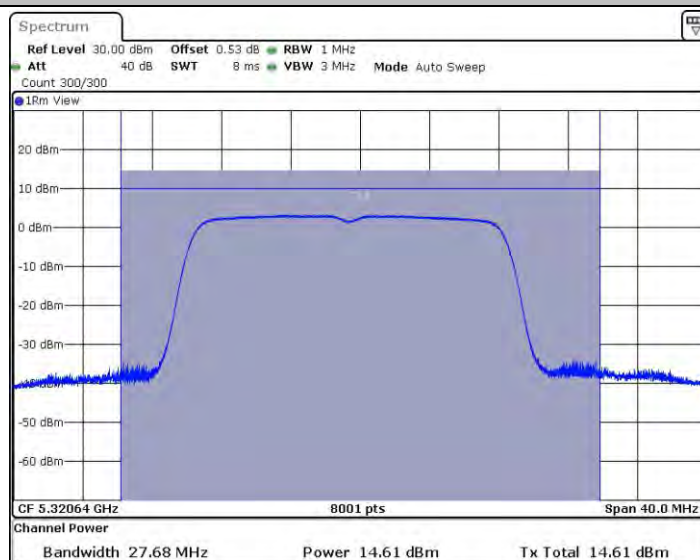
11AC20SISO_Ant1_5260



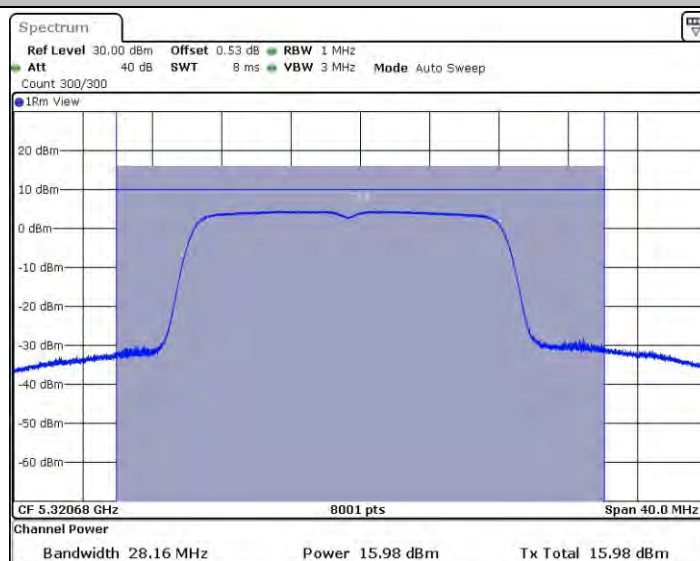
11AC20SISO_Ant0_5280



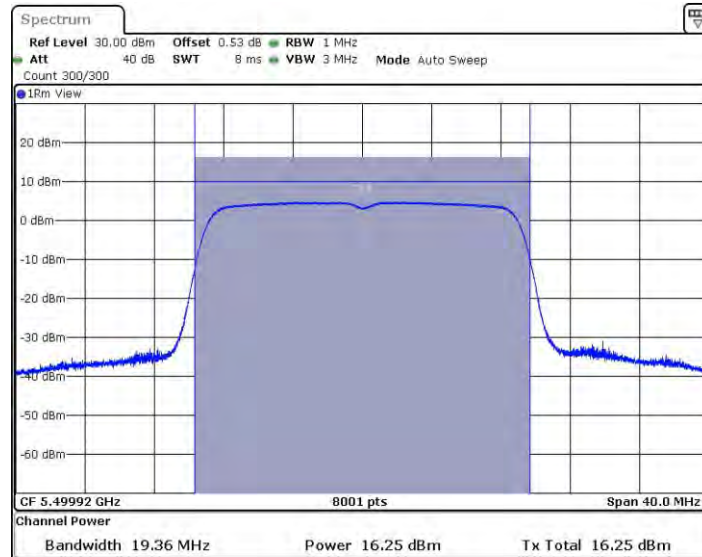
11AC20SISO_Ant1_5280



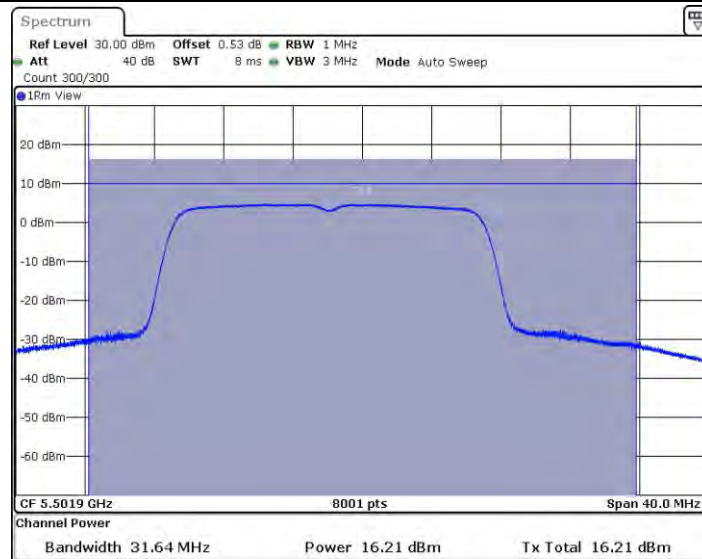
11AC20SISO_Ant0_5320



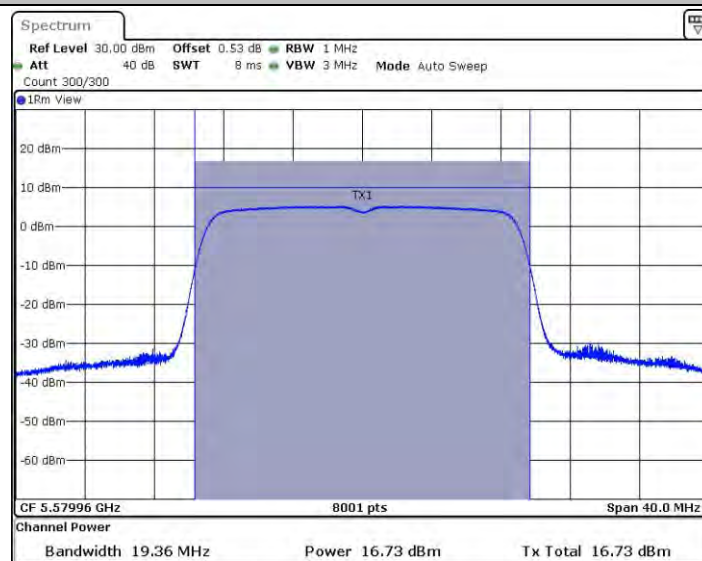
11AC20SISO_Ant1_5320



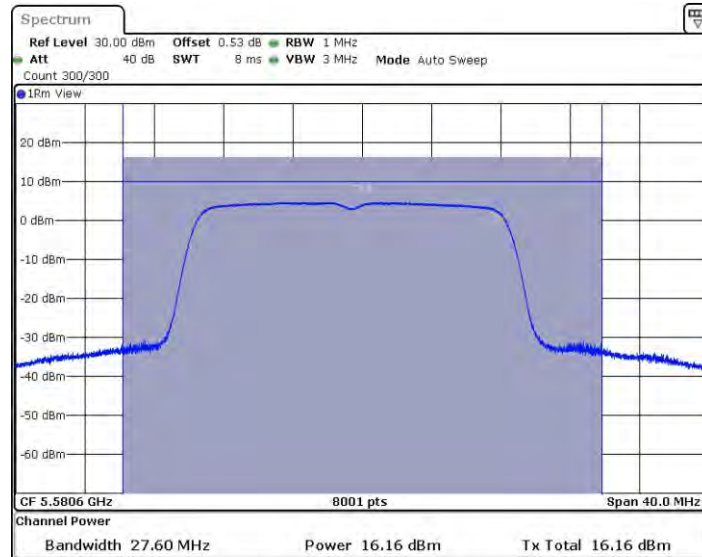
11AC20SISO_Ant0_5500



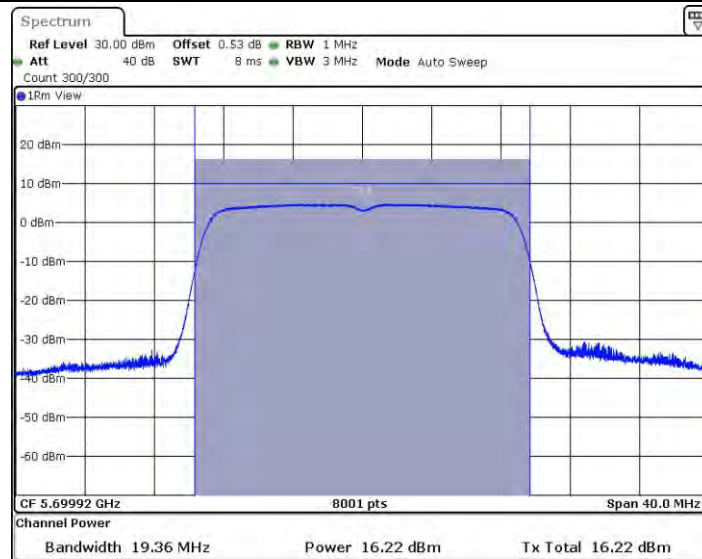
11AC20SISO_Ant1_5500



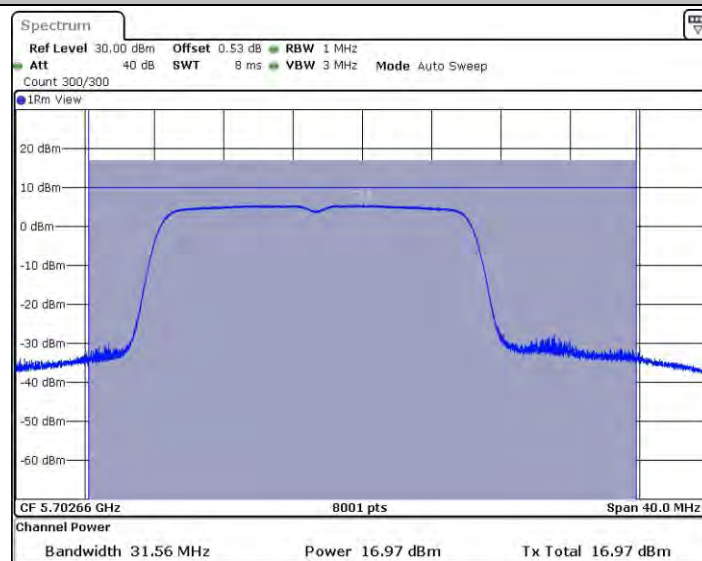
11AC20SISO_Ant0_5580



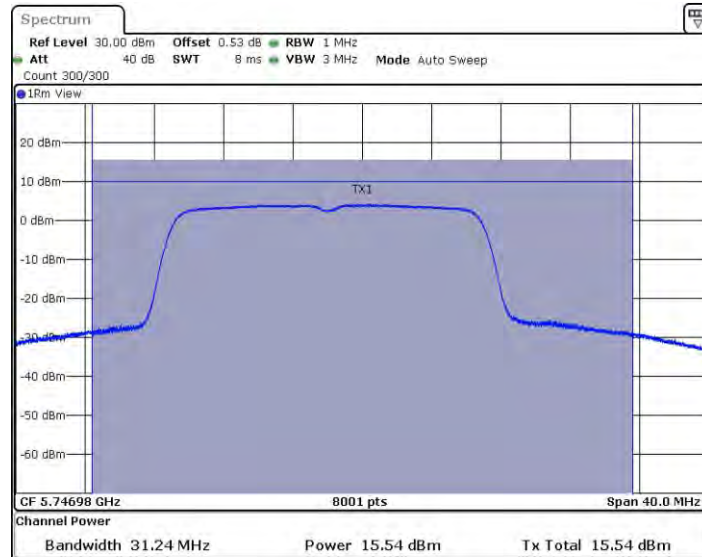
11AC20SISO_Ant1_5580



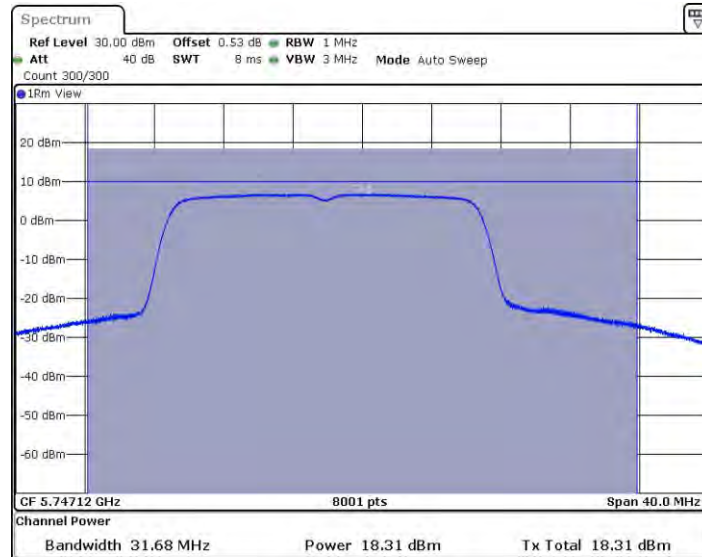
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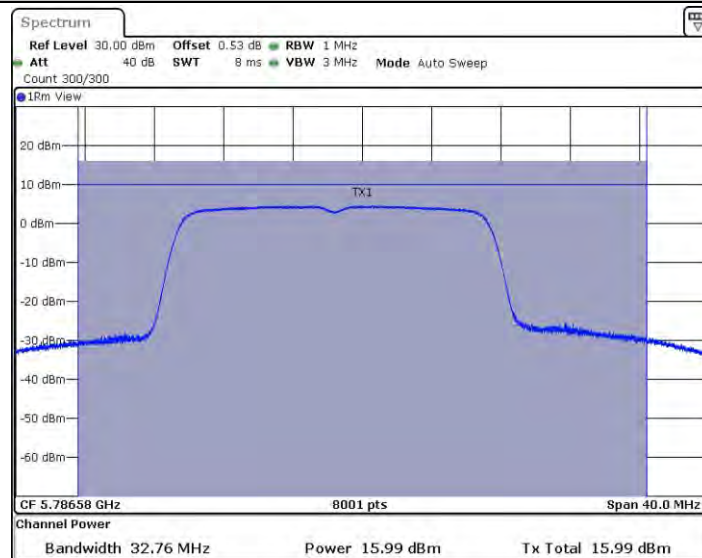
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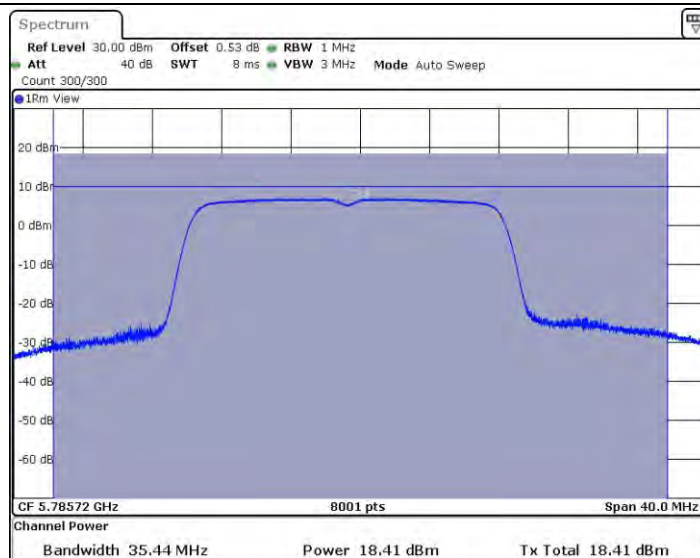
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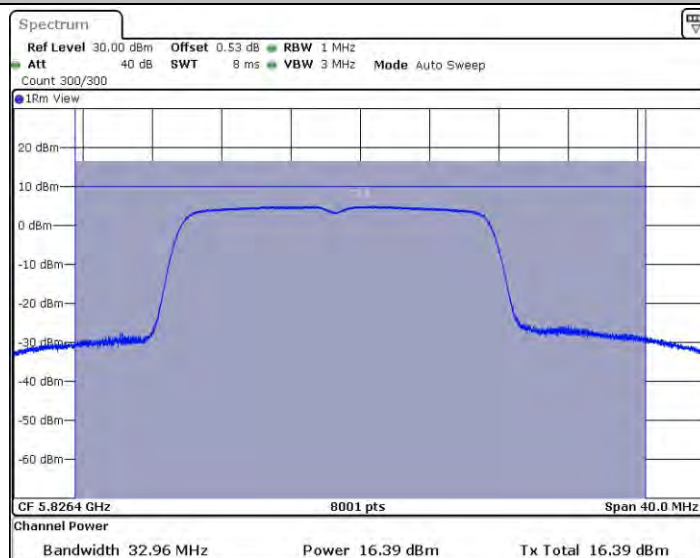
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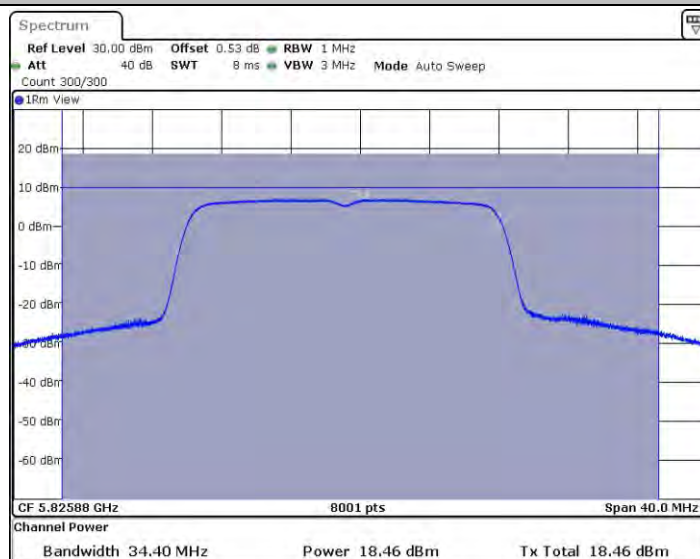
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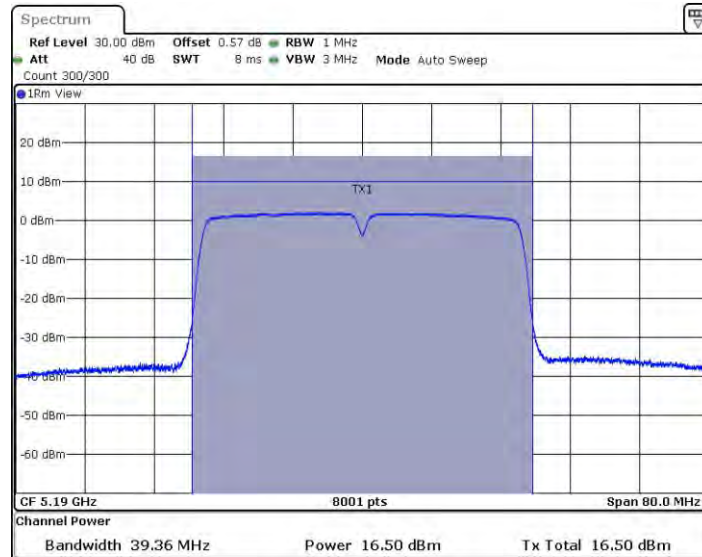
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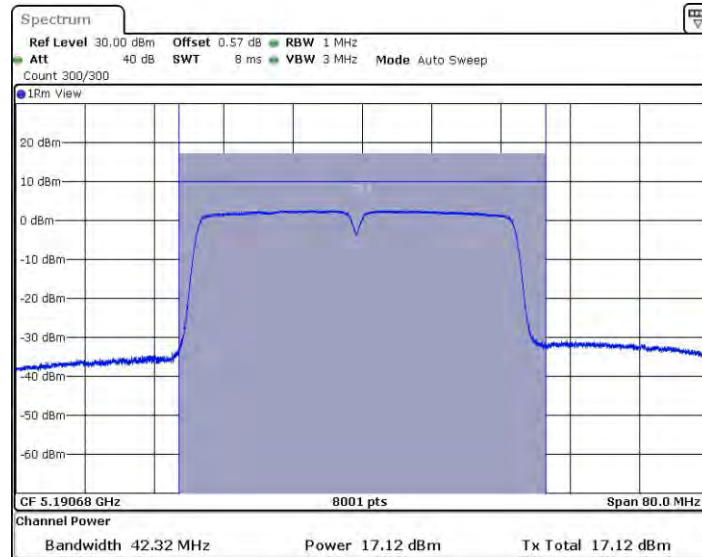
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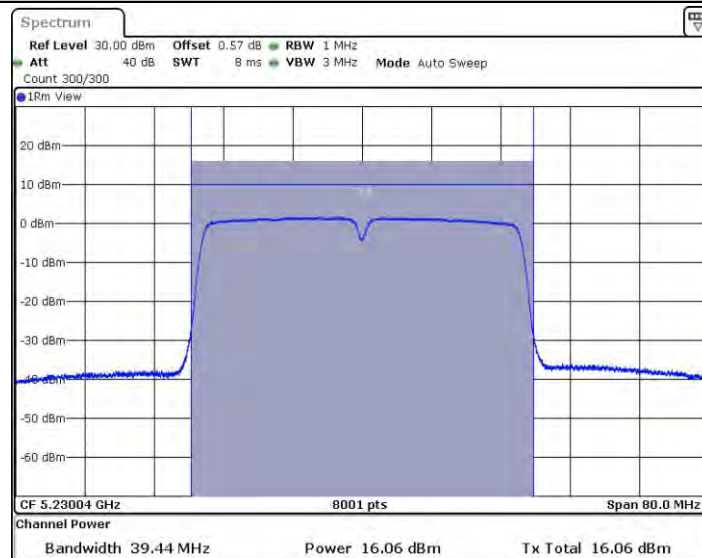
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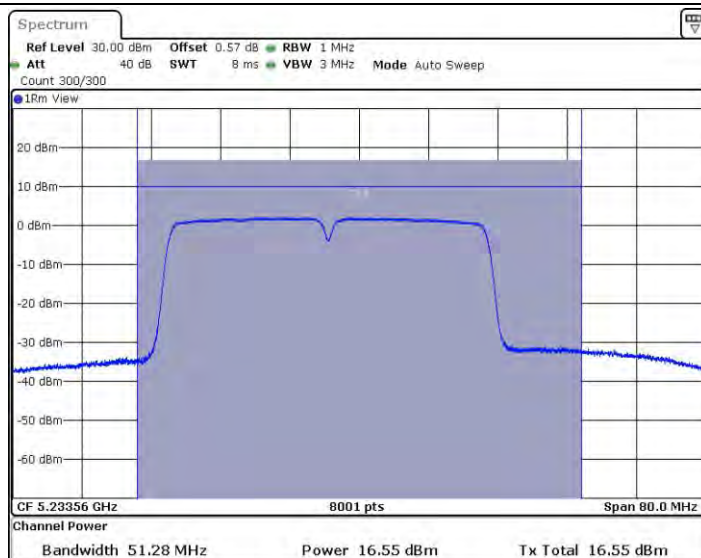
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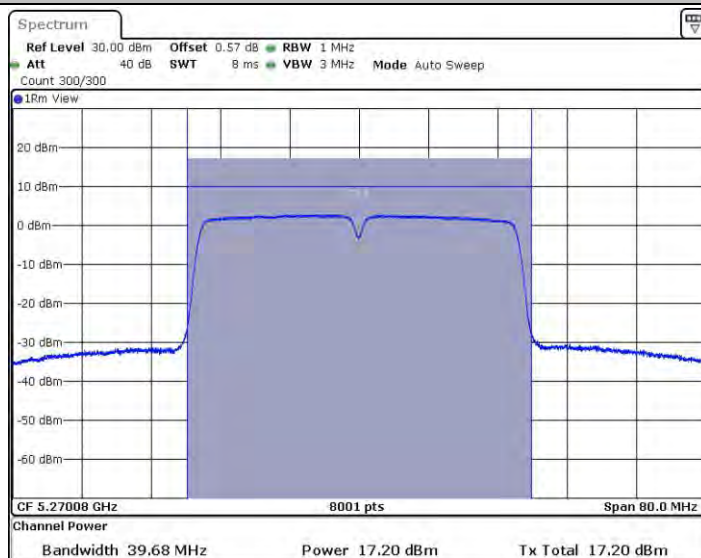
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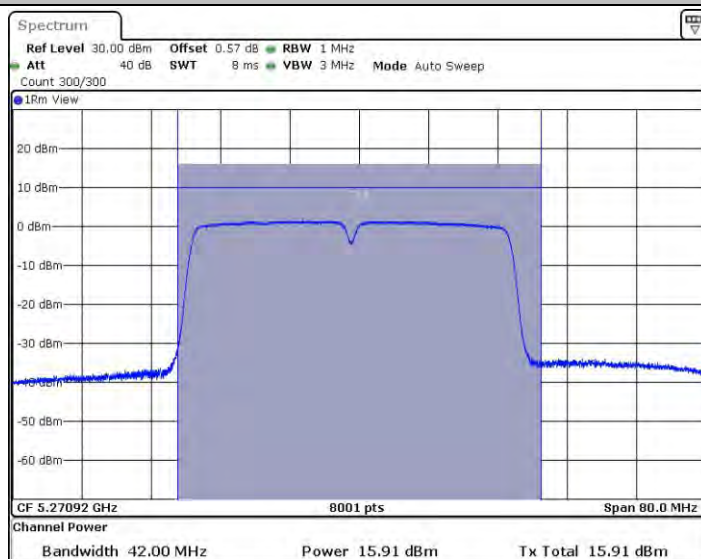
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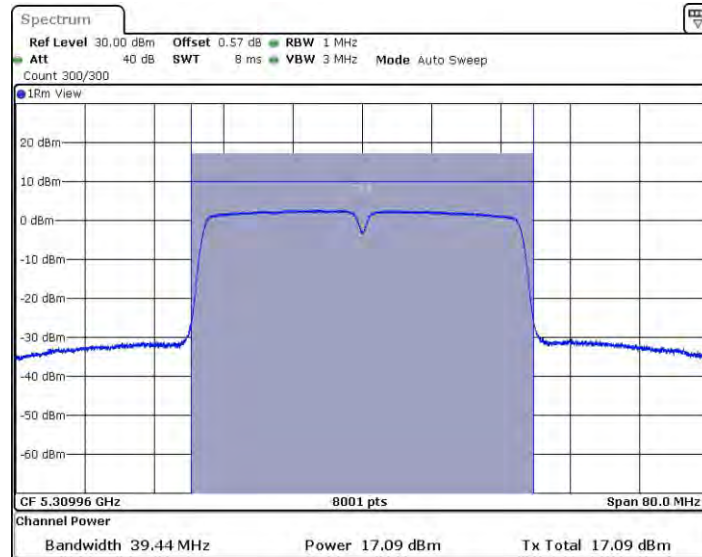
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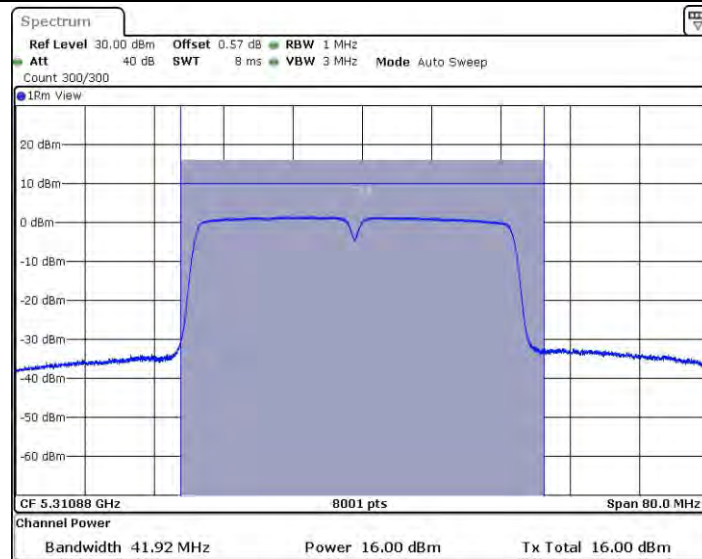
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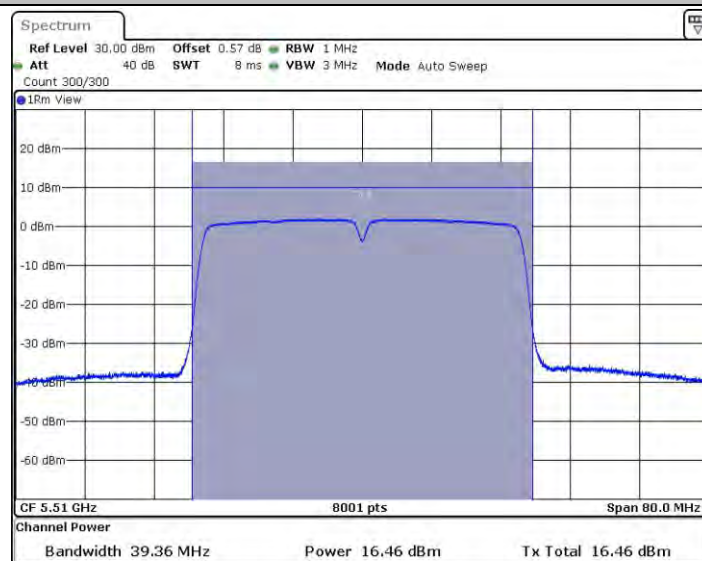
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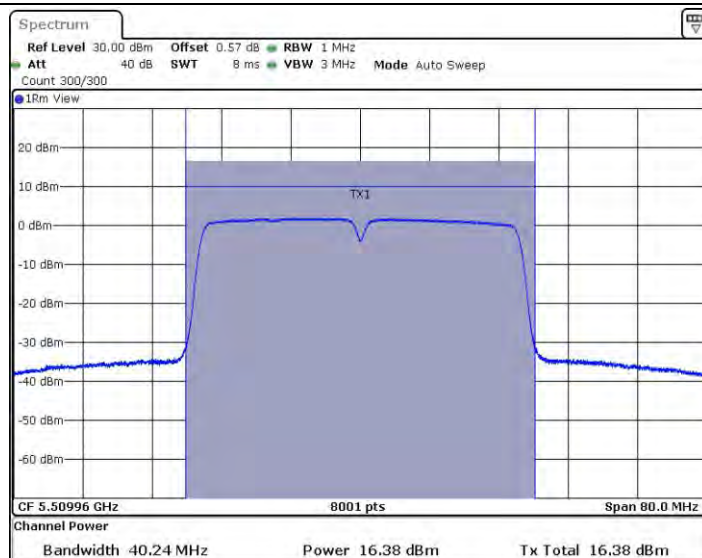
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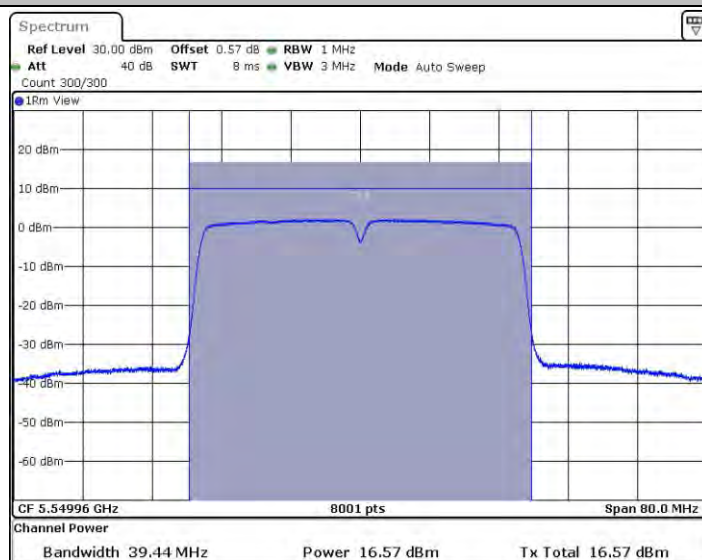
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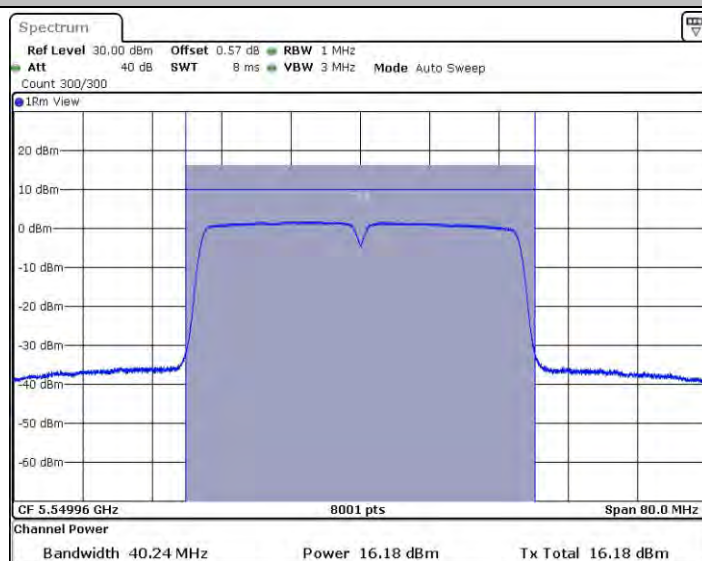
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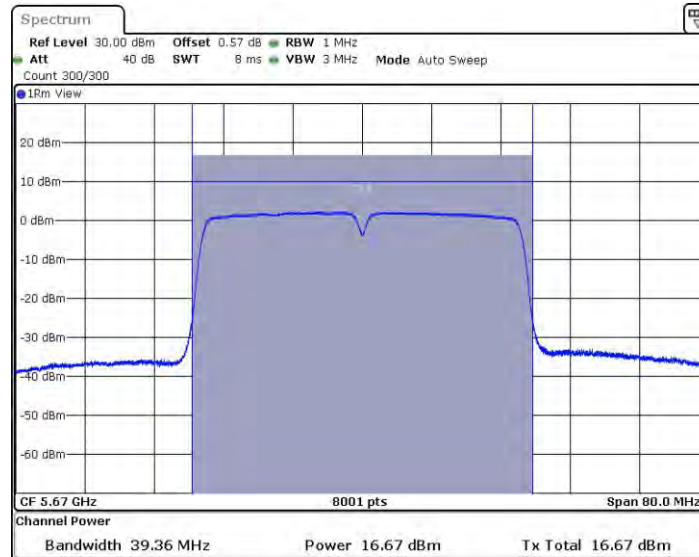
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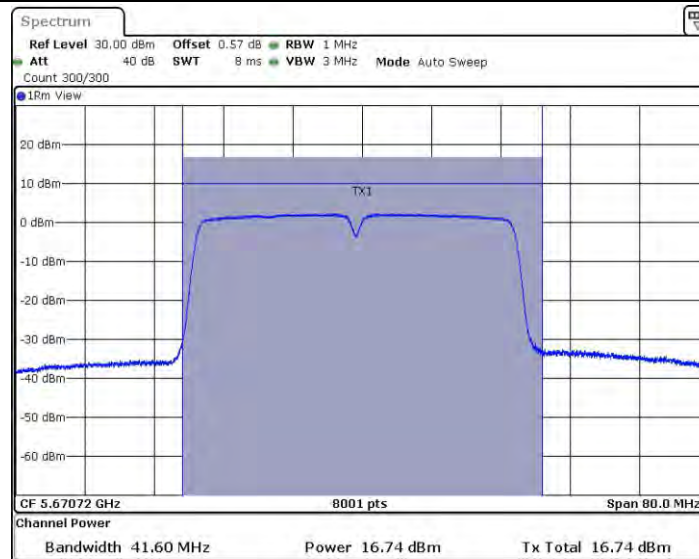
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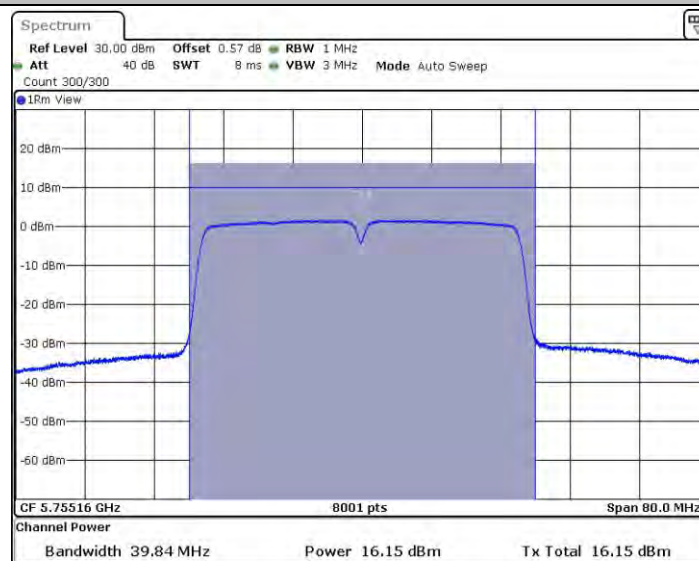
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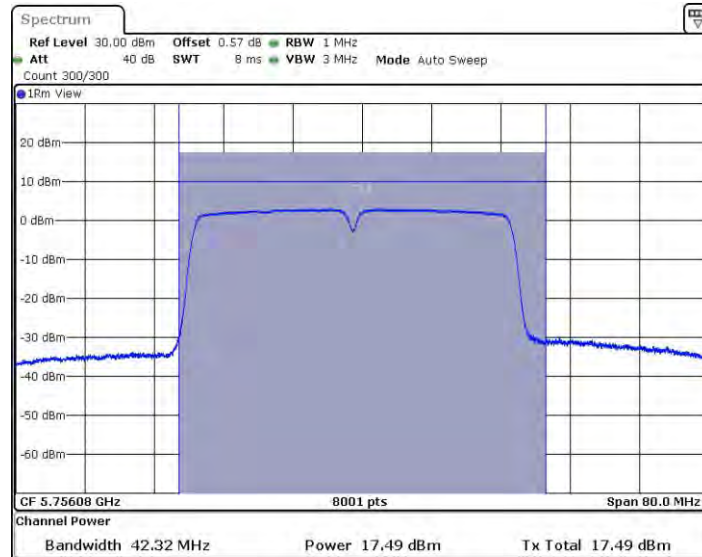
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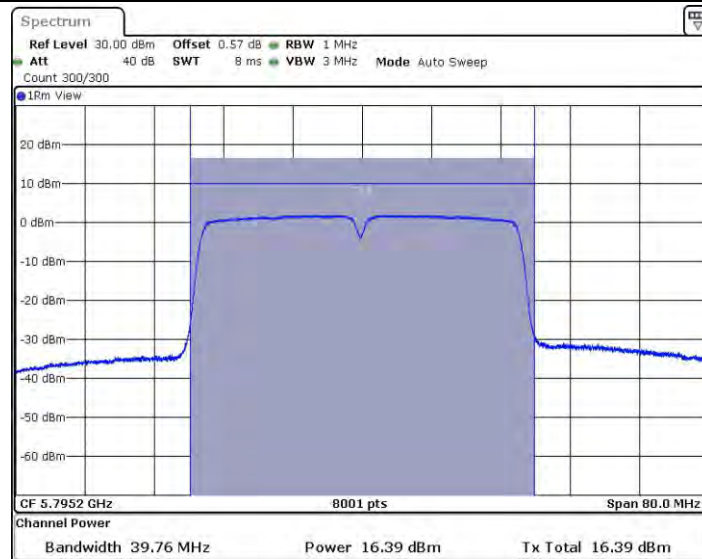
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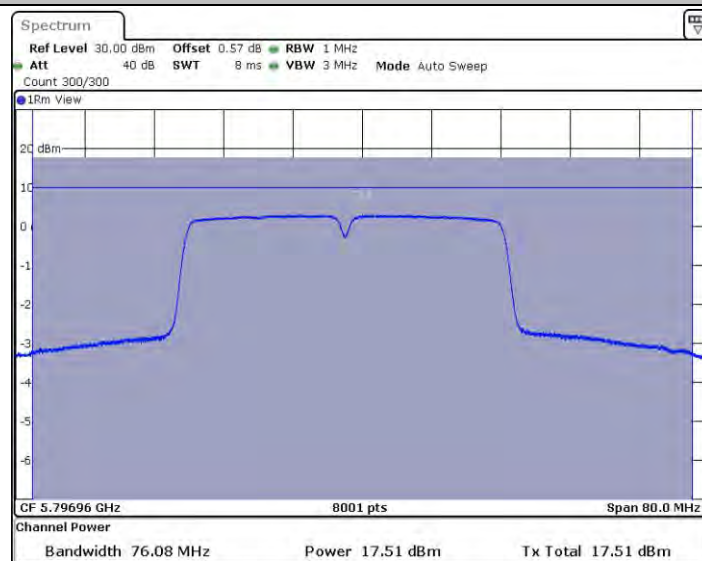
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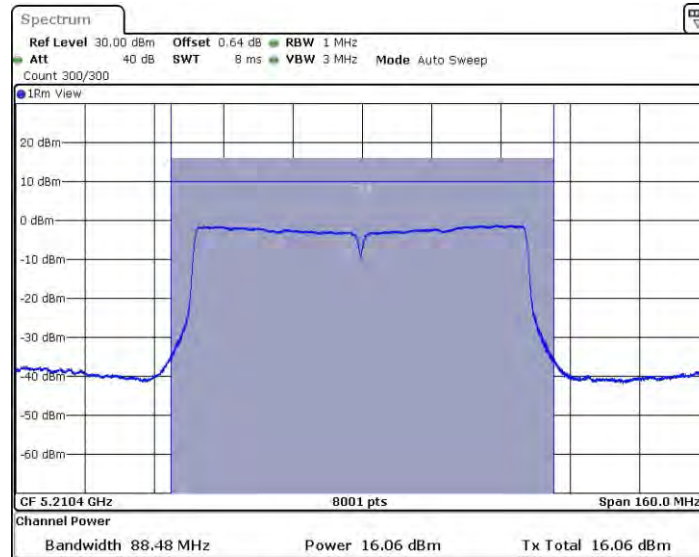
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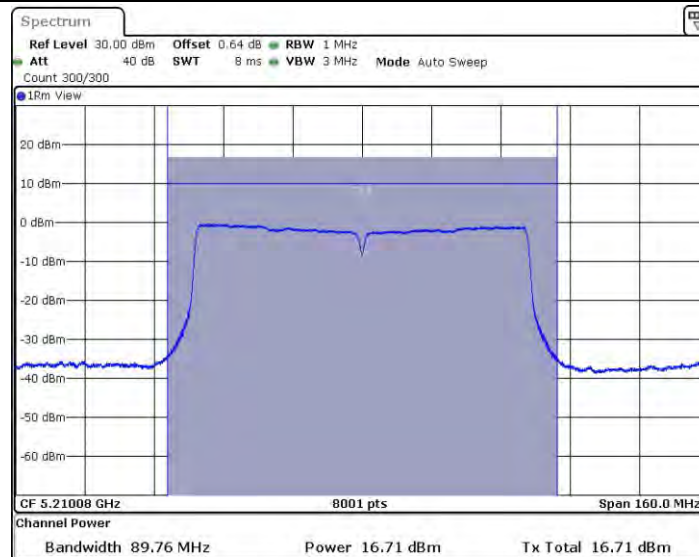
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11AC40SISO_Ant1_5795



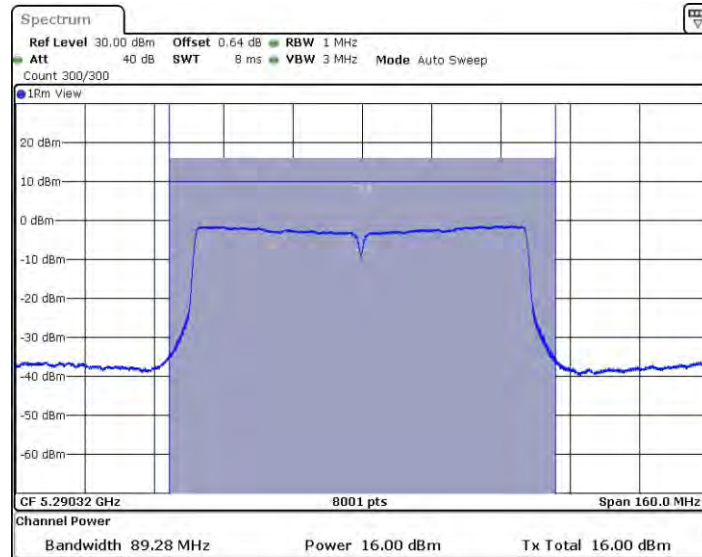
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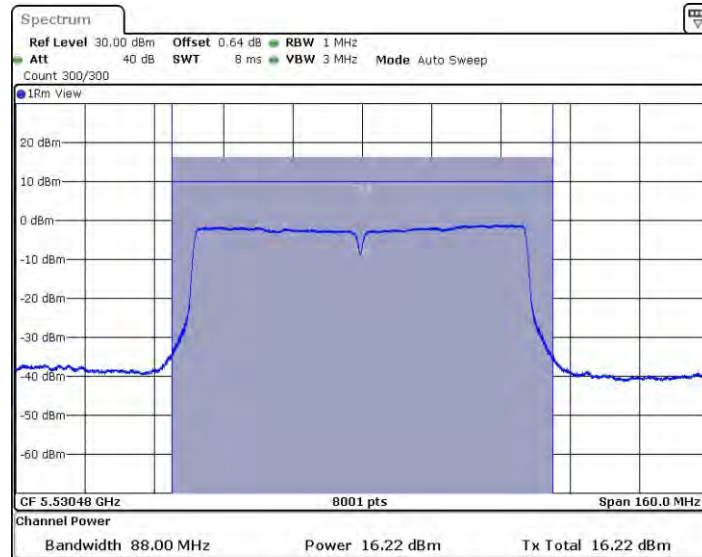
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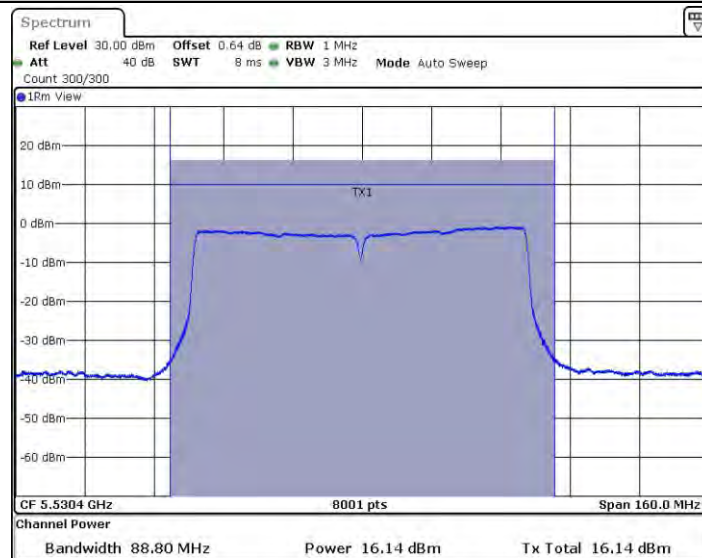
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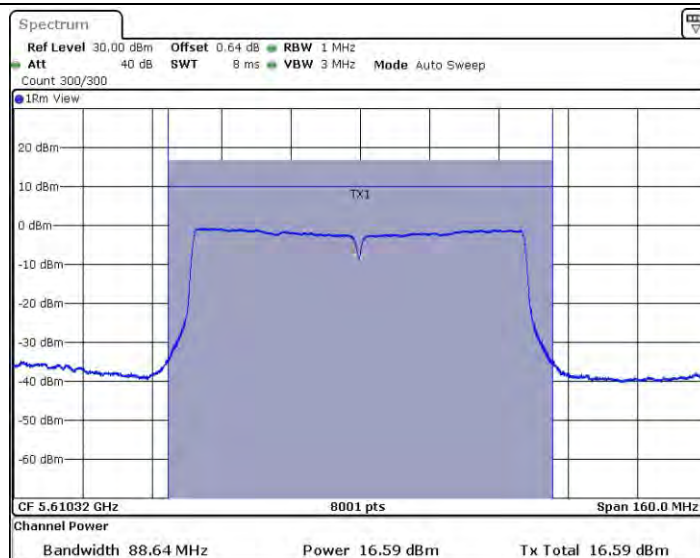
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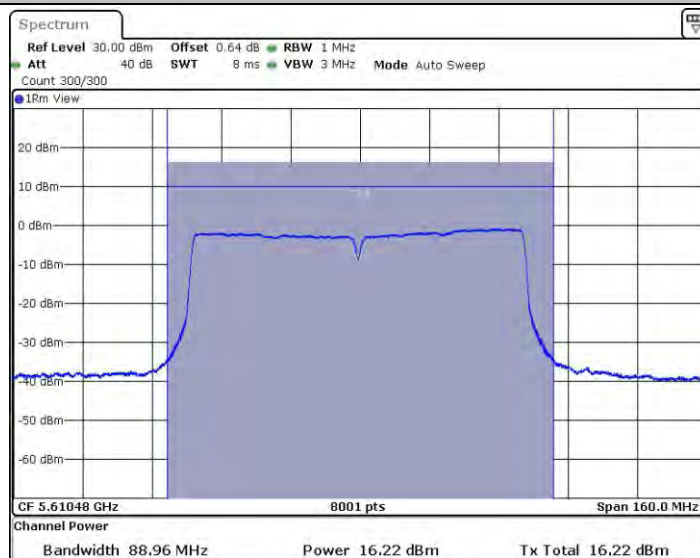
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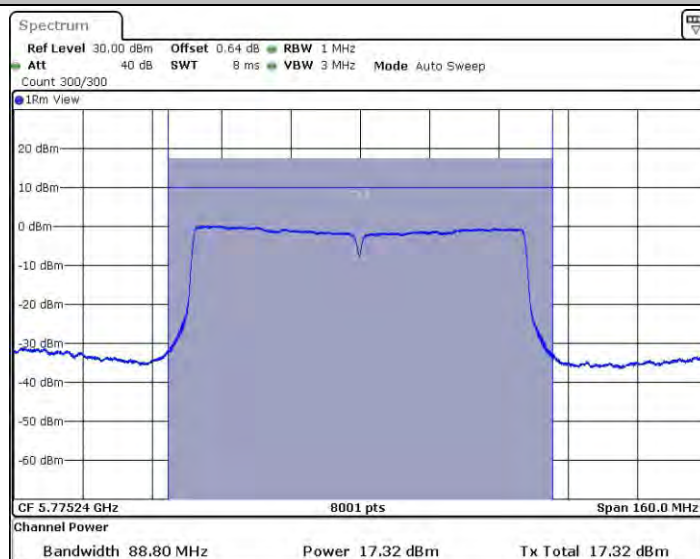
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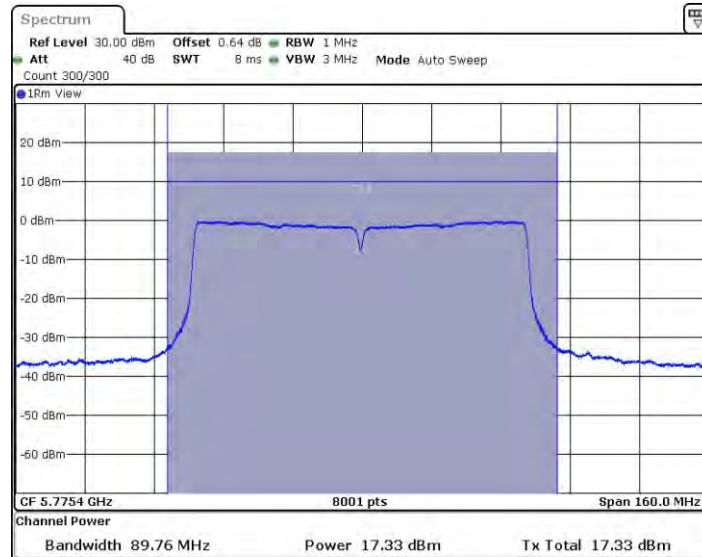
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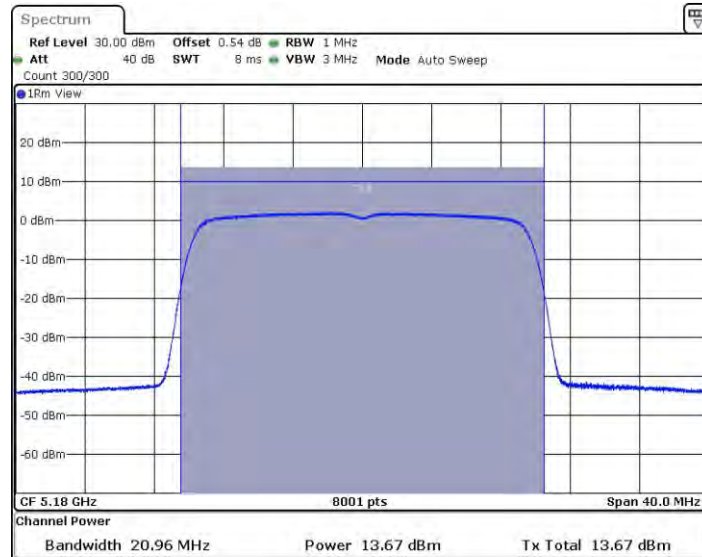
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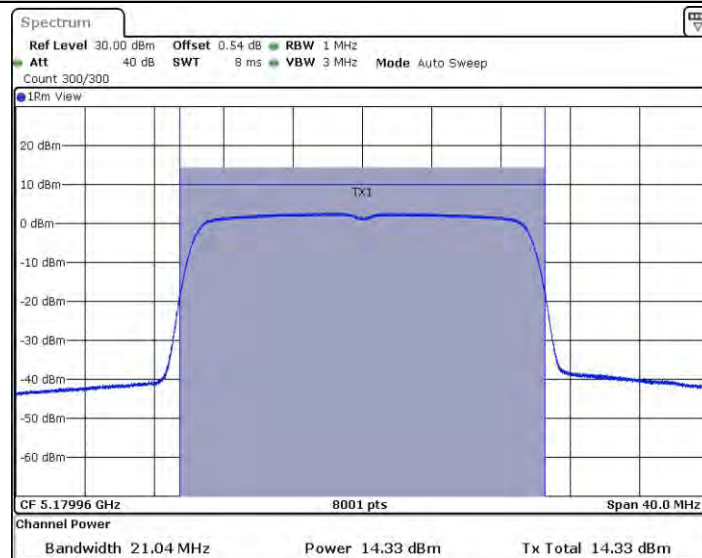
11AC80SISO_Ant0_5775



11AC80SISO_Ant1_5775



11AX20SISO_Ant0_5180



11AX20SISO_Ant1_5180