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Appendix



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Effective (Isotropic) Radiated Power Output Data for SA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	EIRP (dBm)	Limit (dBm)	Verdict
N41	15	10	DFT-PI2BPSK	L	Edge_1RB_Left	26.02	27.54	33	PASS
N41	15	10	DFT-PI2BPSK	L	Edge_1RB_Right	26.38	27.9	33	PASS
N41	15	10	DFT-PI2BPSK	L	Outer_Full	25.97	27.49	33	PASS
N41	15	10	DFT-PI2BPSK	L	Inner_Full	25.89	27.41	33	PASS
N41	15	10	DFT-QPSK	L	Edge_1RB_Left	25.9	27.42	33	PASS
N41	15	10	DFT-QPSK	L	Edge_1RB_Right	26.27	27.79	33	PASS
N41	15	10	DFT-QPSK	L	Outer_Full	25.87	27.39	33	PASS
N41	15	10	DFT-QPSK	L	Inner_Full	25.82	27.34	33	PASS
N41	15	10	DFT-16QAM	L	Edge_1RB_Left	25.45	26.97	33	PASS
N41	15	10	DFT-16QAM	L	Edge_1RB_Right	25.8	27.32	33	PASS
N41	15	10	DFT-16QAM	L	Outer_Full	25.44	26.96	33	PASS
N41	15	10	DFT-16QAM	L	Inner_Full	23.64	25.16	33	PASS
N41	15	10	DFT-64QAM	L	Edge_1RB_Left	25.72	27.24	33	PASS
N41	15	10	DFT-64QAM	L	Edge_1RB_Right	26.09	27.61	33	PASS
N41	15	10	DFT-64QAM	L	Outer_Full	25.7	27.22	33	PASS
N41	15	10	DFT-64QAM	L	Inner_Full	26.17	27.69	33	PASS
N41	15	10	DFT-256QAM	L	Edge_1RB_Left	25.65	27.17	33	PASS
N41	15	10	DFT-256QAM	L	Edge_1RB_Right	26.03	27.55	33	PASS
N41	15	10	DFT-256QAM	L	Outer_Full	25.64	27.16	33	PASS
N41	15	10	DFT-256QAM	L	Inner_Full	26.2	27.72	33	PASS
N41	15	10	CP-QPSK	L	Edge_1RB_Left	25.16	26.68	33	PASS
N41	15	10	CP-QPSK	L	Edge_1RB_Right	25.67	27.19	33	PASS
N41	15	10	CP-QPSK	L	Outer_Full	25.48	27	33	PASS
N41	15	10	CP-QPSK	L	Inner_Full	25.47	26.99	33	PASS
N41	15	10	CP-16QAM	L	Edge_1RB_Left	25.23	26.75	33	PASS
N41	15	10	CP-16QAM	L	Edge_1RB_Right	25.78	27.3	33	PASS
N41	15	10	CP-16QAM	L	Outer_Full	25.45	26.97	33	PASS
N41	15	10	CP-16QAM	L	Inner_Full	25.45	26.97	33	PASS
N41	15	10	CP-64QAM	L	Edge_1RB_Left	25.43	26.95	33	PASS
N41	15	10	CP-64QAM	L	Edge_1RB_Right	25.93	27.45	33	PASS
N41	15	10	CP-64QAM	L	Outer_Full	25.45	26.97	33	PASS
N41	15	10	CP-64QAM	L	Inner_Full	25.87	27.39	33	PASS



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N41	15	10	CP-256QAM	L	Edge_1RB_Left	25.23	26.75	33	PASS
N41	15	10	CP-256QAM	L	Edge_1RB_Right	25.74	27.26	33	PASS
N41	15	10	CP-256QAM	L	Outer_Full	25.45	26.97	33	PASS
N41	15	10	CP-256QAM	L	Inner_Full	25.86	27.38	33	PASS
N41	15	10	DFT-PI2BPSK	M	Edge_1RB_Left	27.08	28.6	33	PASS
N41	15	10	DFT-PI2BPSK	M	Edge_1RB_Right	26.9	28.42	33	PASS
N41	15	10	DFT-PI2BPSK	M	Outer_Full	26.97	28.49	33	PASS
N41	15	10	DFT-PI2BPSK	M	Inner_Full	27.11	28.63	33	PASS
N41	15	10	DFT-QPSK	M	Edge_1RB_Left	26.87	28.39	33	PASS
N41	15	10	DFT-QPSK	M	Edge_1RB_Right	26.7	28.22	33	PASS
N41	15	10	DFT-QPSK	M	Outer_Full	26.87	28.39	33	PASS
N41	15	10	DFT-QPSK	M	Inner_Full	23.72	25.24	33	PASS
N41	15	10	DFT-16QAM	M	Edge_1RB_Left	26.53	28.05	33	PASS
N41	15	10	DFT-16QAM	M	Edge_1RB_Right	26.33	27.85	33	PASS
N41	15	10	DFT-16QAM	M	Outer_Full	26.58	28.1	33	PASS
N41	15	10	DFT-16QAM	M	Inner_Full	26.79	28.31	33	PASS
N41	15	10	DFT-64QAM	M	Edge_1RB_Left	26.72	28.24	33	PASS
N41	15	10	DFT-64QAM	M	Edge_1RB_Right	26.5	28.02	33	PASS
N41	15	10	DFT-64QAM	M	Outer_Full	26.55	28.07	33	PASS
N41	15	10	DFT-64QAM	M	Inner_Full	26.56	28.08	33	PASS
N41	15	10	DFT-256QAM	M	Edge_1RB_Left	26.58	28.1	33	PASS
N41	15	10	DFT-256QAM	M	Edge_1RB_Right	26.39	27.91	33	PASS
N41	15	10	DFT-256QAM	M	Outer_Full	26.53	28.05	33	PASS
N41	15	10	DFT-256QAM	M	Inner_Full	26.18	27.7	33	PASS
N41	15	10	CP-QPSK	M	Edge_1RB_Left	26.24	27.76	33	PASS
N41	15	10	CP-QPSK	M	Edge_1RB_Right	26.16	27.68	33	PASS
N41	15	10	CP-QPSK	M	Outer_Full	26.15	27.67	33	PASS
N41	15	10	CP-QPSK	M	Inner_Full	26.18	27.7	33	PASS
N41	15	10	CP-16QAM	M	Edge_1RB_Left	26.2	27.72	33	PASS
N41	15	10	CP-16QAM	M	Edge_1RB_Right	26.17	27.69	33	PASS
N41	15	10	CP-16QAM	M	Outer_Full	26.12	27.64	33	PASS
N41	15	10	CP-16QAM	M	Inner_Full	26.18	27.7	33	PASS
N41	15	10	CP-64QAM	M	Edge_1RB_Left	26.35	27.87	33	PASS
N41	15	10	CP-64QAM	M	Edge_1RB_Right	26.27	27.79	33	PASS
N41	15	10	CP-64QAM	M	Outer_Full	23.71	25.23	33	PASS
N41	15	10	CP-64QAM	M	Inner_Full	26.2	27.72	33	PASS
N41	15	10	CP-256QAM	M	Edge_1RB_Left	26.2	27.72	33	PASS



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N41	15	10	CP-256QAM	M	Edge_1RB_Right	23.36	24.88	33	PASS
N41	15	10	CP-256QAM	M	Outer_Full	26.17	27.69	33	PASS
N41	15	10	CP-256QAM	M	Inner_Full	26.17	27.69	33	PASS
N41	15	10	DFT-PI2BPSK	H	Edge_1RB_Left	26.97	28.49	33	PASS
N41	15	10	DFT-PI2BPSK	H	Edge_1RB_Right	26.62	28.14	33	PASS
N41	15	10	DFT-PI2BPSK	H	Outer_Full	27.13	28.65	33	PASS
N41	15	10	DFT-PI2BPSK	H	Inner_Full	26.77	28.29	33	PASS
N41	15	10	DFT-QPSK	H	Edge_1RB_Left	26.84	28.36	33	PASS
N41	15	10	DFT-QPSK	H	Edge_1RB_Right	26.42	27.94	33	PASS
N41	15	10	DFT-QPSK	H	Outer_Full	26.7	28.22	33	PASS
N41	15	10	DFT-QPSK	H	Inner_Full	23.68	25.2	33	PASS
N41	15	10	DFT-16QAM	H	Edge_1RB_Left	26.42	27.94	33	PASS
N41	15	10	DFT-16QAM	H	Edge_1RB_Right	26.05	27.57	33	PASS
N41	15	10	DFT-16QAM	H	Outer_Full	26.4	27.92	33	PASS
N41	15	10	DFT-16QAM	H	Inner_Full	26.35	27.87	33	PASS
N41	15	10	DFT-64QAM	H	Edge_1RB_Left	26.58	28.1	33	PASS
N41	15	10	DFT-64QAM	H	Edge_1RB_Right	26.17	27.69	33	PASS
N41	15	10	DFT-64QAM	H	Outer_Full	26.32	27.84	33	PASS
N41	15	10	DFT-64QAM	H	Inner_Full	24.12	25.64	33	PASS
N41	15	10	DFT-256QAM	H	Edge_1RB_Left	26.48	28	33	PASS
N41	15	10	DFT-256QAM	H	Edge_1RB_Right	26.07	27.59	33	PASS
N41	15	10	DFT-256QAM	H	Outer_Full	26.34	27.86	33	PASS
N41	15	10	DFT-256QAM	H	Inner_Full	26.27	27.79	33	PASS
N41	15	10	CP-QPSK	H	Edge_1RB_Left	26.14	27.66	33	PASS
N41	15	10	CP-QPSK	H	Edge_1RB_Right	25.88	27.4	33	PASS
N41	15	10	CP-QPSK	H	Outer_Full	25.96	27.48	33	PASS
N41	15	10	CP-QPSK	H	Inner_Full	25.94	27.46	33	PASS
N41	15	10	CP-16QAM	H	Edge_1RB_Left	26.09	27.61	33	PASS
N41	15	10	CP-16QAM	H	Edge_1RB_Right	25.85	27.37	33	PASS
N41	15	10	CP-16QAM	H	Outer_Full	25.94	27.46	33	PASS
N41	15	10	CP-16QAM	H	Inner_Full	23.96	25.48	33	PASS
N41	15	10	CP-64QAM	H	Edge_1RB_Left	26.32	27.84	33	PASS
N41	15	10	CP-64QAM	H	Edge_1RB_Right	26.04	27.56	33	PASS
N41	15	10	CP-64QAM	H	Outer_Full	26.01	27.53	33	PASS
N41	15	10	CP-64QAM	H	Inner_Full	26.3	27.82	33	PASS
N41	15	10	CP-256QAM	H	Edge_1RB_Left	26.1	27.62	33	PASS
N41	15	10	CP-256QAM	H	Edge_1RB_Right	25.86	27.38	33	PASS



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N41	15	10	CP-256QAM	H	Outer_Full	25.97	27.49	33	PASS
N41	15	10	CP-256QAM	H	Inner_Full	23.66	25.18	33	PASS
N41	15	15	DFT-PI2BPSK	L	Edge_1RB_Left	25.84	27.36	33	PASS
N41	15	15	DFT-PI2BPSK	L	Edge_1RB_Right	26.35	27.87	33	PASS
N41	15	15	DFT-PI2BPSK	L	Outer_Full	26.32	27.84	33	PASS
N41	15	15	DFT-PI2BPSK	L	Inner_Full	23.7	25.22	33	PASS
N41	15	15	DFT-QPSK	L	Edge_1RB_Left	25.84	27.36	33	PASS
N41	15	15	DFT-QPSK	L	Edge_1RB_Right	26.34	27.86	33	PASS
N41	15	15	DFT-QPSK	L	Outer_Full	26.3	27.82	33	PASS
N41	15	15	DFT-QPSK	L	Inner_Full	26.23	27.75	33	PASS
N41	15	15	DFT-16QAM	L	Edge_1RB_Left	25.49	27.01	33	PASS
N41	15	15	DFT-16QAM	L	Edge_1RB_Right	26.01	27.53	33	PASS
N41	15	15	DFT-16QAM	L	Outer_Full	26.04	27.56	33	PASS
N41	15	15	DFT-16QAM	L	Inner_Full	23.71	25.23	33	PASS
N41	15	15	DFT-64QAM	L	Edge_1RB_Left	25.75	27.27	33	PASS
N41	15	15	DFT-64QAM	L	Edge_1RB_Right	26.29	27.81	33	PASS
N41	15	15	DFT-64QAM	L	Outer_Full	26.01	27.53	33	PASS
N41	15	15	DFT-64QAM	L	Inner_Full	23.69	25.21	33	PASS
N41	15	15	DFT-256QAM	L	Edge_1RB_Left	25.67	27.19	33	PASS
N41	15	15	DFT-256QAM	L	Edge_1RB_Right	26.21	27.73	33	PASS
N41	15	15	DFT-256QAM	L	Outer_Full	26.06	27.58	33	PASS
N41	15	15	DFT-256QAM	L	Inner_Full	26.03	27.55	33	PASS
N41	15	15	CP-QPSK	L	Edge_1RB_Left	25.31	26.83	33	PASS
N41	15	15	CP-QPSK	L	Edge_1RB_Right	25.88	27.4	33	PASS
N41	15	15	CP-QPSK	L	Outer_Full	25.68	27.2	33	PASS
N41	15	15	CP-QPSK	L	Inner_Full	25.67	27.19	33	PASS
N41	15	15	CP-16QAM	L	Edge_1RB_Left	25.25	26.77	33	PASS
N41	15	15	CP-16QAM	L	Edge_1RB_Right	25.92	27.44	33	PASS
N41	15	15	CP-16QAM	L	Outer_Full	25.67	27.19	33	PASS
N41	15	15	CP-16QAM	L	Inner_Full	25.65	27.17	33	PASS
N41	15	15	CP-64QAM	L	Edge_1RB_Left	25.45	26.97	33	PASS
N41	15	15	CP-64QAM	L	Edge_1RB_Right	26.15	27.67	33	PASS
N41	15	15	CP-64QAM	L	Outer_Full	25.64	27.16	33	PASS
N41	15	15	CP-64QAM	L	Inner_Full	25.66	27.18	33	PASS
N41	15	15	CP-256QAM	L	Edge_1RB_Left	23.2	24.72	33	PASS
N41	15	15	CP-256QAM	L	Edge_1RB_Right	25.98	27.5	33	PASS
N41	15	15	CP-256QAM	L	Outer_Full	25.71	27.23	33	PASS



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N41	15	15	CP-256QAM	L	Inner_Full	25.7	27.22	33	PASS
N41	15	15	DFT-PI2BPSK	M	Edge_1RB_Left	27.06	28.58	33	PASS
N41	15	15	DFT-PI2BPSK	M	Edge_1RB_Right	26.75	28.27	33	PASS
N41	15	15	DFT-PI2BPSK	M	Outer_Full	27.32	28.84	33	PASS
N41	15	15	DFT-PI2BPSK	M	Inner_Full	27.07	28.59	33	PASS
N41	15	15	DFT-QPSK	M	Edge_1RB_Left	26.86	28.38	33	PASS
N41	15	15	DFT-QPSK	M	Edge_1RB_Right	26.56	28.08	33	PASS
N41	15	15	DFT-QPSK	M	Outer_Full	26.92	28.44	33	PASS
N41	15	15	DFT-QPSK	M	Inner_Full	26.87	28.39	33	PASS
N41	15	15	DFT-16QAM	M	Edge_1RB_Left	26.83	28.35	33	PASS
N41	15	15	DFT-16QAM	M	Edge_1RB_Right	26.21	27.73	33	PASS
N41	15	15	DFT-16QAM	M	Outer_Full	26.62	28.14	33	PASS
N41	15	15	DFT-16QAM	M	Inner_Full	26.6	28.12	33	PASS
N41	15	15	DFT-64QAM	M	Edge_1RB_Left	26.79	28.31	33	PASS
N41	15	15	DFT-64QAM	M	Edge_1RB_Right	26.38	27.9	33	PASS
N41	15	15	DFT-64QAM	M	Outer_Full	26.54	28.06	33	PASS
N41	15	15	DFT-64QAM	M	Inner_Full	26.51	28.03	33	PASS
N41	15	15	DFT-256QAM	M	Edge_1RB_Left	25.16	26.68	33	PASS
N41	15	15	DFT-256QAM	M	Edge_1RB_Right	26.26	27.78	33	PASS
N41	15	15	DFT-256QAM	M	Outer_Full	26.61	28.13	33	PASS
N41	15	15	DFT-256QAM	M	Inner_Full	26.55	28.07	33	PASS
N41	15	15	CP-QPSK	M	Edge_1RB_Left	26.09	27.61	33	PASS
N41	15	15	CP-QPSK	M	Edge_1RB_Right	25.91	27.43	33	PASS
N41	15	15	CP-QPSK	M	Outer_Full	26.11	27.63	33	PASS
N41	15	15	CP-QPSK	M	Inner_Full	26.15	27.67	33	PASS
N41	15	15	CP-16QAM	M	Edge_1RB_Left	26.15	27.67	33	PASS
N41	15	15	CP-16QAM	M	Edge_1RB_Right	25.99	27.51	33	PASS
N41	15	15	CP-16QAM	M	Outer_Full	26.1	27.62	33	PASS
N41	15	15	CP-16QAM	M	Inner_Full	26.14	27.66	33	PASS
N41	15	15	CP-64QAM	M	Edge_1RB_Left	26.28	27.8	33	PASS
N41	15	15	CP-64QAM	M	Edge_1RB_Right	26.12	27.64	33	PASS
N41	15	15	CP-64QAM	M	Outer_Full	26.05	27.57	33	PASS
N41	15	15	CP-64QAM	M	Inner_Full	23.92	25.44	33	PASS
N41	15	15	CP-256QAM	M	Edge_1RB_Left	26.14	27.66	33	PASS
N41	15	15	CP-256QAM	M	Edge_1RB_Right	25.95	27.47	33	PASS
N41	15	15	CP-256QAM	M	Outer_Full	26.08	27.6	33	PASS
N41	15	15	CP-256QAM	M	Inner_Full	26.15	27.67	33	PASS



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N41	15	15	DFT-PI2BPSK	H	Edge_1RB_Left	26.85	28.37	33	PASS
N41	15	15	DFT-PI2BPSK	H	Edge_1RB_Right	26.41	27.93	33	PASS
N41	15	15	DFT-PI2BPSK	H	Outer_Full	26.74	28.26	33	PASS
N41	15	15	DFT-PI2BPSK	H	Inner_Full	26.7	28.22	33	PASS
N41	15	15	DFT-QPSK	H	Edge_1RB_Left	26.65	28.17	33	PASS
N41	15	15	DFT-QPSK	H	Edge_1RB_Right	26.16	27.68	33	PASS
N41	15	15	DFT-QPSK	H	Outer_Full	26.62	28.14	33	PASS
N41	15	15	DFT-QPSK	H	Inner_Full	26.6	28.12	33	PASS
N41	15	15	DFT-16QAM	H	Edge_1RB_Left	23.65	25.17	33	PASS
N41	15	15	DFT-16QAM	H	Edge_1RB_Right	25.84	27.36	33	PASS
N41	15	15	DFT-16QAM	H	Outer_Full	26.42	27.94	33	PASS
N41	15	15	DFT-16QAM	H	Inner_Full	26.4	27.92	33	PASS
N41	15	15	DFT-64QAM	H	Edge_1RB_Left	23.62	25.14	33	PASS
N41	15	15	DFT-64QAM	H	Edge_1RB_Right	26.03	27.55	33	PASS
N41	15	15	DFT-64QAM	H	Outer_Full	26.33	27.85	33	PASS
N41	15	15	DFT-64QAM	H	Inner_Full	26.32	27.84	33	PASS
N41	15	15	DFT-256QAM	H	Edge_1RB_Left	23.65	25.17	33	PASS
N41	15	15	DFT-256QAM	H	Edge_1RB_Right	25.97	27.49	33	PASS
N41	15	15	DFT-256QAM	H	Outer_Full	26.45	27.97	33	PASS
N41	15	15	DFT-256QAM	H	Inner_Full	26.4	27.92	33	PASS
N41	15	15	CP-QPSK	H	Edge_1RB_Left	25.98	27.5	33	PASS
N41	15	15	CP-QPSK	H	Edge_1RB_Right	25.57	27.09	33	PASS
N41	15	15	CP-QPSK	H	Outer_Full	25.96	27.48	33	PASS
N41	15	15	CP-QPSK	H	Inner_Full	25.99	27.51	33	PASS
N41	15	15	CP-16QAM	H	Edge_1RB_Left	26.04	27.56	33	PASS
N41	15	15	CP-16QAM	H	Edge_1RB_Right	25.64	27.16	33	PASS
N41	15	15	CP-16QAM	H	Outer_Full	25.93	27.45	33	PASS
N41	15	15	CP-16QAM	H	Inner_Full	25.97	27.49	33	PASS
N41	15	15	CP-64QAM	H	Edge_1RB_Left	26.22	27.74	33	PASS
N41	15	15	CP-64QAM	H	Edge_1RB_Right	25.79	27.31	33	PASS
N41	15	15	CP-64QAM	H	Outer_Full	25.89	27.41	33	PASS
N41	15	15	CP-64QAM	H	Inner_Full	24.2	25.72	33	PASS
N41	15	15	CP-256QAM	H	Edge_1RB_Left	23.53	25.05	33	PASS
N41	15	15	CP-256QAM	H	Edge_1RB_Right	25.64	27.16	33	PASS
N41	15	15	CP-256QAM	H	Outer_Full	25.95	27.47	33	PASS
N41	15	15	CP-256QAM	H	Inner_Full	25.99	27.51	33	PASS
N41	15	20	DFT-PI2BPSK	L	Edge_1RB_Left	25.88	27.4	33	PASS



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N41	15	20	DFT-PI2BPSK	L	Edge_1RB_Right	26.27	27.79	33	PASS
N41	15	20	DFT-PI2BPSK	L	Outer_Full	26.86	28.38	33	PASS
N41	15	20	DFT-PI2BPSK	L	Inner_Full	26.63	28.15	33	PASS
N41	15	20	DFT-QPSK	L	Edge_1RB_Left	25.76	27.28	33	PASS
N41	15	20	DFT-QPSK	L	Edge_1RB_Right	26.22	27.74	33	PASS
N41	15	20	DFT-QPSK	L	Outer_Full	26.45	27.97	33	PASS
N41	15	20	DFT-QPSK	L	Inner_Full	26.47	27.99	33	PASS
N41	15	20	DFT-16QAM	L	Edge_1RB_Left	25.85	27.37	33	PASS
N41	15	20	DFT-16QAM	L	Edge_1RB_Right	25.99	27.51	33	PASS
N41	15	20	DFT-16QAM	L	Outer_Full	26.25	27.77	33	PASS
N41	15	20	DFT-16QAM	L	Inner_Full	26.23	27.75	33	PASS
N41	15	20	DFT-64QAM	L	Edge_1RB_Left	23.59	25.11	33	PASS
N41	15	20	DFT-64QAM	L	Edge_1RB_Right	26.21	27.73	33	PASS
N41	15	20	DFT-64QAM	L	Outer_Full	26.17	27.69	33	PASS
N41	15	20	DFT-64QAM	L	Inner_Full	26.18	27.7	33	PASS
N41	15	20	DFT-256QAM	L	Edge_1RB_Left	23.29	24.81	33	PASS
N41	15	20	DFT-256QAM	L	Edge_1RB_Right	26.14	27.66	33	PASS
N41	15	20	DFT-256QAM	L	Outer_Full	26.21	27.73	33	PASS
N41	15	20	DFT-256QAM	L	Inner_Full	26.22	27.74	33	PASS
N41	15	20	CP-QPSK	L	Edge_1RB_Left	25.09	26.61	33	PASS
N41	15	20	CP-QPSK	L	Edge_1RB_Right	25.85	27.37	33	PASS
N41	15	20	CP-QPSK	L	Outer_Full	25.79	27.31	33	PASS
N41	15	20	CP-QPSK	L	Inner_Full	25.85	27.37	33	PASS
N41	15	20	CP-16QAM	L	Edge_1RB_Left	25.14	26.66	33	PASS
N41	15	20	CP-16QAM	L	Edge_1RB_Right	25.89	27.41	33	PASS
N41	15	20	CP-16QAM	L	Outer_Full	25.77	27.29	33	PASS
N41	15	20	CP-16QAM	L	Inner_Full	25.84	27.36	33	PASS
N41	15	20	CP-64QAM	L	Edge_1RB_Left	25.35	26.87	33	PASS
N41	15	20	CP-64QAM	L	Edge_1RB_Right	26.14	27.66	33	PASS
N41	15	20	CP-64QAM	L	Outer_Full	25.76	27.28	33	PASS
N41	15	20	CP-64QAM	L	Inner_Full	25.83	27.35	33	PASS
N41	15	20	CP-256QAM	L	Edge_1RB_Left	25.12	26.64	33	PASS
N41	15	20	CP-256QAM	L	Edge_1RB_Right	25.91	27.43	33	PASS
N41	15	20	CP-256QAM	L	Outer_Full	25.75	27.27	33	PASS
N41	15	20	CP-256QAM	L	Inner_Full	25.83	27.35	33	PASS
N41	15	20	DFT-PI2BPSK	M	Edge_1RB_Left	27.01	28.53	33	PASS
N41	15	20	DFT-PI2BPSK	M	Edge_1RB_Right	26.64	28.16	33	PASS



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N41	15	20	DFT-PI2BPSK	M	Outer_Full	27.05	28.57	33	PASS
N41	15	20	DFT-PI2BPSK	M	Inner_Full	27	28.52	33	PASS
N41	15	20	DFT-QPSK	M	Edge_1RB_Left	26.72	28.24	33	PASS
N41	15	20	DFT-QPSK	M	Edge_1RB_Right	26.35	27.87	33	PASS
N41	15	20	DFT-QPSK	M	Outer_Full	26.86	28.38	33	PASS
N41	15	20	DFT-QPSK	M	Inner_Full	26.83	28.35	33	PASS
N41	15	20	DFT-16QAM	M	Edge_1RB_Left	26.35	27.87	33	PASS
N41	15	20	DFT-16QAM	M	Edge_1RB_Right	25.98	27.5	33	PASS
N41	15	20	DFT-16QAM	M	Outer_Full	26.56	28.08	33	PASS
N41	15	20	DFT-16QAM	M	Inner_Full	26.53	28.05	33	PASS
N41	15	20	DFT-64QAM	M	Edge_1RB_Left	26.61	28.13	33	PASS
N41	15	20	DFT-64QAM	M	Edge_1RB_Right	26.22	27.74	33	PASS
N41	15	20	DFT-64QAM	M	Outer_Full	26.55	28.07	33	PASS
N41	15	20	DFT-64QAM	M	Inner_Full	23.79	25.31	33	PASS
N41	15	20	DFT-256QAM	M	Edge_1RB_Left	26.52	28.04	33	PASS
N41	15	20	DFT-256QAM	M	Edge_1RB_Right	26.12	27.64	33	PASS
N41	15	20	DFT-256QAM	M	Outer_Full	26.56	28.08	33	PASS
N41	15	20	DFT-256QAM	M	Inner_Full	26.54	28.06	33	PASS
N41	15	20	CP-QPSK	M	Edge_1RB_Left	26.16	27.68	33	PASS
N41	15	20	CP-QPSK	M	Edge_1RB_Right	25.94	27.46	33	PASS
N41	15	20	CP-QPSK	M	Outer_Full	26.09	27.61	33	PASS
N41	15	20	CP-QPSK	M	Inner_Full	26.18	27.7	33	PASS
N41	15	20	CP-16QAM	M	Edge_1RB_Left	26.13	27.65	33	PASS
N41	15	20	CP-16QAM	M	Edge_1RB_Right	25.94	27.46	33	PASS
N41	15	20	CP-16QAM	M	Outer_Full	26.08	27.6	33	PASS
N41	15	20	CP-16QAM	M	Inner_Full	26.15	27.67	33	PASS
N41	15	20	CP-64QAM	M	Edge_1RB_Left	26.26	27.78	33	PASS
N41	15	20	CP-64QAM	M	Edge_1RB_Right	26.04	27.56	33	PASS
N41	15	20	CP-64QAM	M	Outer_Full	23.62	25.14	33	PASS
N41	15	20	CP-64QAM	M	Inner_Full	26.43	27.95	33	PASS
N41	15	20	CP-256QAM	M	Edge_1RB_Left	26.11	27.63	33	PASS
N41	15	20	CP-256QAM	M	Edge_1RB_Right	25.91	27.43	33	PASS
N41	15	20	CP-256QAM	M	Outer_Full	26.11	27.63	33	PASS
N41	15	20	CP-256QAM	M	Inner_Full	23.62	25.14	33	PASS
N41	15	20	DFT-PI2BPSK	H	Edge_1RB_Left	26.9	28.42	33	PASS
N41	15	20	DFT-PI2BPSK	H	Edge_1RB_Right	26.29	27.81	33	PASS
N41	15	20	DFT-PI2BPSK	H	Outer_Full	26.83	28.35	33	PASS



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N41	15	20	DFT-PI2BPSK	H	Inner_Full	26.79	28.31	33	PASS
N41	15	20	DFT-QPSK	H	Edge_1RB_Left	26.7	28.22	33	PASS
N41	15	20	DFT-QPSK	H	Edge_1RB_Right	26.04	27.56	33	PASS
N41	15	20	DFT-QPSK	H	Outer_Full	26.72	28.24	33	PASS
N41	15	20	DFT-QPSK	H	Inner_Full	26.72	28.24	33	PASS
N41	15	20	DFT-16QAM	H	Edge_1RB_Left	23.61	25.13	33	PASS
N41	15	20	DFT-16QAM	H	Edge_1RB_Right	25.74	27.26	33	PASS
N41	15	20	DFT-16QAM	H	Outer_Full	26.52	28.04	33	PASS
N41	15	20	DFT-16QAM	H	Inner_Full	26.5	28.02	33	PASS
N41	15	20	DFT-64QAM	H	Edge_1RB_Left	23.61	25.13	33	PASS
N41	15	20	DFT-64QAM	H	Edge_1RB_Right	23.38	24.9	33	PASS
N41	15	20	DFT-64QAM	H	Outer_Full	26.47	27.99	33	PASS
N41	15	20	DFT-64QAM	H	Inner_Full	23.65	25.17	33	PASS
N41	15	20	DFT-256QAM	H	Edge_1RB_Left	26.41	27.93	33	PASS
N41	15	20	DFT-256QAM	H	Edge_1RB_Right	25.2	26.72	33	PASS
N41	15	20	DFT-256QAM	H	Outer_Full	26.66	28.18	33	PASS
N41	15	20	DFT-256QAM	H	Inner_Full	23.59	25.11	33	PASS
N41	15	20	CP-QPSK	H	Edge_1RB_Left	26.85	28.37	33	PASS
N41	15	20	CP-QPSK	H	Edge_1RB_Right	24.66	26.18	33	PASS
N41	15	20	CP-QPSK	H	Outer_Full	25.01	26.53	33	PASS
N41	15	20	CP-QPSK	H	Inner_Full	26.27	27.79	33	PASS
N41	15	20	CP-16QAM	H	Edge_1RB_Left	25.69	27.21	33	PASS
N41	15	20	CP-16QAM	H	Edge_1RB_Right	24.91	26.43	33	PASS
N41	15	20	CP-16QAM	H	Outer_Full	24.61	26.13	33	PASS
N41	15	20	CP-16QAM	H	Inner_Full	26.21	27.73	33	PASS
N41	15	20	CP-64QAM	H	Edge_1RB_Left	26.43	27.95	33	PASS
N41	15	20	CP-64QAM	H	Edge_1RB_Right	25.94	27.46	33	PASS
N41	15	20	CP-64QAM	H	Outer_Full	26.13	27.65	33	PASS
N41	15	20	CP-64QAM	H	Inner_Full	26.27	27.79	33	PASS
N41	15	20	CP-256QAM	H	Edge_1RB_Left	25.32	26.84	33	PASS
N41	15	20	CP-256QAM	H	Edge_1RB_Right	25.47	26.99	33	PASS
N41	15	20	CP-256QAM	H	Outer_Full	26.26	27.78	33	PASS
N41	15	20	CP-256QAM	H	Inner_Full	26.33	27.85	33	PASS
N41	15	30	DFT-PI2BPSK	L	Edge_1RB_Left	26.23	27.75	33	PASS
N41	15	30	DFT-PI2BPSK	L	Edge_1RB_Right	26.65	28.17	33	PASS
N41	15	30	DFT-PI2BPSK	L	Outer_Full	27.03	28.55	33	PASS
N41	15	30	DFT-PI2BPSK	L	Inner_Full	26.99	28.51	33	PASS



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N41	15	30	DFT-QPSK	L	Edge_1RB_Left	26.08	27.6	33	PASS
N41	15	30	DFT-QPSK	L	Edge_1RB_Right	26.6	28.12	33	PASS
N41	15	30	DFT-QPSK	L	Outer_Full	26.78	28.3	33	PASS
N41	15	30	DFT-QPSK	L	Inner_Full	27.03	28.55	33	PASS
N41	15	30	DFT-16QAM	L	Edge_1RB_Left	25.72	27.24	33	PASS
N41	15	30	DFT-16QAM	L	Edge_1RB_Right	26.34	27.86	33	PASS
N41	15	30	DFT-16QAM	L	Outer_Full	26.53	28.05	33	PASS
N41	15	30	DFT-16QAM	L	Inner_Full	26.33	27.85	33	PASS
N41	15	30	DFT-64QAM	L	Edge_1RB_Left	25.91	27.43	33	PASS
N41	15	30	DFT-64QAM	L	Edge_1RB_Right	26.56	28.08	33	PASS
N41	15	30	DFT-64QAM	L	Outer_Full	26.47	27.99	33	PASS
N41	15	30	DFT-64QAM	L	Inner_Full	23.27	24.79	33	PASS
N41	15	30	DFT-256QAM	L	Edge_1RB_Left	25.79	27.31	33	PASS
N41	15	30	DFT-256QAM	L	Edge_1RB_Right	26.44	27.96	33	PASS
N41	15	30	DFT-256QAM	L	Outer_Full	26.44	27.96	33	PASS
N41	15	30	DFT-256QAM	L	Inner_Full	26.55	28.07	33	PASS
N41	15	30	CP-QPSK	L	Edge_1RB_Left	25.44	26.96	33	PASS
N41	15	30	CP-QPSK	L	Edge_1RB_Right	26.17	27.69	33	PASS
N41	15	30	CP-QPSK	L	Outer_Full	26.09	27.61	33	PASS
N41	15	30	CP-QPSK	L	Inner_Full	23.88	25.4	33	PASS
N41	15	30	CP-16QAM	L	Edge_1RB_Left	25.4	26.92	33	PASS
N41	15	30	CP-16QAM	L	Edge_1RB_Right	26.14	27.66	33	PASS
N41	15	30	CP-16QAM	L	Outer_Full	26.09	27.61	33	PASS
N41	15	30	CP-16QAM	L	Inner_Full	26.19	27.71	33	PASS
N41	15	30	CP-64QAM	L	Edge_1RB_Left	25.52	27.04	33	PASS
N41	15	30	CP-64QAM	L	Edge_1RB_Right	26.42	27.94	33	PASS
N41	15	30	CP-64QAM	L	Outer_Full	26.06	27.58	33	PASS
N41	15	30	CP-64QAM	L	Inner_Full	26.18	27.7	33	PASS
N41	15	30	CP-256QAM	L	Edge_1RB_Left	23.99	25.51	33	PASS
N41	15	30	CP-256QAM	L	Edge_1RB_Right	26.18	27.7	33	PASS
N41	15	30	CP-256QAM	L	Outer_Full	26.07	27.59	33	PASS
N41	15	30	CP-256QAM	L	Inner_Full	26.18	27.7	33	PASS
N41	15	30	DFT-PI2BPSK	M	Edge_1RB_Left	26.46	27.98	33	PASS
N41	15	30	DFT-PI2BPSK	M	Edge_1RB_Right	27.03	28.55	33	PASS
N41	15	30	DFT-PI2BPSK	M	Outer_Full	27.13	28.65	33	PASS
N41	15	30	DFT-PI2BPSK	M	Inner_Full	27.1	28.62	33	PASS
N41	15	30	DFT-QPSK	M	Edge_1RB_Left	27.08	28.6	33	PASS



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N41	15	30	DFT-QPSK	M	Edge_1RB_Right	26.89	28.41	33	PASS
N41	15	30	DFT-QPSK	M	Outer_Full	27.17	28.69	33	PASS
N41	15	30	DFT-QPSK	M	Inner_Full	26.98	28.5	33	PASS
N41	15	30	DFT-16QAM	M	Edge_1RB_Left	26.56	28.08	33	PASS
N41	15	30	DFT-16QAM	M	Edge_1RB_Right	26.36	27.88	33	PASS
N41	15	30	DFT-16QAM	M	Outer_Full	26.66	28.18	33	PASS
N41	15	30	DFT-16QAM	M	Inner_Full	26.66	28.18	33	PASS
N41	15	30	DFT-64QAM	M	Edge_1RB_Left	26.73	28.25	33	PASS
N41	15	30	DFT-64QAM	M	Edge_1RB_Right	26.5	28.02	33	PASS
N41	15	30	DFT-64QAM	M	Outer_Full	25.62	27.14	33	PASS
N41	15	30	DFT-64QAM	M	Inner_Full	23.74	25.26	33	PASS
N41	15	30	DFT-256QAM	M	Edge_1RB_Left	26.68	28.2	33	PASS
N41	15	30	DFT-256QAM	M	Edge_1RB_Right	26.44	27.96	33	PASS
N41	15	30	DFT-256QAM	M	Outer_Full	26.6	28.12	33	PASS
N41	15	30	DFT-256QAM	M	Inner_Full	23.77	25.29	33	PASS
N41	15	30	CP-QPSK	M	Edge_1RB_Left	26.94	28.46	33	PASS
N41	15	30	CP-QPSK	M	Edge_1RB_Right	26.18	27.7	33	PASS
N41	15	30	CP-QPSK	M	Outer_Full	26.22	27.74	33	PASS
N41	15	30	CP-QPSK	M	Inner_Full	26.44	27.96	33	PASS
N41	15	30	CP-16QAM	M	Edge_1RB_Left	26.33	27.85	33	PASS
N41	15	30	CP-16QAM	M	Edge_1RB_Right	26.19	27.71	33	PASS
N41	15	30	CP-16QAM	M	Outer_Full	26.2	27.72	33	PASS
N41	15	30	CP-16QAM	M	Inner_Full	26.22	27.74	33	PASS
N41	15	30	CP-64QAM	M	Edge_1RB_Left	26.38	27.9	33	PASS
N41	15	30	CP-64QAM	M	Edge_1RB_Right	26.3	27.82	33	PASS
N41	15	30	CP-64QAM	M	Outer_Full	26.19	27.71	33	PASS
N41	15	30	CP-64QAM	M	Inner_Full	26.22	27.74	33	PASS
N41	15	30	CP-256QAM	M	Edge_1RB_Left	24.9	26.42	33	PASS
N41	15	30	CP-256QAM	M	Edge_1RB_Right	26.42	27.94	33	PASS
N41	15	30	CP-256QAM	M	Outer_Full	26.24	27.76	33	PASS
N41	15	30	CP-256QAM	M	Inner_Full	26.25	27.77	33	PASS
N41	15	30	DFT-PI2BPSK	H	Edge_1RB_Left	27.09	28.61	33	PASS
N41	15	30	DFT-PI2BPSK	H	Edge_1RB_Right	26.56	28.08	33	PASS
N41	15	30	DFT-PI2BPSK	H	Outer_Full	26.39	27.91	33	PASS
N41	15	30	DFT-PI2BPSK	H	Inner_Full	27.01	28.53	33	PASS
N41	15	30	DFT-QPSK	H	Edge_1RB_Left	26.92	28.44	33	PASS
N41	15	30	DFT-QPSK	H	Edge_1RB_Right	26.41	27.93	33	PASS



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N41	15	30	DFT-QPSK	H	Outer_Full	26.85	28.37	33	PASS
N41	15	30	DFT-QPSK	H	Inner_Full	26.88	28.4	33	PASS
N41	15	30	DFT-16QAM	H	Edge_1RB_Left	26.5	28.02	33	PASS
N41	15	30	DFT-16QAM	H	Edge_1RB_Right	26.01	27.53	33	PASS
N41	15	30	DFT-16QAM	H	Outer_Full	26.57	28.09	33	PASS
N41	15	30	DFT-16QAM	H	Inner_Full	26.62	28.14	33	PASS
N41	15	30	DFT-64QAM	H	Edge_1RB_Left	26.65	28.17	33	PASS
N41	15	30	DFT-64QAM	H	Edge_1RB_Right	26.15	27.67	33	PASS
N41	15	30	DFT-64QAM	H	Outer_Full	26.5	28.02	33	PASS
N41	15	30	DFT-64QAM	H	Inner_Full	25.21	26.73	33	PASS
N41	15	30	DFT-256QAM	H	Edge_1RB_Left	26.58	28.1	33	PASS
N41	15	30	DFT-256QAM	H	Edge_1RB_Right	26.06	27.58	33	PASS
N41	15	30	DFT-256QAM	H	Outer_Full	26.49	28.01	33	PASS
N41	15	30	DFT-256QAM	H	Inner_Full	26.55	28.07	33	PASS
N41	15	30	CP-QPSK	H	Edge_1RB_Left	26.13	27.65	33	PASS
N41	15	30	CP-QPSK	H	Edge_1RB_Right	25.69	27.21	33	PASS
N41	15	30	CP-QPSK	H	Outer_Full	26.14	27.66	33	PASS
N41	15	30	CP-QPSK	H	Inner_Full	26.21	27.73	33	PASS
N41	15	30	CP-16QAM	H	Edge_1RB_Left	26.22	27.74	33	PASS
N41	15	30	CP-16QAM	H	Edge_1RB_Right	25.79	27.31	33	PASS
N41	15	30	CP-16QAM	H	Outer_Full	26.1	27.62	33	PASS
N41	15	30	CP-16QAM	H	Inner_Full	26.19	27.71	33	PASS
N41	15	30	CP-64QAM	H	Edge_1RB_Left	26.35	27.87	33	PASS
N41	15	30	CP-64QAM	H	Edge_1RB_Right	25.91	27.43	33	PASS
N41	15	30	CP-64QAM	H	Outer_Full	26.09	27.61	33	PASS
N41	15	30	CP-64QAM	H	Inner_Full	26.27	27.79	33	PASS
N41	15	30	CP-256QAM	H	Edge_1RB_Left	26.25	27.77	33	PASS
N41	15	30	CP-256QAM	H	Edge_1RB_Right	25.8	27.32	33	PASS
N41	15	30	CP-256QAM	H	Outer_Full	26.15	27.67	33	PASS
N41	15	30	CP-256QAM	H	Inner_Full	26.23	27.75	33	PASS
N41	15	40	DFT-PI2BPSK	L	Edge_1RB_Left	26.09	27.61	33	PASS
N41	15	40	DFT-PI2BPSK	L	Edge_1RB_Right	25.85	27.37	33	PASS
N41	15	40	DFT-PI2BPSK	L	Outer_Full	26.99	28.51	33	PASS
N41	15	40	DFT-PI2BPSK	L	Inner_Full	27	28.52	33	PASS
N41	15	40	DFT-QPSK	L	Edge_1RB_Left	25.96	27.48	33	PASS
N41	15	40	DFT-QPSK	L	Edge_1RB_Right	25.87	27.39	33	PASS
N41	15	40	DFT-QPSK	L	Outer_Full	26.69	28.21	33	PASS



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N41	15	40	DFT-QPSK	L	Inner_Full	23.76	25.28	33	PASS
N41	15	40	DFT-16QAM	L	Edge_1RB_Left	25.62	27.14	33	PASS
N41	15	40	DFT-16QAM	L	Edge_1RB_Right	25.69	27.21	33	PASS
N41	15	40	DFT-16QAM	L	Outer_Full	26.47	27.99	33	PASS
N41	15	40	DFT-16QAM	L	Inner_Full	24.95	26.47	33	PASS
N41	15	40	DFT-64QAM	L	Edge_1RB_Left	23.74	25.26	33	PASS
N41	15	40	DFT-64QAM	L	Edge_1RB_Right	25.99	27.51	33	PASS
N41	15	40	DFT-64QAM	L	Outer_Full	26.48	28	33	PASS
N41	15	40	DFT-64QAM	L	Inner_Full	26.71	28.23	33	PASS
N41	15	40	DFT-256QAM	L	Edge_1RB_Left	25.72	27.24	33	PASS
N41	15	40	DFT-256QAM	L	Edge_1RB_Right	25.83	27.35	33	PASS
N41	15	40	DFT-256QAM	L	Outer_Full	26.43	27.95	33	PASS
N41	15	40	DFT-256QAM	L	Inner_Full	26.67	28.19	33	PASS
N41	15	40	CP-QPSK	L	Edge_1RB_Left	25.22	26.74	33	PASS
N41	15	40	CP-QPSK	L	Edge_1RB_Right	25.57	27.09	33	PASS
N41	15	40	CP-QPSK	L	Outer_Full	26.13	27.65	33	PASS
N41	15	40	CP-QPSK	L	Inner_Full	26.36	27.88	33	PASS
N41	15	40	CP-16QAM	L	Edge_1RB_Left	25.29	26.81	33	PASS
N41	15	40	CP-16QAM	L	Edge_1RB_Right	25.59	27.11	33	PASS
N41	15	40	CP-16QAM	L	Outer_Full	26.1	27.62	33	PASS
N41	15	40	CP-16QAM	L	Inner_Full	26.33	27.85	33	PASS
N41	15	40	CP-64QAM	L	Edge_1RB_Left	23.74	25.26	33	PASS
N41	15	40	CP-64QAM	L	Edge_1RB_Right	23.74	25.26	33	PASS
N41	15	40	CP-64QAM	L	Outer_Full	26.13	27.65	33	PASS
N41	15	40	CP-64QAM	L	Inner_Full	26.35	27.87	33	PASS
N41	15	40	CP-256QAM	L	Edge_1RB_Left	25.3	26.82	33	PASS
N41	15	40	CP-256QAM	L	Edge_1RB_Right	23.76	25.28	33	PASS
N41	15	40	CP-256QAM	L	Outer_Full	23.75	25.27	33	PASS
N41	15	40	CP-256QAM	L	Inner_Full	26.41	27.93	33	PASS
N41	15	40	DFT-PI2BPSK	M	Edge_1RB_Left	27.02	28.54	33	PASS
N41	15	40	DFT-PI2BPSK	M	Edge_1RB_Right	26.86	28.38	33	PASS
N41	15	40	DFT-PI2BPSK	M	Outer_Full	27.32	28.84	33	PASS
N41	15	40	DFT-PI2BPSK	M	Inner_Full	23.26	24.78	33	PASS
N41	15	40	DFT-QPSK	M	Edge_1RB_Left	26.87	28.39	33	PASS
N41	15	40	DFT-QPSK	M	Edge_1RB_Right	26.68	28.2	33	PASS
N41	15	40	DFT-QPSK	M	Outer_Full	26.94	28.46	33	PASS
N41	15	40	DFT-QPSK	M	Inner_Full	23.67	25.19	33	PASS



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N41	15	40	DFT-16QAM	M	Edge_1RB_Left	26.5	28.02	33	PASS
N41	15	40	DFT-16QAM	M	Edge_1RB_Right	26.34	27.86	33	PASS
N41	15	40	DFT-16QAM	M	Outer_Full	26.65	28.17	33	PASS
N41	15	40	DFT-16QAM	M	Inner_Full	26.71	28.23	33	PASS
N41	15	40	DFT-64QAM	M	Edge_1RB_Left	23.69	25.21	33	PASS
N41	15	40	DFT-64QAM	M	Edge_1RB_Right	26.5	28.02	33	PASS
N41	15	40	DFT-64QAM	M	Outer_Full	26.62	28.14	33	PASS
N41	15	40	DFT-64QAM	M	Inner_Full	26.67	28.19	33	PASS
N41	15	40	DFT-256QAM	M	Edge_1RB_Left	25.98	27.5	33	PASS
N41	15	40	DFT-256QAM	M	Edge_1RB_Right	26.41	27.93	33	PASS
N41	15	40	DFT-256QAM	M	Outer_Full	26.6	28.12	33	PASS
N41	15	40	DFT-256QAM	M	Inner_Full	26.66	28.18	33	PASS
N41	15	40	CP-QPSK	M	Edge_1RB_Left	26.23	27.75	33	PASS
N41	15	40	CP-QPSK	M	Edge_1RB_Right	26.12	27.64	33	PASS
N41	15	40	CP-QPSK	M	Outer_Full	26.24	27.76	33	PASS
N41	15	40	CP-QPSK	M	Inner_Full	26.27	27.79	33	PASS
N41	15	40	CP-16QAM	M	Edge_1RB_Left	26.2	27.72	33	PASS
N41	15	40	CP-16QAM	M	Edge_1RB_Right	26.12	27.64	33	PASS
N41	15	40	CP-16QAM	M	Outer_Full	26.24	27.76	33	PASS
N41	15	40	CP-16QAM	M	Inner_Full	26.27	27.79	33	PASS
N41	15	40	CP-64QAM	M	Edge_1RB_Left	26.36	27.88	33	PASS
N41	15	40	CP-64QAM	M	Edge_1RB_Right	26.22	27.74	33	PASS
N41	15	40	CP-64QAM	M	Outer_Full	26.22	27.74	33	PASS
N41	15	40	CP-64QAM	M	Inner_Full	26.32	27.84	33	PASS
N41	15	40	CP-256QAM	M	Edge_1RB_Left	26.23	27.75	33	PASS
N41	15	40	CP-256QAM	M	Edge_1RB_Right	26.1	27.62	33	PASS
N41	15	40	CP-256QAM	M	Outer_Full	26.44	27.96	33	PASS
N41	15	40	CP-256QAM	M	Inner_Full	26.34	27.86	33	PASS
N41	15	40	DFT-PI2BPSK	H	Edge_1RB_Left	26.99	28.51	33	PASS
N41	15	40	DFT-PI2BPSK	H	Edge_1RB_Right	26.5	28.02	33	PASS
N41	15	40	DFT-PI2BPSK	H	Outer_Full	26.43	27.95	33	PASS
N41	15	40	DFT-PI2BPSK	H	Inner_Full	27.08	28.6	33	PASS
N41	15	40	DFT-QPSK	H	Edge_1RB_Left	26.79	28.31	33	PASS
N41	15	40	DFT-QPSK	H	Edge_1RB_Right	26.34	27.86	33	PASS
N41	15	40	DFT-QPSK	H	Outer_Full	26.87	28.39	33	PASS
N41	15	40	DFT-QPSK	H	Inner_Full	27	28.52	33	PASS
N41	15	40	DFT-16QAM	H	Edge_1RB_Left	26.36	27.88	33	PASS



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N41	15	40	DFT-16QAM	H	Edge_1RB_Right	25.97	27.49	33	PASS
N41	15	40	DFT-16QAM	H	Outer_Full	26.58	28.1	33	PASS
N41	15	40	DFT-16QAM	H	Inner_Full	26.71	28.23	33	PASS
N41	15	40	DFT-64QAM	H	Edge_1RB_Left	26.49	28.01	33	PASS
N41	15	40	DFT-64QAM	H	Edge_1RB_Right	26.11	27.63	33	PASS
N41	15	40	DFT-64QAM	H	Outer_Full	23.43	24.95	33	PASS
N41	15	40	DFT-64QAM	H	Inner_Full	26.71	28.23	33	PASS
N41	15	40	DFT-256QAM	H	Edge_1RB_Left	26.44	27.96	33	PASS
N41	15	40	DFT-256QAM	H	Edge_1RB_Right	26.06	27.58	33	PASS
N41	15	40	DFT-256QAM	H	Outer_Full	26.55	28.07	33	PASS
N41	15	40	DFT-256QAM	H	Inner_Full	26.69	28.21	33	PASS
N41	15	40	CP-QPSK	H	Edge_1RB_Left	25.97	27.49	33	PASS
N41	15	40	CP-QPSK	H	Edge_1RB_Right	25.64	27.16	33	PASS
N41	15	40	CP-QPSK	H	Outer_Full	26.19	27.71	33	PASS
N41	15	40	CP-QPSK	H	Inner_Full	26.31	27.83	33	PASS
N41	15	40	CP-16QAM	H	Edge_1RB_Left	26.1	27.62	33	PASS
N41	15	40	CP-16QAM	H	Edge_1RB_Right	25.73	27.25	33	PASS
N41	15	40	CP-16QAM	H	Outer_Full	26.17	27.69	33	PASS
N41	15	40	CP-16QAM	H	Inner_Full	26.29	27.81	33	PASS
N41	15	40	CP-64QAM	H	Edge_1RB_Left	26.2	27.72	33	PASS
N41	15	40	CP-64QAM	H	Edge_1RB_Right	25.88	27.4	33	PASS
N41	15	40	CP-64QAM	H	Outer_Full	26.16	27.68	33	PASS
N41	15	40	CP-64QAM	H	Inner_Full	24.53	26.05	33	PASS
N41	15	40	CP-256QAM	H	Edge_1RB_Left	26.03	27.55	33	PASS
N41	15	40	CP-256QAM	H	Edge_1RB_Right	25.67	27.19	33	PASS
N41	15	40	CP-256QAM	H	Outer_Full	26.15	27.67	33	PASS
N41	15	40	CP-256QAM	H	Inner_Full	26.28	27.8	33	PASS
N41	15	50	DFT-PI2BPSK	L	Edge_1RB_Left	26.19	27.71	33	PASS
N41	15	50	DFT-PI2BPSK	L	Edge_1RB_Right	26.32	27.84	33	PASS
N41	15	50	DFT-PI2BPSK	L	Outer_Full	26.64	28.16	33	PASS
N41	15	50	DFT-PI2BPSK	L	Inner_Full	27.1	28.62	33	PASS
N41	15	50	DFT-QPSK	L	Edge_1RB_Left	25.98	27.5	33	PASS
N41	15	50	DFT-QPSK	L	Edge_1RB_Right	26.26	27.78	33	PASS
N41	15	50	DFT-QPSK	L	Outer_Full	26.61	28.13	33	PASS
N41	15	50	DFT-QPSK	L	Inner_Full	26.75	28.27	33	PASS
N41	15	50	DFT-16QAM	L	Edge_1RB_Left	26.01	27.53	33	PASS
N41	15	50	DFT-16QAM	L	Edge_1RB_Right	26.07	27.59	33	PASS



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N41	15	50	DFT-16QAM	L	Outer_Full	26.48	28	33	PASS
N41	15	50	DFT-16QAM	L	Inner_Full	26.65	28.17	33	PASS
N41	15	50	DFT-64QAM	L	Edge_1RB_Left	25.96	27.48	33	PASS
N41	15	50	DFT-64QAM	L	Edge_1RB_Right	26.32	27.84	33	PASS
N41	15	50	DFT-64QAM	L	Outer_Full	26.42	27.94	33	PASS
N41	15	50	DFT-64QAM	L	Inner_Full	26.58	28.1	33	PASS
N41	15	50	DFT-256QAM	L	Edge_1RB_Left	23.09	24.61	33	PASS
N41	15	50	DFT-256QAM	L	Edge_1RB_Right	26.17	27.69	33	PASS
N41	15	50	DFT-256QAM	L	Outer_Full	26.42	27.94	33	PASS
N41	15	50	DFT-256QAM	L	Inner_Full	26.55	28.07	33	PASS
N41	15	50	CP-QPSK	L	Edge_1RB_Left	25.3	26.82	33	PASS
N41	15	50	CP-QPSK	L	Edge_1RB_Right	25.91	27.43	33	PASS
N41	15	50	CP-QPSK	L	Outer_Full	26.11	27.63	33	PASS
N41	15	50	CP-QPSK	L	Inner_Full	26.33	27.85	33	PASS
N41	15	50	CP-16QAM	L	Edge_1RB_Left	25.31	26.83	33	PASS
N41	15	50	CP-16QAM	L	Edge_1RB_Right	25.97	27.49	33	PASS
N41	15	50	CP-16QAM	L	Outer_Full	26.11	27.63	33	PASS
N41	15	50	CP-16QAM	L	Inner_Full	26.35	27.87	33	PASS
N41	15	50	CP-64QAM	L	Edge_1RB_Left	25.57	27.09	33	PASS
N41	15	50	CP-64QAM	L	Edge_1RB_Right	25.44	26.96	33	PASS
N41	15	50	CP-64QAM	L	Outer_Full	26.17	27.69	33	PASS
N41	15	50	CP-64QAM	L	Inner_Full	26.37	27.89	33	PASS
N41	15	50	CP-256QAM	L	Edge_1RB_Left	25.47	26.99	33	PASS
N41	15	50	CP-256QAM	L	Edge_1RB_Right	26.04	27.56	33	PASS
N41	15	50	CP-256QAM	L	Outer_Full	26.13	27.65	33	PASS
N41	15	50	CP-256QAM	L	Inner_Full	26.35	27.87	33	PASS
N41	15	50	DFT-PI2BPSK	M	Edge_1RB_Left	27.06	28.58	33	PASS
N41	15	50	DFT-PI2BPSK	M	Edge_1RB_Right	27.02	28.54	33	PASS
N41	15	50	DFT-PI2BPSK	M	Outer_Full	27.1	28.62	33	PASS
N41	15	50	DFT-PI2BPSK	M	Inner_Full	27.03	28.55	33	PASS
N41	15	50	DFT-QPSK	M	Edge_1RB_Left	26.84	28.36	33	PASS
N41	15	50	DFT-QPSK	M	Edge_1RB_Right	26.75	28.27	33	PASS
N41	15	50	DFT-QPSK	M	Outer_Full	26.92	28.44	33	PASS
N41	15	50	DFT-QPSK	M	Inner_Full	26.87	28.39	33	PASS
N41	15	50	DFT-16QAM	M	Edge_1RB_Left	26.45	27.97	33	PASS
N41	15	50	DFT-16QAM	M	Edge_1RB_Right	26.36	27.88	33	PASS
N41	15	50	DFT-16QAM	M	Outer_Full	26.62	28.14	33	PASS



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N41	15	50	DFT-16QAM	M	Inner_Full	26.59	28.11	33	PASS
N41	15	50	DFT-64QAM	M	Edge_1RB_Left	26.66	28.18	33	PASS
N41	15	50	DFT-64QAM	M	Edge_1RB_Right	26.51	28.03	33	PASS
N41	15	50	DFT-64QAM	M	Outer_Full	26.56	28.08	33	PASS
N41	15	50	DFT-64QAM	M	Inner_Full	24.76	26.28	33	PASS
N41	15	50	DFT-256QAM	M	Edge_1RB_Left	26.58	28.1	33	PASS
N41	15	50	DFT-256QAM	M	Edge_1RB_Right	26.43	27.95	33	PASS
N41	15	50	DFT-256QAM	M	Outer_Full	26.55	28.07	33	PASS
N41	15	50	DFT-256QAM	M	Inner_Full	26.51	28.03	33	PASS
N41	15	50	CP-QPSK	M	Edge_1RB_Left	26.24	27.76	33	PASS
N41	15	50	CP-QPSK	M	Edge_1RB_Right	26.15	27.67	33	PASS
N41	15	50	CP-QPSK	M	Outer_Full	26.18	27.7	33	PASS
N41	15	50	CP-QPSK	M	Inner_Full	26.23	27.75	33	PASS
N41	15	50	CP-16QAM	M	Edge_1RB_Left	26.12	27.64	33	PASS
N41	15	50	CP-16QAM	M	Edge_1RB_Right	26.17	27.69	33	PASS
N41	15	50	CP-16QAM	M	Outer_Full	26.16	27.68	33	PASS
N41	15	50	CP-16QAM	M	Inner_Full	26.22	27.74	33	PASS
N41	15	50	CP-64QAM	M	Edge_1RB_Left	26.08	27.6	33	PASS
N41	15	50	CP-64QAM	M	Edge_1RB_Right	26.09	27.61	33	PASS
N41	15	50	CP-64QAM	M	Outer_Full	26.16	27.68	33	PASS
N41	15	50	CP-64QAM	M	Inner_Full	26.21	27.73	33	PASS
N41	15	50	CP-256QAM	M	Edge_1RB_Left	26.15	27.67	33	PASS
N41	15	50	CP-256QAM	M	Edge_1RB_Right	26.12	27.64	33	PASS
N41	15	50	CP-256QAM	M	Outer_Full	26.15	27.67	33	PASS
N41	15	50	CP-256QAM	M	Inner_Full	26.2	27.72	33	PASS
N41	15	50	DFT-PI2BPSK	H	Edge_1RB_Left	26.16	27.68	33	PASS
N41	15	50	DFT-PI2BPSK	H	Edge_1RB_Right	26.57	28.09	33	PASS
N41	15	50	DFT-PI2BPSK	H	Outer_Full	26.87	28.39	33	PASS
N41	15	50	DFT-PI2BPSK	H	Inner_Full	26.92	28.44	33	PASS
N41	15	50	DFT-QPSK	H	Edge_1RB_Left	26.7	28.22	33	PASS
N41	15	50	DFT-QPSK	H	Edge_1RB_Right	26.32	27.84	33	PASS
N41	15	50	DFT-QPSK	H	Outer_Full	26.77	28.29	33	PASS
N41	15	50	DFT-QPSK	H	Inner_Full	26.8	28.32	33	PASS
N41	15	50	DFT-16QAM	H	Edge_1RB_Left	26.32	27.84	33	PASS
N41	15	50	DFT-16QAM	H	Edge_1RB_Right	26.05	27.57	33	PASS
N41	15	50	DFT-16QAM	H	Outer_Full	26.57	28.09	33	PASS
N41	15	50	DFT-16QAM	H	Inner_Full	26.62	28.14	33	PASS



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N41	15	50	DFT-64QAM	H	Edge_1RB_Left	26.55	28.07	33	PASS
N41	15	50	DFT-64QAM	H	Edge_1RB_Right	26.33	27.85	33	PASS
N41	15	50	DFT-64QAM	H	Outer_Full	26.54	28.06	33	PASS
N41	15	50	DFT-64QAM	H	Inner_Full	26.59	28.11	33	PASS
N41	15	50	DFT-256QAM	H	Edge_1RB_Left	26.41	27.93	33	PASS
N41	15	50	DFT-256QAM	H	Edge_1RB_Right	26.08	27.6	33	PASS
N41	15	50	DFT-256QAM	H	Outer_Full	26.68	28.2	33	PASS
N41	15	50	DFT-256QAM	H	Inner_Full	26.62	28.14	33	PASS
N41	15	50	CP-QPSK	H	Edge_1RB_Left	26.14	27.66	33	PASS
N41	15	50	CP-QPSK	H	Edge_1RB_Right	25.74	27.26	33	PASS
N41	15	50	CP-QPSK	H	Outer_Full	26.15	27.67	33	PASS
N41	15	50	CP-QPSK	H	Inner_Full	26.28	27.8	33	PASS
N41	15	50	CP-16QAM	H	Edge_1RB_Left	26.04	27.56	33	PASS
N41	15	50	CP-16QAM	H	Edge_1RB_Right	25.79	27.31	33	PASS
N41	15	50	CP-16QAM	H	Outer_Full	26.14	27.66	33	PASS
N41	15	50	CP-16QAM	H	Inner_Full	26.28	27.8	33	PASS
N41	15	50	CP-64QAM	H	Edge_1RB_Left	26.01	27.53	33	PASS
N41	15	50	CP-64QAM	H	Edge_1RB_Right	25.92	27.44	33	PASS
N41	15	50	CP-64QAM	H	Outer_Full	26.16	27.68	33	PASS
N41	15	50	CP-64QAM	H	Inner_Full	26.3	27.82	33	PASS
N41	15	50	CP-256QAM	H	Edge_1RB_Left	26.1	27.62	33	PASS
N41	15	50	CP-256QAM	H	Edge_1RB_Right	25.83	27.35	33	PASS
N41	15	50	CP-256QAM	H	Outer_Full	26.14	27.66	33	PASS
N41	15	50	CP-256QAM	H	Inner_Full	26.28	27.8	33	PASS
N41	30	10	DFT-PI2BPSK	L	Edge_1RB_Left	26.73	28.25	33	PASS
N41	30	10	DFT-PI2BPSK	L	Edge_1RB_Right	26.11	27.63	33	PASS
N41	30	10	DFT-PI2BPSK	L	Outer_Full	26.54	28.06	33	PASS
N41	30	10	DFT-PI2BPSK	L	Inner_Full	26.25	27.77	33	PASS
N41	30	10	DFT-QPSK	L	Edge_1RB_Left	26.06	27.58	33	PASS
N41	30	10	DFT-QPSK	L	Edge_1RB_Right	25.98	27.5	33	PASS
N41	30	10	DFT-QPSK	L	Outer_Full	26.23	27.75	33	PASS
N41	30	10	DFT-QPSK	L	Inner_Full	26.33	27.85	33	PASS
N41	30	10	DFT-16QAM	L	Edge_1RB_Left	25.7	27.22	33	PASS
N41	30	10	DFT-16QAM	L	Edge_1RB_Right	25.92	27.44	33	PASS
N41	30	10	DFT-16QAM	L	Outer_Full	25.99	27.51	33	PASS
N41	30	10	DFT-16QAM	L	Inner_Full	25.96	27.48	33	PASS
N41	30	10	DFT-64QAM	L	Edge_1RB_Left	25.94	27.46	33	PASS



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N41	30	10	DFT-64QAM	L	Edge_1RB_Right	25.86	27.38	33	PASS
N41	30	10	DFT-64QAM	L	Outer_Full	25.95	27.47	33	PASS
N41	30	10	DFT-64QAM	L	Inner_Full	26.01	27.53	33	PASS
N41	30	10	DFT-256QAM	L	Edge_1RB_Left	25.86	27.38	33	PASS
N41	30	10	DFT-256QAM	L	Edge_1RB_Right	25.69	27.21	33	PASS
N41	30	10	DFT-256QAM	L	Outer_Full	26.01	27.53	33	PASS
N41	30	10	DFT-256QAM	L	Inner_Full	25.98	27.5	33	PASS
N41	30	10	CP-QPSK	L	Edge_1RB_Left	26.06	27.58	33	PASS
N41	30	10	CP-QPSK	L	Edge_1RB_Right	26.21	27.73	33	PASS
N41	30	10	CP-QPSK	L	Outer_Full	25.65	27.17	33	PASS
N41	30	10	CP-QPSK	L	Inner_Full	25.65	27.17	33	PASS
N41	30	10	CP-16QAM	L	Edge_1RB_Left	25.99	27.51	33	PASS
N41	30	10	CP-16QAM	L	Edge_1RB_Right	25.48	27	33	PASS
N41	30	10	CP-16QAM	L	Outer_Full	25.62	27.14	33	PASS
N41	30	10	CP-16QAM	L	Inner_Full	25.69	27.21	33	PASS
N41	30	10	CP-64QAM	L	Edge_1RB_Left	25.66	27.18	33	PASS
N41	30	10	CP-64QAM	L	Edge_1RB_Right	25.6	27.12	33	PASS
N41	30	10	CP-64QAM	L	Outer_Full	25.63	27.15	33	PASS
N41	30	10	CP-64QAM	L	Inner_Full	25.97	27.49	33	PASS
N41	30	10	CP-256QAM	L	Edge_1RB_Left	25.44	26.96	33	PASS
N41	30	10	CP-256QAM	L	Edge_1RB_Right	25.39	26.91	33	PASS
N41	30	10	CP-256QAM	L	Outer_Full	26.04	27.56	33	PASS
N41	30	10	CP-256QAM	L	Inner_Full	25.66	27.18	33	PASS
N41	30	10	DFT-PI2BPSK	M	Edge_1RB_Left	27.21	28.73	33	PASS
N41	30	10	DFT-PI2BPSK	M	Edge_1RB_Right	27.01	28.53	33	PASS
N41	30	10	DFT-PI2BPSK	M	Outer_Full	27.33	28.85	33	PASS
N41	30	10	DFT-PI2BPSK	M	Inner_Full	27.21	28.73	33	PASS
N41	30	10	DFT-QPSK	M	Edge_1RB_Left	26.99	28.51	33	PASS
N41	30	10	DFT-QPSK	M	Edge_1RB_Right	26.8	28.32	33	PASS
N41	30	10	DFT-QPSK	M	Outer_Full	27.19	28.71	33	PASS
N41	30	10	DFT-QPSK	M	Inner_Full	27.05	28.57	33	PASS
N41	30	10	DFT-16QAM	M	Edge_1RB_Left	26.64	28.16	33	PASS
N41	30	10	DFT-16QAM	M	Edge_1RB_Right	26.53	28.05	33	PASS
N41	30	10	DFT-16QAM	M	Outer_Full	26.82	28.34	33	PASS
N41	30	10	DFT-16QAM	M	Inner_Full	26.75	28.27	33	PASS
N41	30	10	DFT-64QAM	M	Edge_1RB_Left	26.81	28.33	33	PASS
N41	30	10	DFT-64QAM	M	Edge_1RB_Right	26.58	28.1	33	PASS



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N41	30	10	DFT-64QAM	M	Outer_Full	26.76	28.28	33	PASS
N41	30	10	DFT-64QAM	M	Inner_Full	26.74	28.26	33	PASS
N41	30	10	DFT-256QAM	M	Edge_1RB_Left	26.74	28.26	33	PASS
N41	30	10	DFT-256QAM	M	Edge_1RB_Right	26.39	27.91	33	PASS
N41	30	10	DFT-256QAM	M	Outer_Full	26.81	28.33	33	PASS
N41	30	10	DFT-256QAM	M	Inner_Full	26.79	28.31	33	PASS
N41	30	10	CP-QPSK	M	Edge_1RB_Left	27.03	28.55	33	PASS
N41	30	10	CP-QPSK	M	Edge_1RB_Right	26.95	28.47	33	PASS
N41	30	10	CP-QPSK	M	Outer_Full	27.17	28.69	33	PASS
N41	30	10	CP-QPSK	M	Inner_Full	27.1	28.62	33	PASS
N41	30	10	CP-16QAM	M	Edge_1RB_Left	26.84	28.36	33	PASS
N41	30	10	CP-16QAM	M	Edge_1RB_Right	26.2	27.72	33	PASS
N41	30	10	CP-16QAM	M	Outer_Full	26.42	27.94	33	PASS
N41	30	10	CP-16QAM	M	Inner_Full	26.51	28.03	33	PASS
N41	30	10	CP-64QAM	M	Edge_1RB_Left	26.81	28.33	33	PASS
N41	30	10	CP-64QAM	M	Edge_1RB_Right	26.57	28.09	33	PASS
N41	30	10	CP-64QAM	M	Outer_Full	26.51	28.03	33	PASS
N41	30	10	CP-64QAM	M	Inner_Full	26.47	27.99	33	PASS
N41	30	10	CP-256QAM	M	Edge_1RB_Left	26.76	28.28	33	PASS
N41	30	10	CP-256QAM	M	Edge_1RB_Right	26.48	28	33	PASS
N41	30	10	CP-256QAM	M	Outer_Full	26.51	28.03	33	PASS
N41	30	10	CP-256QAM	M	Inner_Full	26.48	28	33	PASS
N41	30	10	DFT-PI2BPSK	H	Edge_1RB_Left	27.41	28.93	33	PASS
N41	30	10	DFT-PI2BPSK	H	Edge_1RB_Right	27.06	28.58	33	PASS
N41	30	10	DFT-PI2BPSK	H	Outer_Full	27.21	28.73	33	PASS
N41	30	10	DFT-PI2BPSK	H	Inner_Full	27.11	28.63	33	PASS
N41	30	10	DFT-QPSK	H	Edge_1RB_Left	27.24	28.76	33	PASS
N41	30	10	DFT-QPSK	H	Edge_1RB_Right	26.88	28.4	33	PASS
N41	30	10	DFT-QPSK	H	Outer_Full	27.05	28.57	33	PASS
N41	30	10	DFT-QPSK	H	Inner_Full	26.96	28.48	33	PASS
N41	30	10	DFT-16QAM	H	Edge_1RB_Left	26.84	28.36	33	PASS
N41	30	10	DFT-16QAM	H	Edge_1RB_Right	26.41	27.93	33	PASS
N41	30	10	DFT-16QAM	H	Outer_Full	26.75	28.27	33	PASS
N41	30	10	DFT-16QAM	H	Inner_Full	26.67	28.19	33	PASS
N41	30	10	DFT-64QAM	H	Edge_1RB_Left	26.95	28.47	33	PASS
N41	30	10	DFT-64QAM	H	Edge_1RB_Right	26.6	28.12	33	PASS
N41	30	10	DFT-64QAM	H	Outer_Full	26.68	28.2	33	PASS



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N41	30	10	DFT-64QAM	H	Inner_Full	26.59	28.11	33	PASS
N41	30	10	DFT-256QAM	H	Edge_1RB_Left	26.88	28.4	33	PASS
N41	30	10	DFT-256QAM	H	Edge_1RB_Right	26.43	27.95	33	PASS
N41	30	10	DFT-256QAM	H	Outer_Full	26.68	28.2	33	PASS
N41	30	10	DFT-256QAM	H	Inner_Full	26.62	28.14	33	PASS
N41	30	10	CP-QPSK	H	Edge_1RB_Left	26.71	28.23	33	PASS
N41	30	10	CP-QPSK	H	Edge_1RB_Right	26.2	27.72	33	PASS
N41	30	10	CP-QPSK	H	Outer_Full	26.29	27.81	33	PASS
N41	30	10	CP-QPSK	H	Inner_Full	26.33	27.85	33	PASS
N41	30	10	CP-16QAM	H	Edge_1RB_Left	26.58	28.1	33	PASS
N41	30	10	CP-16QAM	H	Edge_1RB_Right	26.18	27.7	33	PASS
N41	30	10	CP-16QAM	H	Outer_Full	26.27	27.79	33	PASS
N41	30	10	CP-16QAM	H	Inner_Full	26.32	27.84	33	PASS
N41	30	10	CP-64QAM	H	Edge_1RB_Left	26.63	28.15	33	PASS
N41	30	10	CP-64QAM	H	Edge_1RB_Right	26.22	27.74	33	PASS
N41	30	10	CP-64QAM	H	Outer_Full	26.28	27.8	33	PASS
N41	30	10	CP-64QAM	H	Inner_Full	26.33	27.85	33	PASS
N41	30	10	CP-256QAM	H	Edge_1RB_Left	26.55	28.07	33	PASS
N41	30	10	CP-256QAM	H	Edge_1RB_Right	26.23	27.75	33	PASS
N41	30	10	CP-256QAM	H	Outer_Full	26.33	27.85	33	PASS
N41	30	10	CP-256QAM	H	Inner_Full	26.32	27.84	33	PASS
N41	30	15	DFT-PI2BPSK	L	Edge_1RB_Left	26.32	27.84	33	PASS
N41	30	15	DFT-PI2BPSK	L	Edge_1RB_Right	26.78	28.3	33	PASS
N41	30	15	DFT-PI2BPSK	L	Outer_Full	26.64	28.16	33	PASS
N41	30	15	DFT-PI2BPSK	L	Inner_Full	26.58	28.1	33	PASS
N41	30	15	DFT-QPSK	L	Edge_1RB_Left	26.25	27.77	33	PASS
N41	30	15	DFT-QPSK	L	Edge_1RB_Right	26.71	28.23	33	PASS
N41	30	15	DFT-QPSK	L	Outer_Full	26.52	28.04	33	PASS
N41	30	15	DFT-QPSK	L	Inner_Full	26.45	27.97	33	PASS
N41	30	15	DFT-16QAM	L	Edge_1RB_Left	25.9	27.42	33	PASS
N41	30	15	DFT-16QAM	L	Edge_1RB_Right	26.51	28.03	33	PASS
N41	30	15	DFT-16QAM	L	Outer_Full	26.26	27.78	33	PASS
N41	30	15	DFT-16QAM	L	Inner_Full	26.24	27.76	33	PASS
N41	30	15	DFT-64QAM	L	Edge_1RB_Left	26.11	27.63	33	PASS
N41	30	15	DFT-64QAM	L	Edge_1RB_Right	26.52	28.04	33	PASS
N41	30	15	DFT-64QAM	L	Outer_Full	26.18	27.7	33	PASS
N41	30	15	DFT-64QAM	L	Inner_Full	26.15	27.67	33	PASS



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