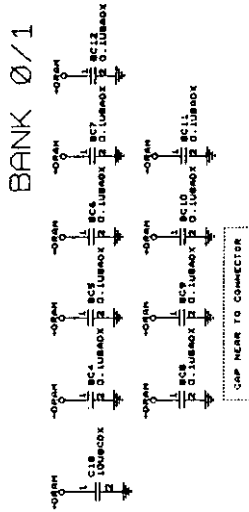
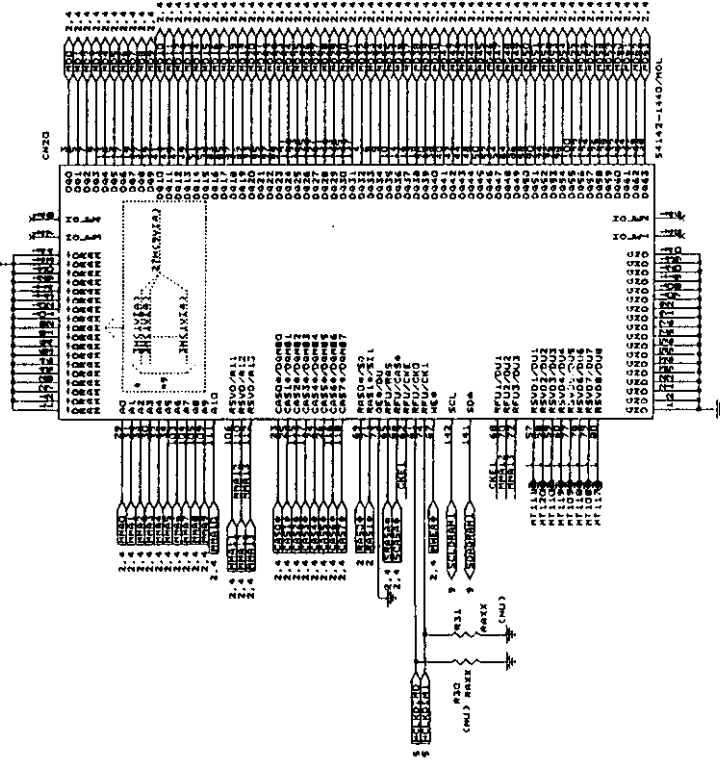


1-4 CLK GATE GATE

INPUT
-1V
OUTPUT
-0.5V

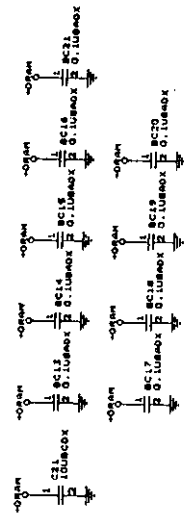


NU PARTS :
R30, R31



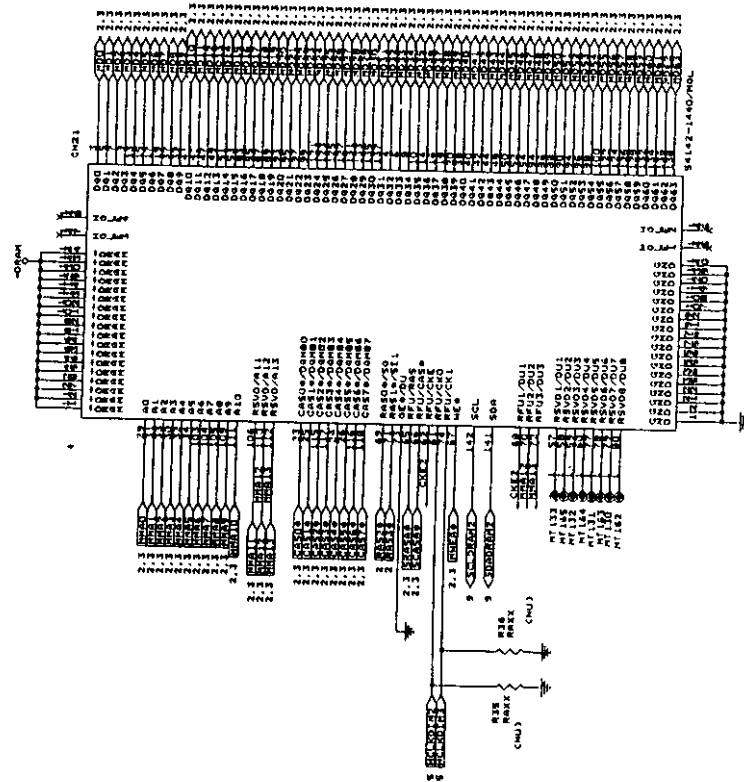
BANKS 0/1

2.3 CK2 CK1 CK2
R37 0u04



BANK 2/3

NU PARTS :
R35, R36

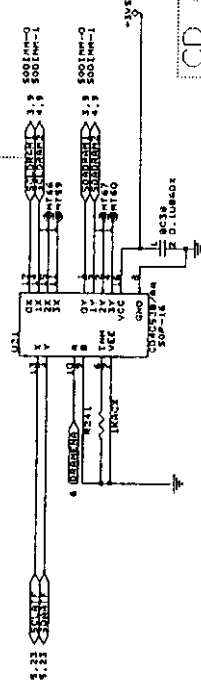


BANKS 2/3

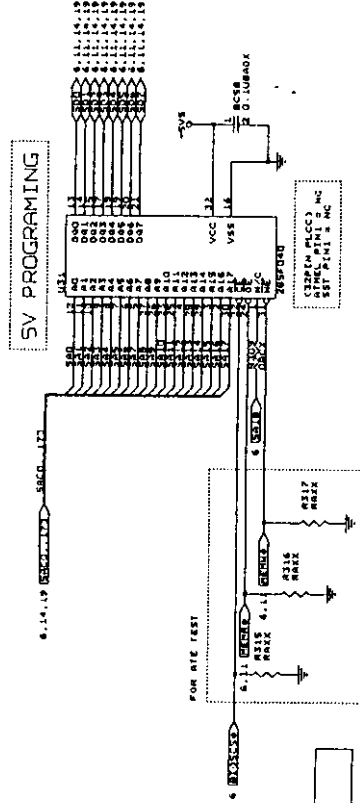
[illegible]

R255, R240, R242, R251

Read	SORAM-2	slot	EPROM	need	DRAMENA:H
Read <td>SORAM-1 <td>slot <td>EPROM <td>need <td>DRAMENA:L </td></td></td></td></td>	SORAM-1 <td>slot <td>EPROM <td>need <td>DRAMENA:L </td></td></td></td>	slot <td>EPROM <td>need <td>DRAMENA:L </td></td></td>	EPROM <td>need <td>DRAMENA:L </td></td>	need <td>DRAMENA:L </td>	DRAMENA:L



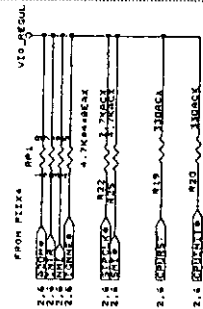
CD 4052 True table	
INH	B
0	0
0	0
0	1
0	1
0	1
1	1



NU PARTS :
R315,R316,R317

NOTE: BIOS Socket 1002-B32I1R

CPU BUS PULL RESISTOR



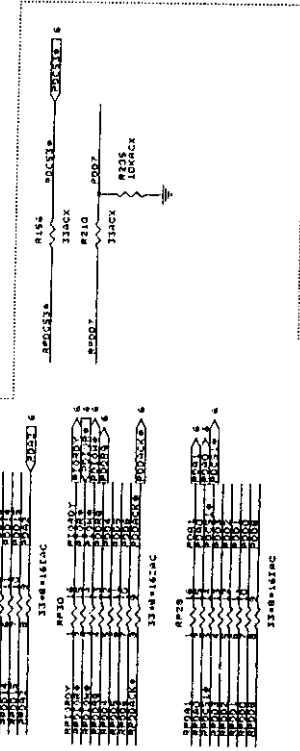
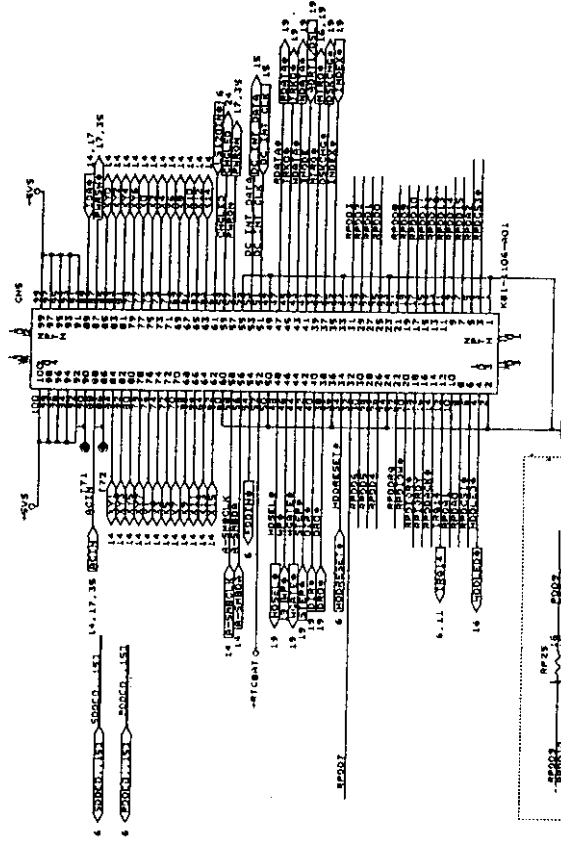
HDD connector
CONNECTION: 24V MAX 0.3A

POWER
1.5A
1.5-20 MHz
CONNECTION: 1.5A
CONNECTION: 1.5A

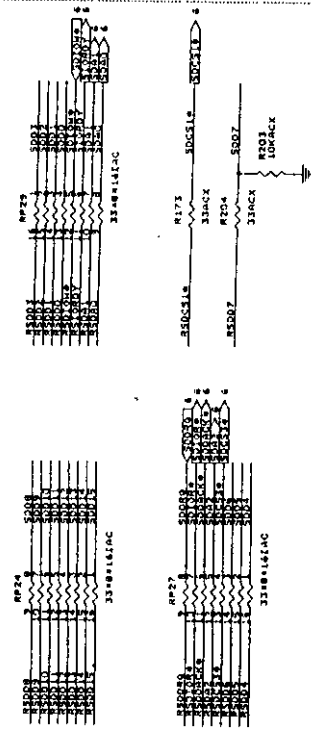
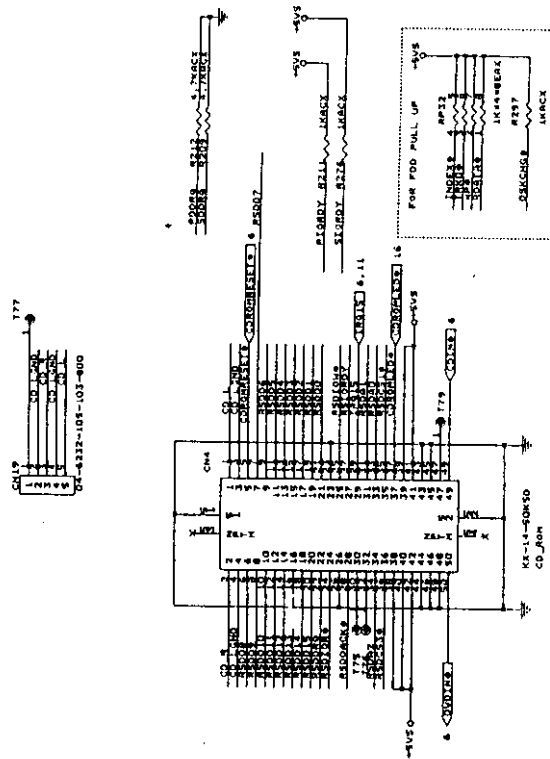
FDD connector
FDD MAX 0.75A
CONNECTION: MAX 1A

REV. 3.43) REV. 4
7/3 CANCEL 170 THEN CHANGE
TO 200A

DEVICE connector circuit to I/O Board



ABOVE PARTS CLOSE TO PIIX4

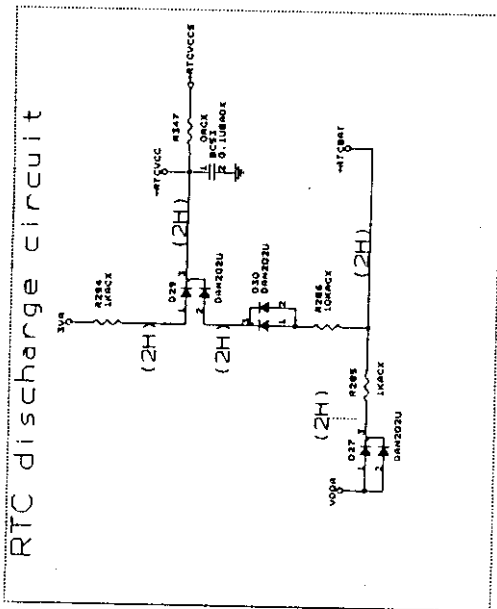


ABOVE PARTS CLOSE TO PIIX4

設計
87.8.3
機密文件

FILE
HDD-CD-ROM, FDD, K/B, GLIDE, MIO, RTC/CF
P/C NUMBER

REV. 0.4 AND REV. 0.5
8/2 800 834703 THEN CONNECT TO -RTCVCCS



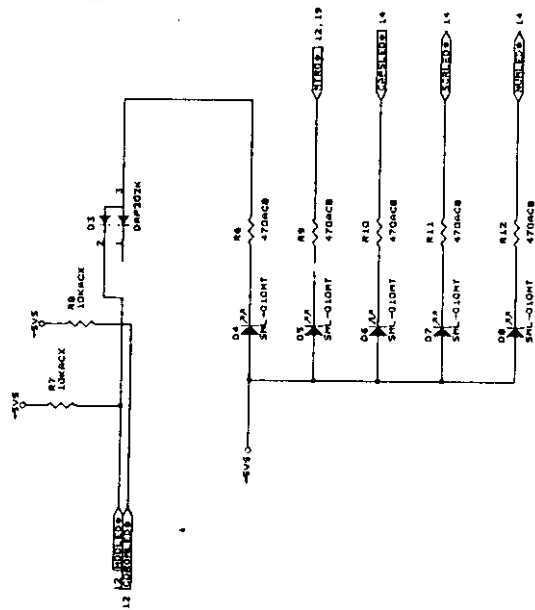
Under AC Adapter		
Power ON	Suspend	Power Off
Charge	Charge	Charge

Under Battery		
Power ON	Suspend	Power Off
Charge	Charge	Charge

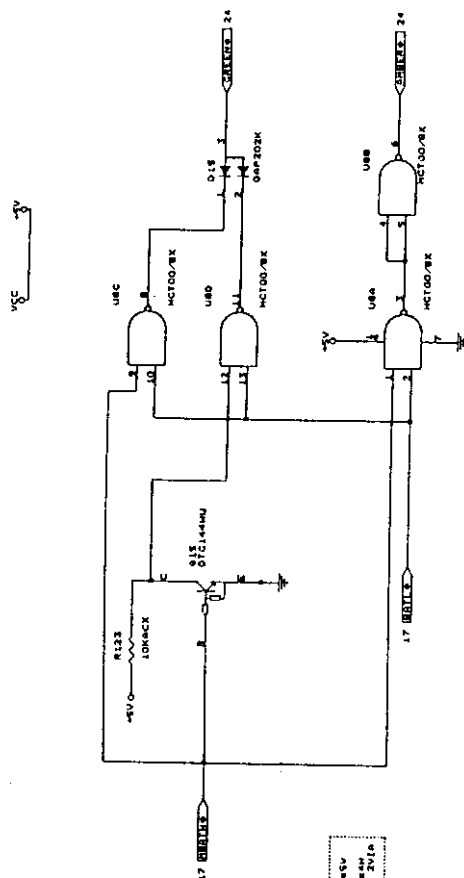
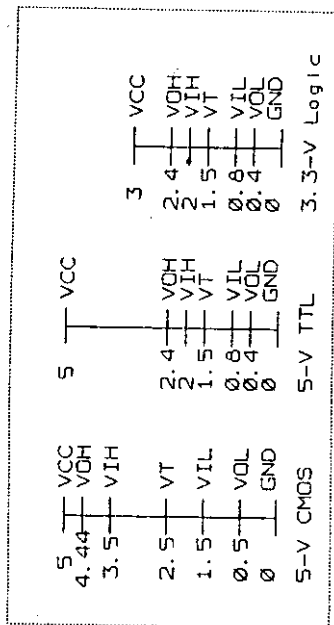
REV.0.4 REV.0.5
0/2 OUT DEXIS TRACE

LED STATUS

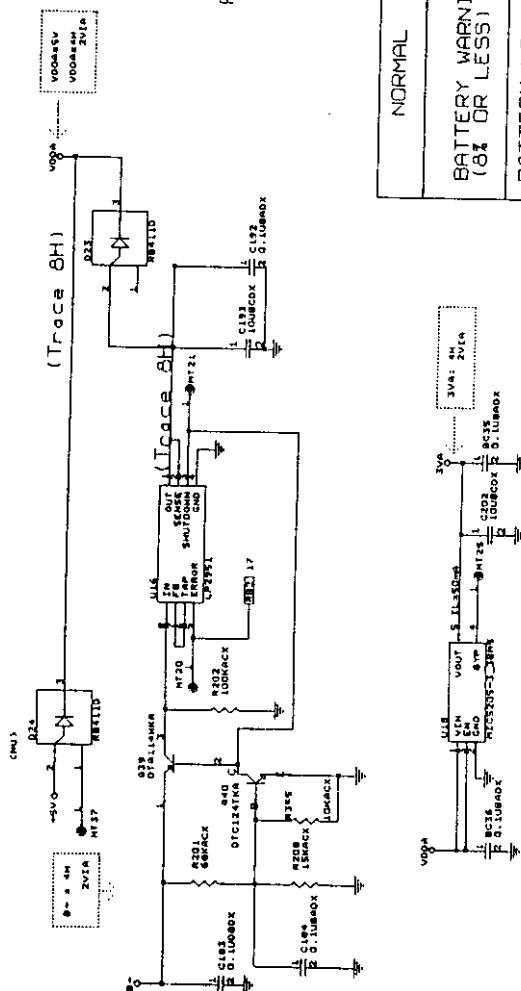
☐	☐	☐	☐	☐	
HDD&CDROM	FDD	Caps	Scr	Num	



RB4110	WT 150.24V/0.30C/2.10mm
Forward voltage	VF=1.0V/IF=500mA



POWER STATUS LED TRUTH TABLE :

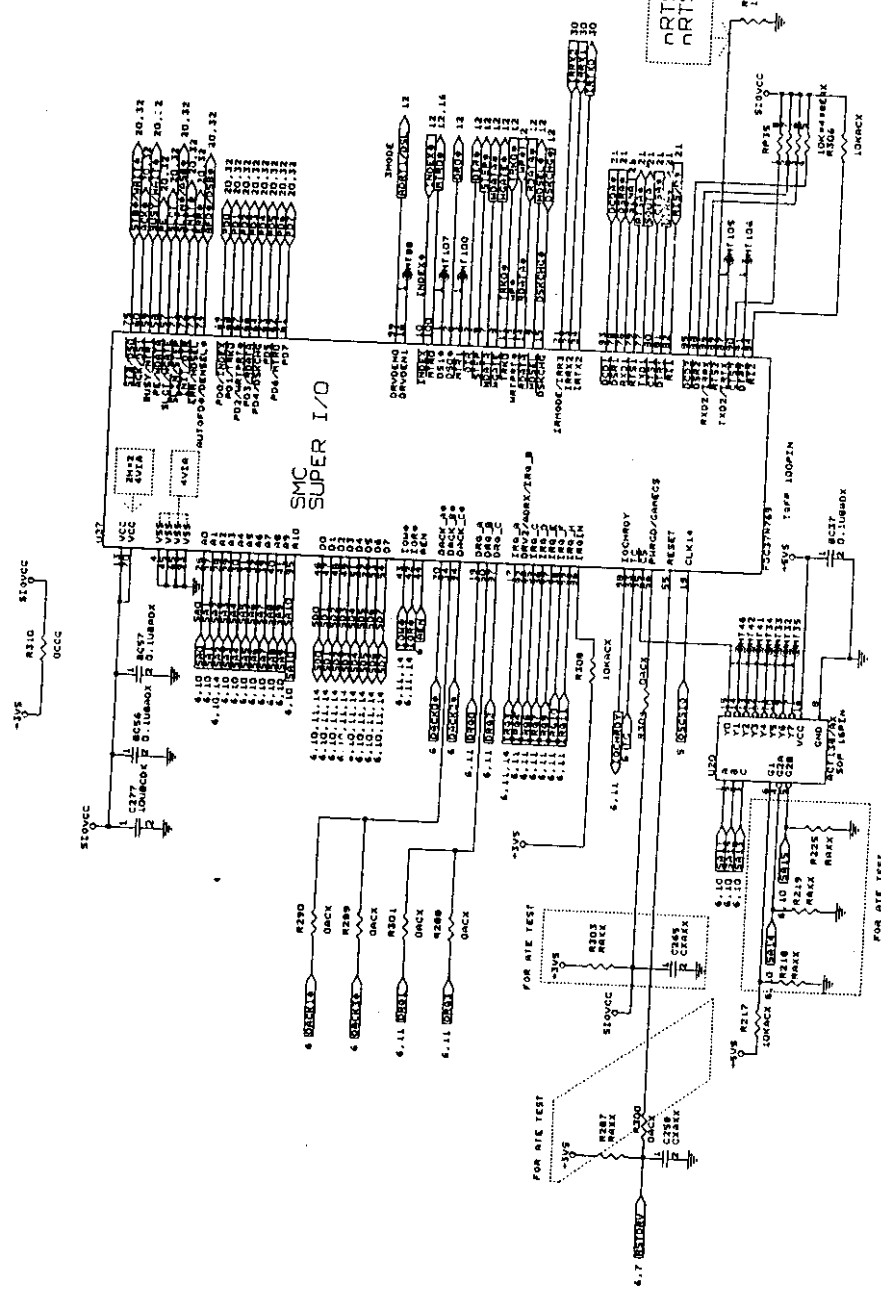


UN PARTS:

	UNDER AC			GREEN#	AMBER#
	MBATW#	BATL#			
NORMAL	1	1		0	1
BATTERY WARNING (8% OR LESS)	1	1		0	1
BATTERY LOW (3% OR LESS)	1	1		0	1
	UNDER AC			GREEN#	AMBER#
	MBATW#	BATL#			
NORMAL	1	1		0	1
BATTERY WARNING (8% OR LESS)	0	1		0	0
BATTERY LOW (3% OR LESS)	1	0		1	0

設計處
87.8.19
機密文件

FILE	FIC AMCH	FIC POWER
1	1	1
2	2	2
3	3	3
4	4	4
5	5	5
6	6	6
7	7	7
8	8	8
9	9	9
10	10	10
11	11	11
12	12	12
13	13	13
14	14	14
15	15	15
16	16	16
17	17	17
18	18	18
19	19	19
20	20	20
21	21	21
22	22	22
23	23	23
24	24	24
25	25	25
26	26	26
27	27	27
28	28	28
29	29	29
30	30	30
31	31	31
32	32	32
33	33	33
34	34	34
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36	36	36
37	37	37
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64	64	64
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83	83	83
84	84	84
85	85	85
86	86	86
87	87	87
88	88	88
89	89	89
90	90	90
91	91	91
92	92	92
93	93	93
94	94	94
95	95	95
96	96	96
97	97	97
98	98	98
99	99	99
100	100	100

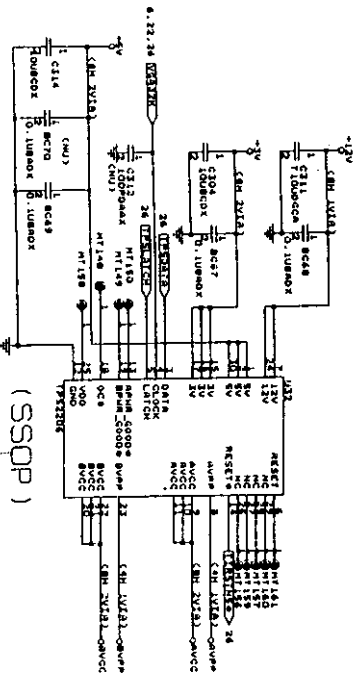
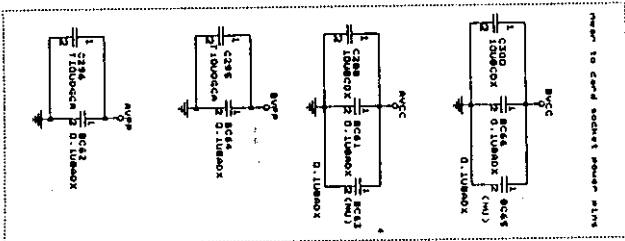


NU PARTS :

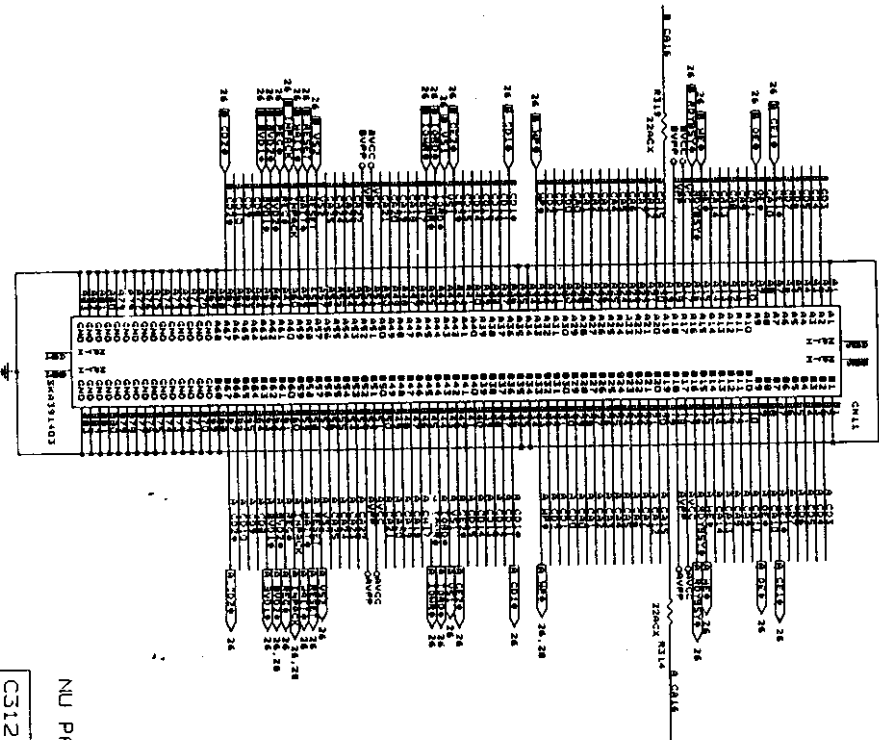
R289, R288, R287, C258, R303, C265, R218, R219, R225

87.8.10
機密文件

PEC NUMBER
SUPER I/O 30057C6460PLV

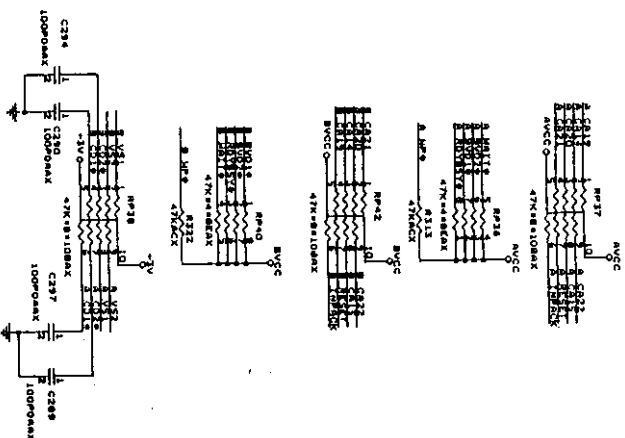


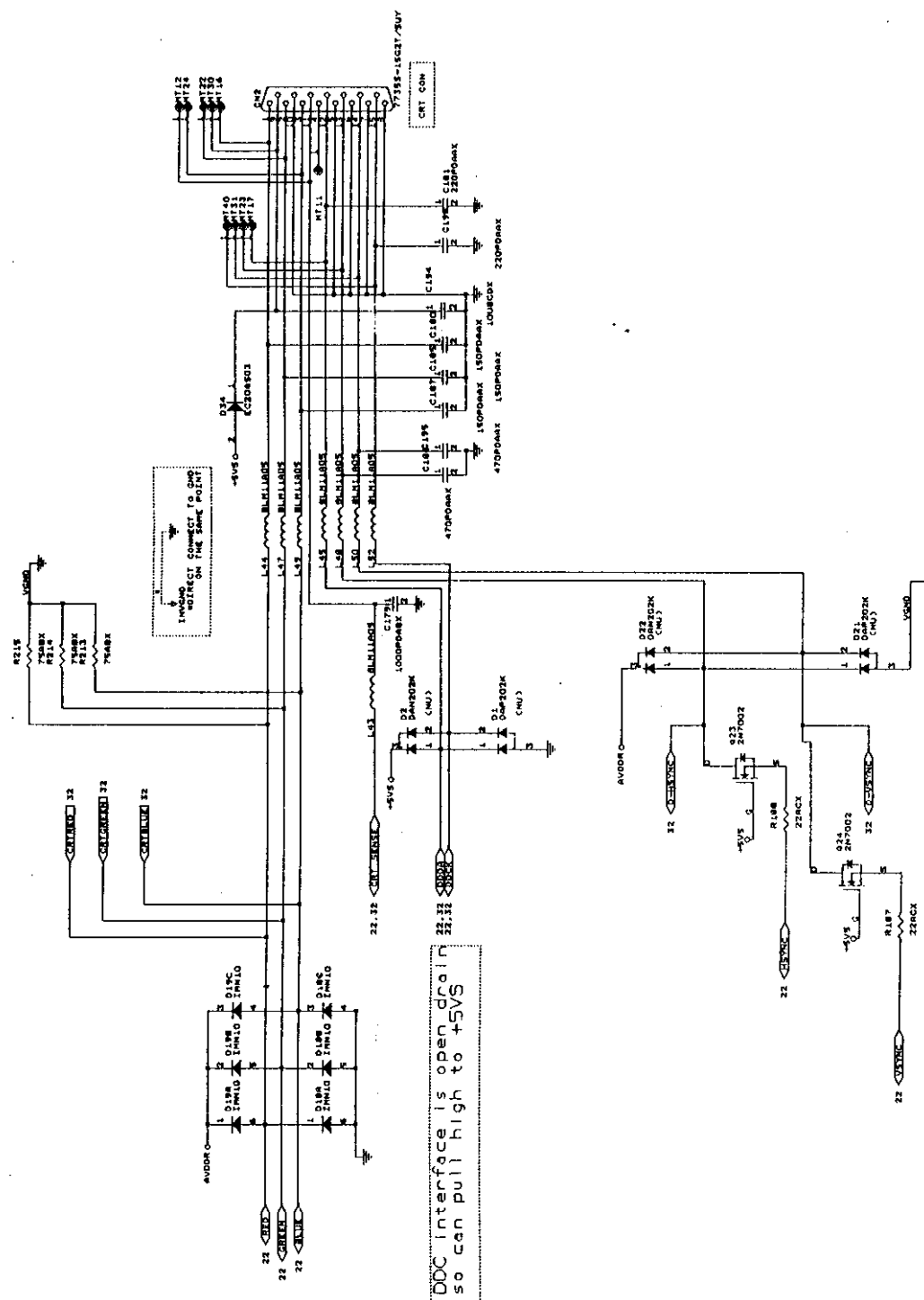
U21 use SSDP package



NU PARTS :

C312



REV. O. 4 22> REV. O. 5
1/2 PL CHANGE TO 034<EC204503>

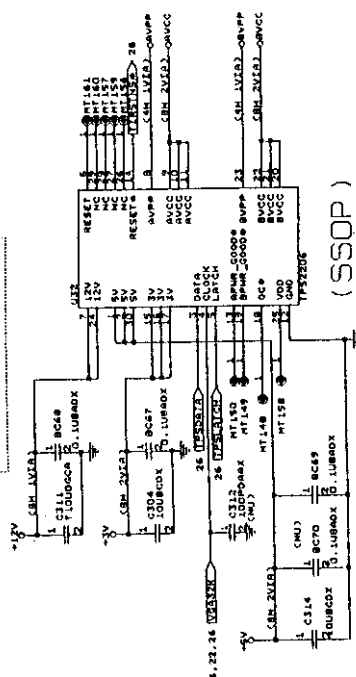
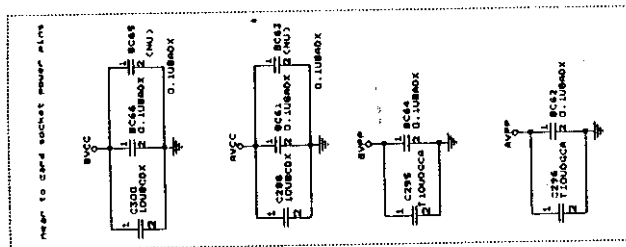
DDC interface is open drain so can pull high to +5VS

NO DIRECT CONTACT TO GAO
THE SAME
INVESTIGATING
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QY
QZ
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ZD
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ZH
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ZO
ZP
ZQ
ZR
ZS
ZT
ZU
ZV
ZW
ZX
ZY
ZZ

CRT COM

	PIC AMBER	
	VGA CRT CONNECTOR	
	Issue Document Number:	REV

6/17 C298,C296,C111 CHANCE TO YOUNGCAH



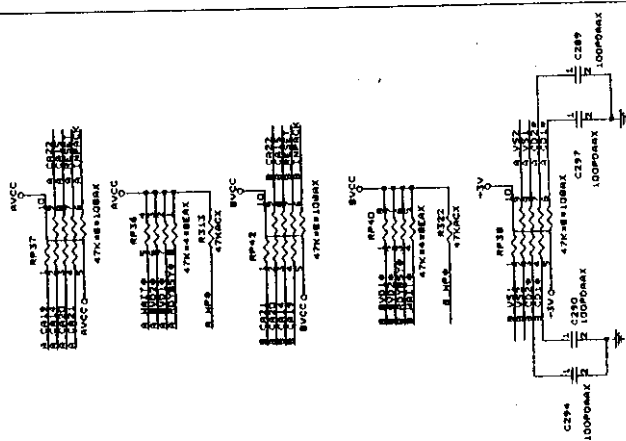
(2025)

U21 use 550P package

26. 20 8 2846 251 8 CACG 257
26 8 CACG 03 8 CACG 153 26
26 8 CACG 03 8 CACG 257
26 8 CACG 03 8 CACG 153 26

NU PARTS :

C312



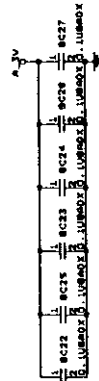
FIC AMOER

can't miss socket!

can't miss socket!

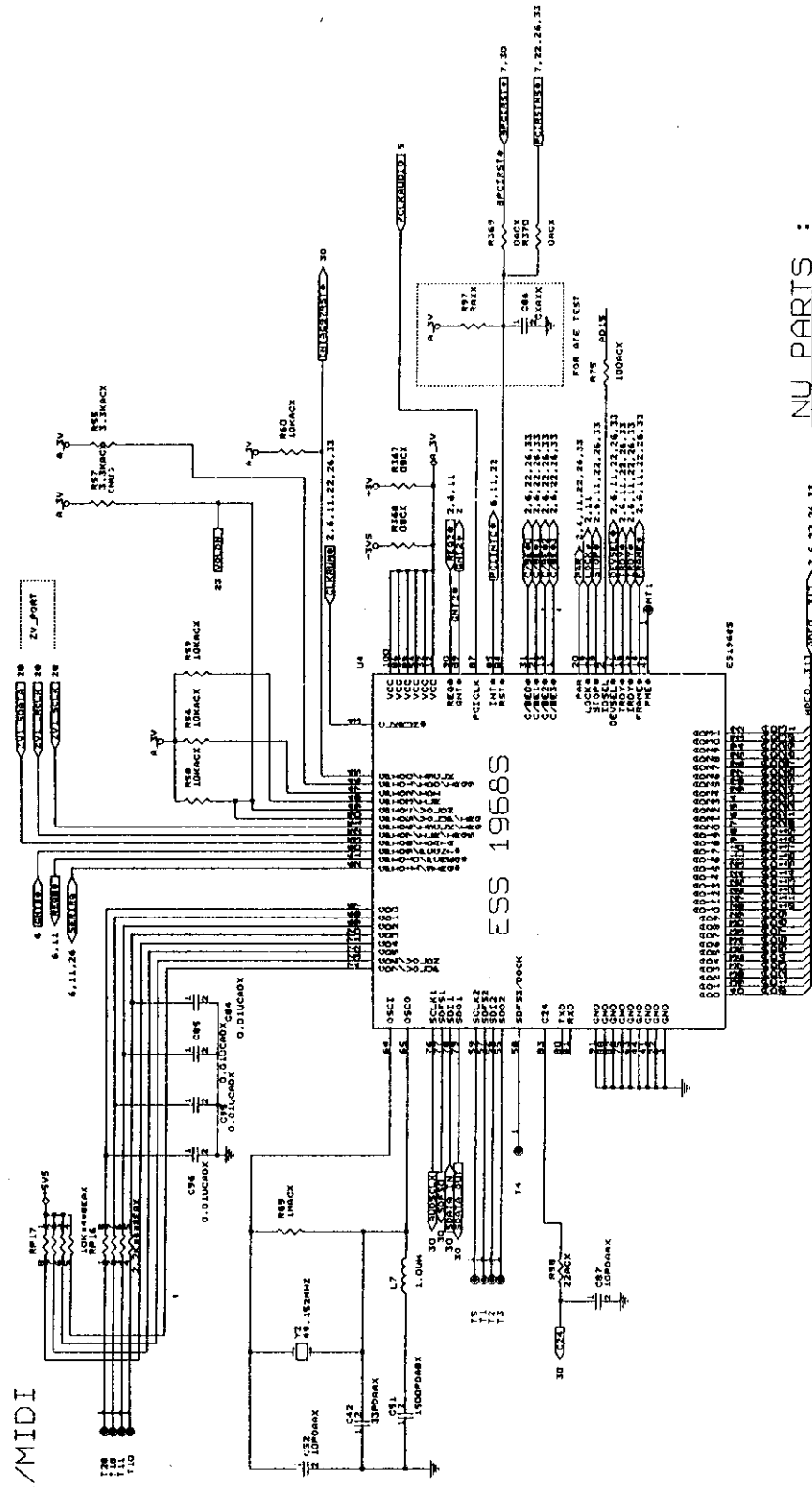
can't miss socket!

can't miss socket!



JOYSTICK/MIDI

REV. 0.1 REV. 0.2 MODIFICATION
 6/1 V2 CHANGE TYPE THEN CANCEL RT4,RT5
 REV.0.4 REV.0.5
 8/2 RT5 CHANGE TO NU PART
 8/2 R97,R98,R99,R97,R98,R99,LOC100...1,R97 CONNECT TO R23V
 8/2 LOC100...1 ADD R338,R337 TO -3VS,-3V
 8/2 LOC100...1 ADD R338,R337 TO -3VS,-3V
 8/2 LOC100...1 ADD R338,R337 TO -3VS,-3V
 8/2 LOC100...1 ADD R338,R337 TO -3VS,-3V



ESS 1968S

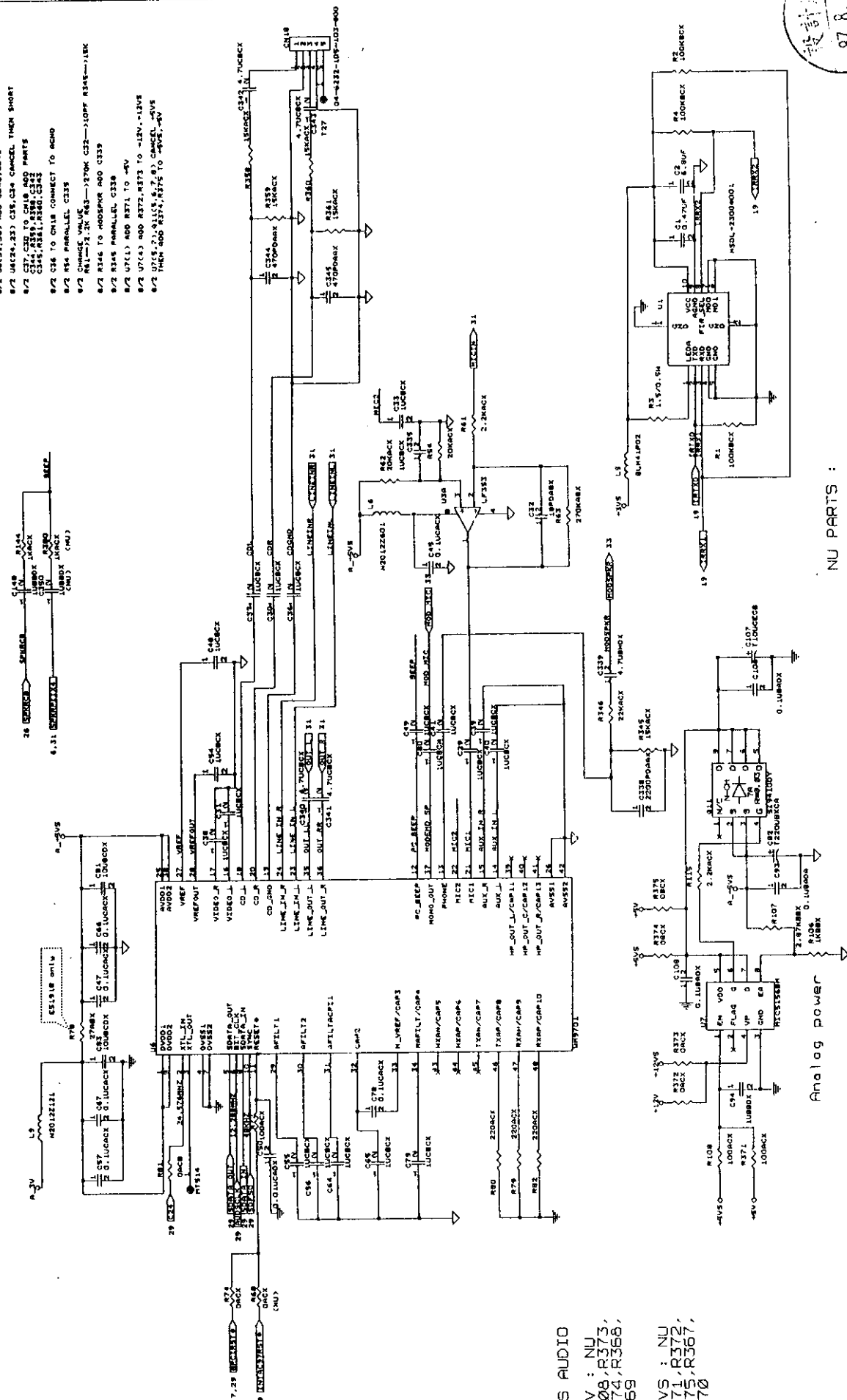
NU PARTS :
 R97, C86, R57



PIC NUMBER	MOESTRO 2
FILE	
REVISION NUMBER	
DATE	

REV. 2 -> REV. 3
6/17 HOOPNR ADD R346, R348 22K
REV. 0.4 383 REV. 0.5

6/2 C350, R380 CHANGE TO NU PARTS
6/2 L9 CONNECT TO A-3V
6/2 C59, C58, R73, Y1 CANCEL
6/2 U63N, 361 ADD C340, C341
6/2 U63N, 361 C35, C36 CANCEL THEN SHORT
C34, C39, R381, R382 PARTS
C345, R381, R380, C343
6/2 C36 TO CN18 CONNECT TO ACHO
L985X (NU)
6/2 R54 PARALLEL C335
6/2 CHANGE VALUE
R81 -> 2.2K R63 -> 270K C32 -> 100PF R346 -> 15K
6/2 R346 TO HOOPNR ADD C339
6/2 R346 PARALLEL C339
6/2 U7(1) ADD R371 TO -12V, -12V5
6/2 U7(4) ADD R372, R373 TO -12V, -12V5
6/2 U7(5, 7, 8) CANCEL -4V5
THEN ADD R374, R375 TO -4V5, -4V



NU PARTS :

R78, C59, C58, Y1, R73
R68, C78, C79, R80
R79, R82, C350, R360

Analog power

ESS AUDIO

+5V : NU
R108, R373,
R374, R368,
R369

+5V5 : NU
R371, R372,
R375, R367,
R370

設計 87.8.19 機密文件
PIC AMBER
Garnet audio board M27 Cores
Size DOCUMENT NUMBER
240075C.504 (FOR VER.0.4)
REV

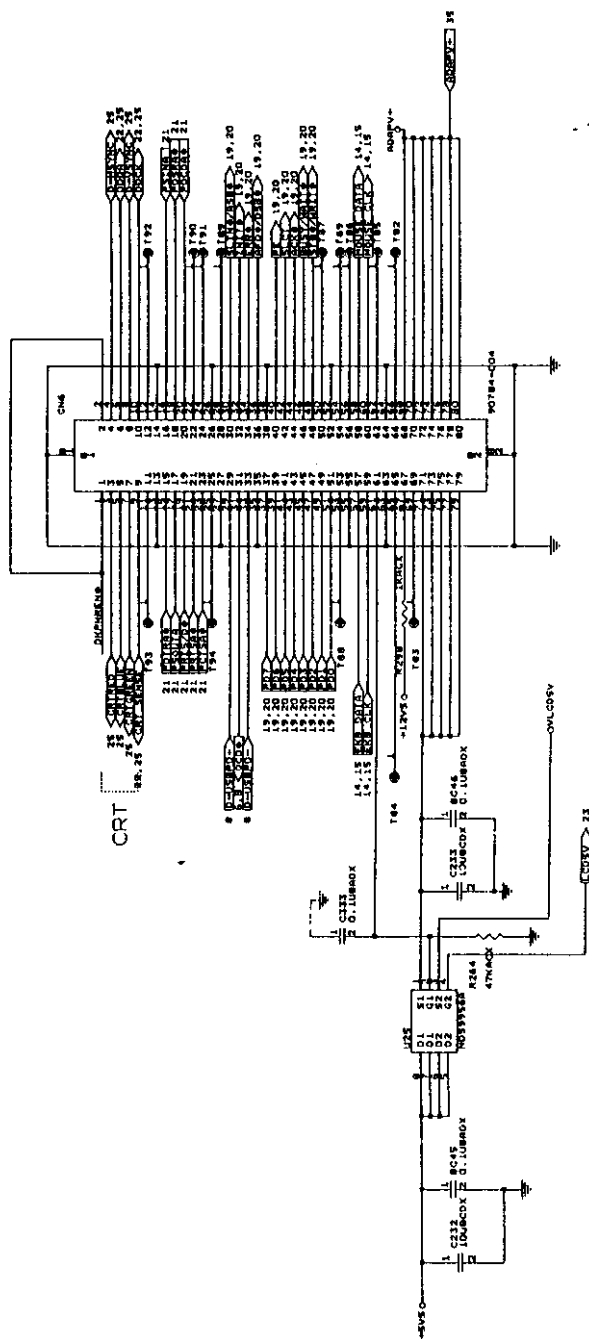
REVO.2 ---> REV0.3

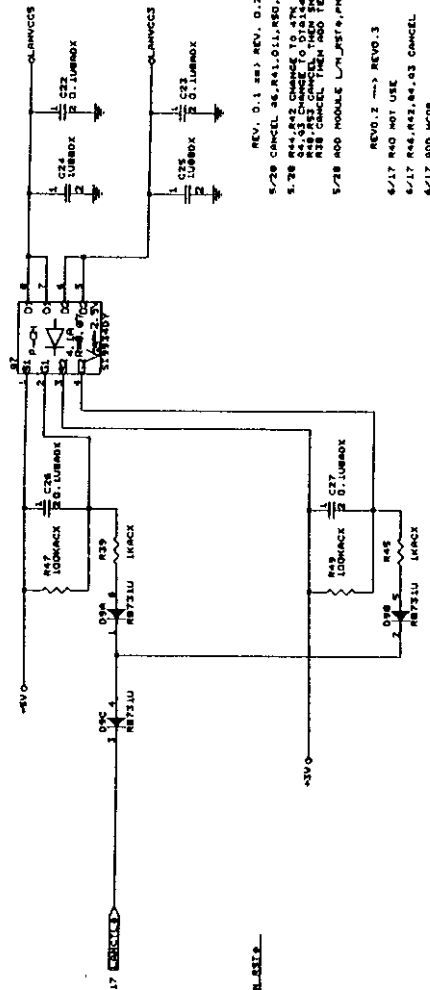
5/17 U25 P1A2 000 0.1UF TO GND

5/17 U25 P1A3 CHANGE TO VLCD5V

U25 P1A4 CHANGE TO LCD5V

5/17 GND P1N7 L.73, 75, 77, 79 SHORT



MODEM/LAN
CONNECTOR

REV. 0.1 xm) REV. 0.2 MODIFICATION

5/20 CANCEL 36,R41,011,R50,R52,010B,05 R43,U2

5 20 R44,R42 CANCEL TO 47M

04 93 CANCEL TO 074100M

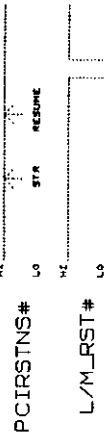
R48 R43 CANCEL THEN SHORT

R38 CANCEL THEN ADD TEST POINT R170,M171

5/28 ADD MODULE W4,R57a,P4C8

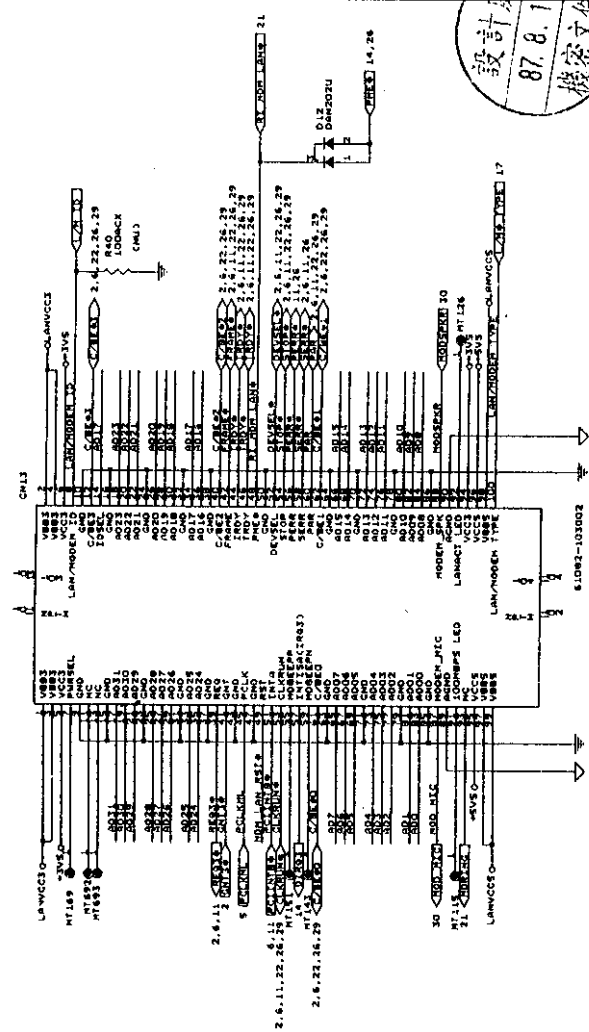
REVO.2 --> REVO.3
6/17 R40 NOT USE
6/17 R46,R47,R48 CANCEL
6/17 ADD MCOB

STR RESET TIMING



LAN/MODEM POWER CONTROL TRUTH TABLE

PHON	STAR	MAKEUP	ACIM	LYNE TYPE	LANCILE
1	1	0	1	1	0
1	0	0	1	1	0
0	1	X	1	1	1
1	0	1	1	1	1
1	1	0	0	1	0
1	0	1	0	1	1
0	1	X	0	1	1
1	1	0	1	0	0
1	0	0	1	0	0
0	1	X	1	0	1
1	0	1	1	0	1
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0	1	X	0	0	1
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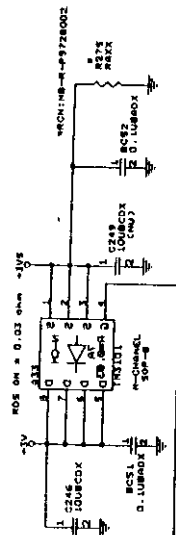
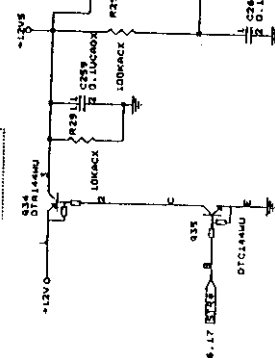
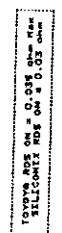
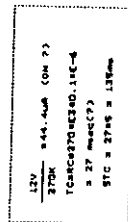
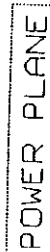


設計處
87.8.19
機密文件

	ON	SIR	STD	ME OFF
+5V	5	5	0	0
+5VS	5	0	0	0
+12V	12	12	0	0
+12VS	12	0	0	0
+3V	3.3	3.3	0	0
+3VS	3.3	0	0	0
VDDA	5	5	5	0
BM+	10~20	10~20	0	0
B+	10~20	10~20	10~20	0
VIO_REGUL	2.5	0	0	0
+DRAM	3.3	3.3	0	0
3VA	3.3	3.3	3.3	0
VCCSUS	3.3	3.3	3.3	0
+R1CVCS	3.3	3.3	3.3	3.3

072 936(4) 800 R378.C349 THEN C349 IS MU PARTY

R295, R275, C349

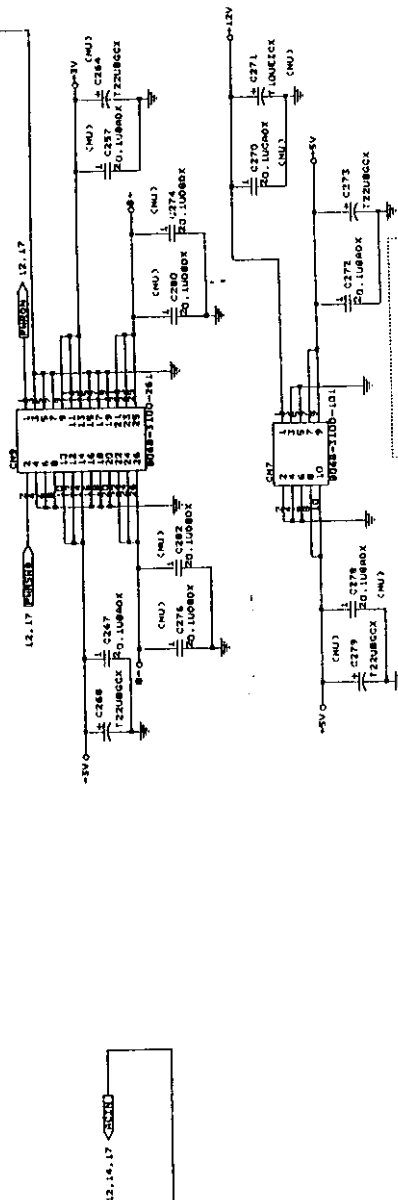


the presence of the substance must be adjusted with

4/17 CH16 P1MZ.3 SHORT
4/17 CH15.C126 CHANGE TO 11000CA

[illegible]

DC/B to M/B connector



NU PARTS :

02274, 02280, 02282, 02275
02285, 02287, 02283, 02286
02288, 02289, 02290, 02291
02292, 02293, 02294, 02295
02296, 02297, 02298, 02299

FILE NUMBER	DC-OC /IF	REV	D-5
Size Document Number		DCZIF SCH C PCB VERSION :	
Q			

