



EMC Testing/Engineering Services



NVLAP ACCREDITED
NARTE Certified Engineer
Professional Engineer

Federal Communications Commission

Equipment Approval Services, P.O. Box 358315
Pittsburgh, PA 15251-5315
Attention : Authorization & Evaluation Division

Re: Application for Certification of Johnson Controls Transmitter under 47CFR 15.231.
FCC ID: **CB2ACTLHL3**

Gentlemen:

On behalf of the applicant, Johnson Controls Interiors, LLC, please find attached the submittal materials for certification of the JCI Universal Garage Door Opener, Model CB2ACTHL3. This model, a part of their Homelink® III series, is capable of learning the current garage door transmit frequencies from 288MHz through 420MHz except in the forbidden frequency regions.

The complete List of the Exhibits in this submittal package appears on Page 2 of this cover letter.

Johnson Controls Interiors has invested considerable resources into developing this Homelink® series. Having sections of Exhibit B, listed below, available to 'competition' would negate the advantage achieved in developing this product. Since their Homelink® series transmitters will be a major product line for Johnson Controls Interiors, not protecting the details of the design will result in a financial hardship for the company.

Pursuant to 47CFR 0.459, Johnson Controls Interiors requests that these listed exhibits be held confidential.

Circuit Block Diagrams	page 2 of Exhibit B
Theory/Description of Operation,	page 3 of Exhibit B
Schematics	page 4 & 5 of Exhibit B

The Federal Communications Commission will be notified, in writing, of any changes in the software/programming of the device that could affect the device's RF characteristics.

Your prompt consideration of this application for product certification will be greatly appreciated. Should you have any questions regarding the content of this report, kindly contact me.

Sincerely,

Ted Chaffee,
Technical Lab Manager
Narte Certified Engineer, #EMC-002025-NE
tel/fax: 616. 424.7014
email: tchaffee@ahde.com, or ahd@locallink.net

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EXHIBIT B:		
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Circuit Block Diagram [2.1033(b5)]	Total Pages	1
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