EXHIBIT 2-1 CFS8DL5800TSTAT

CIRCUIT OPERATION

The RF signal is fed from switched diversity antennas (ANT1, ANT2) via SW1 to the LNA composed of FL1 and mmic U3, from there it is fed into a single conversion receiver chip U4. IC U4 contains a rf amp., mixer, phase locked L.O. (locked to Y1), IF amplifiers (fed by FL2) the raw base-band is fed into U1 and U11 for conditioning prior to processing by the microprocessor U5.