APPLICANT:

TOSHIBA CORPORATION

Band Class 0 CDMA Channel

TRANSCEIVER TYPE: CJ6DCE37529A

2.1033 (c) TECHNICAL REPORT

Subsection 2.1033 (c)(1). APPLICANT: TOSHIBA Corporation

ADDRESS: 1-1, Shibaura 1-Chome, Minato-ku, Tokyo, 105-8001 JAPAN

Subsection					
<u>2.1033 (c) (2)</u>	FCC ID: CJ6	DCE37529.	A		
Subsection					
<u>2.1033 (c) (3)</u>	Instruction B	ook:			
	Operating In	struction		(EXI	HIBIT No.13)
Subsection					
<u>2.1033 (c) (4)</u>	Types of Emi	ssion: 40K(0F8W, 40K0F1D), 1M25F9W	
Subsection					
<u>2.1033 (c) (5)</u> F	requency Rang	e			
ANSI/TIA/EIA	-95-B Analog S	ystem and	Band Class 0 CI	DMA Channel	
Transr	nit Frequency I	Band :	824.025-848.9	85 MHz	
Receive	e Frequency Ba	nd:	869.025-893.9	85 MHz	
ANSI/TIA/EIA	-95-B Band Cla	ss 1 CDMA	A Channel		
Transr	nit Frequency I	Band :	1850-1910 MH	Iz	
Receive	e Frequency Ba	nd:	1930-1990 MH	łz	
Subsection					
<u>2.1033 (c) (6)</u> S	pecific Operati	ng Power R	lange		
Analog System;					
Power ATT	Г. 0dВ	+27.8dBn	n +/-0.5dB ERP		
Power ATT	Г. 4dB	+23.8dBn	n +2.0/-4.0dB EF	RP	
Power ATT	Г. 8dB	+19.8dBn	n +2.0/-4.0dB EF	RP	
Power ATT	Г. 12dB	+15.8dBi	m +2.0/-4.0dB E	RP	
Power ATT	Г. 16dB	+11.8dBi	m +2.0/-4.0dB E	RP	
Power ATT	Г. 20dB	+ 7.80	dBm +2.0/-4.0dB	ERP	
Note; Ma	iximum antenn	a gain is 1.	3 dBd.		
Band Class 0 C	CDMA Channel	Mobile	Station III;		
Operating	Range:	-50dB	3m ERP to 25.0d	Bm ERP (Step ran	ge: 1 dB)
Note; Ma	ximum antenn	a gain is 1.	0 dBd.		
Band Class 1 C	CDMA Channel	Mobile	Station II;		
Operating	Range:	-50dB	Bm EIRP to 24.5	dBm EIRP (Step ra	ange: 1 dB)
Note; Ma	ximum antenn	a gain is 0.	5 dBi.		
Maximum trai	nsmit power is	monitored	by detecting PA	output power and	controlled by transmit IF
signal to maint	tain the maxim	um transm	it power within	the specific level.	
ر The transmit	power attenuat	ion and ste	p are determine	d by calculating ch	aracteristics of
transmit power	r versus TX_AC	C_Adjust	ment Voltage.		
Subsection					
<u>2.1033 (c) (7)</u> N	faximum Powe	r Rating			
Analog System	L	-		Upper Limit:	-2.2dBW(0.6W)ERP

Mobile Station III:

-7dBW(0.2W)ERP PAGE 1 OF EXHIBIT Type "Operation Description"

Lower Limit:

APPLICANT:						TRANSCEIVER TYPE:
TOSHIBA CO	RPORATION					CJ6DCE37529A
Band Class 1	CDMA Channel	Mobile	e Station II:		Upper Limit: Lower Limit: Upper Limit:	0dBw(1.0W)ERP -7dBW(0.2W)EIRP 0dBw(1.0W)EIRP
Subsection						
<u>2.1033 (c) (8)</u>	DC Voltage and C Supply Vol Collector V Collector C	Current i tage oltage urrent	nto the Final = 3.7Vdc = 3.5Vdc = 0.7 - 0.1Am	l Ampli np.	fier Module:	
Subsection						
<u>2.1033 (c) (9)</u>	Tune-up Procedur Tune-up Proced	re: lure			(EXI	HIBIT No.8)
	Standard Test Co The following Co transmitter.	nditions ondition	:: is and Procedi	ures w	ere applied dur	ing Testing of this
	Room Temper	rature	= 23 - 27degr	rees Ce	lsius	
	Room Humid	ity	= 30 - 50perc	cent R	elative Humidi	ty
	Supply Voltag	ge	= 3.7V	_	_	
. 1.	Prior to Testing	g, the U	nit should be	tuned	up according to	o the Manufacturer's
Alig	nment Procedure.					
<u>2.1033 (c) (10)</u>	Circuit Description	ns				
	Moone for Energy	nov Ctal	hilizotion			

Means for Frequency Stabilization: The output frequency of the VCTCXO is frequency locked to the RF signal transmitted from a cellular base station by an AFC circuit. Therefore the frequency stability of an RF signal transmitted from a cellular base station is maintained. The Transmitter Frequencies are determined by a Voltage Controlled Oscillator Phase Locked to a 30kHz

Frequencies are determined by a Voltage Controlled Oscillator Phase Locked to a 30kHz Signal for operation in 800MHz band and to a 10KHz Signal for operation in 1.9GHz band derived from this High Stability Oscillator known as the Reference Oscillator

Means for suppression of Spurious Emissions:

Spurious and Harmonic suppression is obtained by proper Shielding Techniques, and the use of (800Mhz Band) Band pass SAW Filters (1900MHz Band)Band pass Dielectric Filters. Under digital mode, in order to keep the transmission level within the linear operation range of the power amplifier, the highest gain of the Transmission Variable Amplifier is limited.

This enable to suppress "conductive Spurious Emissions" under the specified values

Means for Limiting Modulation:

This transmitter is equipped with a device that prevent modulation in excess of 100%. This device, an instantaneous deviation control (IDC) precedes the modulator of the transmitter. It is instantaneous in action for controlling the modulating wave from introduced into the transmitter's frequency modulator. The deviation limit is set by means of an electronic master deviation adjust stage (Exhibit No.6). This allows maximum audio deviation to be set to +/-12kHz. Under digital mode, in order to limit the Modulation Frequency Range, the low pass filter which limits the frequency range is adopted to each of DBP transmitter and ABP transmitter.

TOSHIBA CORPORATION

TRANSCEIVER TYPE:

CJ6DCE37529A

Means for Limiting Power:

A Peak Detector at the Output of the Power Amplifier provides a Carrier Power Indicator Signal. This Signal level is Compared with limited power by CPU. A power level is limited setting for level controlled by CPU.

Technical Descriptions:

This Transmitter has been specifically designed for the Domestic Public Cellular Radiotelephone Communications Service.

This transmitter is available as a triple mode phone. Namely, this transmitter is designed to operate under CDMA (Code Division Multiple Access) digital cellular system, AMPS analog cellular system and PCS (CDMA)digital cellular system. This transmitter is available as a triple mode phone. Namely, this transmitter is designed to operate under CDMA (Code Division Multiple Access) digital cellular system, AMPS analog cellular system and PCS (CDMA)digital cellular system. CDMA signals are fed to a quadrature phase modulator of 130.38MHz IF signal which is divided by 2 from 260.76MHz signal of a voltage controlled oscillator(T_VCO). The T_VCO also works as a FM modulator for analog mode and locked to 19.68MHz signal of a VC-TCXO.

The IF signal is amplified and frequency up-converted by a RF local signal into RF signal of 824-849MHz band or 1850-1910MHz band. The RF local signal is generated by a dual band voltage controlled oscillator (Dual_VCO) and locked to 19.68MHz signal of the VC-TCXO.

The VC-TCXO is also used for a frequency reference of receiving circuit and adjusted to synchronize the received RF signal from base station.

A transmit automatic gain control (AGC) function is performed at the IF amplifier section to adjust specific transmit output power level.

The up-converted signal is switched and fed to a 800MHz band power amplifier power amplifier lineup or to a 1.9GHz band power amplifier lineup. DC power is only supplied to the activated lineup.

The 800MHz band power amplifier lineup consists of an inter stage BPF1(INT-BPF1), a driver 1 (Drv1), an INT_BPF2, a power amplifier (PA), an isolator and a duplexer which are dedicated for 800MHz band.

The 1.9GHz band power amplifier lineup also consists of an INT-BPF1, a Drv1, an INT_BPF2, a PA, an isolator and a duplexer which are dedicated for 1.9GHz band. INT_BPF2 of 1.9GHz band is split into upper and lower band of 1.9GHz band. The selection the split band is based on the assigned channel.

The output of 800MHz band duplexer and 1.9GHz band duplexer are connected to diplexer to use a dual band antenna.

A peak power detector for both 800MHz and 1.9GHZ band amplifiers provides a carrier power indication signal. The signal level is monitored by a CPU to keep the transmit power not to exceed to the maximum limit.

List of Schematics:

Block Diagram of HANDHELD PORTABLE TRANSCEIVER RF/BB (EXHIBIT No.2)

Schematic Diagram of HANDHELD PORTABLE TRANSCEIVER RF/BB (EXHIBIT No.10)

APPLICANT:

TOSHIBA CORPORATION

TRANSCEIVER TYPE: CJ6DCE37529A

2.1033 (c) (11) Equipment Identification:

Equipment's Identification label and its intended Location are as shown in EXHIBIT No.5 (FCC ID Nameplate), and in EXHIBIT No.6(Photograph of inside)

APPLICANT:

TOSHIBA CORPORATION

TRANSCEIVER TYPE: CJ6DCE37529A

Subsection

<u>2.1033 (c) (12)</u> Photographs:

A complete set of the Photographs showing External and Internal Views of Circuit Details and Construction are provided by from EXHIBIT No.4(External Photos) and No.6(Internal Photos).

Subsection

<u>2.1033 (c) (13).</u> Digital Modulation

sends

the data packet to the transmit subsystem, where the information bits are convolutionally encoded, interleaved, modulated by means of 64-ary orthogonal modulation, spread into 1.2288 Mbps I/Q base band signals by means of direct sequence spreading, and passed DAC to covert analog signal.

The process gains for the Rate Set 1 and Rate Set 2 are 128(21dB) and 85.3(19.3dB) respectively.

The I/Q base band signals are filtered and passed to a 130.38 MHz quadrature

spreader.

Transmitter IF signal generation at 130.38MHz is performed by combining digitally generated I and Q signals. The tin-and quadrate-phase 130.38MHz local signals are derived from a 260.76MHz phase locked loop.

Subsection

2.1033 (c) (14). The Data Required (EXHIBIT No.11

- (2.1046) RF power output
- (2.1057) Modulation characteristics
- (2.1049) Occupied bandwidth
- (2.1057) Spurious emission at antenna terminals
- (2.1058) Field strength of spurious radiation
- (2.1059) Frequency stability
- (2.1060) Frequency spectrum to be investigated

Subsection

2.1033 (c) (15).	External Power Amplifier
Not applied	

Subsection

<u>2.1033 (c) (16).</u>	AM Broadcast Stereophonic Exciter-generator
NT / 1· 1	

Not applied

Subsection <u>2.1033 (c) (17).</u> Composite System Not applied

TRANSMITTER FREQUENCY RESPONSE

Subsection

2.987 (a). The Test Set-up for the TRANSMITTER FREQUENCY RESPONSE is as per PAGE 1 of EXHIBIT No.12(Using HP8901B Modulation Analyzer). With the Audio Signal Generator adjusted to 1,000 Hz, and +/-1.0kHz Deviation, the 0dB Reference Level was determined. With Input Levels held constant and below Limiting at all Frequencies, the Audio SG was varied from 100 to 5,000Hz. The Response in dB, relative to 1,000Hz was measured.

The results are shown in EXHIBIT No.11.

MODULATION LIMITING

Subsection

2.987 (b).

The Test Set-up for the MODULATION LIMITING is as per PAGE 1 of EXHIBIT No.12. The Deviation is to be observed by varying the Input Voltage. Test has been performed for three Different Modulation Frequencies.

The results are shown in EXHIBIT No.11.

OCCUPIED BANDWIDTH

Subsection

2.989 (c)(1) The Test Set-up for the OCCUPIED BANDWIDTH is Figure in SPURIOUS EMISSIONS AT ANTENNA TERMINALS, PAGE 1 of EXHIBIT No.12.

Analog mode:

The Audio SG was adjusted to the Frequency of Maximum Response. The Output Level was set to +/-6kHz Deviation.

With Level constant, the Frequency was set to 2,500Hz. Then the Audio Signal level was increased by 16dB.

The measurements ware made by Spectrum Analyzer, and the results are shown in EXIBIT No.11.

In addition, Occupied Bandwidth Data was obtained for the SAT (Supervisory Audio Tone) and ST (Signaling Tone). The results are shown in EXIBIT No.11.

Digital mode:

Modulate the transmitter with OQPSK modulation, using pseudo random date.

List of Photographs:

SPURIOUS EMISSIONS AT ANTENNA TERMINALS

Subsection <u>2.991</u> <u>22.917.</u>

The Test Set-up for the SPURIOUS EMISSION AT ANTENNA TERMINALS is as per Figure in SPURIOUS EMISSIONS AT ANTENNA TERMINALS, PAGE 2 of EXHIBIT No.12.

The Level of the Carrier and the various Conducted Spurious and Harmonic Frequencies were measured by means of a Calibrated Receiving System used to compare the Output of the Transmitter with than of a Standard Signal Generator at the Spurious Frequency. The Spectrum was scanned from the Lowest Frequency generated in the Equipment to 10GHz.

The results are shown in EXHIBIT No.11.

ESN PROTECTION

The ESN data is programmed in the Memory IC (EEPROM) which is used with other data. And the Encoring Technique includes the multiplication by a polynomial. This Memory (EEPROM) is soldered on the PCB as the attached photo shows.

Our operation software's legitimacy is checked by check-sum of whole program. So unauthorized alteration of that unit's operating software render the unit inoperative.

We have attached the other necessary documents as follows:

- Photo showing the location of the ESN IC and the software ROM
- Schematics showing the ESN IC and the software ROM
- ROM (contained the operating software) writer system Diagram

TRANSCEIVER TYPE: CJ6DCE37529A



TRANSCEIVER TYPE: CJ6DCE37529A





High-speed Operation Minimizes Write Time

Hardware implementation of address generation, pin array conversion and sum generation functions boosts operation speed. Executes JEDEC standard (x8 bit, 1 Mbit EPROM) CONT processing in about 20 seconds. And because algorithms are executed by a microprocessor and dedicated hardware, the 1930 supports a wide range of PROMs.

Specifications

Standard 4 Mbyte Capacity Expandable to 16 Mbyte

As PROM capacities continue to rise, the 1930 comes with 4 Mbyte standard, and can be expanded to a full 16 Mbyte: more than enough power to handle PROM cards.

Algorithms Provided for Write to Leading Vendor Devices

including AMD, Fujitsu, Hitachi, Intel, Mitsubishi Electric, National Semiconductor, NEC, Ricoh, Sharp, Texas Instruments, Toshiba, WSI and XICOR.



Standard Support for 13 Data Formats

The RS-232C and parallel interfaces provide data transfer capacity, with support for 13 data formats to match your needs: Minato (8-bit), Intel hex (8-/16-bit), HP64000ABS, binary, Motorola, ASCII octal, ASCII hex, Tektronix hex (8-bit), Tektronix hex (16-bit), TI SDSMAC,

ASCII BHLF, ASCII BNPF, no format.

Two Device Selection Methods

The devices to be written can be selected from a menu, or picked up by the silicon signature. With the menu, all you have to do is select the displayed name from the easy-to-read liquid crystal panel. If silicon signature is used, the device is automatically selected by checking the PROM internal ID code.

Note: It may be impossible to automatically read the ID code from certain PROM varieties.

T5716200	cdEB10
Vpp=12.50v	/ 25uS
TC578200	cdEA10
Vpp=12.50v	· 50us

uPD_8000 cdEA11 VPP=12.50v 50us

Selectable Verify Parameters

Normally the verify V_{CC} voltage is set to 4.75/5.25V, but this can be changed to handle specific user requirements and make possible a new dimension in precision verification.

Write Error Alert with Buzzer and Red LED

If a write error is detected, all 16 red LEDs will light, and the buzzer will sound to alert the operator. The buzzer can be disabled if desired.

Open Lever Reduces PROM Load/unload Time

ence by isolating each socket.

Enhanced Reliability Boosts Your Yield

Socket unlock can be completed by merely throwing the open levers, each locking eight sockets. With fast unlocking, you can unload and reload faster, too.

The overall reliability of the programmer has been significantly improved

through integration of the circuity, reduction in the number of required adjustments, enhancement of circuit performance and function, and

adjustments, enhancement of circuit performance and rolledon, and lower internal temperature through precision design. Write algorithms were developed cooperatively with the PROM manufacturers to provide

optimum performance. And the system itself is designed to minimize noise generated by PROM operation, and eliminate inter-socket interfer-

Simple Upgrades

We have made it simple to upgrade your system to maintain support for the latest PROM chips. With a flash PROM inside, you can update your system yourself easily.

Model 1930 Specifications (main unit)

Devices supported:	64 Kbit and larger EPROM and EEPROM
Simultaneous virite:	16 ching
Memory capacity:	A Movie standard (22 Mbit)
memory ouplicity.	16 Mbyte standard (32 Mbit)
Operation modes:	COPY: Data convitrom PPOM to momon
operation modes:	BI ANK: PROM blank check
	PROGRAM: Write data to PROM
	VERIEV: Compare PROM and memory
	data
	CONT: Continuous execution of
	BLANK-PROGRAM-VERIEV cycle
Data input:	BS-232C and Centronics-compatible
•	parallel.
	Master ROM copy.
	LAN (option; external)
Interfaces:	RS-232C and Centronics-compatible
	parallel.
	LAN (option; external)
Set programming:	16 patterns
Display:	16-character × 2-row LCD panel
Operating temperature:	5 to 35°C
Power supply:	AC85 to 264V (stepless), 47 to 66Hz
Power consumption:	100VA (peak)
Dimensions:	380W × 150H × 380D (mm)
Specifications (SU30	00)
Devices supported:	28- to 42-nin DIP EPROMs and EEPROMs
Socket:	42-pin DIP
	Can also be used for 28-, 32- and 40-pin
	JEDEC and 40-pin non-JEDEC packages
Power supply:	Supplied from 1930 main unit
Dimensions:	$380W \times 45H \times 260D$ (mm)

Specifications and external appearances indicated in this catalog are subject to change without notice. The contents of this catalog are accurate as of June, 1993.

MINATO ELECTRONICS INC.

4105 Minamiyamada-cho, Kohoku-ku Yokohama 223, Japan Phone: (045) 591-5611 Fax: (045) 591-5618 Telex: 3822-244-MINATO J

Distributed by:

Printed in Japan 93-05-3000FA

MODEL-1891	/92 専用				
刑备	旧刑番	価格	デバイス形状	1891/92専用	デバイス
35. 8			, , , , , , , , , , , , , , , , , , , ,	ユニット	-
*H02-200	H910-38	¥23,000	32DIP_32DIP	9132A	P28F001BX
%H02·201	H910·51	¥18,000	32DIP_32PLCC	9132A	N28F001BX
%H02-203	H910-50	¥24,000	32DIP_32TSOP	9140A	E28F001BX
H02-400	H010.41	¥28.000	40TSOP	91324	F28F002 004
H02.401	H010-40	¥28,000	401301	9132A	BA29F200 400
H02-401	H010-20	¥24,000	4430F	01224	F28F200 400
H02-402	H910-35	¥31,000	10130F	9132A 0122A	E28F200 400
NU2-403	H910-44	+31,000	46130F	9132A	E20F200,400 PA90F900 直田
×H02-404	H910-68	+28,000	44SOP	9132A	FA26F600守用
%HU2-405	H910-69	1 33,000	561SOP	9132A	
×H02-406	H910-70	¥31,000	481SOP	9132A	
%HUZ-407	H910-71	₹28,000	401SOP	9132A	E28F008导用
MODEL-1930	専用				
型番	旧型番	価格	デバイス形状	ユニット	デバイス
H03-300	H910-8	¥26,000	44SOP	3000(L)	PA28F008SA
H03-301	H910-10	¥27,000	40TSOP	3000(L)	E28F008SA
H03-302	H910-33	¥33,000	56TSOP	3000(L)	E28F016SA
H03-303	H910-37	¥28,000	44SOP	3000(L)	M5M28F016
$HO3 \cdot 3O4$	H910-45	¥28,000	56SSOP	3000(L)	DT28F016SA
H03-305	H910·46	¥30,000	48TSOP	3000(L)	MBM29F080/016
%H03·306	H910-56	¥26,000	44SOP	3000(L)	MBM29F200/400/800
%H03·307	H910·57	¥30,000	48TSOP	3000(L)	MBM29F200/400/800
% Н03-308	H910-73	¥25,000	40SON	3000専用	MBM29LV004T/B
%H03·309	H910-74	¥22,000	40SON	3000L専用	MBM29LV004T/B
%H03-310	H910-75	¥22,000	40SON	3000(L)	MBM29F17
*H03-311 •	H910-77 °	¥28,000	46SON	3000L専用	MBM29LV004/008/080
%H03-312	H910-78	¥24,000	46SON	3000専用	MBM29LV004/008/080
*H03-313××	Н910-80Ҳ	¥28.000	46SON	3000専用	MBM29LV400/800
%H03·314 ∗	11010 01 4	VOL000			N(D) (201 X(400/200
	HAI0-91 .	₹24,000	46SON	3000L専用	MBM29LV400/800
%H03·315	H910-81 " H910-82	¥24,000 ¥25,000	46SON 42DIP_42DIP	3000L専用 3000(L)	MBM29LV4007800 MB8502F016A
%H03·315	H910-81 #	¥25,000	46SON 42DIP_42DIP	3000L専用 3000(L)	MBM29LV400/800 MB8502F016A
ЖН03-315 Н03-400	H910-81 H910-82 H910-12	¥24,000 ¥25,000 ¥27,000	46SON 42DIP_42DIP 40TSOP	3000L専用 3000(L) 3000(L)	MBM29L V400/800 MB8502F016A E28F002,004
<pre>※H03-315 H03-400 H03-401</pre>	H910-81 H910-82 H910-12 H910-13	¥25,000 ¥25,000 ¥27,000 ¥26,000	46SON 42DIP_42DIP 40TSOP 44SOP	3000L导用 3000(L) 3000(L) 3000(L)	MBM29L V400/800 MB8502F016A E28F002,004 PA28F200, 400
<pre>%H03·315 H03·400 H03·401 H03·402</pre>	H910-81 H910-82 H910-12 H910-13 H910-19	¥25,000 ¥25,000 ¥26,000 ¥33,000	46SON 42DIP_42DIP 40TSOP 44SOP 56TSOP	3000L导用 3000(L) 3000(L) 3000(L) 3000(L)	MBM29LV400/800 MB8502F016A E28F002,004 PA28F200, 400 E28F200, 400
<pre>%H03·315 H03·400 H03·401 H03·402 H03·403</pre>	H910-81 H910-12 H910-13 H910-19 H910-35	¥25,000 ¥25,000 ¥26,000 ¥33,000 ¥30,000	46SON 42DIP_42DIP 40TSOP 44SOP 56TSOP 48TSOP	3000L导用 3000(L) 3000(L) 3000(L) 3000(L) 3000(L)	MBM29LV400/800 MB8502F016A E28F002,004 PA28F200, 400 E28F200, 400 E28F200, 400
<pre>%H03-315 H03-400 H03-401 H03-402 H03-403 %H03-403 %H03-404</pre>	H910-81 H910-82 H910-13 H910-13 H910-35 H910-60	¥24,000 ¥25,000 ¥27,000 ¥26,000 ¥33,000 ¥30,000 ¥26,000	46SON 42DIP_42DIP 40TSOP 44SOP 56TSOP 48TSOP 44SOP	3000L导用 3000(L) 3000(L) 3000(L) 3000(L) 3000(L) 3000(L)	MBM29LV400/800 MB8502F016A E28F002,004 PA28F200, 400 E28F200, 400 E28F200, 400 PA28F800専用
<pre>%H03-315 H03-400 H03-401 H03-402 H03-403 %H03-403 %H03-404 %H03-405</pre>	H910-81 H910-12 H910-13 H910-19 H910-35 H910-60 H910-61	¥24,000 ¥25,000 ¥26,000 ¥33,000 ¥30,000 ¥26,000 ¥33,000	46SON 42DIP_42DIP 40TSOP 44SOP 56TSOP 48TSOP 44SOP 56TSOP	3000L导用 3000(L) 3000(L) 3000(L) 3000(L) 3000(L) 3000(L) 3000(L)	MBM29L V400/800 MB8502F016A E28F002,004 PA28F200,400 E28F200,400 E28F200,400 PA28F800専用 E28F800専用
<pre>%H03·315 H03·400 H03·401 H03·402 H03·403 %H03·404 %H03·404 %H03·405 %H03·406</pre>	H910-81 H910-12 H910-13 H910-19 H910-35 H910-60 H910-61 H910-62	¥24,000 ¥25,000 ¥26,000 ¥33,000 ¥30,000 ¥26,000 ¥33,000 ¥33,000	46SON 42DIP_42DIP 40TSOP 44SOP 56TSOP 48TSOP 44SOP 56TSOP 48TSOP	3000L导用 3000(L) 3000(L) 3000(L) 3000(L) 3000(L) 3000(L) 3000(L) 3000(L)	MBM29L V400/800 MB8502F016A E28F002,004 PA28F200,400 E28F200,400 E28F200,400 PA28F800専用 E28F800専用 E28F800専用

☆ユニットの () は、SU-3000 or 3000L どちらでも可

-

Socket adapter

4