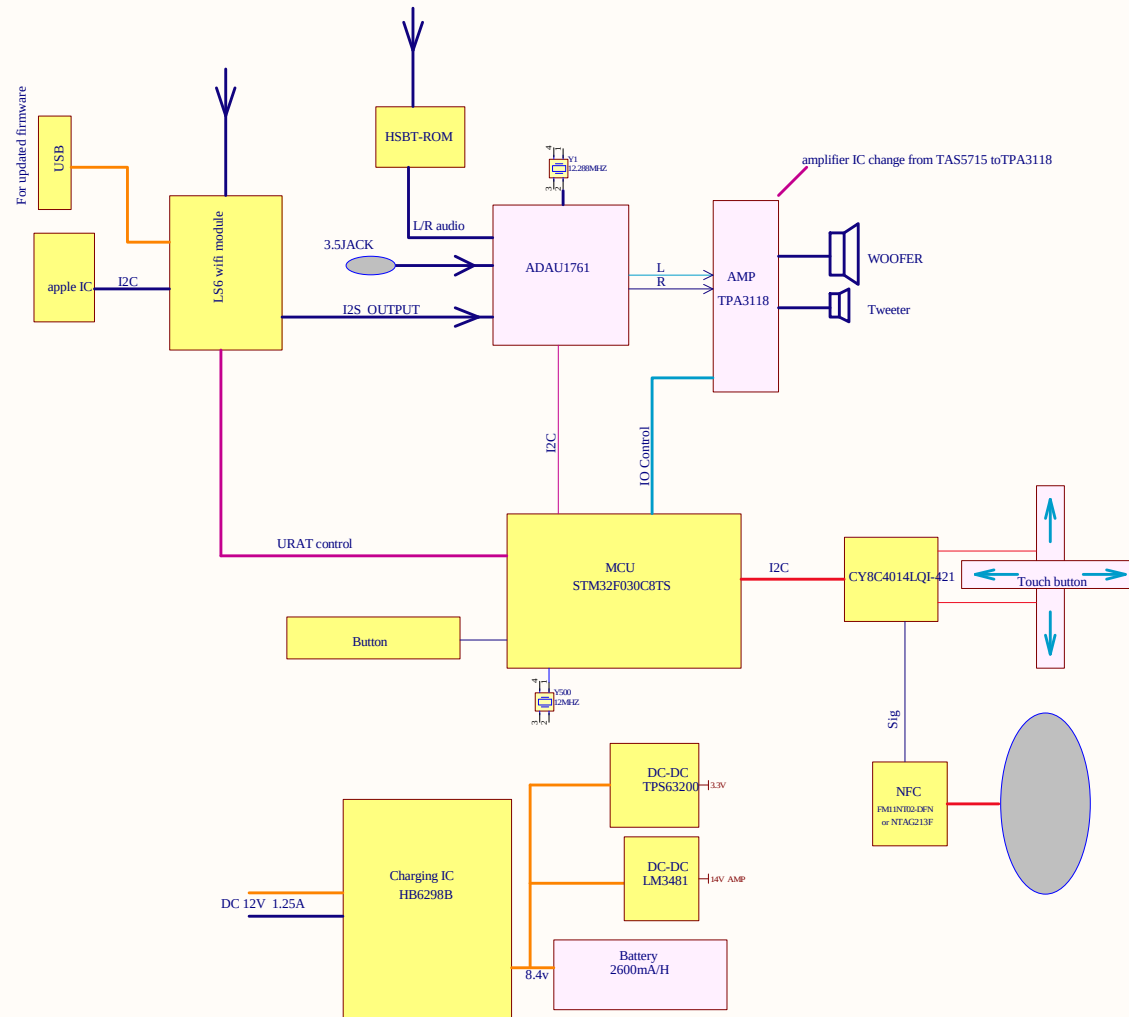
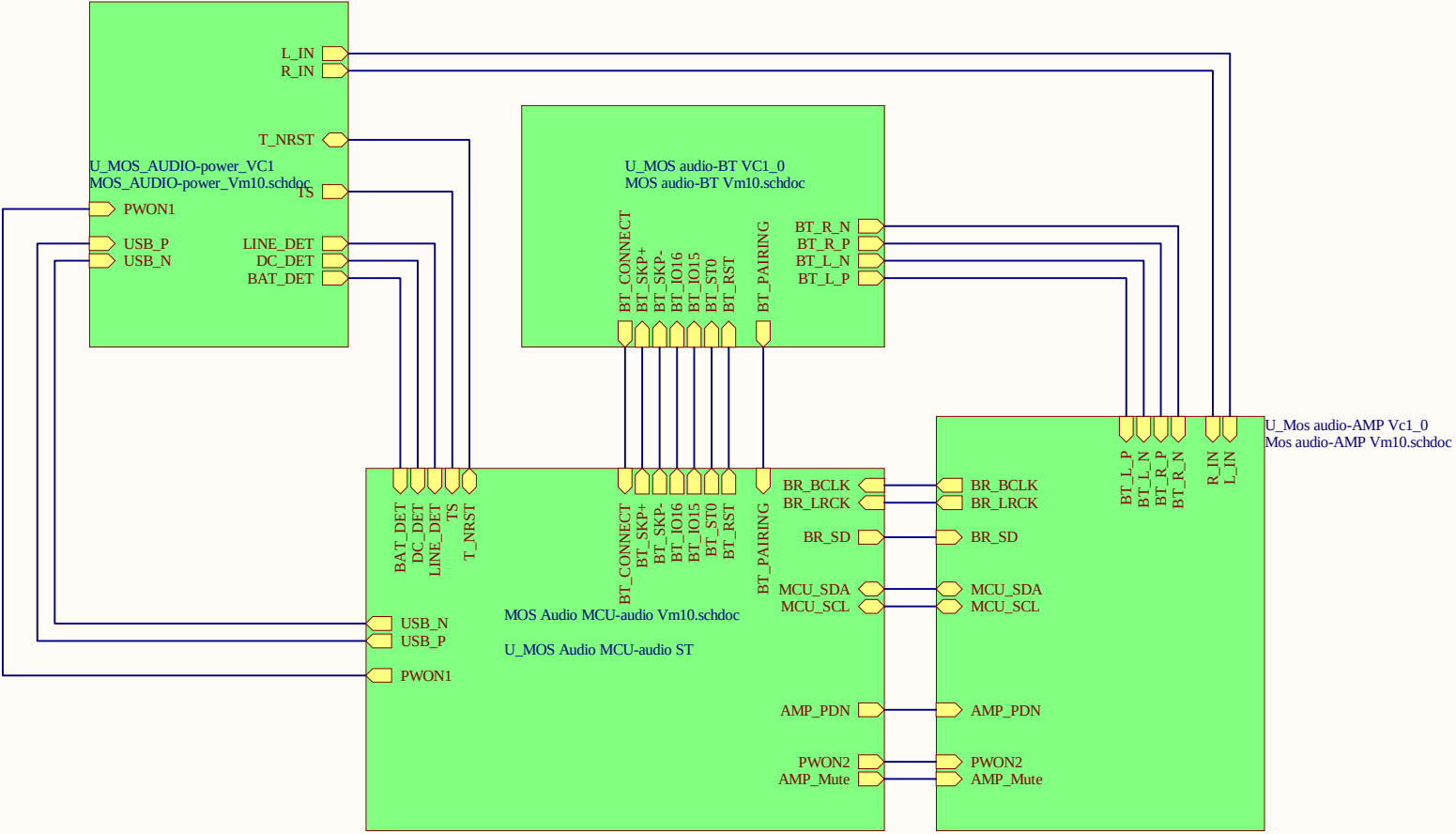
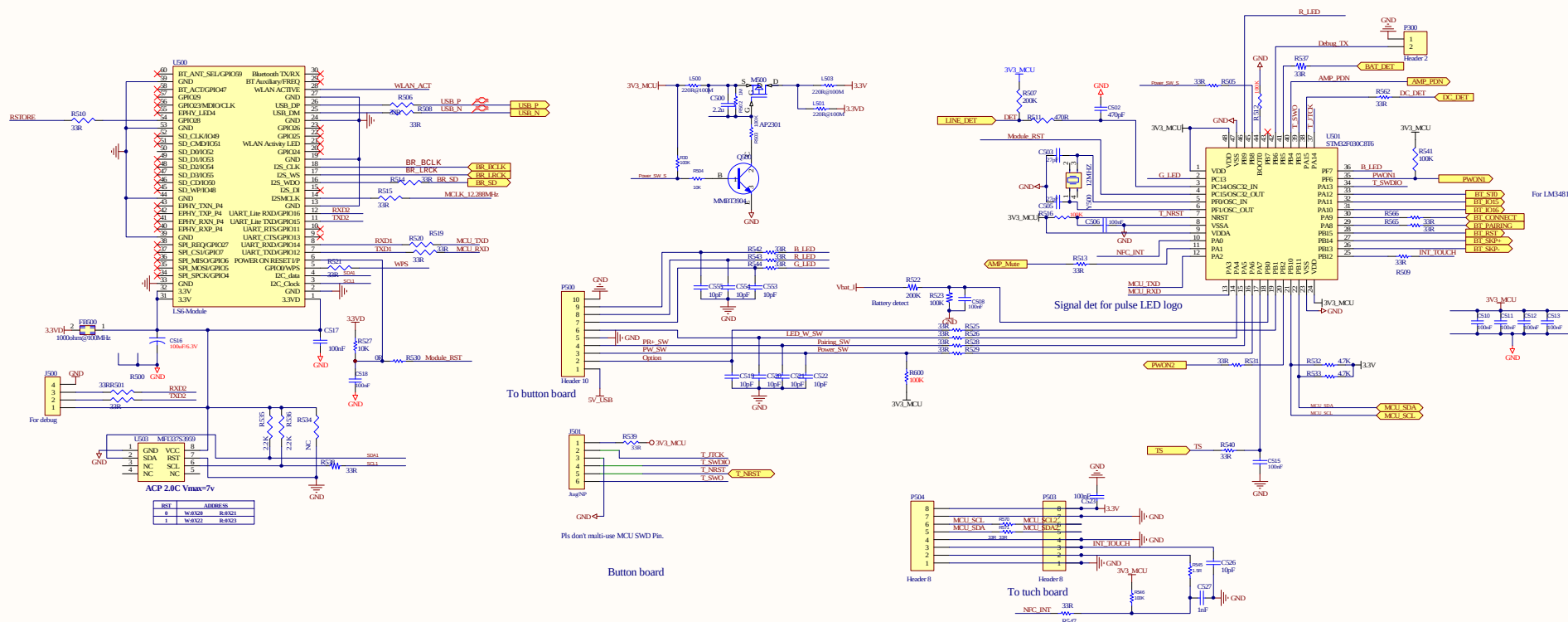




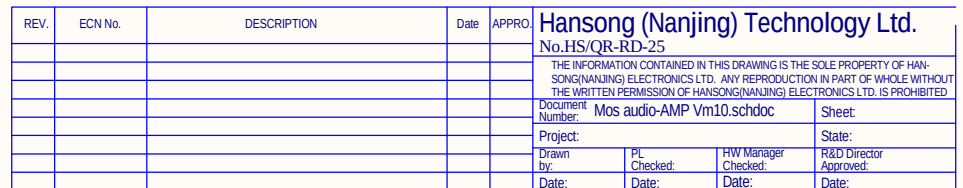
REV.	ECN No.	DESCRIPTION	Date	APPRO.	Hansong (Nanjing) Technology Ltd. No.HS/QR-RD-25	
					THE INFORMATION CONTAINED IN THIS DRAWING IS THE SOLE PROPERTY OF HANSONG TECHNOLOGY CO.,LTD. ANY REPRODUCTION IN PART OF WHOLE WITHOUT THE WRITTEN PERMISSION OF HANSONG TECHNOLOGY CO.,LTD. IS PROHIBITED	
					Document Number: HS40-Audio M PLUS_ B09 VM10-??-SC	Sheet:
					Project:	State:
					Drafted by:	PL Checked:
					Date:	HW Manager Checked:
						R&D Director Approved:
						Date:

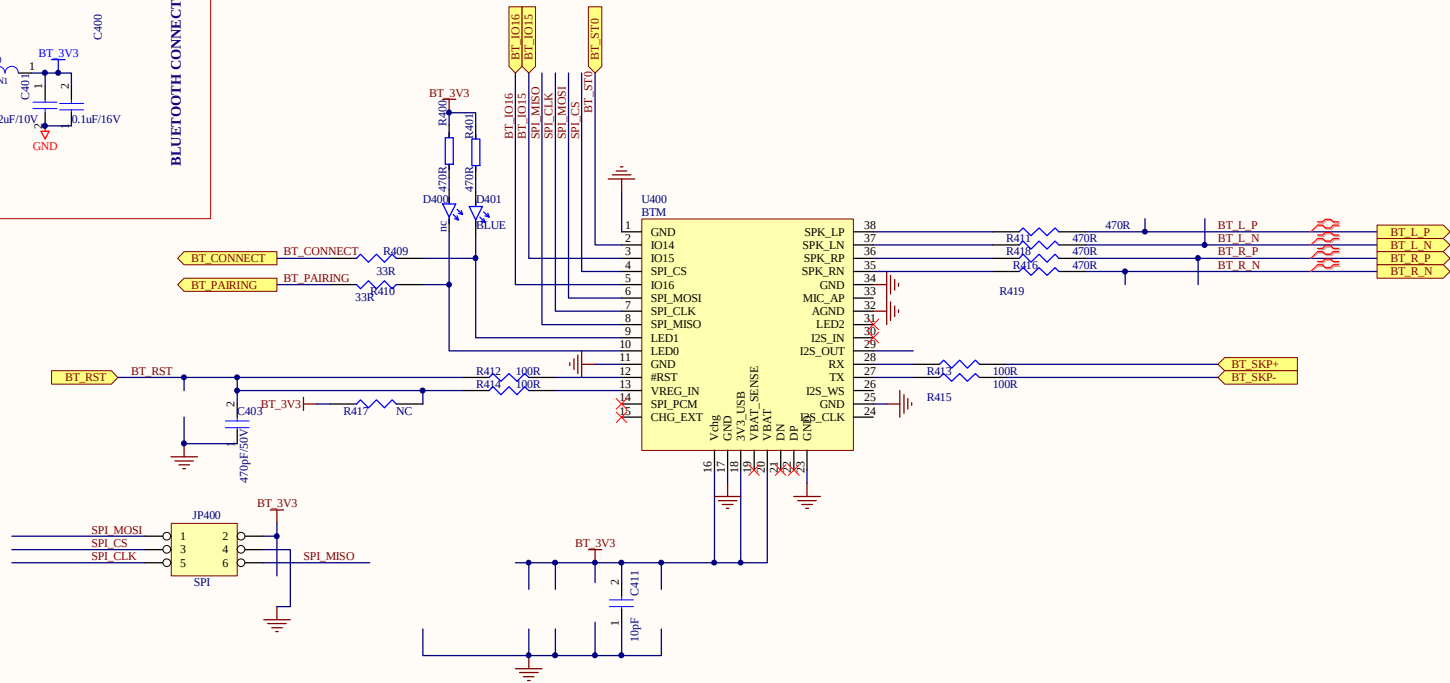
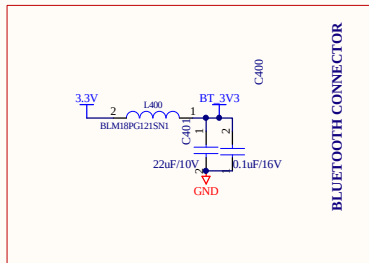


Title		
Size	Number	Revision
A4		
Date:	2015-7-16	Sheet of
File:	D:\Project\Signal flowchart.SchDoc	Drawn By:

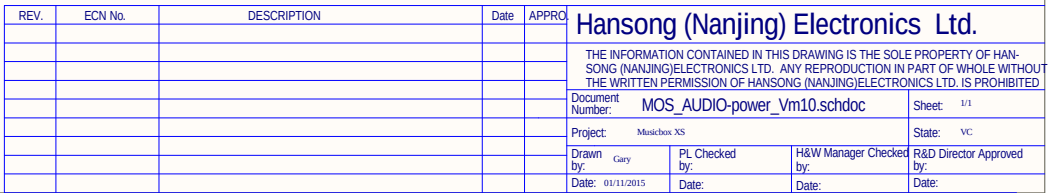


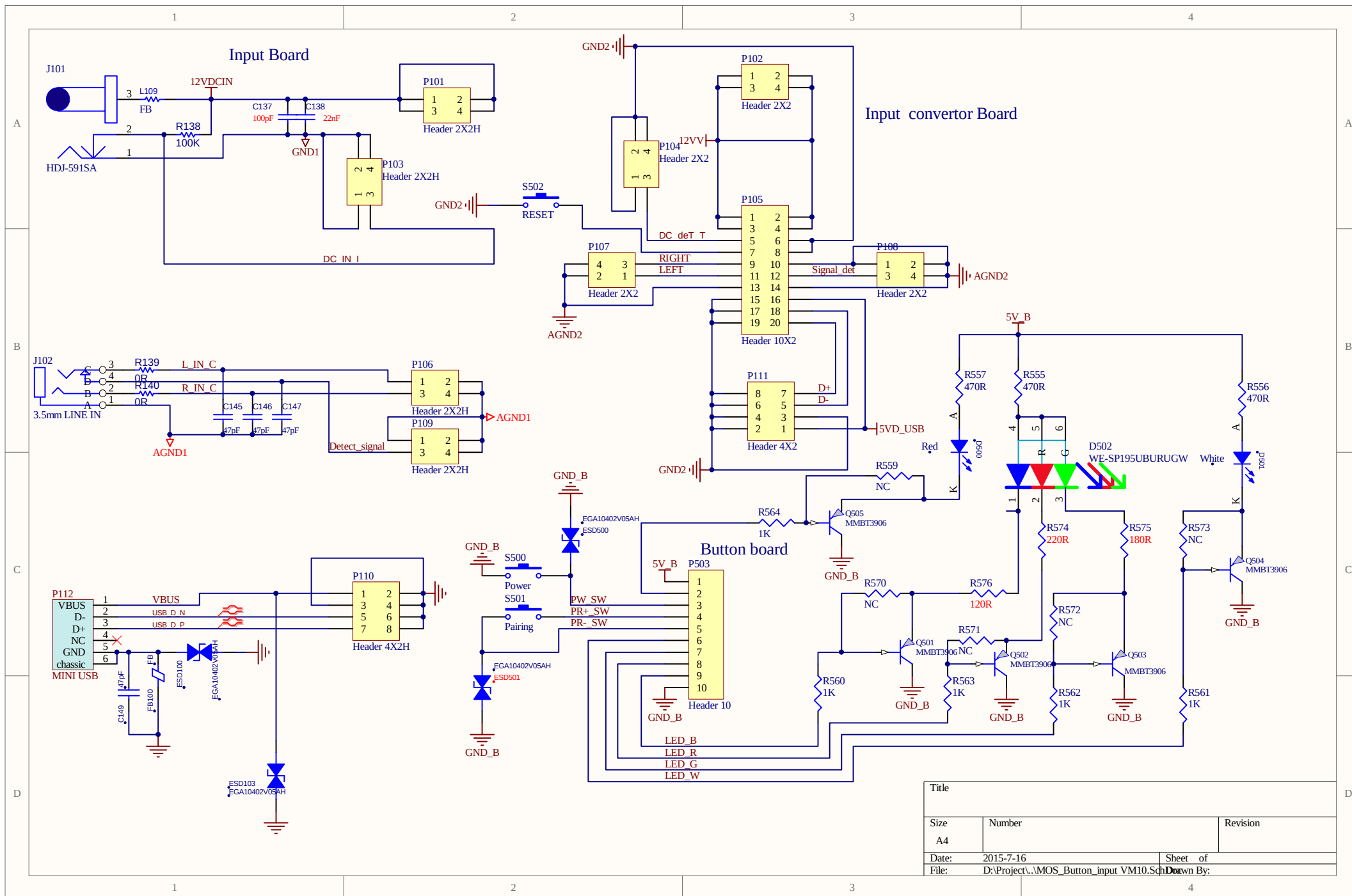
REV.	ECN No.	DESCRIPTION	Date	APPRO.	Hansong (Nanjing) Technology Ltd.			
					No.HS/QR-RD-25			
					THE INFORMATION CONTAINED IN THIS DRAWING IS THE SOLE PROPERTY OF HANSONG TECHNOLOGY CO.,LTD. ANY REPRODUCTION IN PART OF WHOLE WITHOUT THE WRITTEN PERMISSION OF HANSONG TECHNOLOGY CO.,LTD. IS PROHIBITED			
					Document Number: HS40-??_B09 V??-??_SCH			Sheet:
					Project:			State:
					Drafted by:	PL Checked:	HW Manager Checked:	R&D Director Approved:
					Date:	Date:		Date:



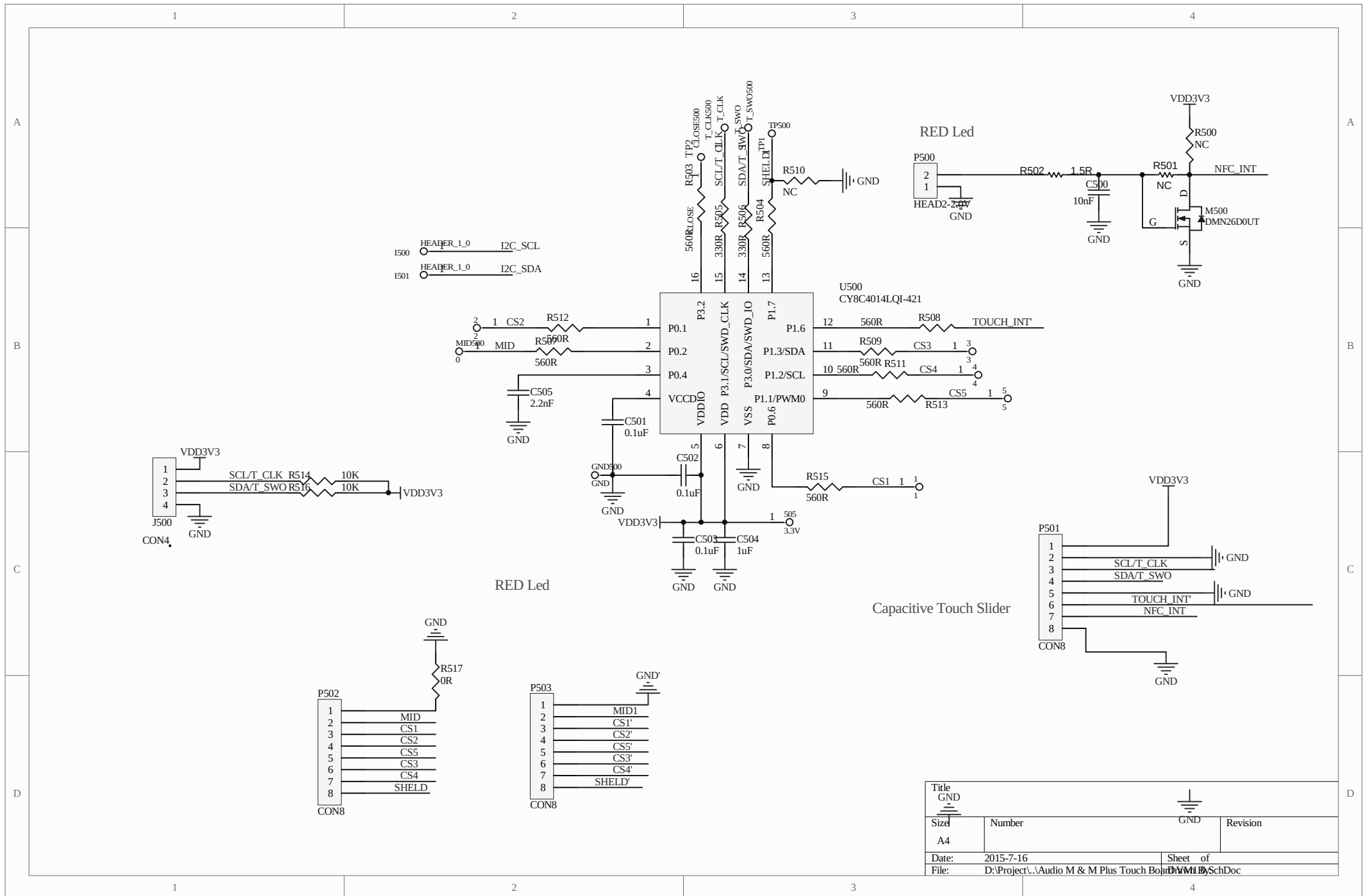


REV.	ECN No.	DESCRIPTION	Date	APPRO.	Hansong (Nanjing) Technology Ltd.			
					THE INFORMATION CONTAINED IN THIS DRAWING IS THE SOLE PROPERTY OF HANSONG TECHNOLOGY CO.,LTD. ANY REPRODUCTION IN PART OF WHOLE WITHOUT THE WRITTEN PERMISSION OF HANSONG TECHNOLOGY CO.,LTD. IS PROHIBITED			
					Document Number: HS40-B008-MOSAudio-BT Vm10 .SCH	Sheet: 1		
					Project:	State: VC1		
					Drafted by:	PL Checked:	HW Manager Checked:	R&D Director Approved:
					Date:	Date:		Date:

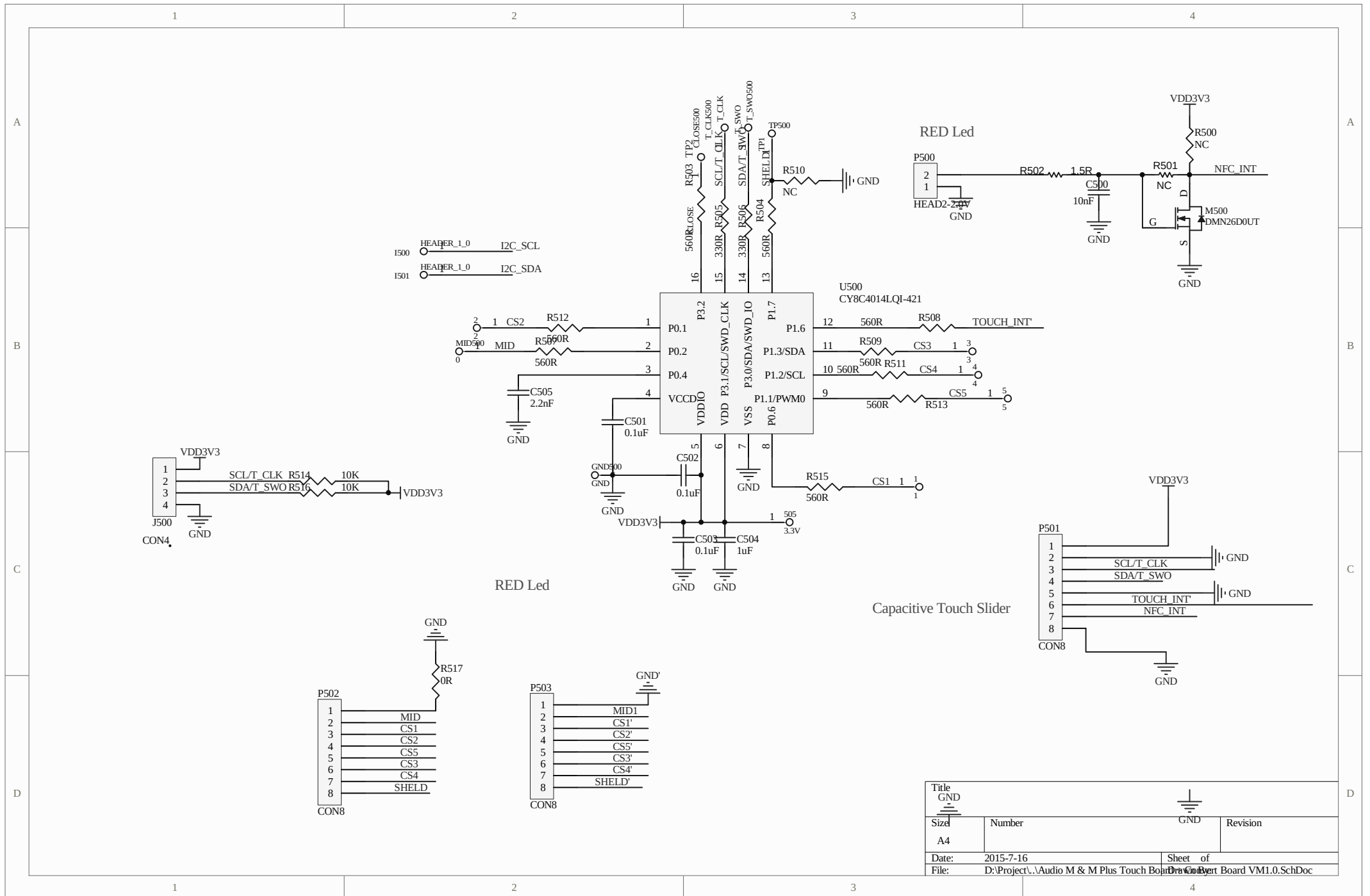




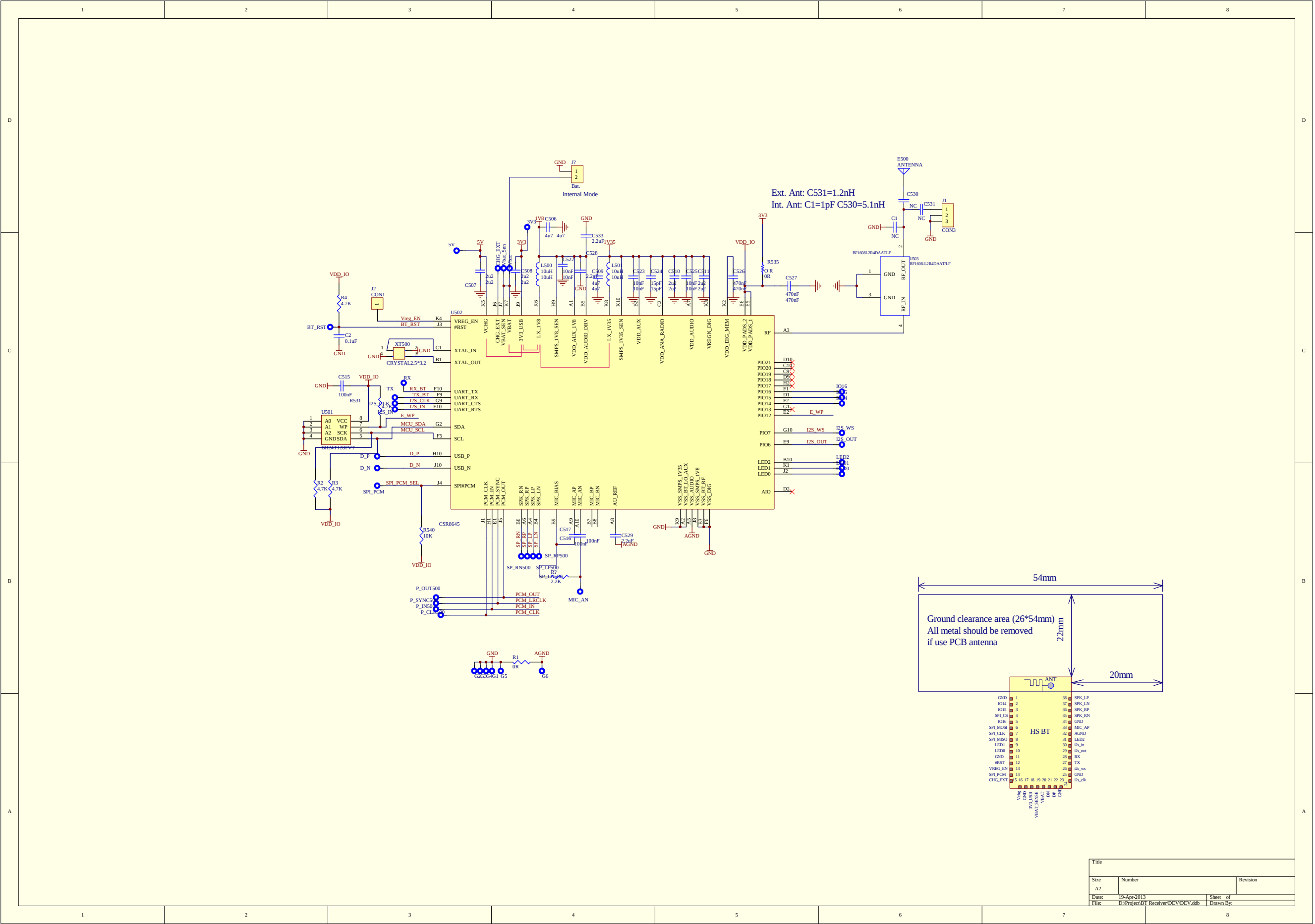
Title		
Size	Number	Revision
A4		
Date:	2015-7-16	Sheet of
File:	D:\Project\...\MOS_Button_input VM10.Sch Down By:	



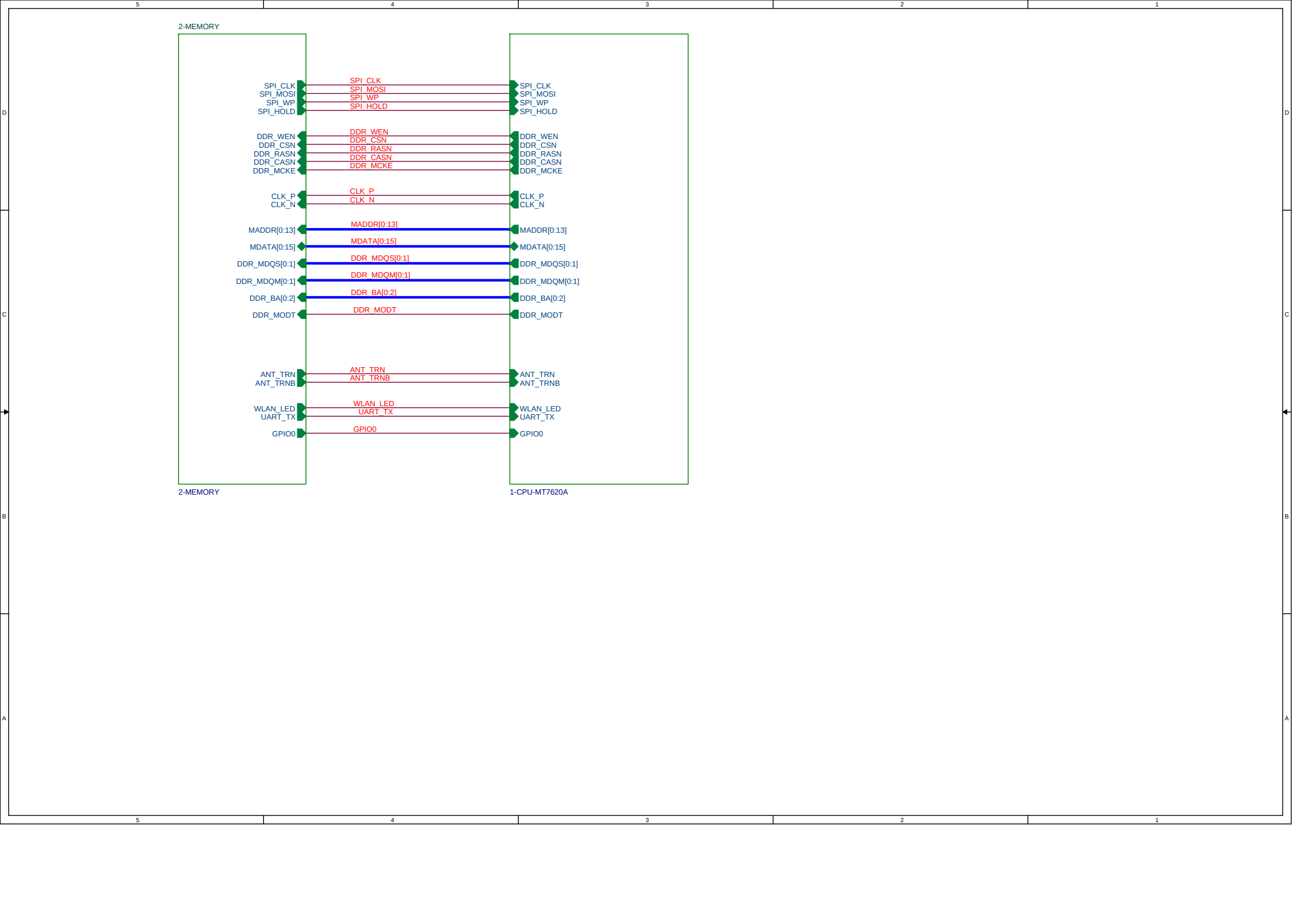
Title	GND		
Size	Number	GND	Revision
A4			
Date:	2015-7-16	Sheet	of
File:	D:\Project\Audio M & M Plus Touch Board\Audio M & M Plus Touch Board.sch	Drawn By	SchDoc

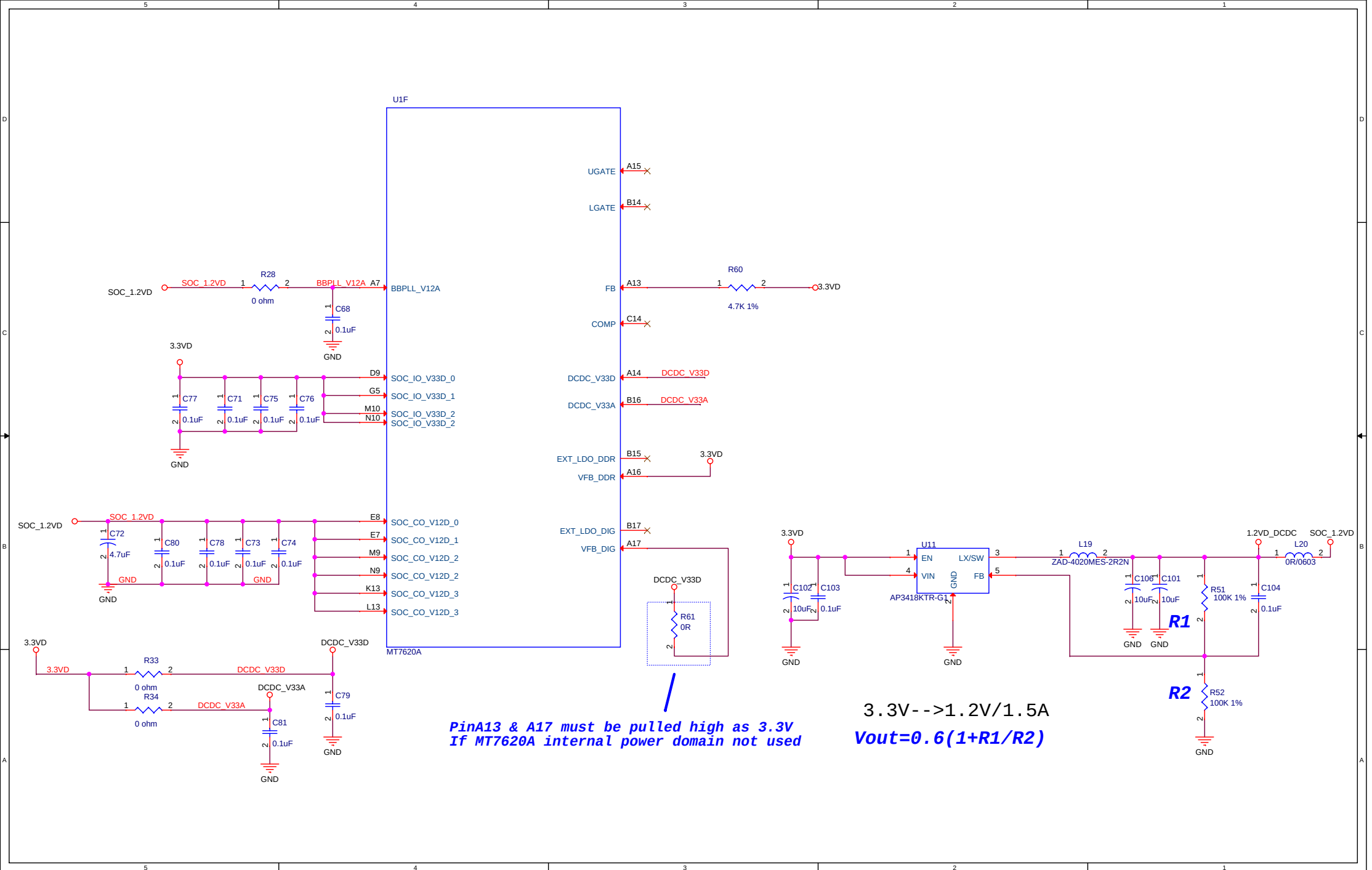


Title	GND		
Size	Number	GND	Revision
A4			
Date:	2015-7-16	Sheet	of
File:	D:\Project\Audio M & M Plus Touch Board\Drawings\Board VM1.0.SchDoc		

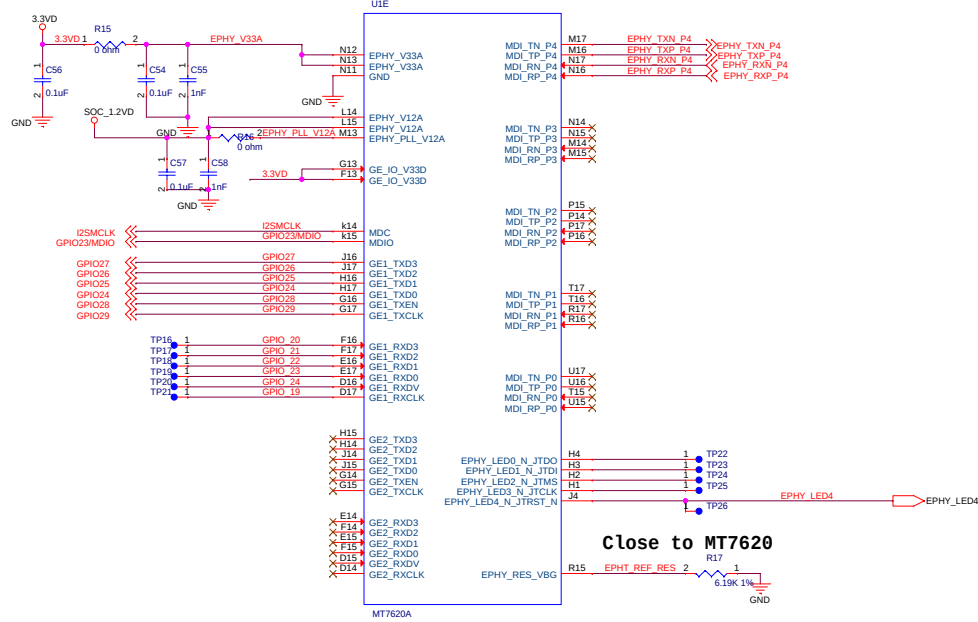


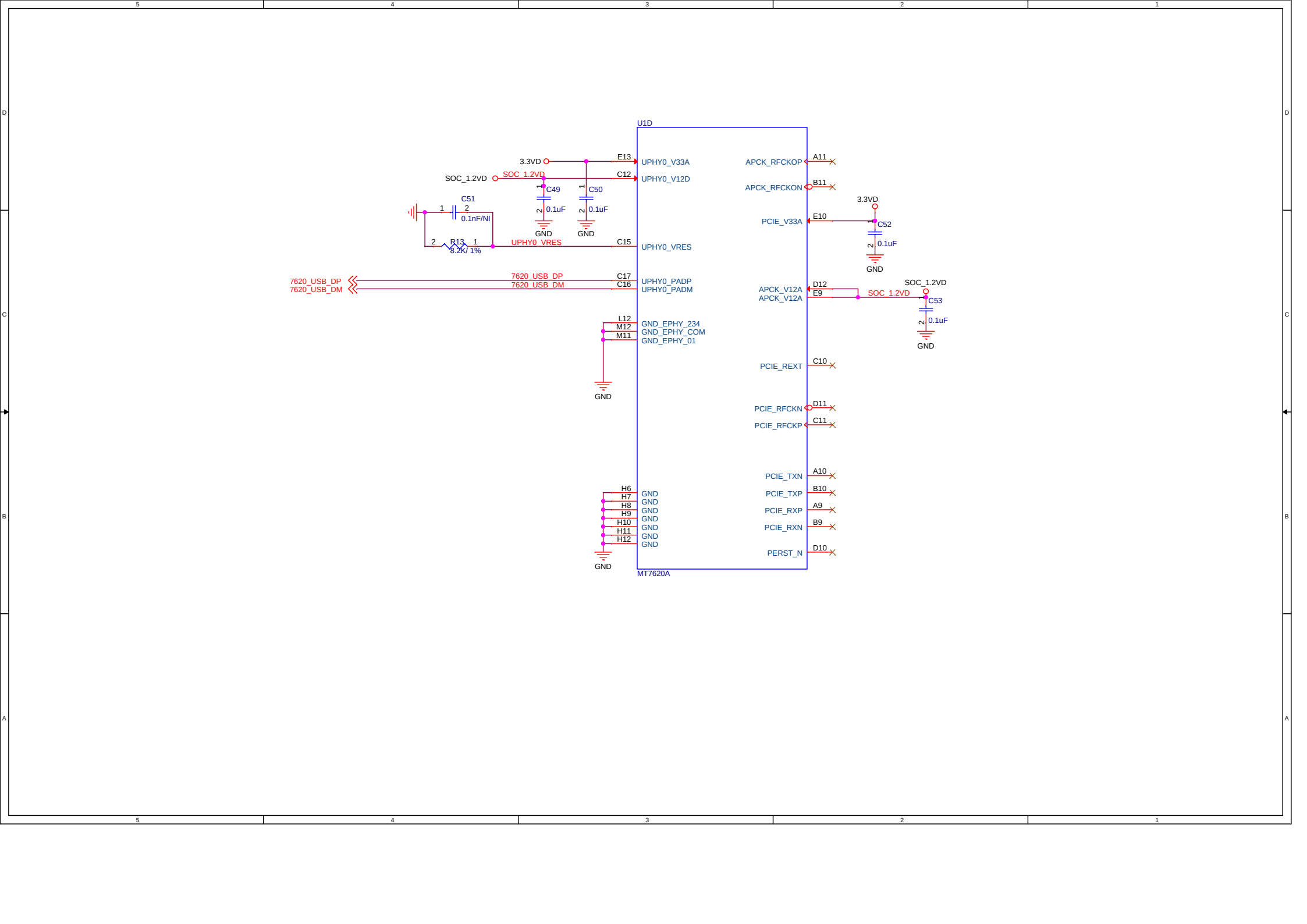
Title		
Size	Number	Revision
A2		
Date:	19-Apr-2013	Sheet of
File:	D:\Project\BT Receiver\DEV\DEV.ddb	Drawn By:



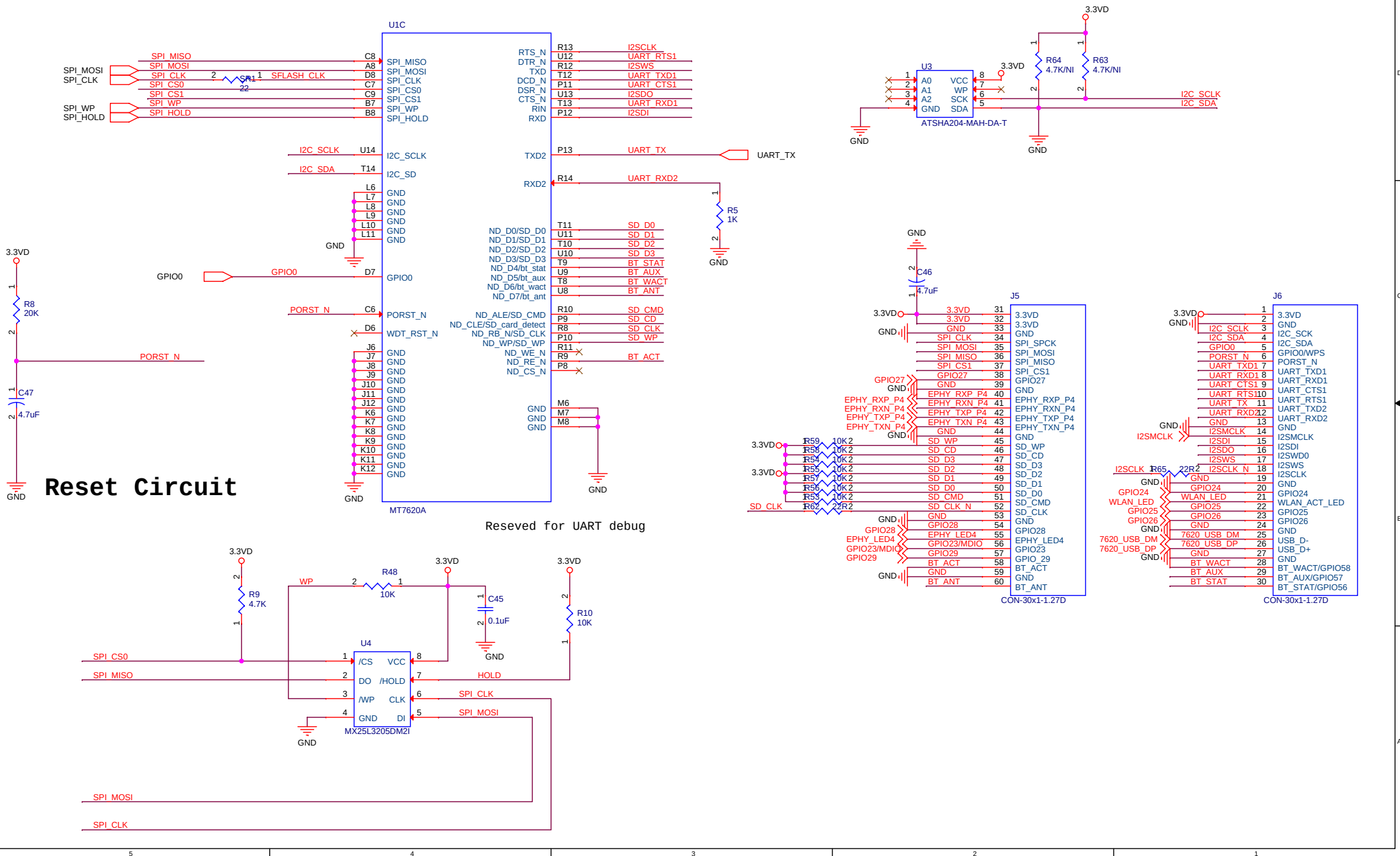


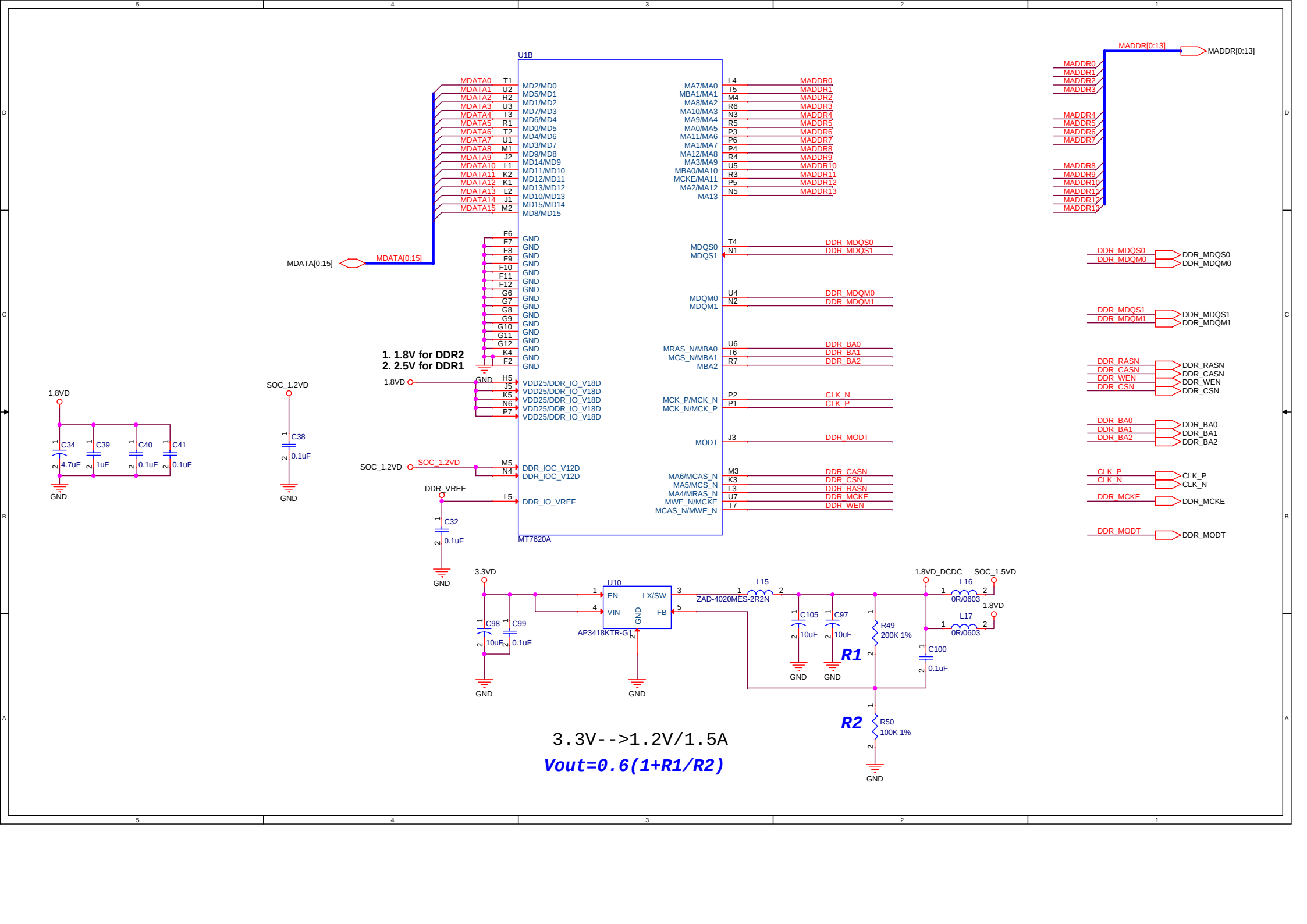
Close to MT7620

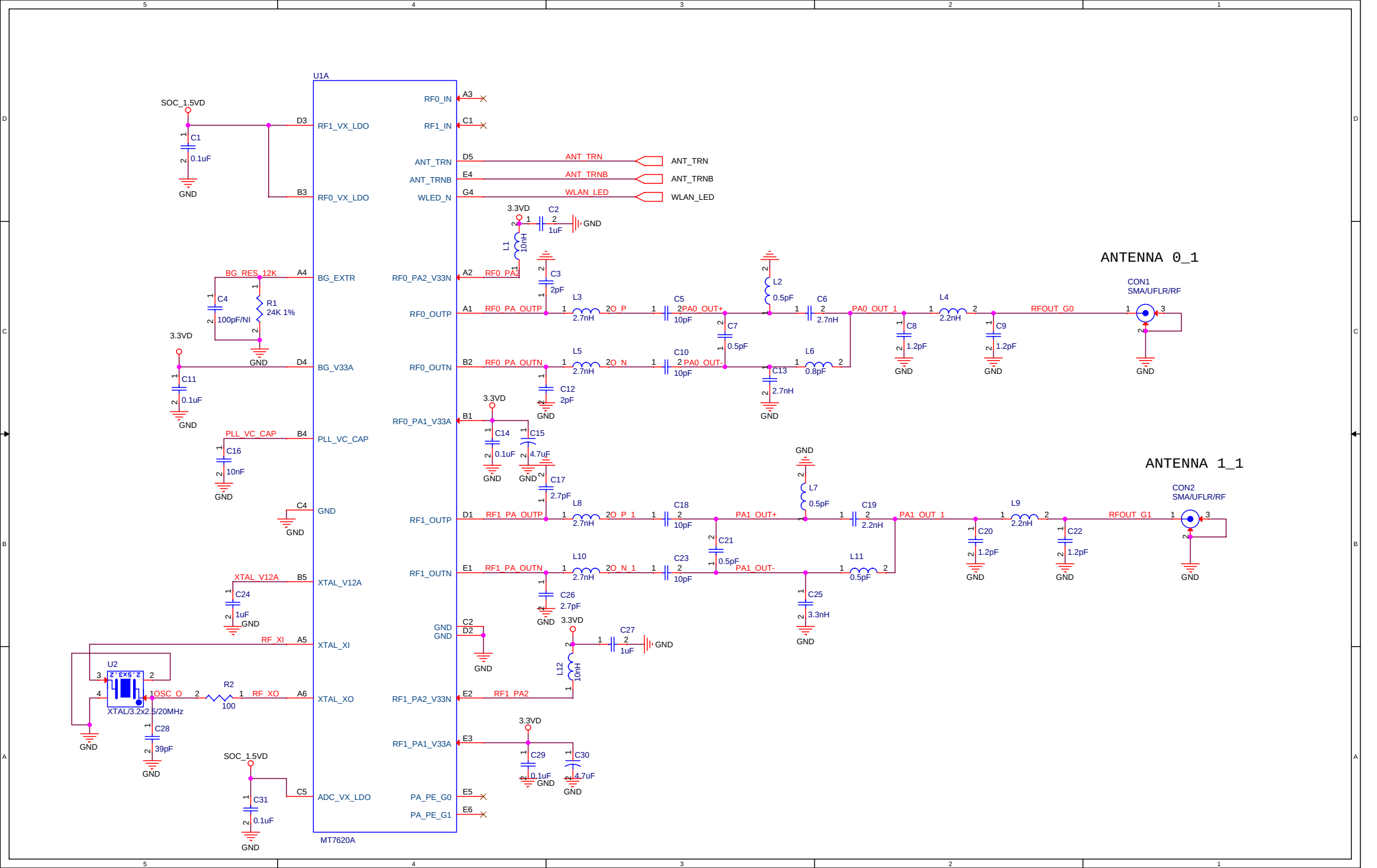


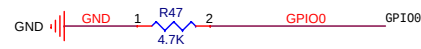
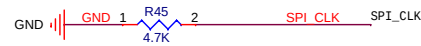
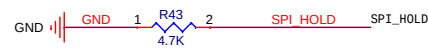
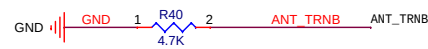
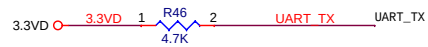
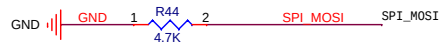
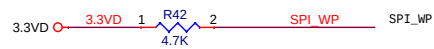
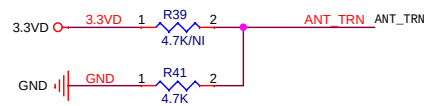
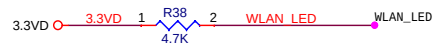
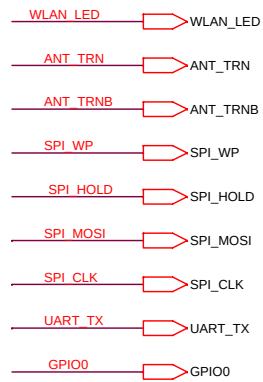


Reset Circuit









MT7620 Boot Up Strapping

Pin Name	Description	Value
WLAN_ACT_LED	DRAM_FROM_EE For non scan mode:	0: DRAM configuration from EEPROM 1: DRAM configuration from Auto Detect
ANT_TRN	DBG_JTAG_MODE	0: EPHY_LED 1: JTAG MODE
ANT_TRNB	XTAL_FREQ_SEL	0: 20MHz
{SPI_WP, SPI_HOLD}	DRAM_TYPE	01: DDR1 (CPU/3) TSOP 10: DDR2 (CPU/3) FBGA 11: SDRAM (CPU/5) (LVTTL 3.3 V) TSOP Package
{SPI_MOSI SPI_CLK, TXD2, GPIO0 }	CHIP_MODE[3:0]	A vector to set chip function/test/debug modes. In non-test/debug operation, 0010: Normal mode (boot from SPI 3Byte Addr)

